



Stony Brook
University

Cold and Warm Validation Data

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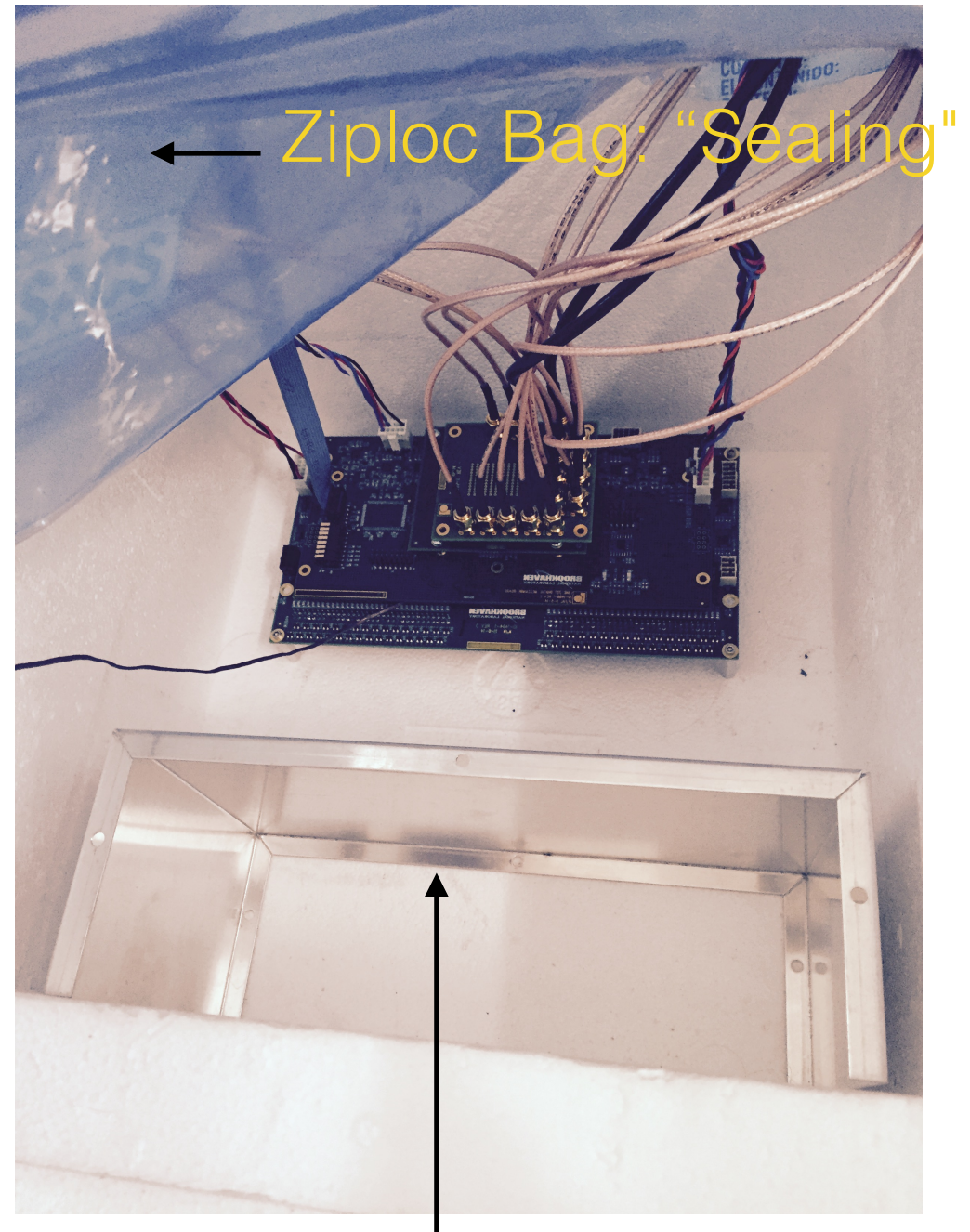
Cryo-Validation Data

- Board F11A11(Front End 11 and Analog board 11);
- Data taken in BNL in room and cryogenic temperatures.
- Validation data set: Baseline/Noise and internal injected pulses to calculate the gain in each channel.



Procedure

- Take warm validation data: can the board be programmed? (I2C, PGP)
- Slowly cool down the temperature with nitrogen gas;
- Take cryo validation data: immediately after it's cold and about 15 min later;
- Check if it's still working after it's warm again (next day).



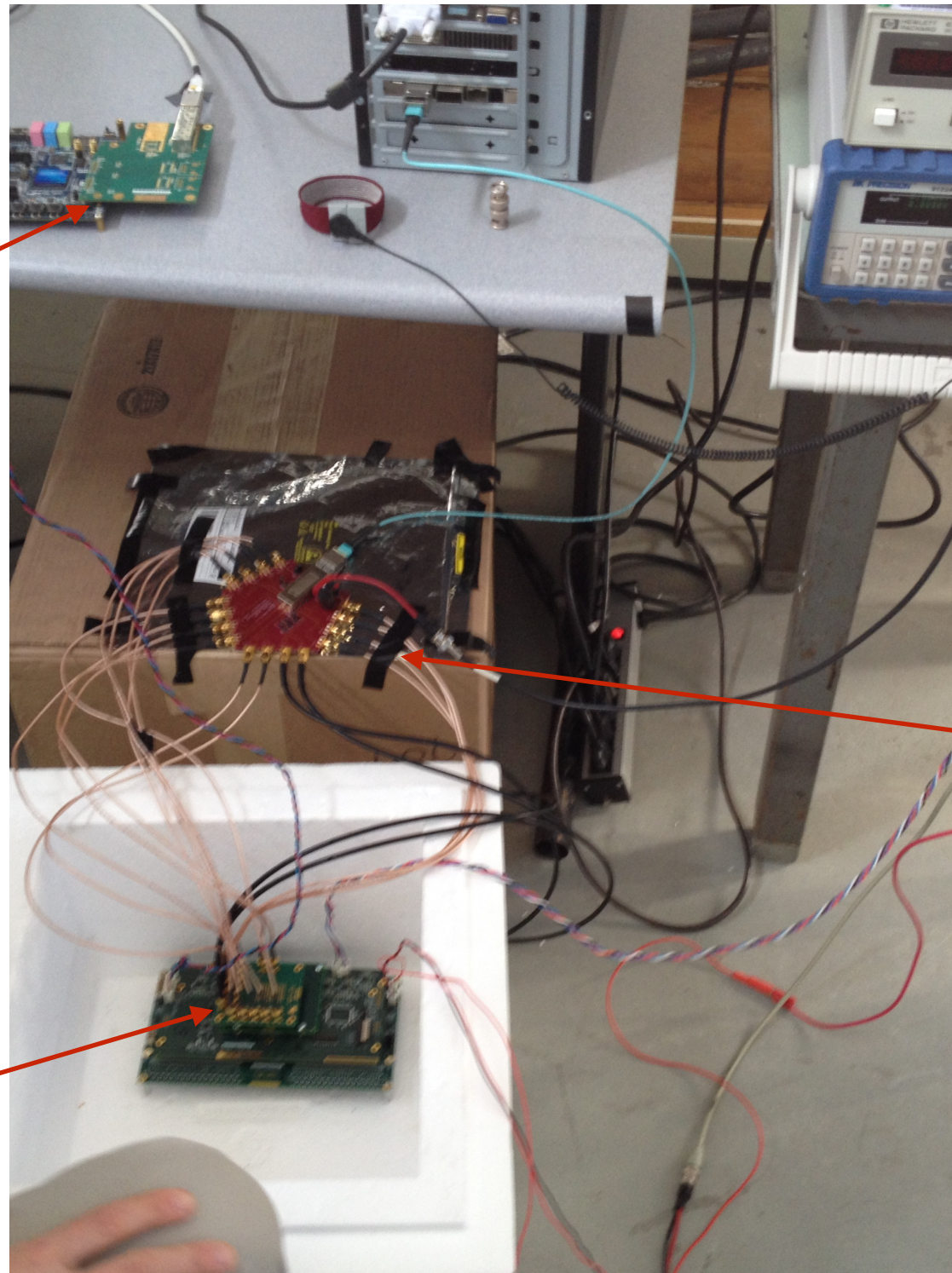
Test Stand

Evaluation
Board - PC CON

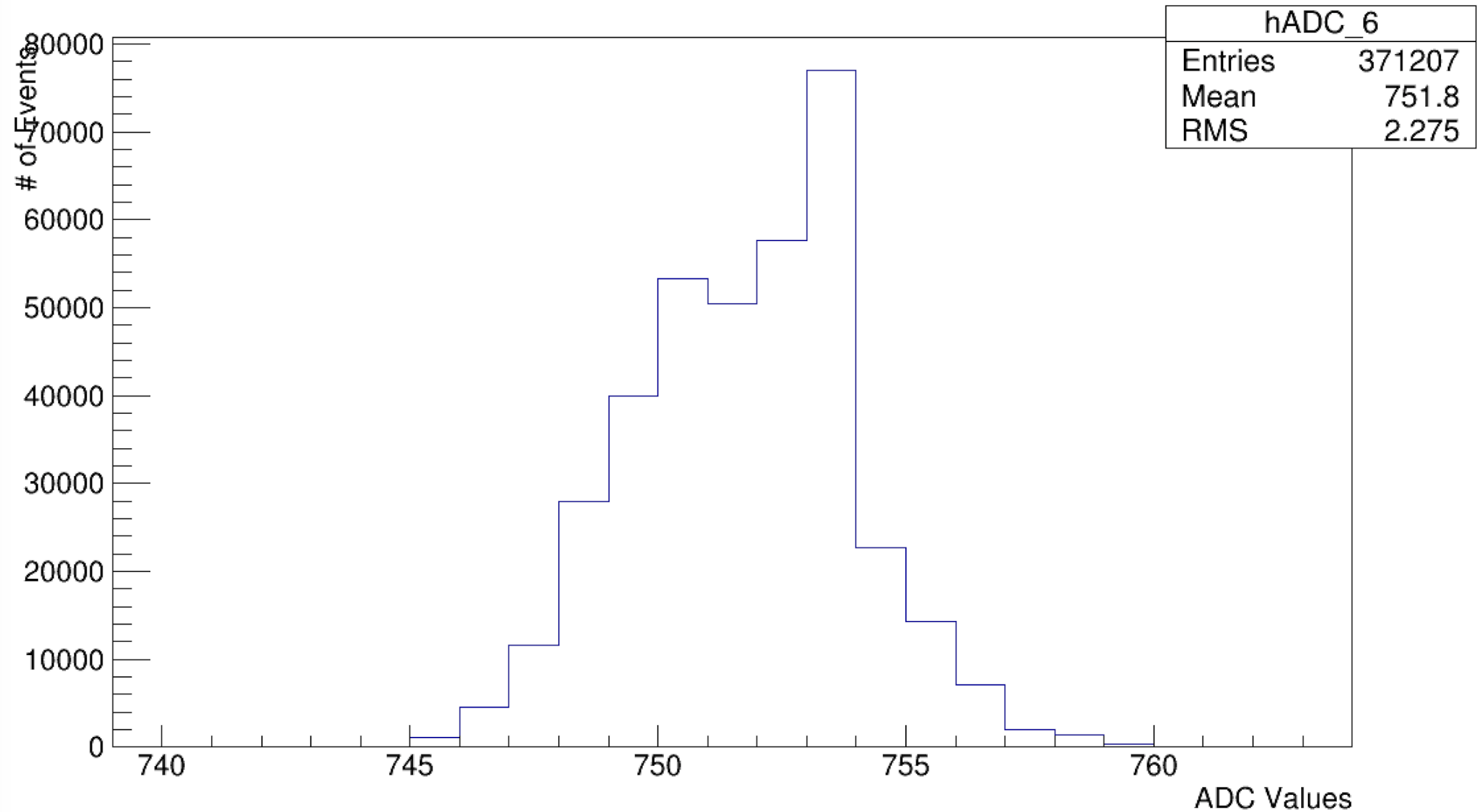
Power
Supplies

Optical
Board

Blaster:
programs the
firmware



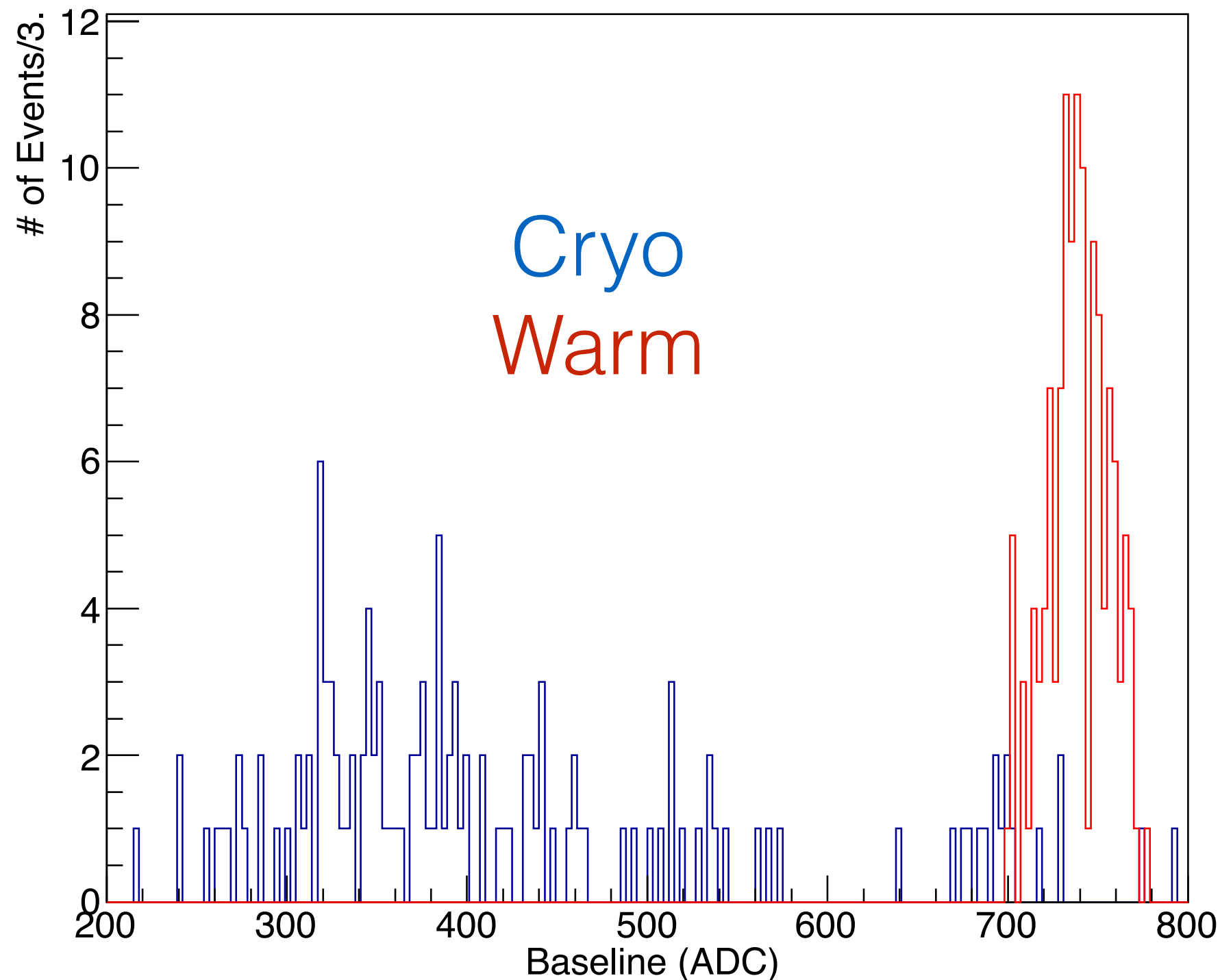
ADC Distribution of Chip3 - Channel 6



Baseline Distribution of F11A11

Gain: 0 and Shaping Time: 0

Baseline Distribution 0 0



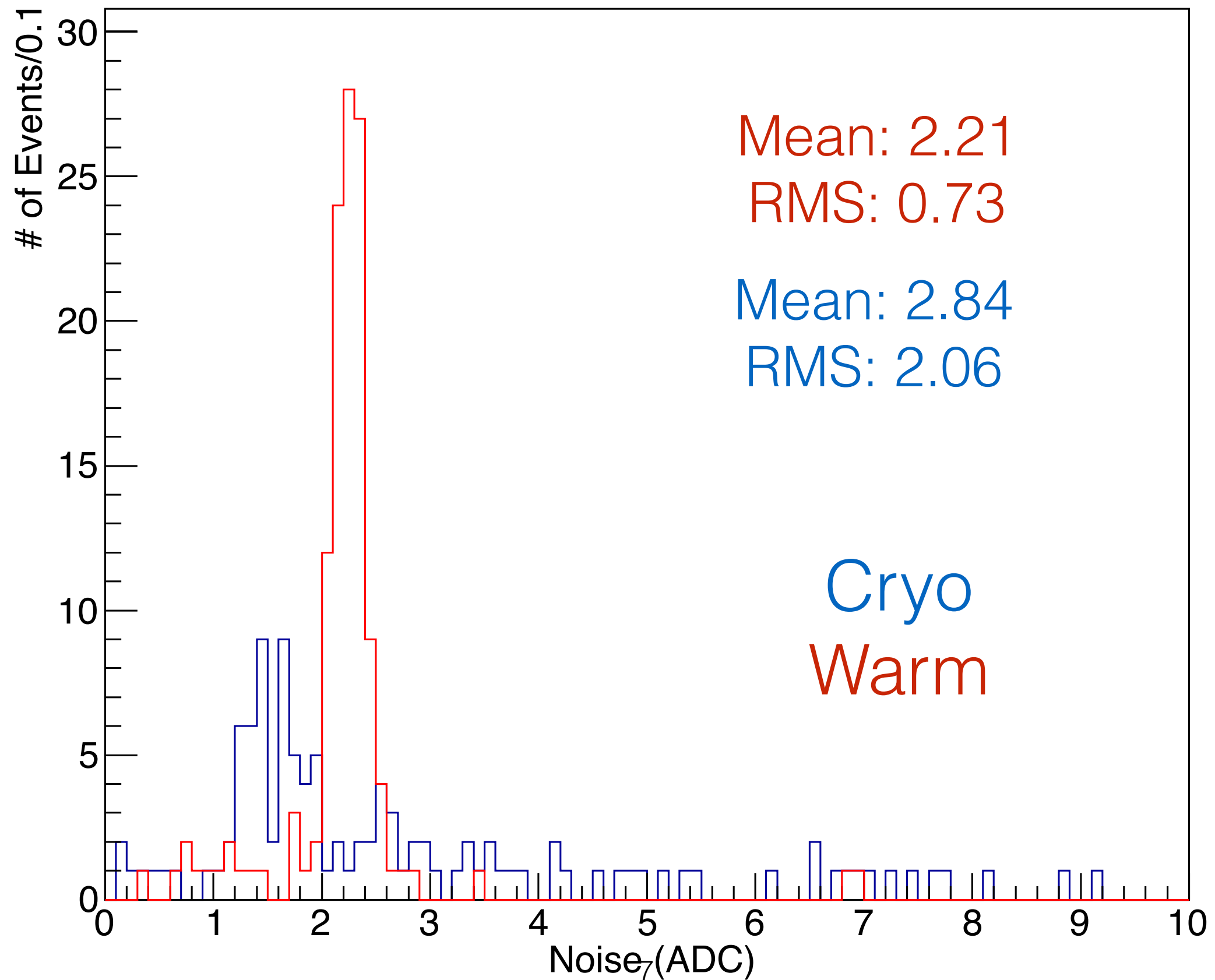
For Test
Cap ON.

Larger Baseline
and
Concentrated
Mean: 739.1
RMS: 17.54

Smaller
Baseline
and Spread
Mean: 424.6
RMS: 133.2

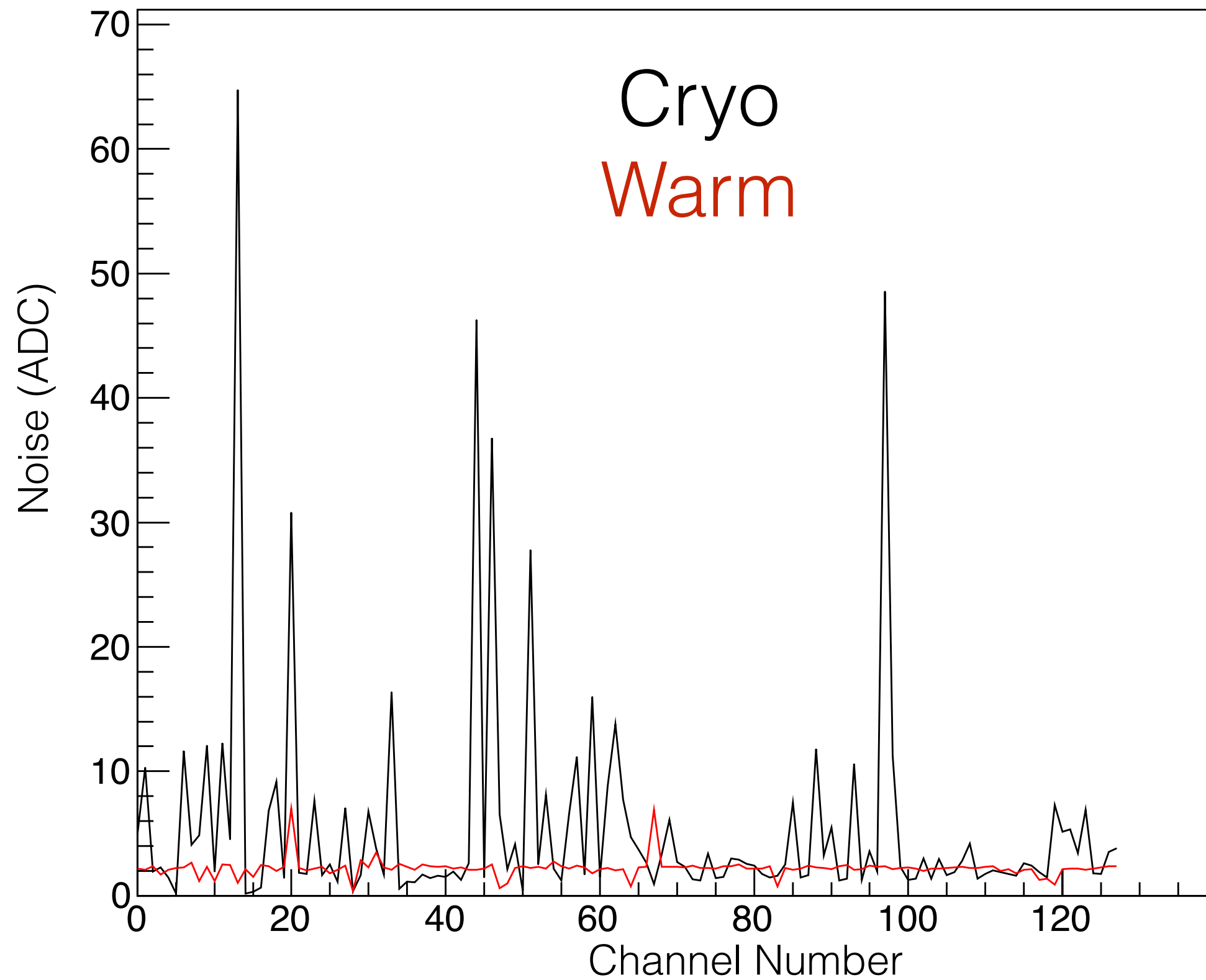
Noise Distribution of F11A11

Noise Distribution 0 0

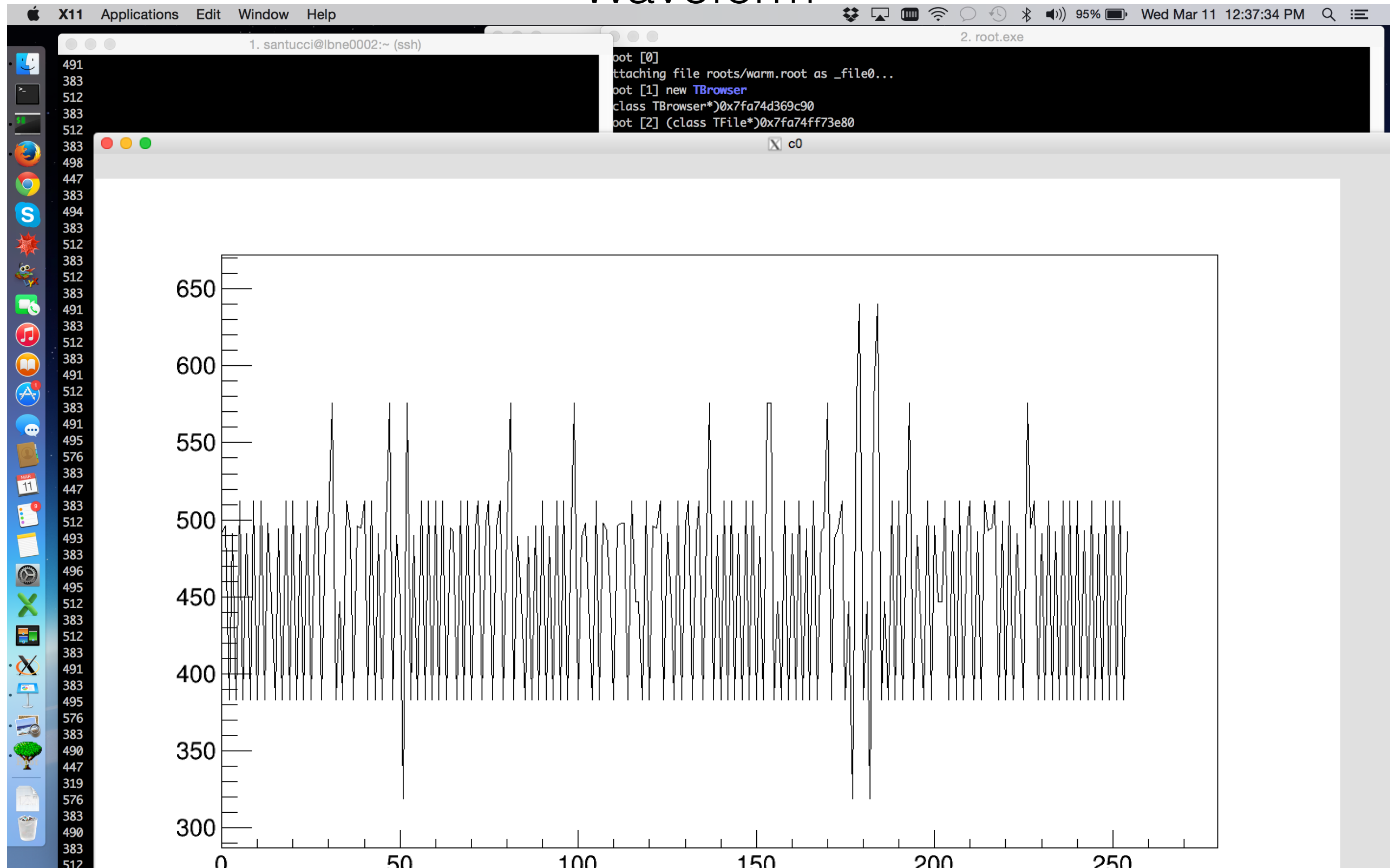


Mean Noise per Channel

Noise per Channel, 0 0

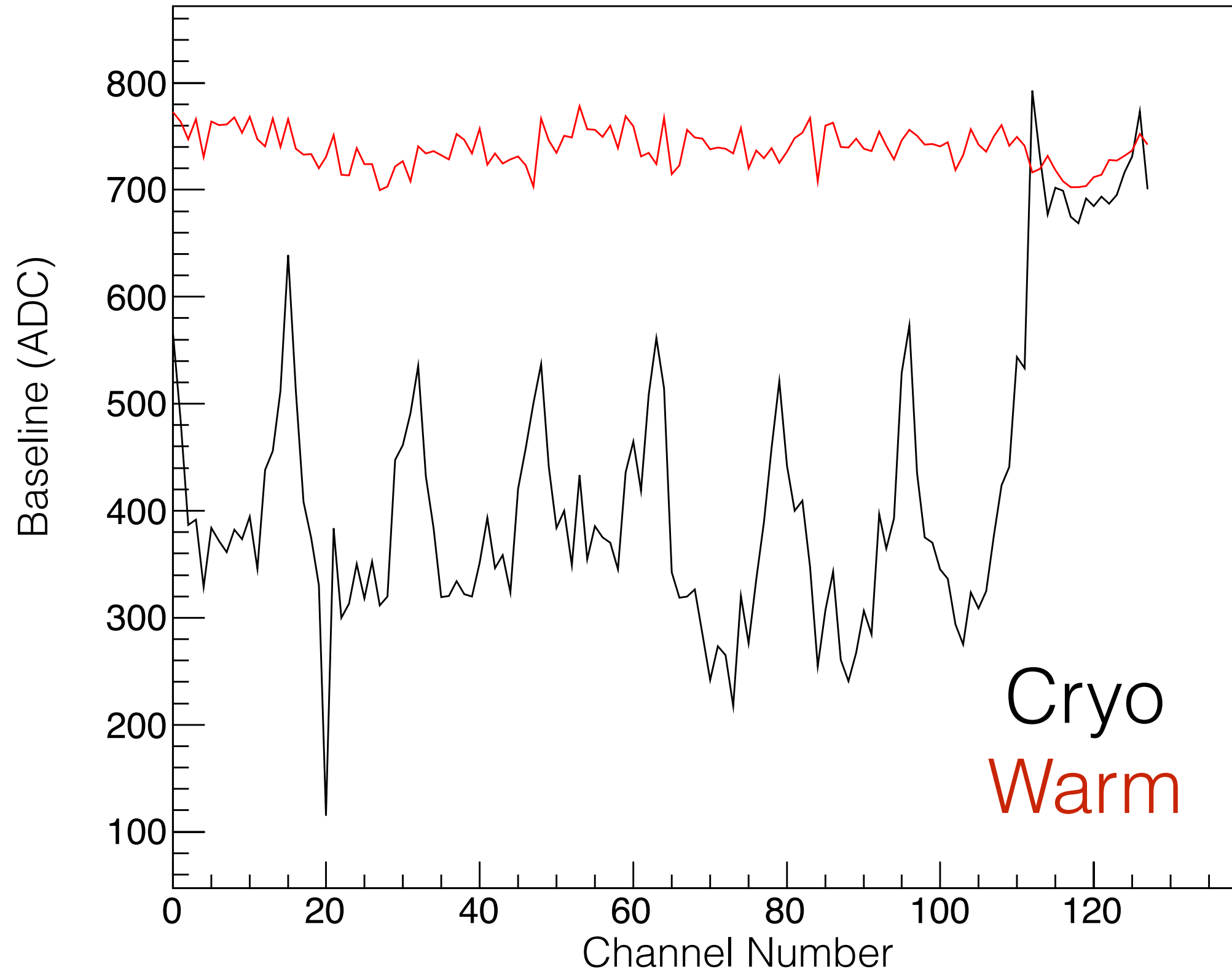


Reason for Large Noise - Cryo Waveform



Mean Baseline per Channel

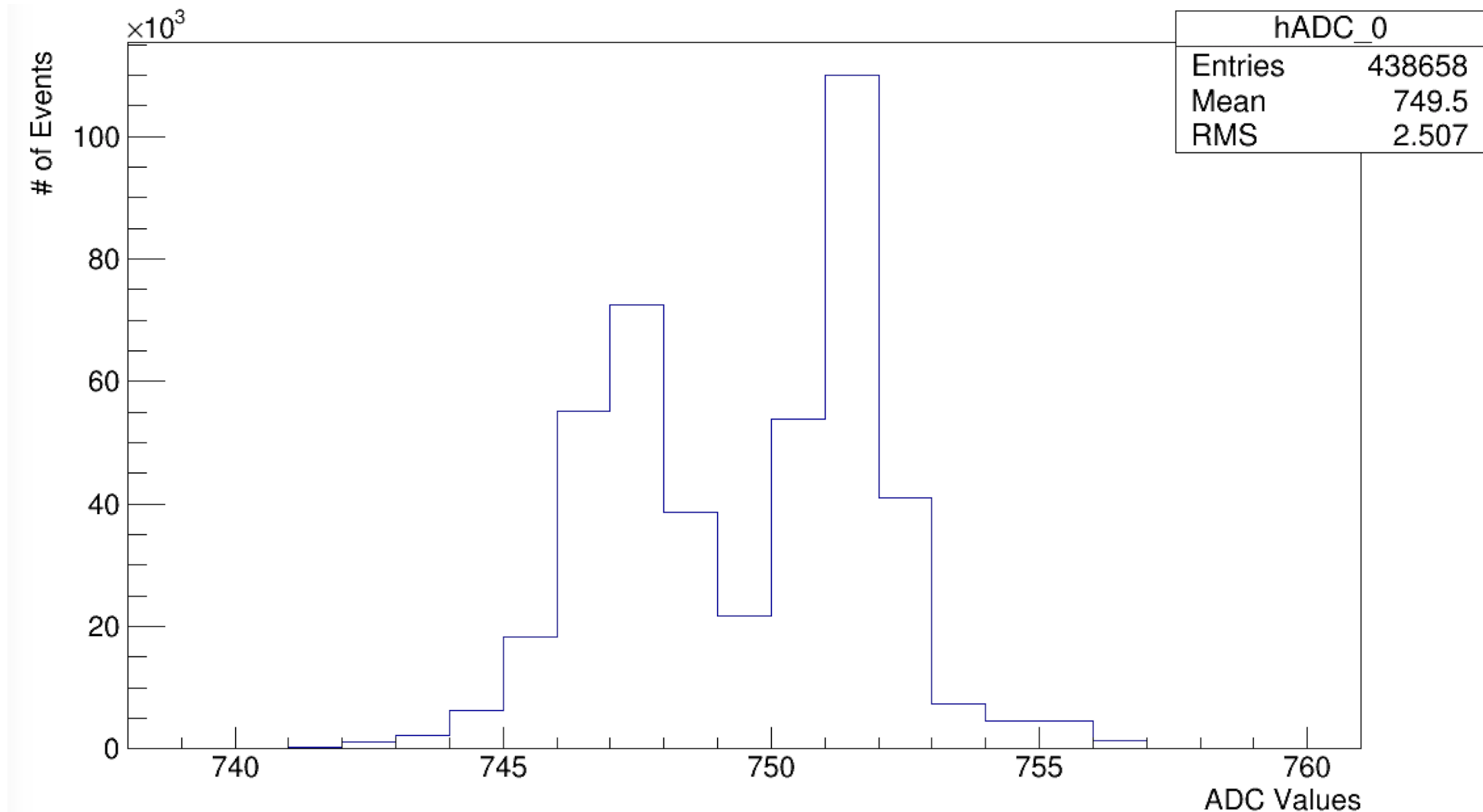
Baseline per Channel, 0 0



Cryo
Warm

Extras

ADC Distribution of Chip3 - Channel 0 Stuck Bit Example

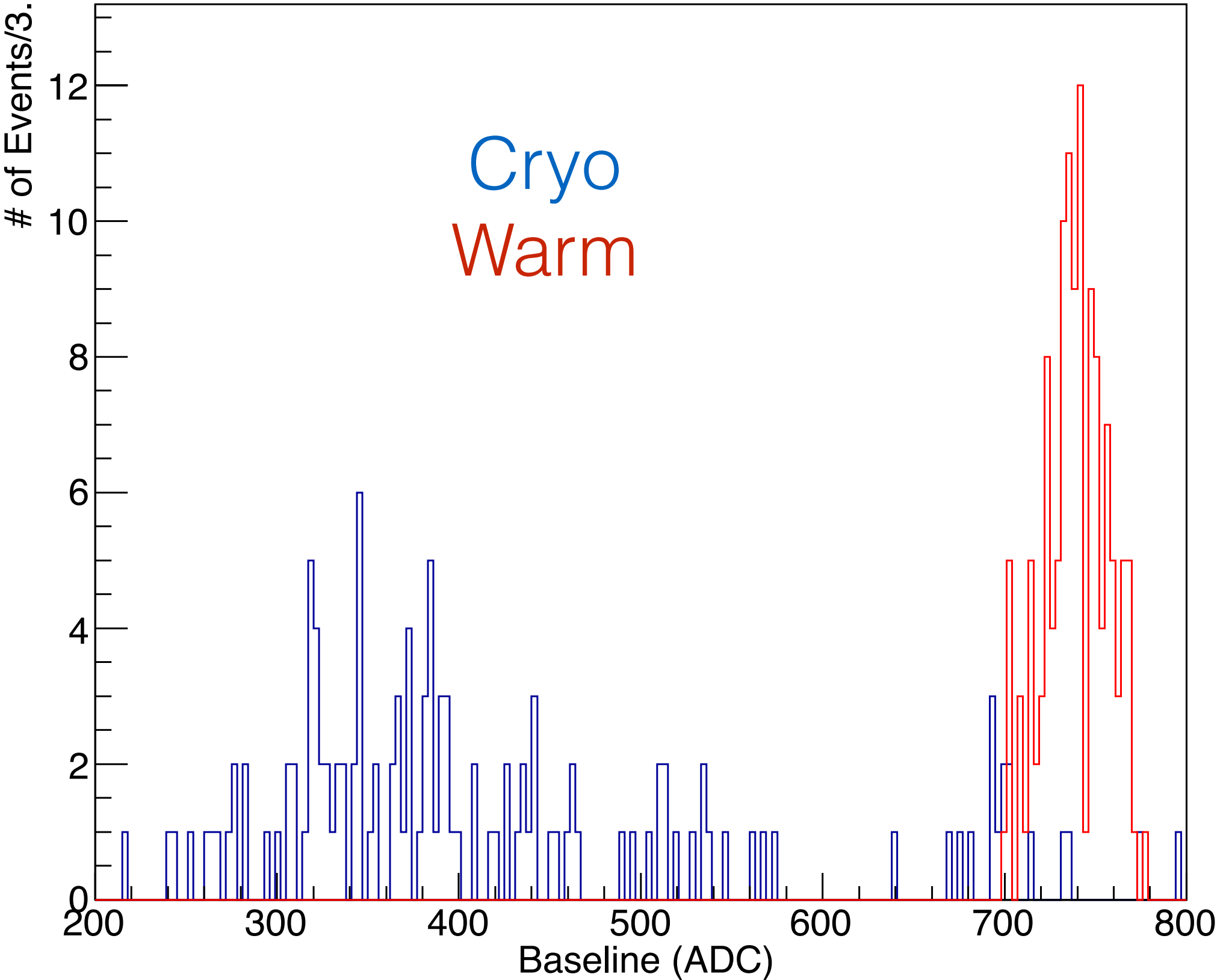


Mean: baseline;
RMS: noise.

Not very reliable.
Need to apply cut!

Baseline Distribution of F11A11

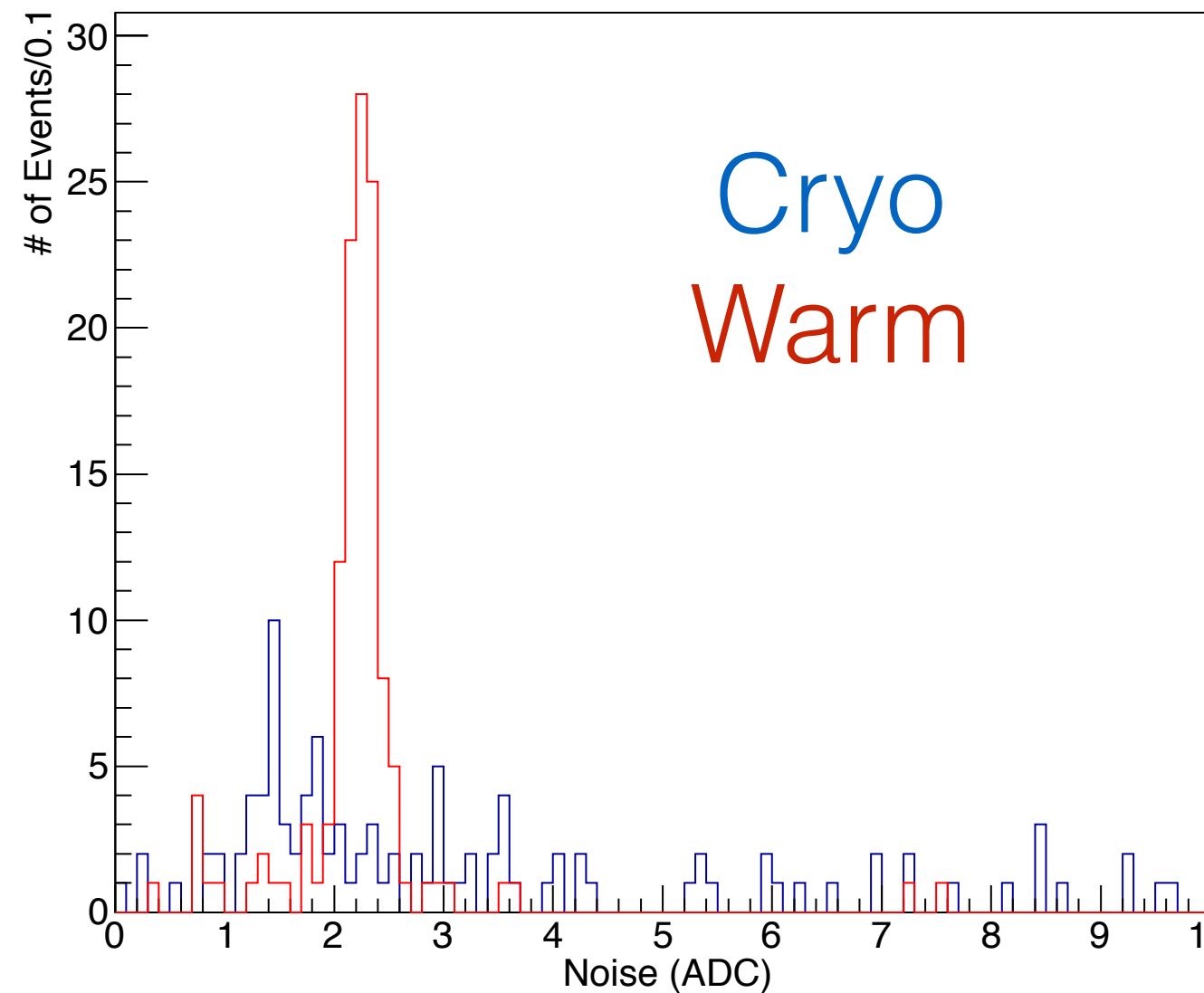
Baseline Distribution 0 0



For Test
Cap OFF.

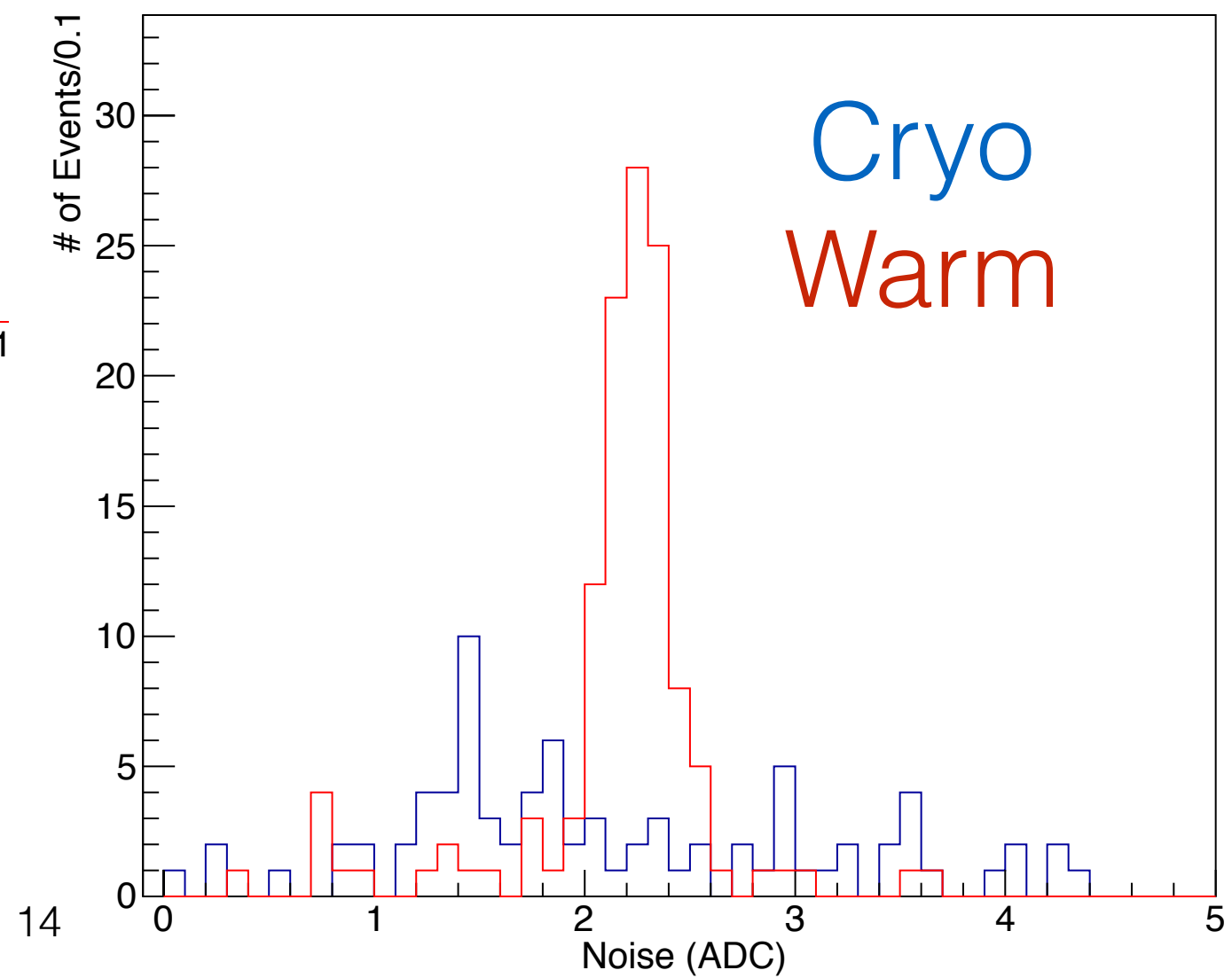
Noise Distribution of F11A11

Noise Distribution 0 0



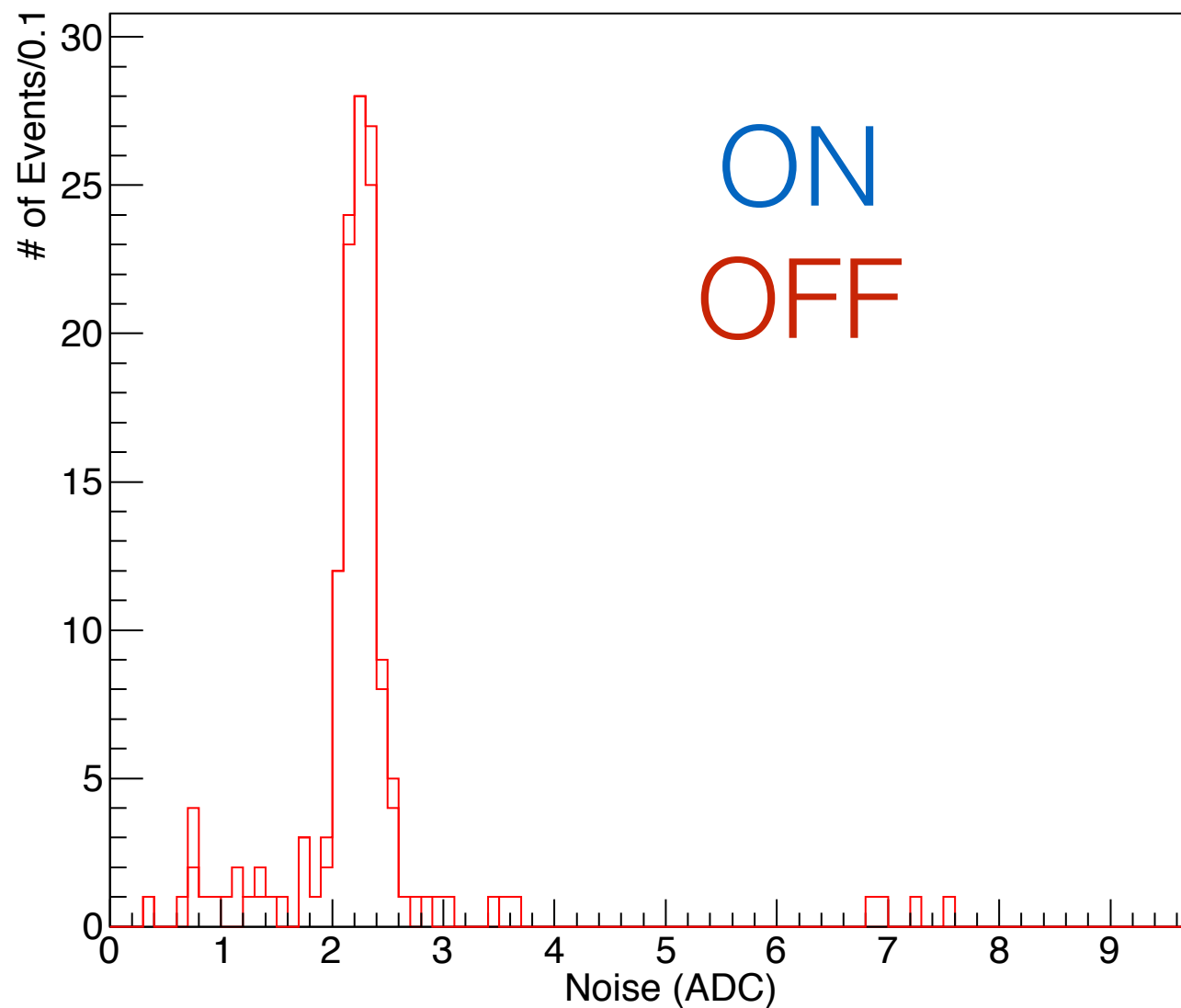
For Test
Cap OFF.

Noise Distribution 0 0



Noise Distribution of F11A11

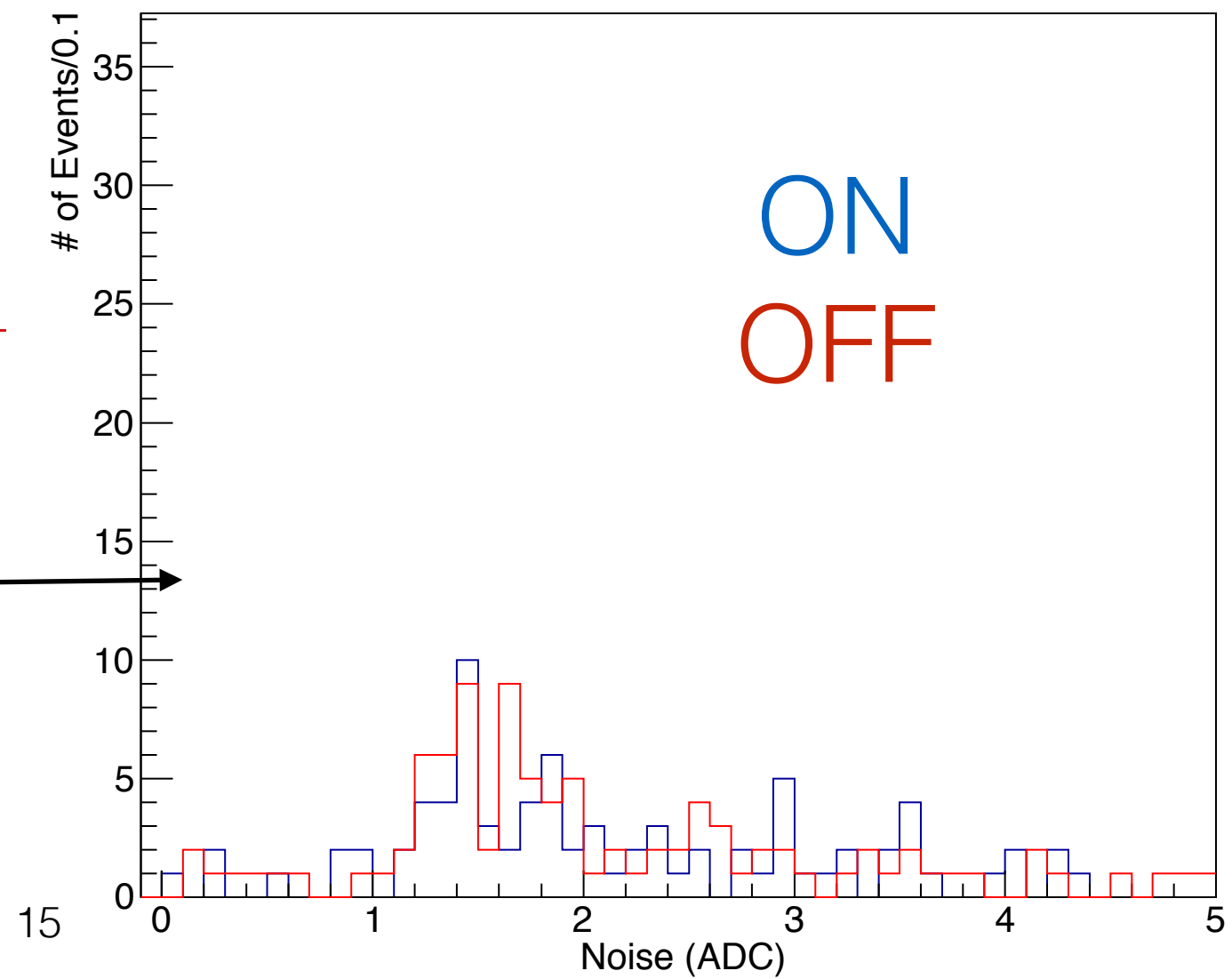
Noise Distribution 0 0



ON
OFF

WARM

Noise Distribution 0 0

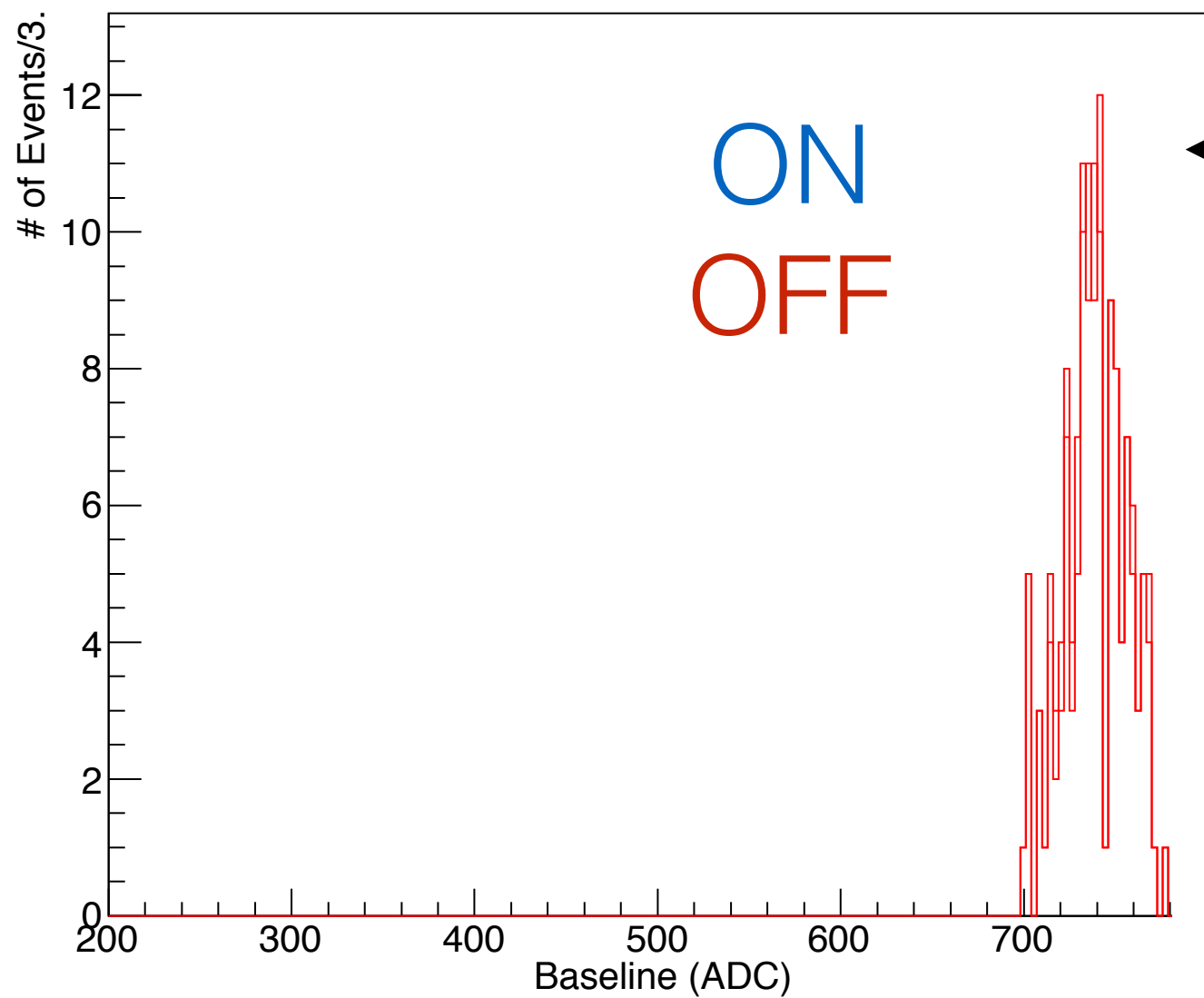


ON
OFF

CRYO

Baseline Distributions

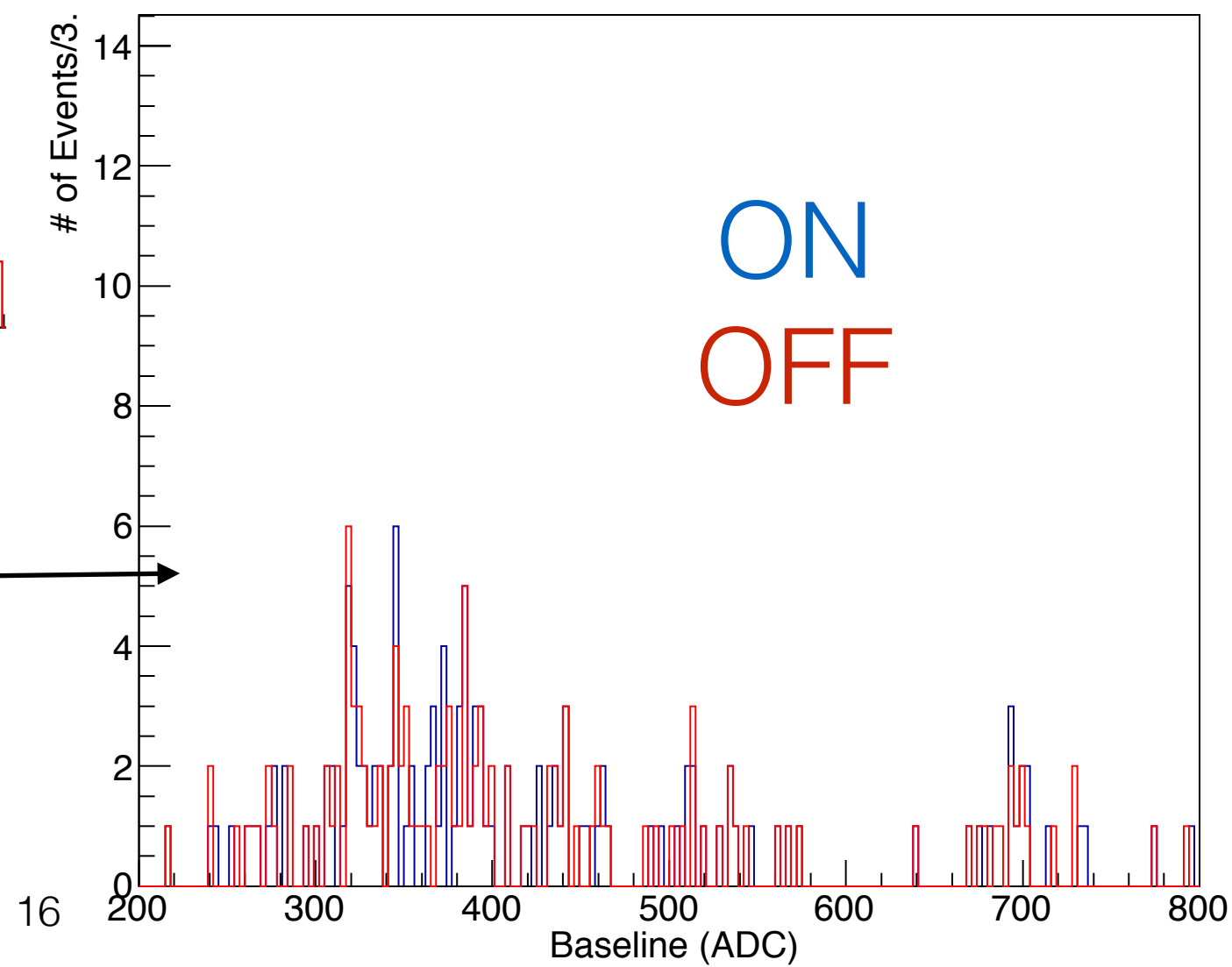
Baseline Distribution 0 0



WARM

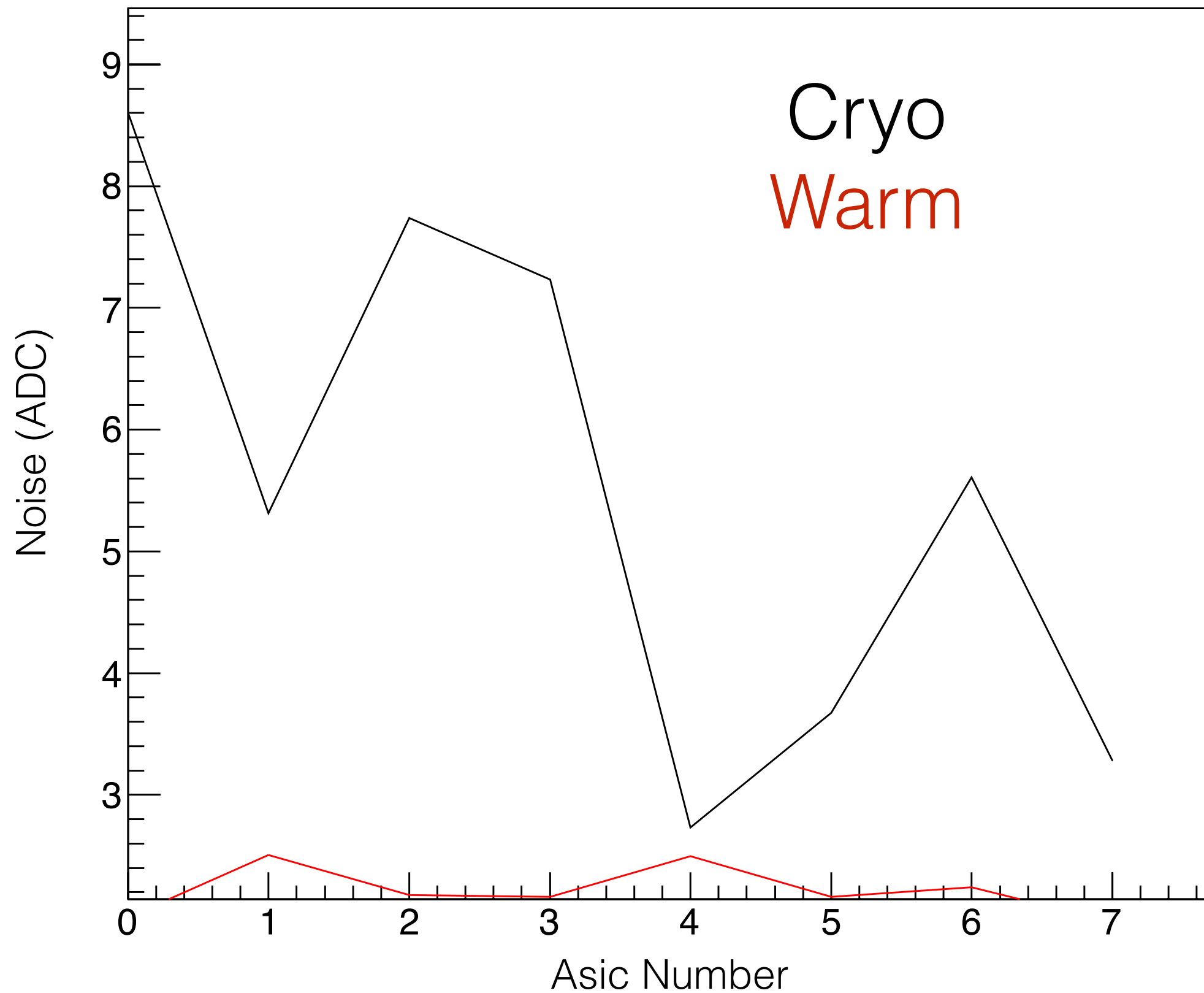
Baseline Distribution 0 0

CRYO



Mean Noise per Asic

Noise per Asic, 0 0



Mean Baseline per Asic

Baseline per Asic, 0 0

