

Subject: Re: [Sphenix-intt-l] INTT Weekly Meeting <minutes>
From: Itaru Nakagawa <itaru@riken.jp>
Date: 2021/05/06 12:17
To: sphenix-intt-l@lists.bnl.gov

Hi All,

Here is the minutes from today's meeting:

o Taiwan Assembly <Cheng-Wei>

- Shifting phase attempt can be doable at LVDS receiver of the ROC according to the FPGA coder expert in NTU. They've already purchased FPGA download cable, so hardware wise they are ready for the attempt.
- **Takashi** sends one or two bus extenders to Taiwan so that they can reproduce the half entry issue in their test bench. Then they can play with shifting phase of the LVDS receiver of the ROC.
- Following Rachid's suggestion, add DP460 glue to the joint between SUS tube and the body of the stave and then perform the twist test in Taiwan.
- Supplemental glue DP460 between the joint between SUS tube and the body of the stave is now considered as a default process to assemble the batch-3 stave production. **Itaru** will update the batch-3 production assembly procedure. Also requires the 45 degrees twist test the joint between SUS and carbon tubes before its pressure and cooling tests. A good joint shouldn't be broken by 45 degrees twist and should be absorbed by the carbon tube. The procedure should be tested and established in Asuka before the 3rd batch production.

o BNL assembly <Milan>

- **Rachid** will pick the diverged choice of HDIs from various LOT. Any suspicious behavior of HDI suppose to be reported to Itaru so that Itaru can feedback to Yamashita.
- Barrel and service design review is schedule on May 25th.

o HDI Update <Itaru>

- **Cheng-Wei** send presentation slides to Rachid of the inspection instruction of the conductivity of HDI thruholes. Rachid also follow the conductivity test of slow control lines of HDI's developed by Taiwan group in BNL as well.
- **Japanese group** will develop an application or macro to take over the conductivity test in somewhat automated way using the interception board. The conductivity of each SC line will be checked by observing the readback signal from each FPHX chip on HDI.

Best regards,

-itaru

On 2021/05/05 18:31, Itaru Nakagawa wrote:

Dear all,

This is an announcement of our weekly meeting.

May 5th Wednesday, 8PM in Eastern time = May 6th Thursday 9AM in Japan = 8AM in Taiwan

*indico

<https://indico.bnl.gov/event/11410/>

*BlueJeans

<https://bluejeans.com/259237842>

Best regards,

-itaru

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