

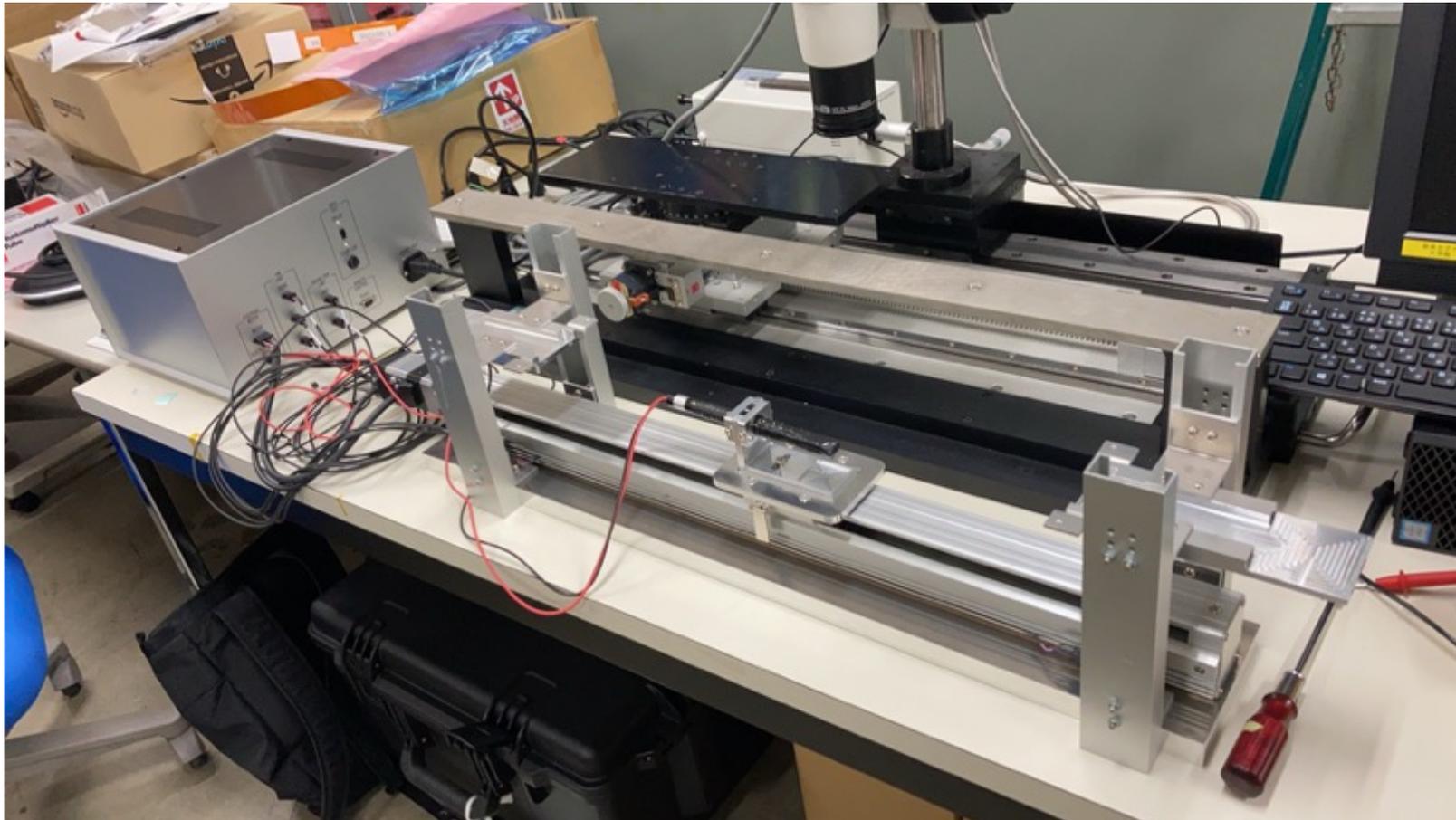
Progress Report

RIKEN/RBRC

Itaru Nakagawa

2nd Source Test Fixture

Delivered to NWU last July 21st.



- Test operation
- Ladder face up/down issue
- Semi-automatic side change
- Measurement condition optimization.
- First observation of silicon signal from the BNL pre-production ladders and quick feedback.
- The progress is to be reported by a NWU student in next couple of weeks.

FPHX Readbacker

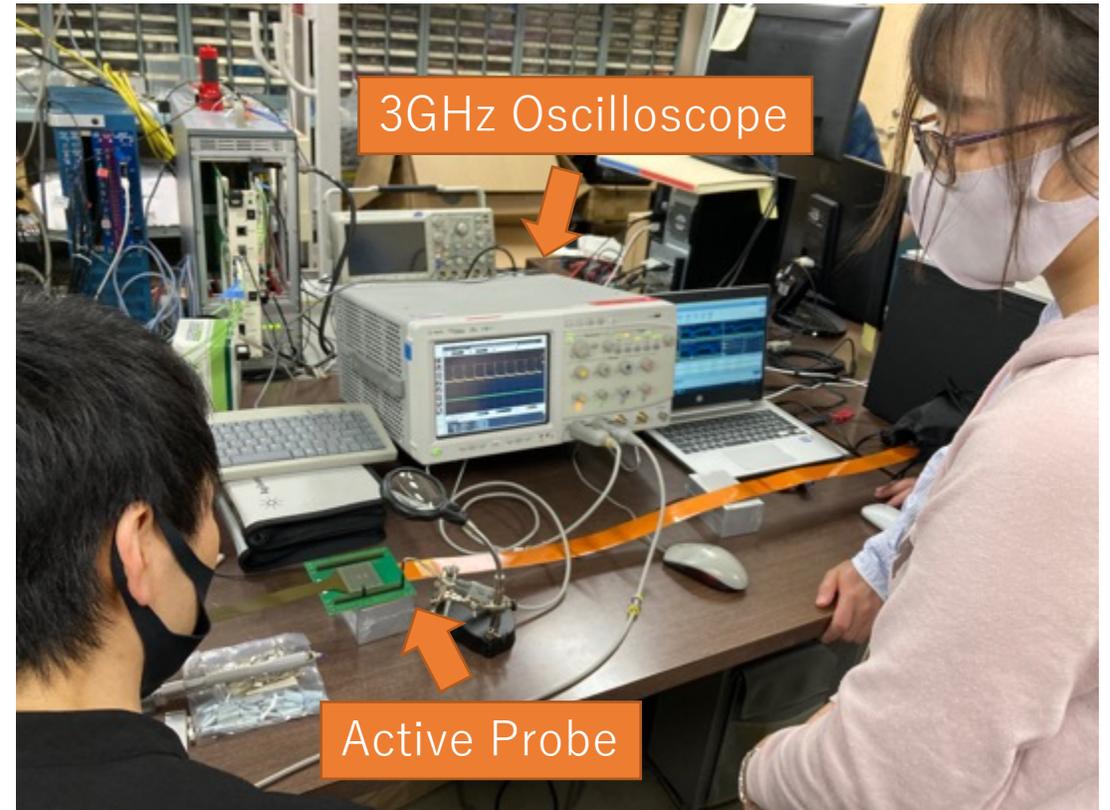


Luna T.

- Development of USB between FEM-IB to PC in the FEM-IB FPGA code is ongoing.
- Once the this data transfer is established, then Genki will implement readback values into Nevis GUI in PC.

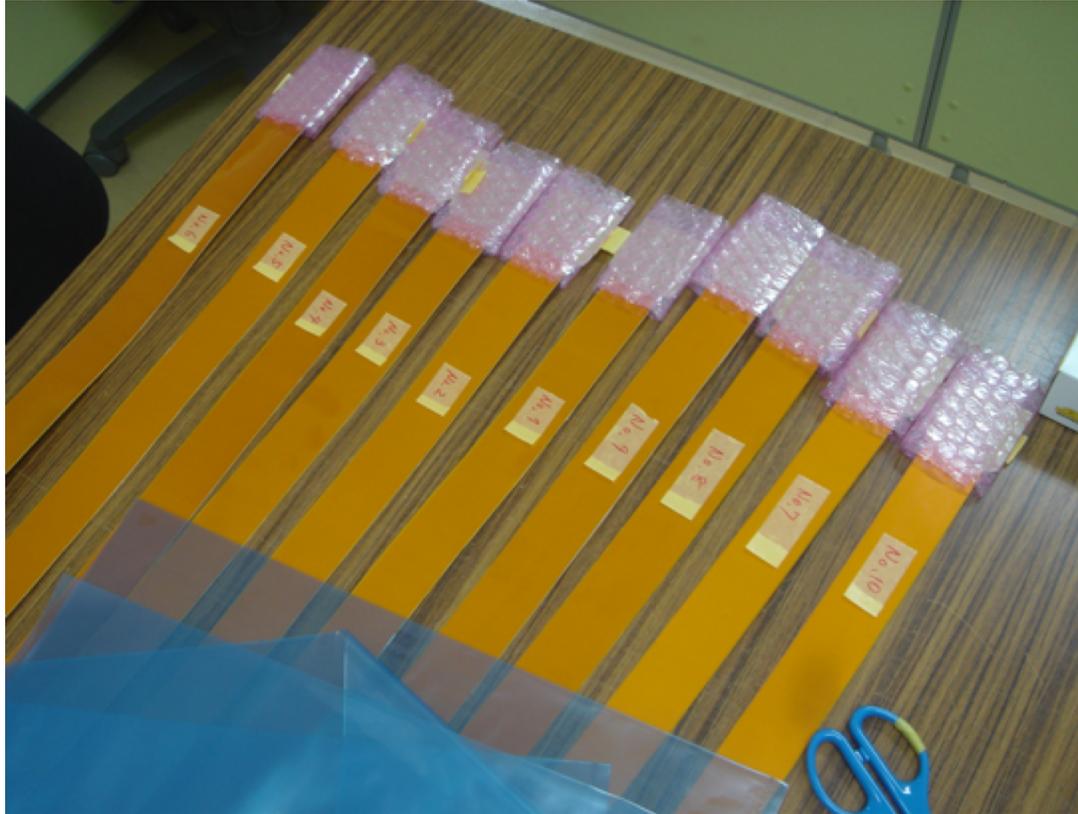
Half Entry Debugging (5/26 ~ 28)

Inviting Takashi Kondo from Tokyo Industrial Research Center for monitoring data transmission.



Best measurement of signal transmission we can do. We will see if we find any anomaly in the signal shape.

1.2meter Pre-Production Bus Extender



- 6 good cables are to be delivered to RIKEN in 5/27. Another ~10 good cables are to be processed next.
- New cables are to be evaluated performance in Tokyo Metropolitan Industrial Research Center and NWU.

HDI Replacements

Itaru's proposal

- heat cycle: -45~125°C for 144 cycles (ramp time & interval 2 minutes, respectively)
- All HDI should go through the heat cycle

Yamashita's recommendation

- Heat cycle: room temp ~ 235 °C for 4 times
- Apply the heat cycle only for sampled HDIs.
- HDIs to be delivered shouldn't go through heat cycle because it can possibly damage thruholes.