

Design of INTT source test platform

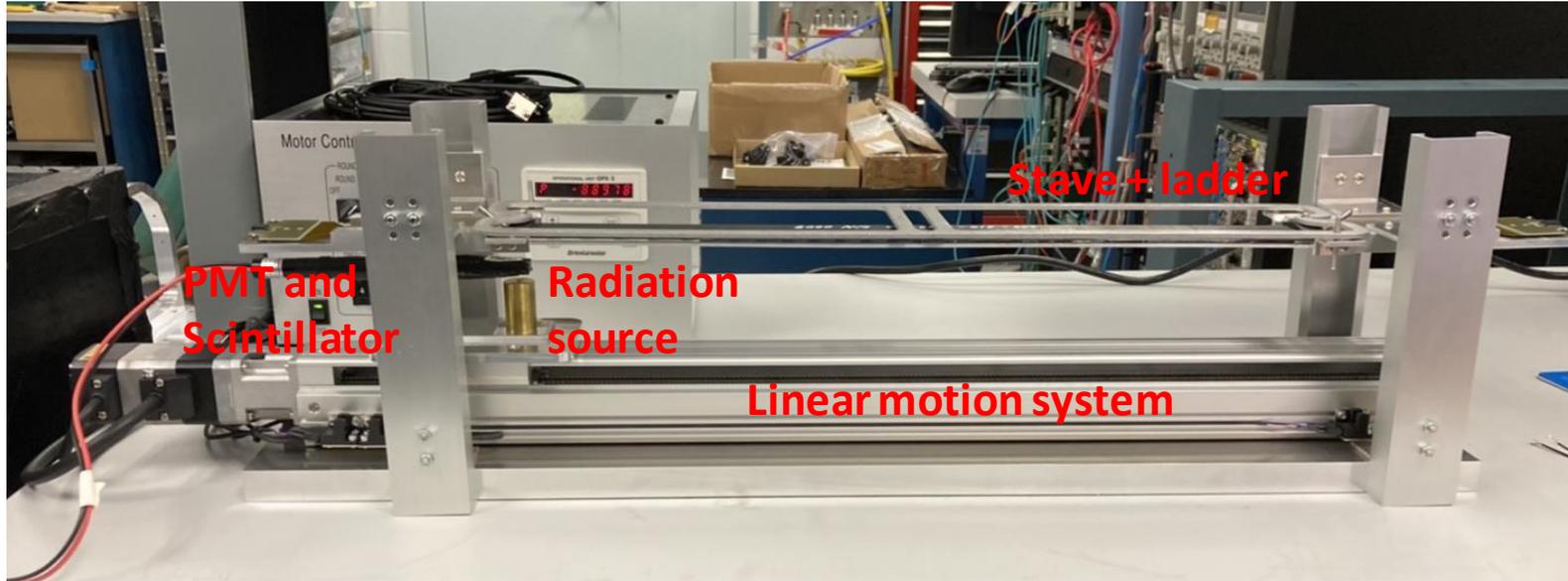
2021/05/26

Purdue University

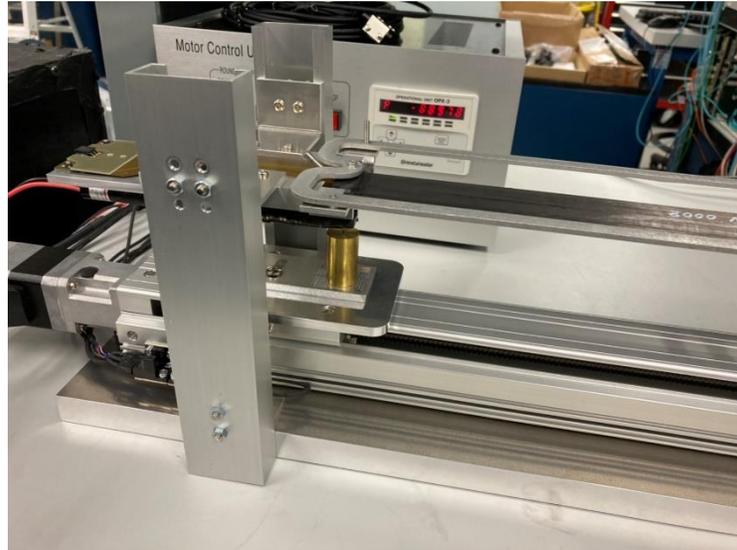
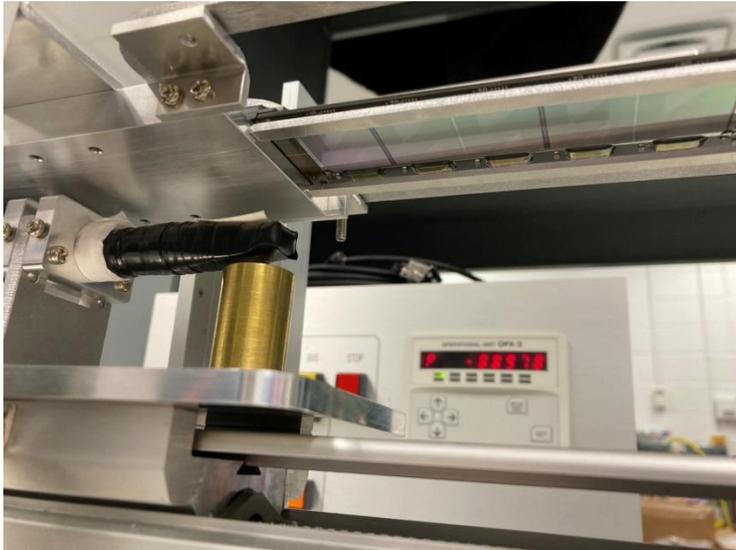
Han-Sheng Li



The setup of source test fixture



We have set up the source test fixture, and wait for the dark box available



Issue about the connection for the test



Three options:

1. Set up HDI's face up on the fixture. The radiation must penetrate through HDI before it gets to silicon, but this can be the compromise.
2. Operate the ROC facing down, but then we lift it a few inches above the table to cool FPGA by a fan.
3. Have an anti-conversion cable.