

Progress Report

RIKEN/RBRC

Itaru Nakagawa

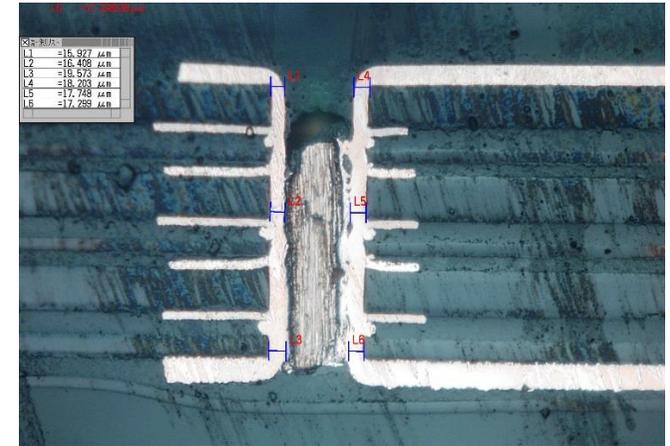
New INTT Meeting Time

- Tuesday 9PM in BNL time = Wednesday 10AM in Japan = Wednesday 9AM in Taiwan
- The new meeting schedule is to be reflected from July 6th (July 7th in Asia).

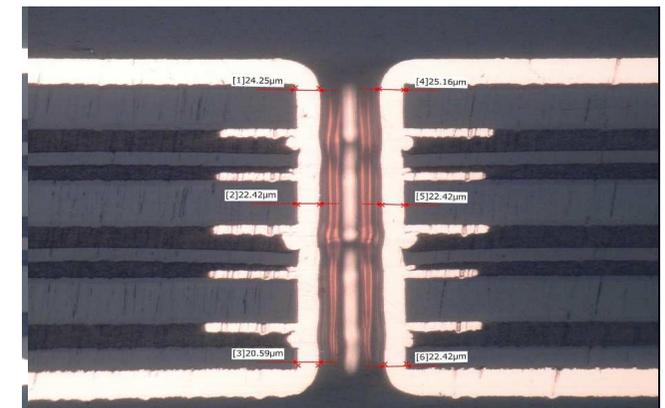
HDI

- 12 HDI's were delivered to RIKEN on June 29th.
- Exporting paper works is in progress. To be shipped to BNL within two weeks.
- We expect the quality of thruholes plating is improved in these new LOT HDIs.
- I propose to give them a priority in the assembly.
- Feedback are appreciated for further extra productions.

Thickness:15~18 μ m



Thickness:20~25 μ m



Readbacker Test in RIKEN Test Bench

- Takashi&Runa's readbacker has been tested in RIKEN test bench.
- Readback values are somehow not exact.
- Looks like readback data is shifted by 1 bit in many cases but not all cases.
- This response is common to all parameters.

Reg	Desc	To Chip	From Chip	Chip Command									
*	Wild	0		Read	Write	Set255	Reset	Default	Display/Modify Configu				
1	Mask	0		Read	Write	Set255	Reset	Default					
2	Dig Ctrl	5	15	Read	Write	Set255	Reset	Default					
3	Vref	1		Read	Write	Set255	Reset	Default	0	1	2	3	4
4	DAC0	8	16	Read	Write	Set255	Reset	Default	0	1	2	3	4
5	DAC1	16	32	Read	Write	Set255	Reset	Default	16	17	18	19	20
6	DAC2	30	60	Read	Write	Set255	Reset	Default	32	33	34	35	36
7	DAC3	35	71	Read	Write	Set255	Reset	Default	48	49	50	51	52
8	DAC4	40	80	Read	Write	Set255	Reset	Default	64	65	66	67	68
9	DAC5	45	91	Read	Write	Set255	Reset	Default	80	81	82	83	84
10	DAC6	50	100	Read	Write	Set255	Reset	Default	96	97	98	99	100
11	DAC7	55	111	Read	Write	Set255	Reset	Default	112	113	114	115	116
12	N1Sel <3:0>	6		Read	Write	Set255	Reset	Default	Mask All				
	N2Sel <7:4>	4											

input readback

Readback debugging in progress

