

Group	Tasks for 2021-2022	timescale
University of Illinois at Chicago	EICSC-WP1 <ul style="list-style-type: none"> Simulation studies for detector optimization 	Work can start now
	EICSC-WP3 (or WP4) <ul style="list-style-type: none"> Testing and characterization of MLR1 (and MLR2) Construction and testing of full scale modules 	Preparation work e.g. acquire equipment etc. can start now

- Experience with Silicon strip/pixel sensor design (TCAD simulation)
- Experience with Silicon strip detector assembly (STAR IST and FST)
- Experience with testing frontend readout ASIC for planar pixel and LGAD sensors (FPIX2, VICTOR, ETROC)
- Experience with firmware and readout system development for ASIC testing
- Cleanroom, lab space, test equipment, access to UIC nanotech center and Fermilab test beam facilities