

Dear all,

Here is the minutes from today's meeting.

o Noise issue in the source test fixture <Yumika>

– Pin down the origin of the noise to be the motor. However we still haven't locate the propagation route from the motor to the sensor and **Yumika** will investigate further.

– Interlock switches were indeed physically located closer to the noisy cell though, they were ruled out as the origin by the attempt that by moved them away from these cell, but the noisy cell stayed.

o Half Entry Issue <Genki>

– Half entry issue is caused by the used of 40cm conversion cable in combination with the bus extender. The half entry is barely observed with the use of 20cm conversion cable.

– BNL is suffered from occasional half entry issue in their test bench and remains unstable. The connection of DF18, power cycle have been executed multiple times already. BNL needs more advanced debugging method/tools to establish stable operation of the test bench by follows:

– **Takashi** will post the readbacker package available. The current Readbacker version works as long as the bus extender is not used in the readout chain. This is useful to debug the FPHX chip response and recommended to be used in BNL and Taiwan.

– **Han-Sheng** should try to play with the DAC0 threshold or LVDS current to debug FPHX response. These parameters should appear in the noise rate and drawing current in the power supply of FPHX chips, respectively. This way he will know the parameters are properly set to FPHX.

– **Han-Sheng** can ask for online video conference session with Japanese group if he has difficulty in debugging the FPHX chip response.

o Statements of the half entry issue in the review <Takashi>

– We don't discuss about 40cm CC results. Remove 40cm data points from P24 plot (This version is available in this meeting indico page). We don't even need it in the backup slides.

– We only focus on straight 20cm CC performance and final design will be curving but less than 20cm length (paste Dan's final design picture).

– The meeting for conversion cable final design with Dan is proposed on Thursday morning 10AM and/or Tuesday morning 9AM (Aug. 3rd). **Takashi** should send e-mail about his availability.

o Taiwan Assembly <Cheng-Wei>

– Reproduced half entry issue with 40cm conversion cable + bus extender in Taiwan test

bench as well.

- **Cheng-Wei** can try column-A of the 2nd ROC. They are compatible with column-C.
- Although Taiwanese screw driver and BNL's are not consistent, we decided that **Cheng-Wei** should stick with BNL screw driver.

o Production status <Itaru>

- Although some spots are found in the Au plating on the surface of 3rd batch HDIs currently running, we think these spots won't affect in the conductivity between bias pads and silicon sensors, so we decided to let them move on.
- FEM-IB design is available in wiki and confirmed there is no copy right issue to be fabricate it in other country. Cheng-Wei should start looking for the parts.

Regards,

-itaru

On 2021/07/28 9:32, Itaru Nakagawa wrote:

Dear all,

Sorry for the last minutes announcement. We'll have weekly INTT meeting on

**July 27th Tuesday 9PM evening in BNL = July 28th Wednesday 10AM in Japan =
Wednesday 9AM in Taiwan**

Will start within a half an hour.

*indico

<https://indico.bnl.gov/event/12610/>

*Zoom

<https://zoom.us/j/92149923535>

Best regards,

-itaru

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