

Subject: Re: [Sphenix-intt-l] INTT Weekly Meeting <minutes>
From: Itaru Nakagawa <itaru@riken.jp>
Date: 2021/09/08 11:32
To: sphenix-intt-l@lists.bnl.gov

Hi all,

Here is the minutes from today's meeting.

o Phase-III Beam Test in Tohoku Univeristy, Japan <Genki>

- 4~5 days of beam time was approved in Tohoku University this Winter. The candidate dates are in Nov/Dec/Jan.

- The beam will be electron < 1GeV at Electron Photon (ELPH) Science Laboratory in Tohoku.

- Any participants from overseas are welcome, although the COVID pandemic situation is unpredictable. Please consider.

- Due to For perperation, any contribution from oversea institutes are also appreciated.

o Taiwan assembly <Cheng-Wei>

- The statistics in ADC0 is not enough to apply Gaussian fit to estimate the mean ADC0. Wei suggested that **Cheng-Wei** should not to apply Gaussian fit and just take the mean of the histogram to be free from the fitting failure.

- 9 out of 12 ladders assembled in Taiwan so far were ranked class-1 within the 8 criteria evaluated at this moment. Itaru commented this result is reasonable compared to BNL status.

- Itaru will fix the number of batch-4 staves once Taiwan complete evaluation of 4 additional ladders in Sept.15th.

o Production Status <Itaru>

- Unfortunately, the fabrication of batch-3 HDI has been stopped in Yamashita due to frozen supply chain of the polyamide material from overseas and no estimate when they can get them.

- **Itaru** will visit Yamashita tomorrow and investigate into 80 yielded HDIs to see if they can be useful. Once Itaru is able to pick up at least 21 HDI's from them, will let them proceed to components implementation, which takes additional two weeks. The best scenario to send 21 HDI's to BNL is end of September.

- The commercial optical splitters are not powered and loose its intensity as many

copies as output uses. It may not be too bad for 1/4 use, but **Itaru** investigate another solution to re-design existing PHENIX beam clock fan out boards under the supervision of Steve Booth.

– **Itaru** will investigate how other sPHENIX subsystems designed the beam clock delivery system to their front end electronics.

– **Itaru** and **Takashi** will consult with Sioan about the trigger capability in FELIX board before he fix the design for INTT.

o GitHub <Genki>

– Genki investigates individual security setting for a private repository especially for FPGA codes because it needs higher security level compared to other codes. BNL SDCC requirement suppose to be hold off for a while.

Best regards,

–itaru

On 2021/09/07 11:53, Itaru Nakagawa wrote:

Dear all,

We'll have weekly INTT meeting on

**Sept 7th Tuesday 9PM evening in BNL = Sept. 8th Wednesday 10AM in Japan =
Wednesday 9AM in Taiwan**

Please be ware, the new meeting day/time starts from this meeting.

*indico

<https://indico.bnl.gov/event/13008/>

*Zoom

<https://zoom.us/j/92149923535>

Best regards,

–itaru

Sphenix-intt-l mailing list

Sphenix-intt-l@lists.bnl.gov

<https://lists.bnl.gov/mailman/listinfo/sphenix-intt-l>