

Dear all,

Here are the minutes from today's meeting. The DST started in US and we'll stick with the same starting time for the next meeting with today's.

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o Beam test data analysis <Genki>

- The cusp between channel 127 and 128 at the boundary of top and bottom cell does survive  $DAC > 30$  cut.
- The cusp appears in at least all high statistic runs. Hard to judge for some small statistics runs.
- Rachid suggested to correlation of the cusp to the beam.
- In the residual calculation, only single hit cluster event was employed.
- Genki should modify the error bar for plots in slide 9 from the mean error of page 18 instead of sigma.
- Rachid suggested to try to check the beam profile stability.
- Rachid suggested to mask strip=0,127,128, and 255 and make the scatter plot of residual vs. channel from tracing to proof the speculation.
- Genki contact with Milan to check for the hot and dead channel map of the ladders used in Tohoku beam test.

o CFC tubes update <Rachid>

- Workshape started fabrication and expected delivery for the inner tube is 6 weeks at maximum. Likely the first piece delivery is sometime in April.

o micro-coax cable update <Itaru>

- Investigated alternative micro-coax brand provided by I-PEX co. The cable structure/material /performances are similar or comparable to these of KEL's.
- I-PEX can provide cables in 1 month (after lockdown of Shenzhen is over).
- Decided to terminate KEL's product and switch to I-PEX's product for the prototype-II.

o Anomaly in the analogue regulator for the A1 port <Itaru>

- Debugged circuit design for the analogue regulator for A1 port. Turned out the AGND is wired to that of A0 regulator. This way, the AGND was floating for the analogue regulator for the A1

port. This explains why the regulator input voltage is measured to be lower than from what we expect from the voltage at power supply.

- The A1 port was used in FVTX, but haven't heard any problem they aware. Itaru's hypothesis is that the AGND of the A1 regulator is grounded through AGND\_2 channel of the A0 power cable and small wedge. On the other hand, the floating ground level is not secured in INTT case because we meant to keep the J1 port and A0 port open.

- Itaru will try to ground the analogue regulator of A1 port as a next step to see Itaru's hypothesis is true. If it is true, the behavior of the A1 analogue regulator should becomes normal.

Regards,

-itaru

On 2022/03/17 10:24, Itaru Nakagawa wrote:

Dear all,

Although we are in the middle of JPS meeting, I'd like to have the weekly meeting in following time. Note there is time change in US due to the summer time. We should discuss about the meeting date/time for the coming season in this meeting.

**March. 17th Thursday 9PM in BNL = March 18th Friday 10AM in Japan = Friday 9AM in Taiwan**

\*indico

<https://indico.bnl.gov/event/15114/>

\*Zoom

<https://zoom.us/j/92149923535>

Best regards,

-itaru

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