

# Detector 1 – TOF PID WG

Kick-Off Meeting – April 25, 2022

# TOF PID Detector Working Group

A few relevant pointers

- Mailing list: [eic-projdet-tofpid-l@lists.bnl.gov](mailto:eic-projdet-tofpid-l@lists.bnl.gov)
  - Subscription information: <https://lists.bnl.gov/mailman/listinfo/eic-projdet-tofpid-l>
- Indico page (TOF PID): <https://indico.bnl.gov/category/414/>
- Wiki page: <https://wiki.bnl.gov/eic-project-detector/index.php/TOFPID>
- Zoom:  
<https://riceuniversity.zoom.us/j/91367321111?pwd=ZGRGZEN1dXg1aFBISVdnMWsyaFE1UT09>
- Default meeting time: **need to find optimum to accommodate all participants and conveners**
  - today's time slot may not be that one
- Conveners contact info:
  - Zhenyu Ye (UIC) - [yezhenyu@uic.edu](mailto:yezhenyu@uic.edu)
  - Wei Li (Rice) – [wl33@rice.edu](mailto:wl33@rice.edu)
  - Constantin Loizides (ORNL) - [constantin.loizides@cern.ch](mailto:constantin.loizides@cern.ch)
  - Frank Geurts (Rice) – [geurts@rice.edu](mailto:geurts@rice.edu)

# Today's Meeting

- Brief review of TOF PID designs and proposals
  - Next meeting: identify non-trivial differences and/or need of further optimization
  
- Review TOF PID workforce
  - integrate new collaborators
  - identify areas of responsibilities and leadership



# Path Forward

- engage with Physics WGs
- engage with Tracker and Cherenkov-PID WGs
- engage with DAQ WG
  
- technology considerations and choices – engage with relevant consortia
- integration, services – engage with the Project
- detector simulation – engage with software & computing

# Charge for the next meetings

- identify non-trivial differences between TOF designs
  - identify need of further optimization
- for each non-trivial difference prepare pro/con list accounting for technical performance risks and costs
  - decision on non-trivial differences done in consultation with the Project
- during the optimization process: continuous validation of performances for physics
- work closely with the Project towards the technical design
  - considering global integration
  - layout of ECCE in CAD to allow for detailed designs including support structures, front-end electronics, services