Hi all,

Here is the minutes from today's meeting.

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o Charge collection response of silicon <Itaru>

- Discussed with a Hamamatsu's engineer. The reason for the observed variation of the full depletion voltage is due to the variation of the silicon purity of ingots for different production batches. So this is expected.

- The reason for lower MIP position at 50V than what expected from the CV curve is because of non-uniform electric field development below full depletion voltage. Especially the region between p^+ doped area of a given strip. The electric field in this area is developed at the last moment of the full depletion voltage thus the charge collection is inefficient until fully depleted. Since the doped width is only 10um for 78um strip width, this area is non-negligibly sizable even near full depletion voltage.

- Unfortunately, it is thus not possible to reproduce the observed MIP position based on theory or CV measurements.

- Cheng-wei attempted DAQ scan using source, however the spectra looks exponential shape in every DAQ scan setting and couldn't take any data to identify any peak which can compare with other bias voltage setting. We'll continue thinking about what we can do to improve Cheng-Wei's measurement.

o Production Status <Itaru>

- 48 Bus extenders are on their way to BNL as scheduled. Expected delivery date is June 28th.

- Data signal was observed for D3 port using high speed oscillscope on June 20th. The signal looks healthy though, the analysis is now underway.

- The INTT collaboration meeting is proposed in August at BNL. Let Itaru know your opinion about this proposal.

Best regards,

-itaru

On 2022/06/22 16:48, Itaru Nakagawa wrote:

> Deal all,
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> We'll have the weekly meeting in following time.
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> June 22nd Thursday 9PM in BNL = June 23rd Friday 10AM in Japan = Friday 9AM in Taiwan
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> *indico
>
> https://indico.bnl.gov/event/16304/
>
> *Zoom
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> https://zoom.us/j/92149923535
>
> Best regards,
>
> -itaru