Production Status

RIKEN/RBRC

Itaru Nakagawa

Collaboration Meeting Aug.16 and 17 Morning

Tuesday

INTT Weekly meeting

Tuesday Aug 16, 2022, 9:00 AM → 12:19 PM US/Eastern

Description Meeting URL

https://zoom.us/j/92149923535

9:00 AM → 9:20 AM	**INTT Barrel Assembly**	Speaker: Rachid Nouicer (Brookhaven National Laboratory)	20m
9:20 AM → 9:40 AM	**Barrel Test**	Speaker: Dr Genki NUKAZUKA (RIKEN BNL Research Center)	20m
9:40 AM → 10:00 AM	**Cooling System**	Speaker: Robert Pisani (BNL, sPhenix)	20m
10:00 AM → 10:20 AM	**LV/HV System**	Speakers: Dr Ivan Kotov (BNL Instrumentation Division), Ivan Kotov (BNL)	20m
10:20 AM → 10:40 AM	Coffee Break		20m
10:40 AM → 11:00 AM	**Barrel Installation**	Speaker: Dan Cacace (BNL)	20m
11:00 AM → 11:20 AM	**Felix Readout**	Speaker: Raul Cecato	20m
11:20 AM → 11:40 AM	**ROC FPGA Modification**	Speaker: Takashi Hachiya (RIKEN)	20m
11:40 AM → 12:00 PM	**Expert GUI**	Speaker: Hikaru Imai (riken/Rikkyo Univ.)	20m

Wednesday

INTT Weekly meeting

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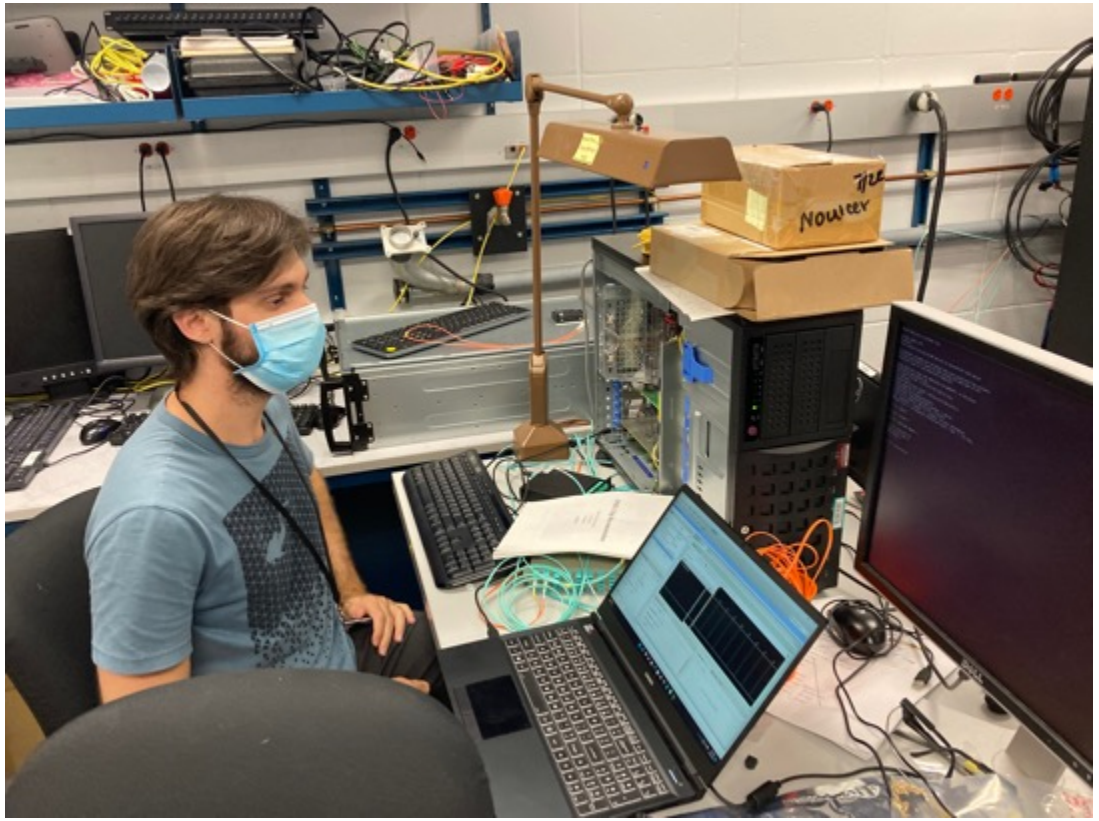
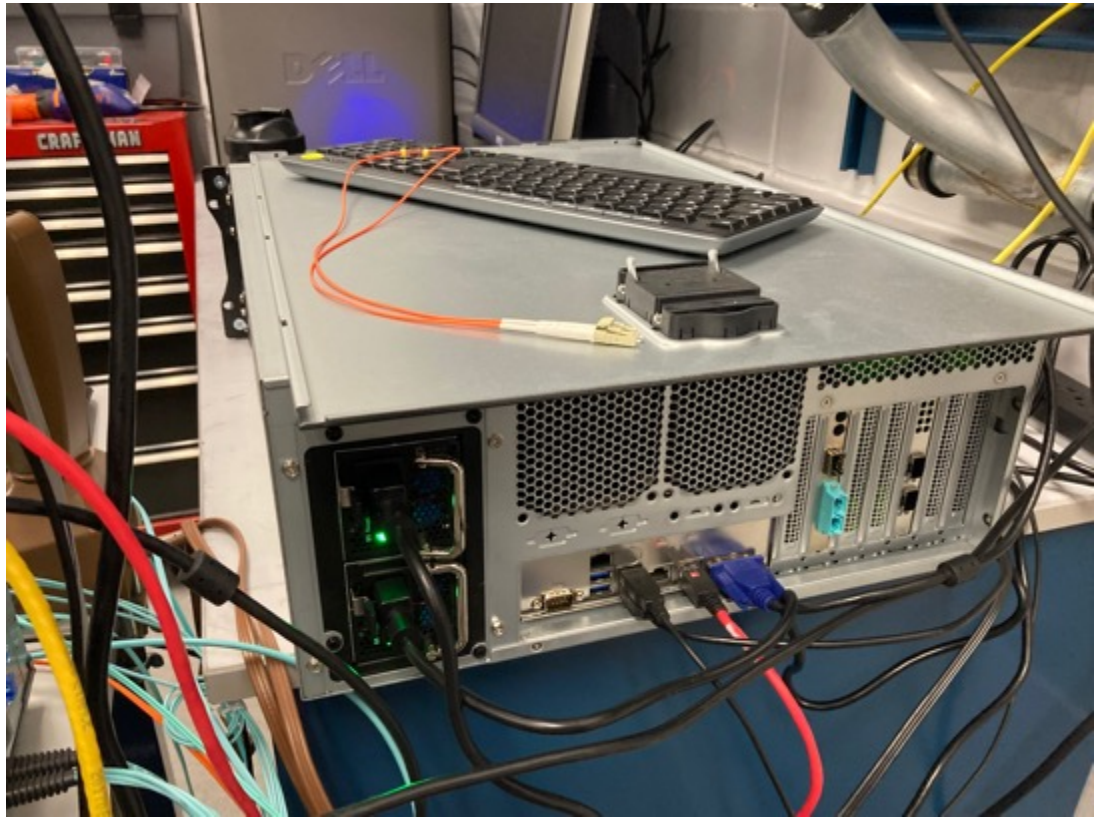
9:00 AM → 9:20 AM	**sPHENIX physics summary**	Speaker: Dr Genki NUKAZUKA (RIKEN BNL Research Center)	20m
9:20 AM → 9:40 AM	**INTT Beam Test Results and Unresolved Issues**	Speaker: cheng wei shih	20m
9:40 AM → 10:00 AM	**Cosmic Ray Measurement at NWU**	Speaker: Yumika Namimoto	20m
10:00 AM → 10:20 AM	**Low level semi-online/offline software**	Speaker: Wei Xie (Purdue University)	20m
10:20 AM → 10:40 AM	Coffee Break		20m
10:40 AM → 11:00 AM	**Online Monitor**	Speaker: Joseph Bertaux (staff@purdue.edu;student@purdue.edu;member@purdue.edu;employee@purdue.edu)	20m
11:00 AM → 11:20 AM	**1008 ROC Repair Status**	Speaker: Dr Itaru Nakagawa (RIKEN)	20m
11:20 AM → 12:00 PM	**•Near/long term future schedule and task lists**	Speaker: All	40m

https://indico.bnl.gov/event/16601/

Felix Readout



2nd Felix Server



- Raul competed setting up 2nd felix server.
- It is ready for data taking.
- The plan is to start data taking with the 2nd felix server next week.

Load Map

- Currently working with Raul to study failure modes of FPHX readback.
- 1-bit off issue also observed in Felix, but some chips are not sending back readback at all.
- Try readback function without bus extender.
- Next step is to work with Takashi to see if ROC FPGA-SC modification works.
- Once this function is established in Felix readout, we'll start implementation of the expert GUI with 2nd Felix server.

Collaboration Meeting

Open Issues

	Issue	Plan
m-coax	Partial data drop	Modify ROC slow control FPGA to make the readbacker working
ROC	Data Fiber Latch	Serializer debugging with John K.
ROC	Unfunctional Calibration Pulse	Re-download ROC Slow Control FPGA