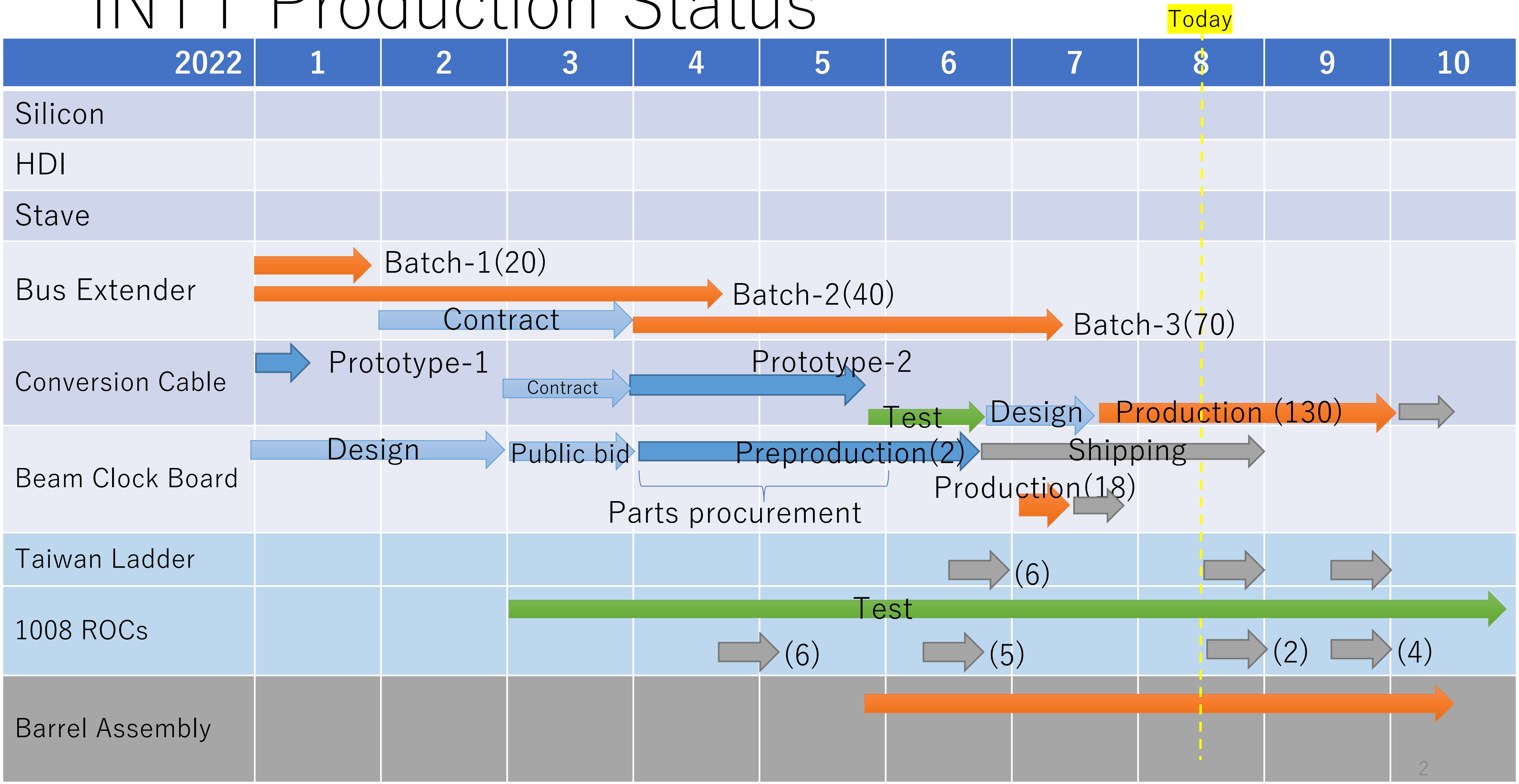
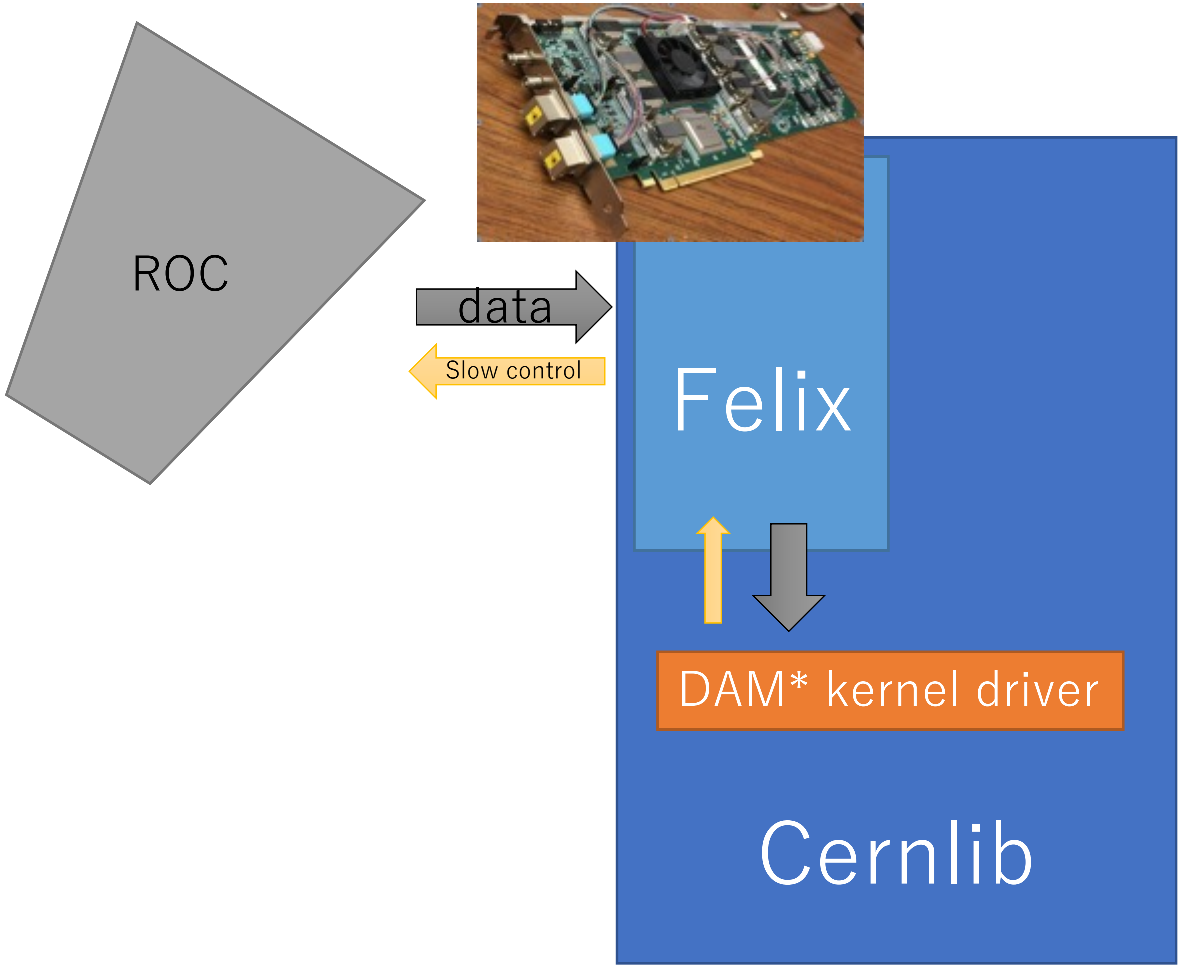
INTT Schedules

INTT Production Status



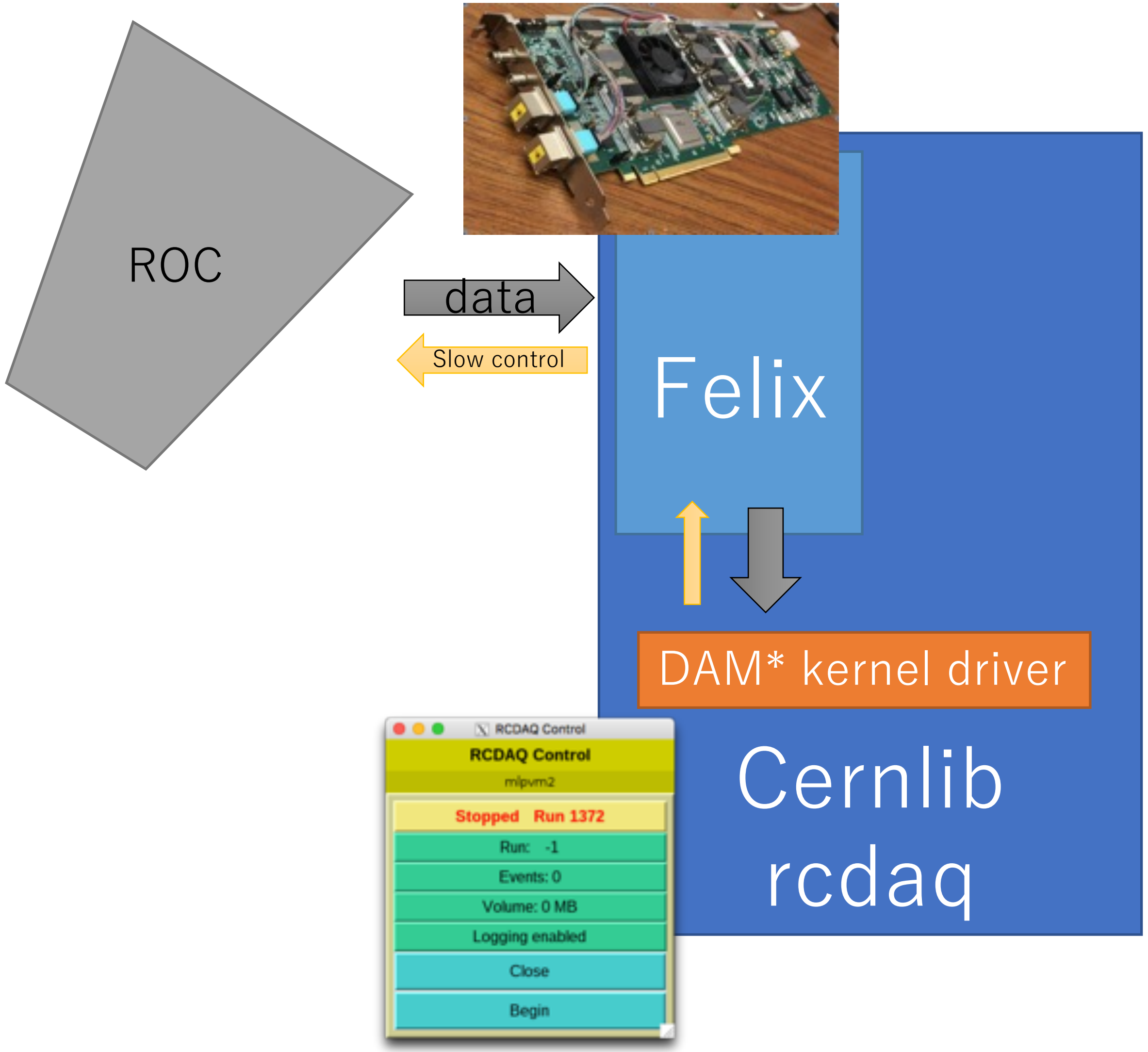
INTT Felix Readout Development

Load map Towards 1008 DAQ Configuration



1. Restore network of felix server-2 (Itaru)
2. Install data format codes and cernlib to Felix server-2 (Itaru)

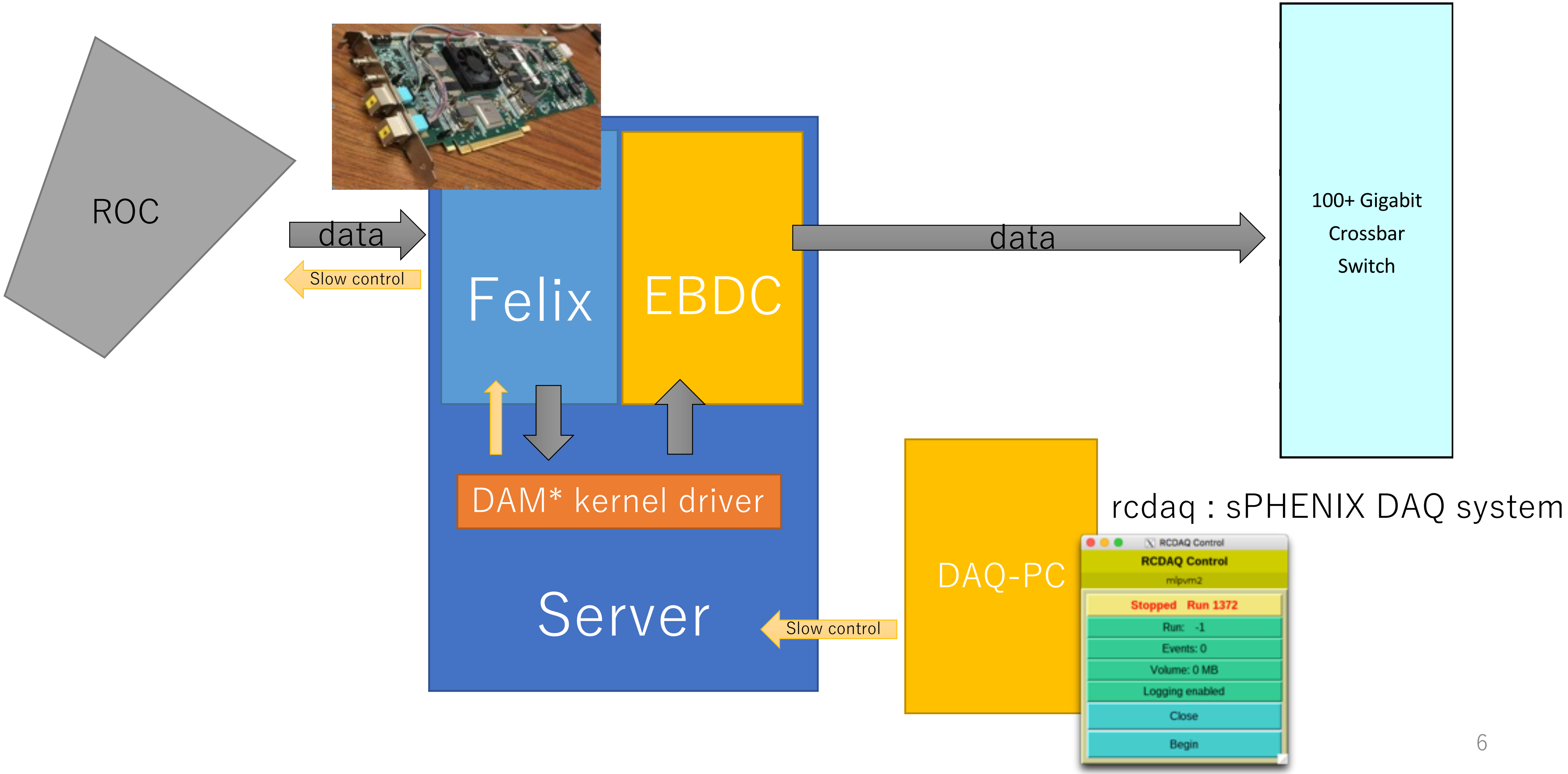
Load map Towards 1008 DAQ Configuration



1. Install rcdaq standalone once Raul+Martin make it ready.
2. Genki + (Joseph/Cheng-Wei) + (Itaru) for testing.

Middle of September

1008 DAQ Configuration

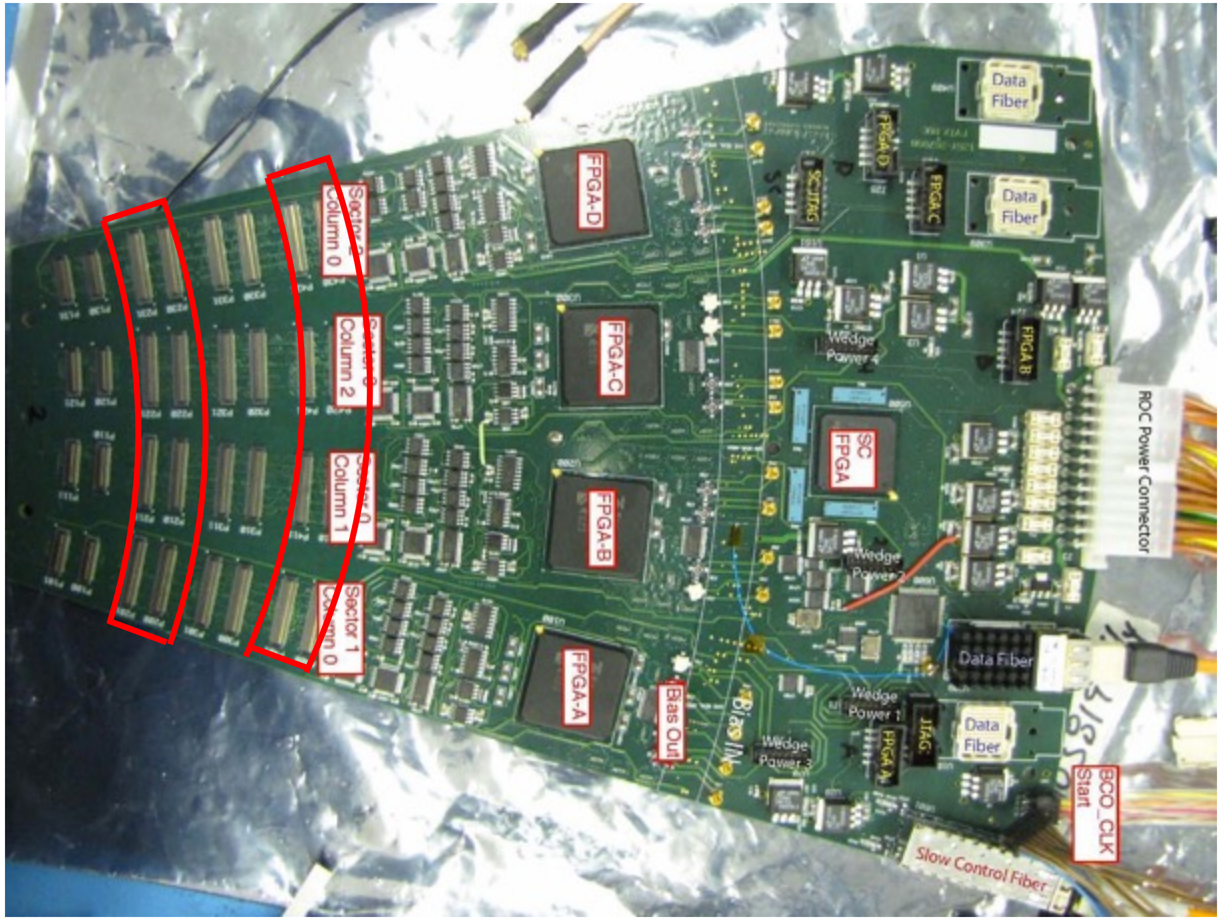


Unresolved issues

- DAC Scan edep difference, 2019 and 2021
- Offset determination
- Felix & FEM # of event difference
- Run 88 & 89 - satellite peak
- Asymmetry hit distribution
- Scattering angle

INTT ROC Ports Configuration

- Only station 1 and 3 will be used.
- Half of DF18 ports are to be remain open



What's Wrong?

Fake hits?

- Many of you experienced fake hits appear on the terminal in the test bench DAQ.
- These fake hits belong to non-existing module, i.e. decoded as hit from empty ports.

How come hits are generated from empty ports?

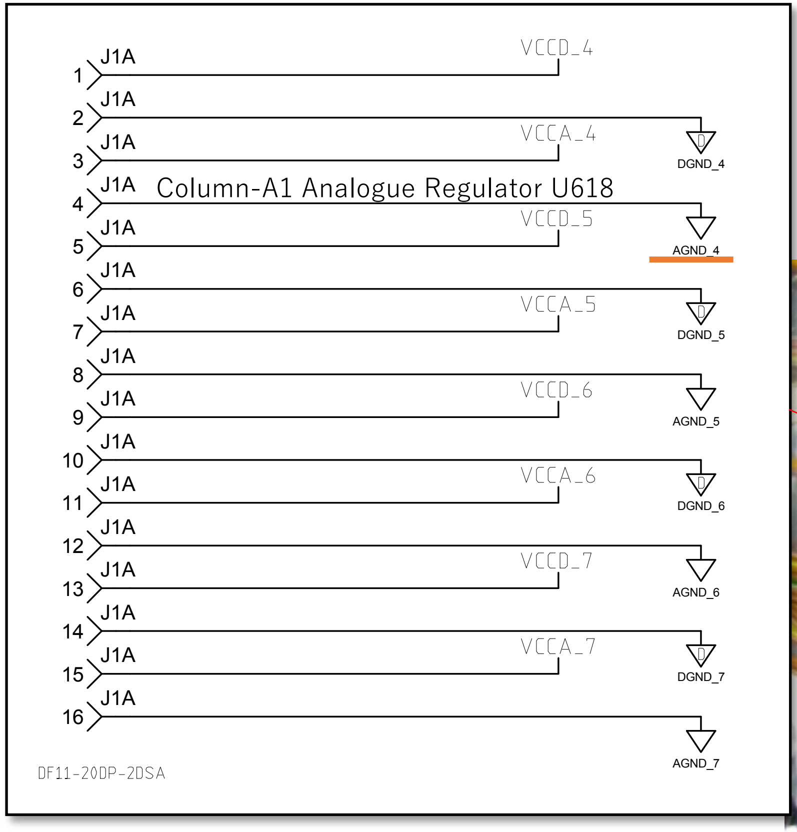
- The current hypothesis is these fake hits are generated at the empty port due to the reflection of digital signal.

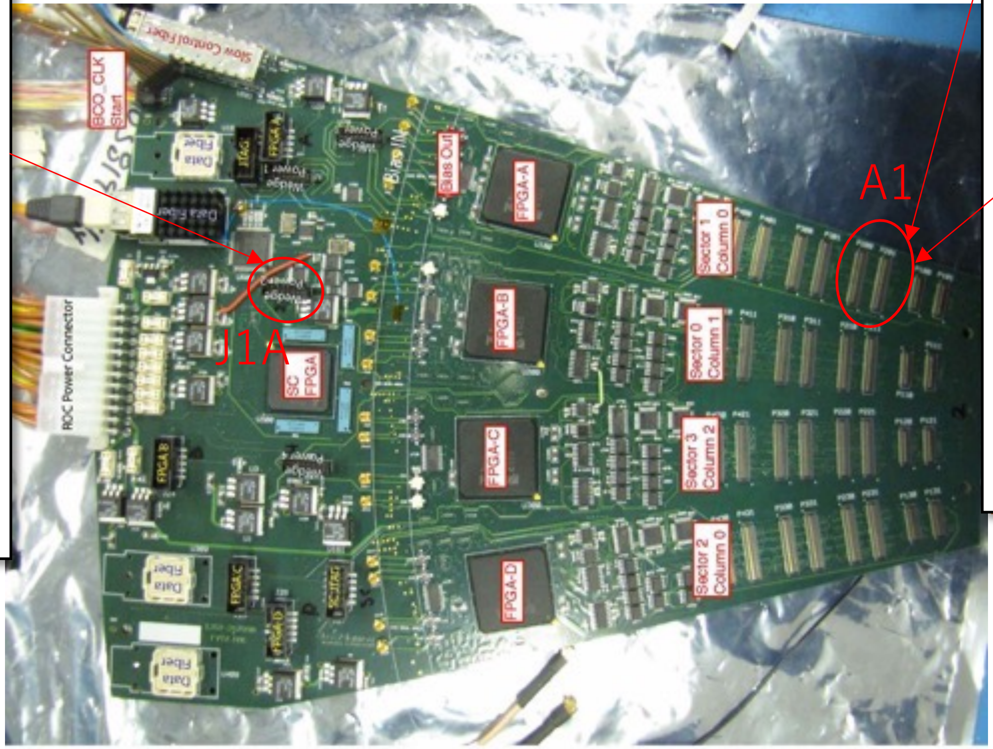
Is this fatal for our data quality?

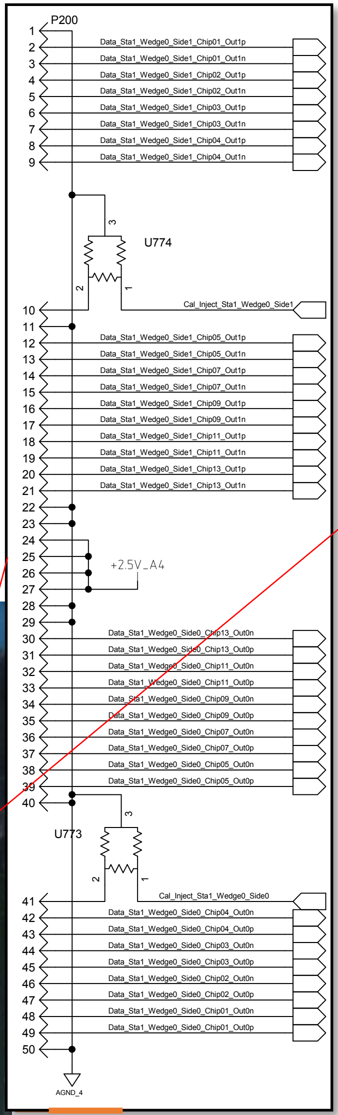
- Probably not. These data will never be processed in the analysis.
- Data size will be the concern.

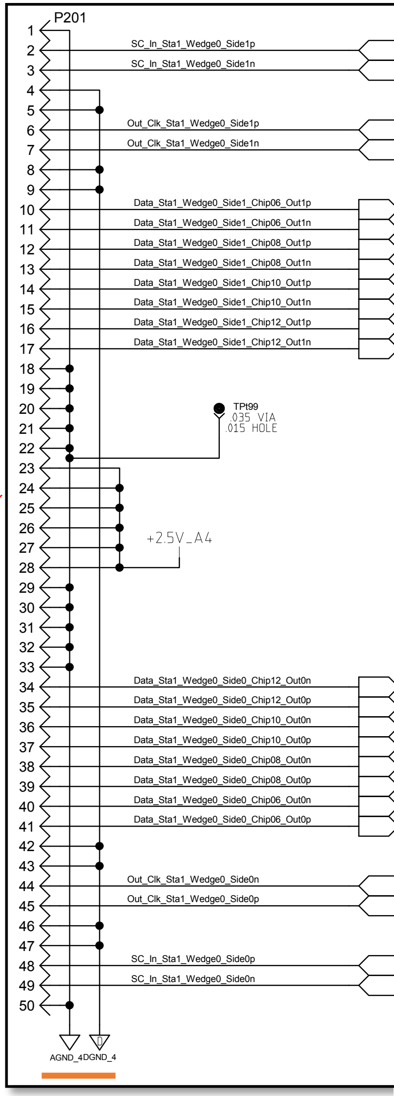
Circuit Design

Traced back circuit for the analogue regulator for A1 port in the ROC circuit drawing.

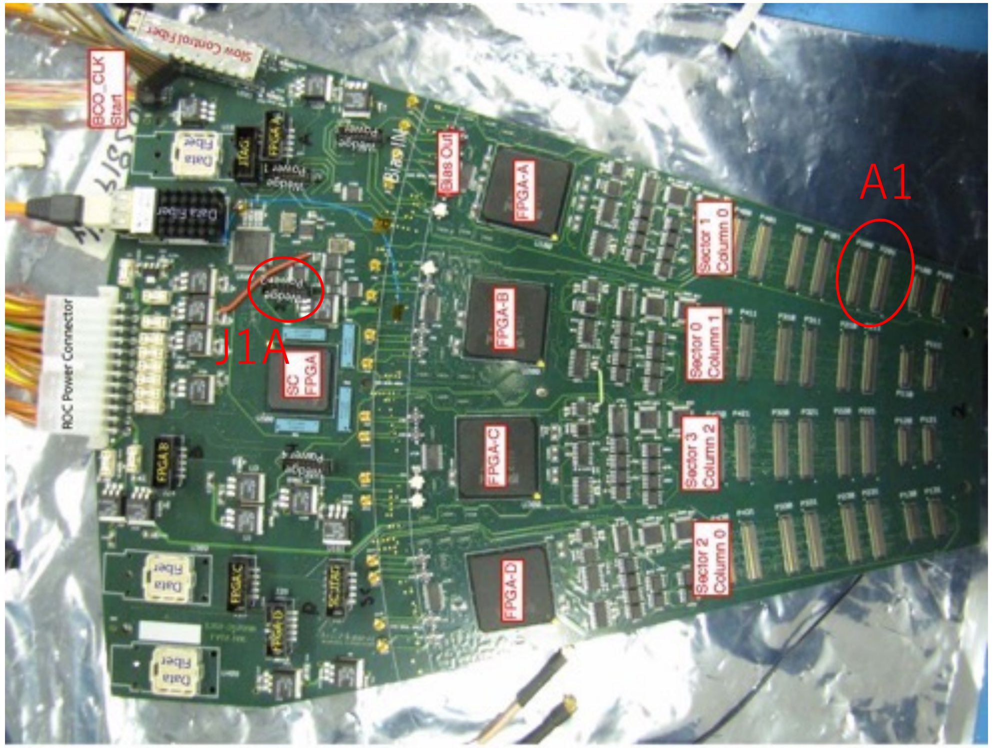


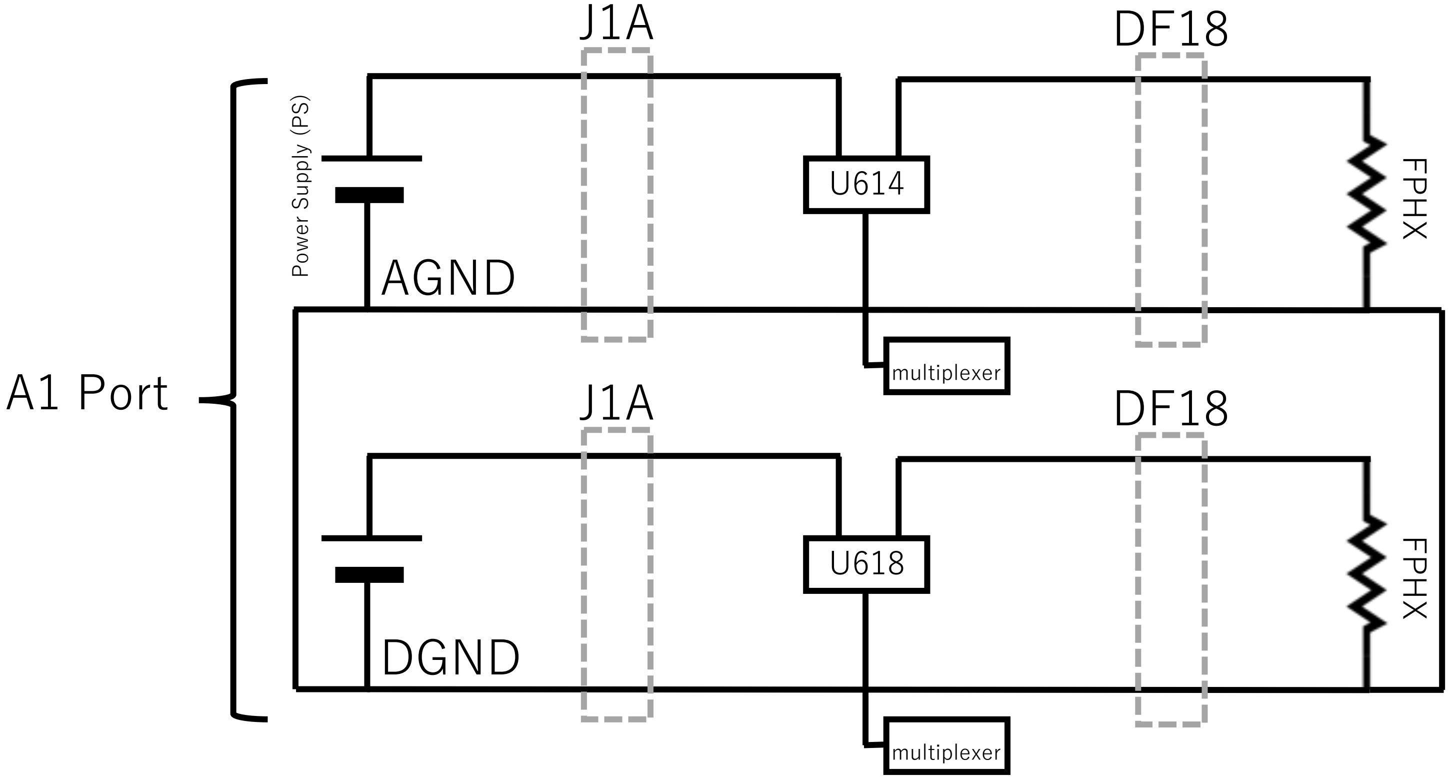






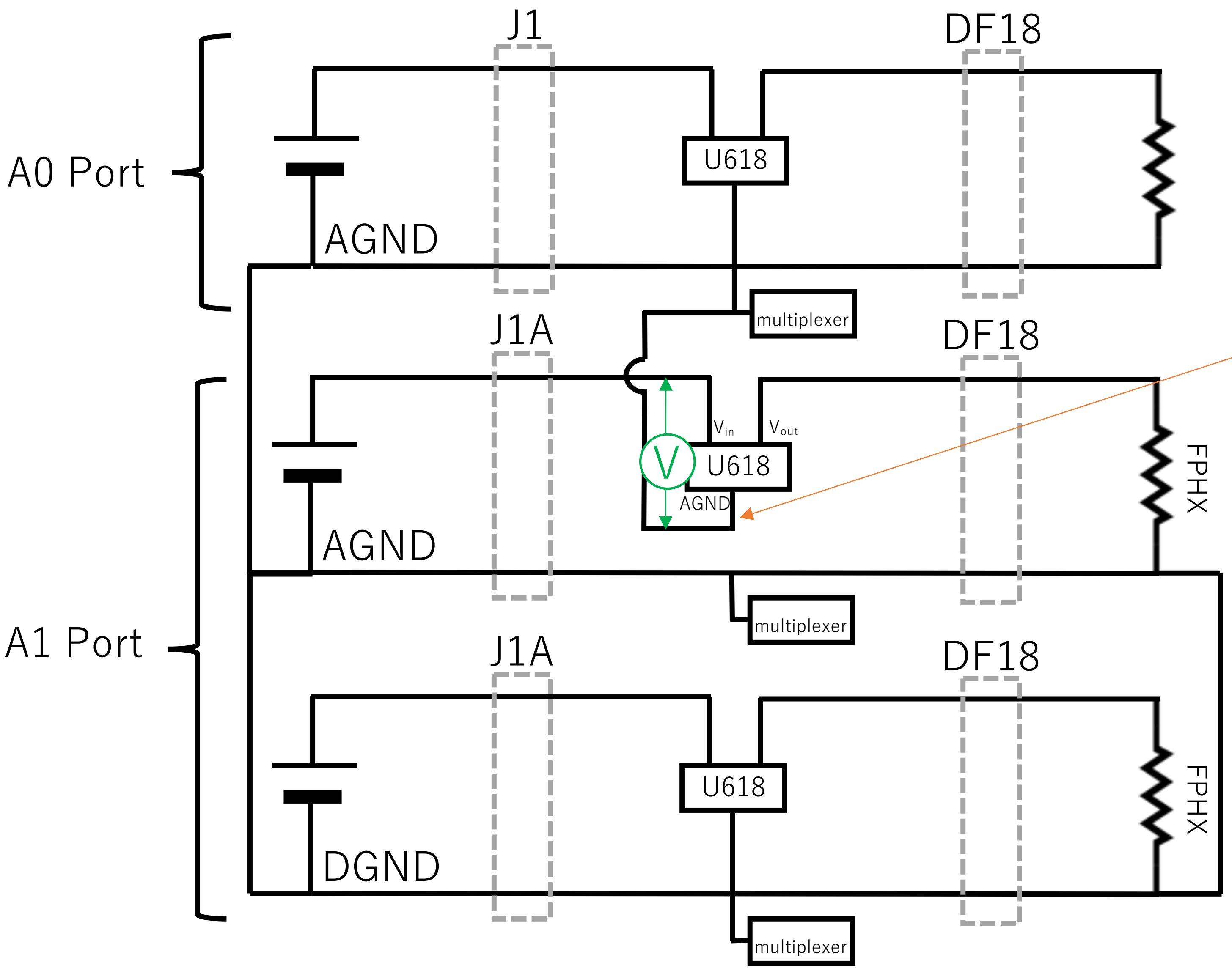
Correct Circuit Configuration





AGND and DGND are shorted at the wedge.

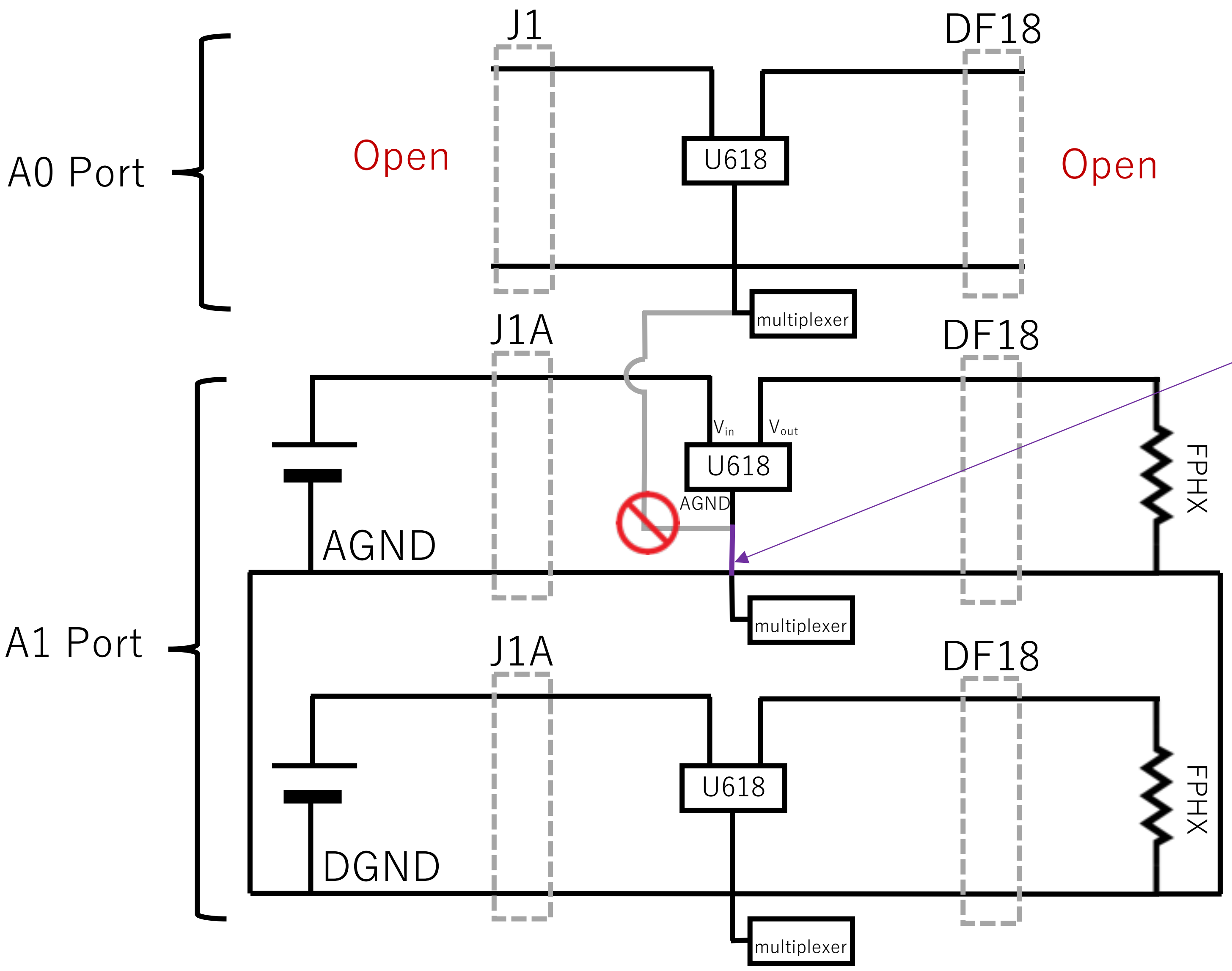
FVTX Case Circuit Diagram



AGND is provided by A0 power supply which is likely to be shared with the GND of A1 analogue power supply. This circuit effectively secures stable operation of U618 regulator presumably.

Not sure how stable U618 regulator output is with the floating ground with open J1 and DF18(P100,P101) for INTT case. It is not guaranteed INTT works as well as FVTX.

How can we recover?



Disable AGND_2 connection and Install new jumper cable.

Need to bridge between primary side and back side of the ROC boards. This is not trivial recovery. Keep searching for more portable solution.