# Readout electronics - Part of eRD109



Status now:
- HGCROCv3 in the mail
- Testboard is getting produced
  - 1k$/board
  - Some missing components (working on that)
- KCU105 (Xilinx eval board) is available in ORNL

Need in future:
- Test with SiPMs connected
- Development of a new readout board:
  - For the SiPM
  - And for the HGCROC
- Bunch of tests for the readout:
  - Gain tests
  - Internal injection tests
  - Trigger data tests
- Development of the FPGA board
  - New board for the FPGA
  - Development of the FPGA program