

Effects of disk tiling on acceptance, tracking performance with larger pixel pitch

L. Gonella, P. G. Jones, S. Maple, P. R. Newman

Acceptance of Disks around beampipe

- Disks consist of tiles of ITS3-like sensors, with length and width determined by the ITS3 reticle
	- The result is that the inner opening of the disks is not perfectly circular \rightarrow instead they are squared off

* P. G. Jones

 This means that there isn't full azimuthal acceptance at the inner radii we've been using (beampipe radius +5mm)

• Studies of how to tile disks with EIC LAS sensor ongoing in the EIC SC.

• For full details, see talk by Peter Jones at <https://indico.bnl.gov/event/17073/>

EIC-SVT Disk-1 Tile

The algorithm

- Aim to keep periphery to larger radii
- Two designs, each based on a central cross pattern smaller than the inner diameter of the disk
- Design #1 = vertical tiles (shown)
- Design #2 = herringbone (alternating vertical and horizontal tiles)
- Limits on the max and min sensor length can be applied
- Study the number of sensor variants that are needed
- **The minimum radius (r_min) is 5 mm larger than the beam pipe (r_bp) for bake out purposes**
- Sensor and periphery must be contained within the min and max radii of the disk (r_m) min and r $_{max}$).
- **For each disk, the algorithm calculates the smallest and largest radii with full acceptance (r_low and r_high)**
- The algorithm does not permit any sensor overlap
- Acceptance at small radii could be improved by allowing some sensor overlap; placing overlapping sensors on the reverse side of the disk (in progress)

Procedure (Fun4All simulations)

- Uniformly generate single particles in ranges $-4 < \eta < -2.5$ and $2.5 < \eta < 4$
	- Pions forward, electrons backward
- Disks of 100% and 95% efficiency
- Measure "Acceptance" of disks as implemented in simulation *Acceptance*= *Num* 3 *hit tracks Total numtracks*
- Initially use disks with perfectly circular openings of radius r_min and r_low, opening centred around x_offset

Acceptance with circular openings

100% efficient disks

95% efficient disks

"Realistic" disk acceptance

- The studies with circular openings give an idea of the upper and lower limits of the disk acceptance – however to get a better idea of this, we need to use disks with partial acceptance in this region
	- Implemented disk modules with inner cutouts in the same shape as the disks as produced by the tiling algorithm:

Pixel pitch comparisons

- ITS3 aims at 10μm pixel pitch
	- Currently working with 15μm and 18μm
- To see effect of this in simulation, implement setup as shown:
	- 5 disks from +-25cm to +-135cm
	- 5 silicon barrel layers
	- Complemented by AC-LGADs and MPGDs
- Run simulations with single pions with silicon layers of pitch 10, 15, 18μm

EPIC – Symmetric disks – 1.7T – Momentum Resolution

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Figure 5.5: Relative momentum resolution for different pixel sizes in the silicon vertex tracker barrel, for momenta between 0 and 50 GeV/c .

EPIC – Symmetric disks – 1.7T – Pointing Resolution

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EPIC – Symmetric disks – 1.7T – Pointing Resolution

- Loss in performance seen at higher momenta as expected
- Position of points with relation to requirement line mostly unchanged

Summary

- Acceptance of disks around the beampipe measured as a function of η and compared for 95% vs 100% efficiency
	- Only 3 disks hit for $|\eta| > -3.2 \rightarrow$ reduces acceptance to -80% if disks are 95% efficient
- Compared 10, 15, and 18μm pixels in terms of momentum and pointing resolution
	- Losses are minimal for (transverse) Momenta in the range 0 to 10 GeV

Next Steps

- **Look at acceptance in terms of one of the measured quantities (such as** Q^2 **electron)**
- **If** Investigate effect of beam spot on performance