

# ePIC DAQ and electronics WG: Workshop Protocols/Interface/Timing and Clock Distribution

Contribution ID: 11

Type: **not specified**

## Detector / ASIC interface requirements and open questions

*Friday, December 9, 2022 9:00 AM (15 minutes)*

Electronic and DAQ interface examples:

CLAS12 RICH Detectors to DAQ system

Experimental Area Trigger Fiber interface/infrastructure

~~~~

ePIC ASICs - Project update: Fernando Barbosa

~~~~

Radiation dose levels: TID, mitigation ideas for managing SEE

Dr. Jin Huang

Dr. Pietro Antonioli

**Co-authors:** Mr CUEVAS, Chris (Jefferson Lab); BARBOSA, Fernando (JLab); HUANG, Jin (Brookhaven National Lab); ANTONIOLI, Pietro (INFN - sezione di Bologna); ANTONIOLI, Pietro (INFN - Bologna)

**Presenters:** Mr CUEVAS, Chris (Jefferson Lab); BARBOSA, Fernando (JLab); HUANG, Jin (Brookhaven National Lab); ANTONIOLI, Pietro (INFN - sezione di Bologna); ANTONIOLI, Pietro (INFN - Bologna)

**Session Classification:** Electronic Topics -