

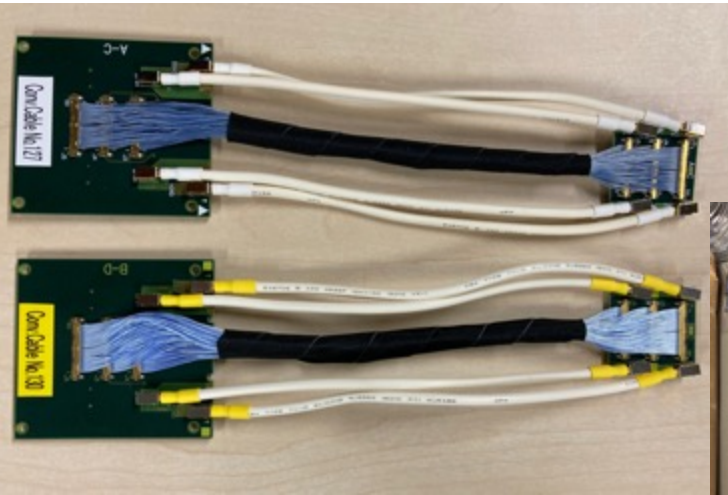
Production Status

RIKEN/RBRC

Itaru Nakagawa

Production Conversion Cables

	AC-Type	BD-Type
15cm Conversion Cable	56 + (10)	56 + (10)
25cm m-Coax Harness	3 Sets x 1 port x 16ROCs	3 Sets x 1 port x 16ROCs
GND/Power Cables	4 x 1 port x 16ROCs	4 x 1 port x 16ROCs
GND/Power Lock Cover	30	



↓ 25cm Harness



↑ 25cm Harness



↓ 25cm GND/Power Cable



Production Conversion Cables

Delivered	AC-Type	BD-Type
15cm Conversion Cable	56 + (10)	56 + (10)
25cm μ -Coax Harness	48 bundles (1 CC/ROC)	48 bundles (1 CC/ROC)
GND/Power Cables	64 cables (1 CC/ROC)	64 cables (1 CC/ROC)
GND/Power Lock Cover	30	

New Order Plan	AC-Type	BD-Type
25cm μ -Coax Harness	75 bundles (1.5 CC/ROC)	75 bundles (1.5 CC/ROC)
GND/Power Cables	100 cables (1.5 CC/ROC)	100 cables (1.5 CC/ROC)

- With new 25cm cables, we can afford 5 long conversion cables per ROC. Depending of funding left, new order can be 50 bundles instead of 75 bundles.
- Waiting for a quote from the company.
- Expected lead time is 6 to 8 weeks due to the Chinese New Year break. The delivery is middle of Feb at the earliest.

TLK2711 and ROC



TLK2711 Purchase Order Status

PO Date	Quantity	Status	Action
2022/11/24	20	Delivered	Repair work in Japan
2022/12/7	30	Delivered	To be shipped to BNL on Dec.28th
2022/12/21	55	Ordered	
2022/1/4?	145	Waiting for a quote	

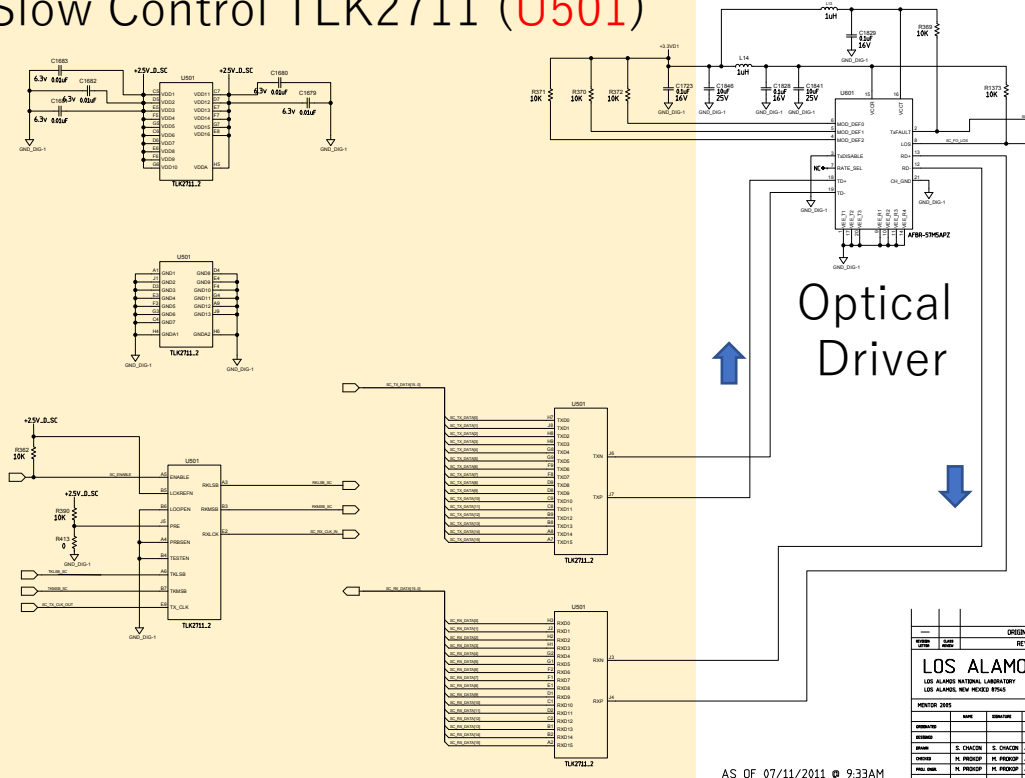
TLK2711 replacement work in the instrumental division can be started by the middle of January. Estimated work is 1~2 hour / ROC according to Joseph Pinz (tech).



Slow Control TLK2711

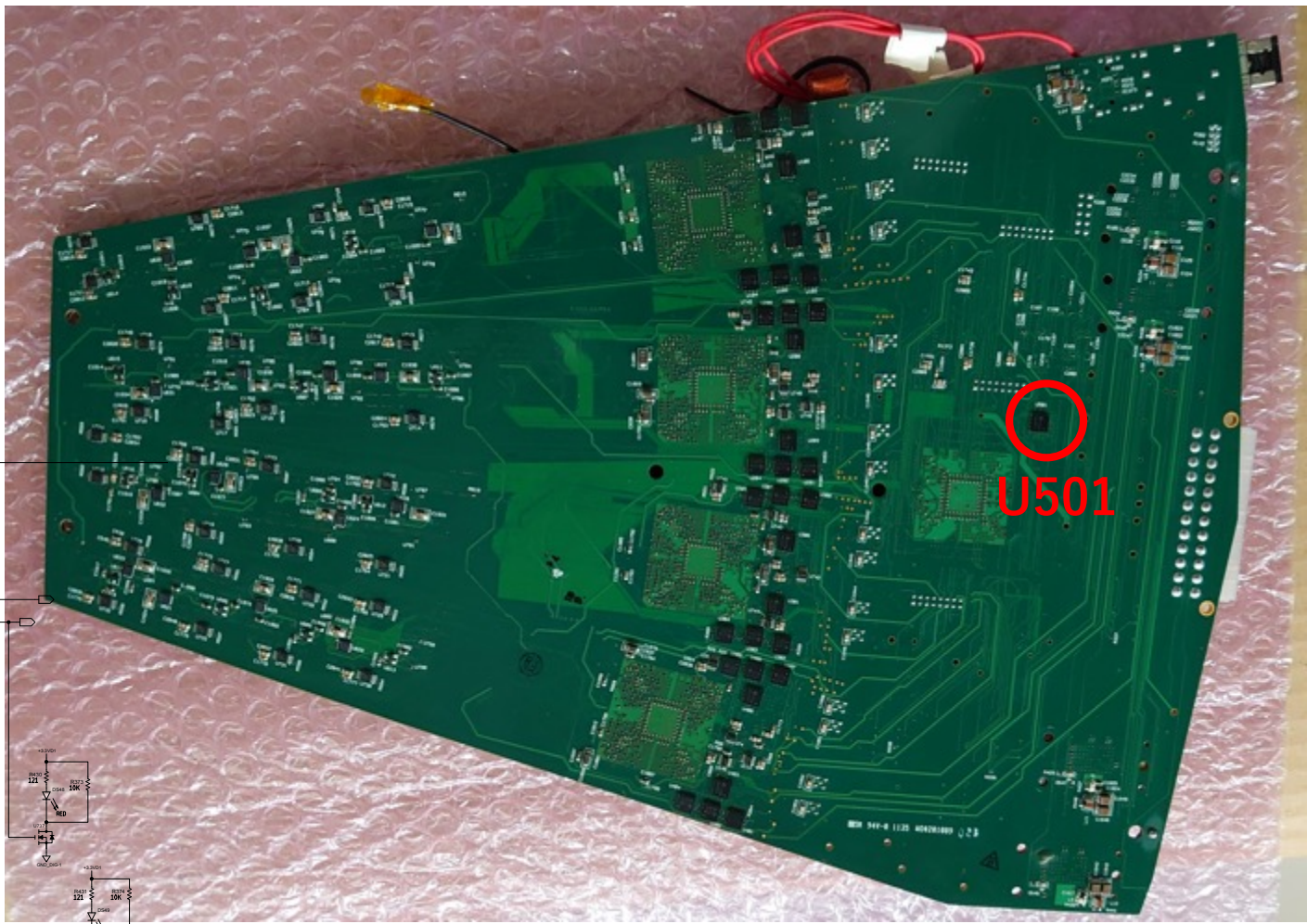
(U501)

Slow Control TLK2711 (U501)



Optical Driver

ORIGINAL ISSUE					REVISIONS				
DATE	BY	REASON	DATE	BY	DATE	BY	REASON	DATE	BY
LOS ALAMOS									
LOS ALAMOS NATIONAL LABORATORY									
LOS ALAMOS NEW RESEARCH 8745									
MENTOR 2005									
REVISION	DATE	BY	REASON	DATE	BY	REASON	DATE	BY	REASON
1	04/01/05	S. CHACON	S. CHACON	04/01/05	ADT-IC				
2	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
3	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
4	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
5	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
6	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
7	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
8	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
9	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
10	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
11	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
12	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
13	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
14	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
15	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
16	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
17	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
18	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
19	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
20	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
21	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
22	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
23	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
24	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
25	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
26	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
27	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
28	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
29	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
30	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
31	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
32	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
33	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
34	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
35	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
36	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
37	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
38	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
39	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
40	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
41	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
42	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
43	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
44	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
45	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
46	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
47	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
48	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
49	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
50	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
51	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
52	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
53	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
54	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
55	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
56	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
57	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
58	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
59	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
60	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
61	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
62	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
63	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
64	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
65	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
66	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
67	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
68	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
69	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
70	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
71	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
72	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
73	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
74	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
75	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
76	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
77	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
78	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
79	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
80	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
81	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
82	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
83	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
84	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
85	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
86	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
87	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
88	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
89	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
90	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
91	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
92	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
93	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
94	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
95	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
96	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
97	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
98	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
99	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				
100	04/01/05	H. PROSE	H. PROSE	04/01/05	ADT-IC				



ROC Repair Status

https://docs.google.com/spreadsheets/d/11jsgrsl7pS02la_Huew4Fw-nbNwOzl6Vhbl0nRYUX-w/edit#gid=1083896993

Index	ROC #	FVTX	Regulator Upgrade	Location	Class	A1	A2	A3	B1	B2	B3	C1	C2	C3	D1	D2	D3	Issue	Status
1	6	NW2	✓	BNL	1														
2	13	NE4	✓	BNL	1														
3	18	NE1	✓	BNL	1														
4	26	SE3	✓	BNL	1														
5	28	SE0	✓	BNL	1														
6	29	-	✓	BNL	1														
7	20	SW5	✓	BNL	1														
8	22	SE2	✓	RPC	2														
9	23	SE1	✓	BNL	1														
10	31	NW1	✓	BNL	1	R	R	R											
11	32	NE2	✓	BNL	1							R	R	R					
12	9	-	✓	BNL	1			R											
13	10	-	✓	BNL	1					R		R							
14	27	SE5	✓	BNL	1							R			R	R	R		L12 Replaced at Column-D
15	19	NE5	✓	BNL	1										R				Fixed by TLK2711 Replacement
16	24	SE4	✓	RPC	2													Fiber Sync	A3,D1 TLKs replaced
17	16	NE3	✓	BNL	3													Calib Pulse	Waiting for SC-FPGA download
18	15	NE0	✓	NWU	2	R												Fiber Sync	
19	7	-	✓	NWU	3		R	R											Replaced all DF18
20	2	NW4	✓	RPC	2													Fiber Sync	
21	21	-	✓	RPC	2	R												Fiber Sync	
22	17	NW3	✓	RPC	2													Fiber Sync	
23	3	NW5	✓	RPC	2	R		R										Fiber Sync	
24	14	NE1	✓	RPC	3														
25	5	-	-	RKN	3														

Good
Good, but occasional failure
No data at all

C : Problem in Calibration pulse
O : Problem in Optical link
P : Problem in Power supply
H : Half Entry
R : Recovered

Under Repair (TLK replacement except for B1 port of NW4)



Leadtime : 2~3 weeks
Expected delivery around middle of January



Expecting up to 4 or 5 new Class-1 ROCs available in BNL by the end of January

ROC Repair Status

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1	6	NW2	✓	BNL	1														
2	13	NE4	✓	BNL	1														
3	18	NE1	✓	BNL	1														
4	26	SE3	✓	BNL	1														
5	28	SE0	✓	BNL	1														
6	29	-	✓	BNL	1														
7	20	SW5	✓	BNL	1														
8	22	SE2	✓	RPC	2	O													
9	23	SE1	✓	BNL	1														
10	31	NW1	✓	BNL	1	R	R	R											
11	32	NE2	✓	BNL	1							R	R	R					
12	9	-	✓	BNL	1			R											
13	10	-	✓	BNL	1					R		R							
14	27	SE5	✓	BNL	1							R			R	R	R		L12 Replaced at Column-D
15	19	NE5	✓	BNL	1										R				Fixed by TLK2711 Replacement
16	24	SE4	✓	RPC	2		O	R							R			Fiber Sync	A3,D1 TLKs replaced
17	16	NE3	✓	BNL	3				C	C	C							Calib Pulse	Waiting for SC-FPGA download
18	15	NE0	✓	NWU	2	R									O	O		Fiber Sync	
19	7	-	✓	NWU	3		R	R		H	R	H			R	H			Replaced all DF18
20	2	NW4	✓	RPC	2					R		R			O	R		Fiber Sync	
21	21	-	✓	RPC	2	R			R		R		R	O	R			Fiber Sync	
22	17	NW3	✓	RPC	2				O							R		Fiber Sync	
23	3	NW5	✓	RPC	2	R		R							H	R	O	Fiber Sync	
24	14	NE1	✓	RPC	3					R		H	R	R					
25	5	-	-	RKN	3														

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TLK replacement for fake hit study if fake hits disappears after the replacement. (To be reported by R. Shishikura)

ROC Repair Status

Slow Control FPGA download attempt by Takashi

Index	ROC #	FVTX	Regulator Upgrade	Location	Class	A1	A2	A3	B1	B2	B3	C1	C2	C3	D1	D2	D3	Issue	Status
1	6	NW2	✓	BNL	1														
2	13	NE4	✓	BNL	1														
3	18	NE1	✓	BNL	1														
4	26	SE3	✓	BNL	1														
5	28	SE0	✓	BNL	1														
6	29	-	✓	BNL	1														
7	20	SW5	✓	BNL	1														
8	22	SE2	✓	RPC	2														
9	23	SE1	✓	BNL	1														
10	31	NW1	✓	BNL	1	R	R	R											
11	32	NE2	✓	BNL	1							R	R	R					
12	9	-	✓	BNL	1			R											
13	10	-	✓	BNL	1					R		R							
14	27	SE5	✓	BNL	1							R			R	R	R		
15	19	NE5	✓	BNL	1										R				
16	24	SE4	✓	RPC	2										R				
17	16	NE3	✓	BNL	3														
18	15	NE0	✓	NWU	2	R													
19	7	-	✓	NWU	3		R	R											
20	2	NW4	✓	RPC	2														
21	21	-	✓	RPC	2	R													
22	17	NW3	✓	RPC	2														
23	3	NW5	✓	RPC	2	R													
24	14	NE1	✓	RPC	3														
25	5	-	-	RKN	3														

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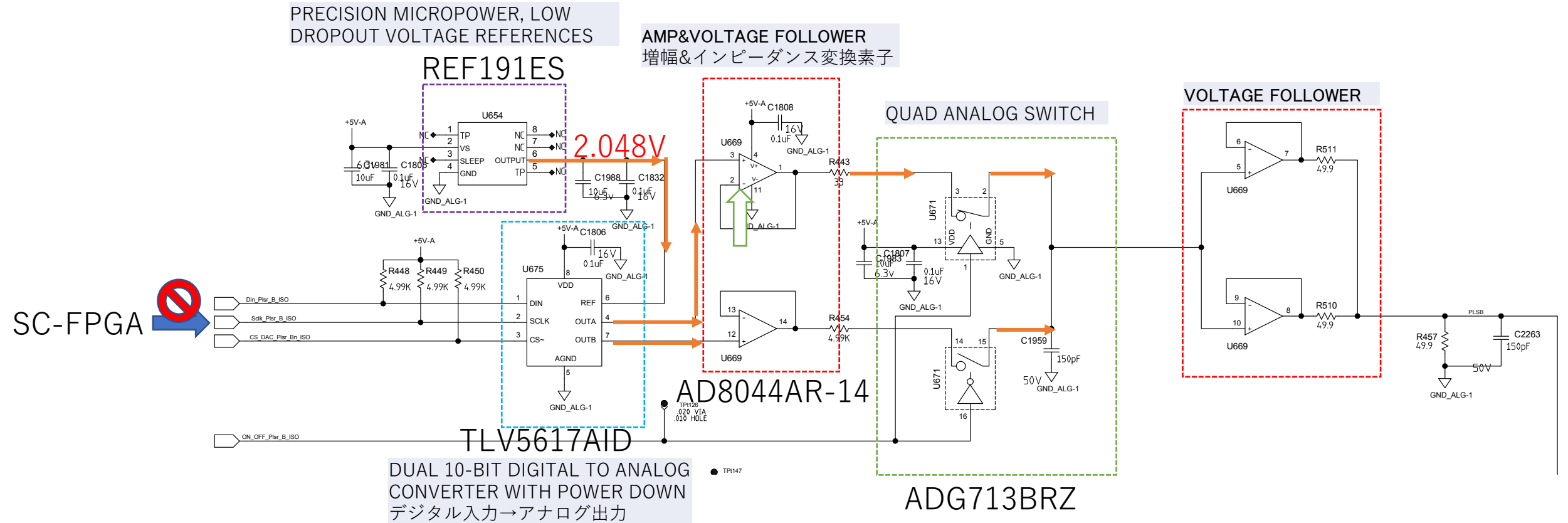
It has a potential to become Class-1 after slow control FPGA code download unless physically damaged.

To be used for SC FPGA testing

26 "Old ROC"BNL

Salvaged for TLKs

Issue in NE3 Column-B



Calibration Pulse Generating Circuit