ROC SlowControl FPGA

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INTT　MT

ROC　SlowControl FPGA

- There is a problem with Readbacker not working properly in a setup with Bus-Extender. We know the cause is in the ROC, and our goal is to modify the SlowControl FPGA that is sending and receiving Readback data in the ROC.

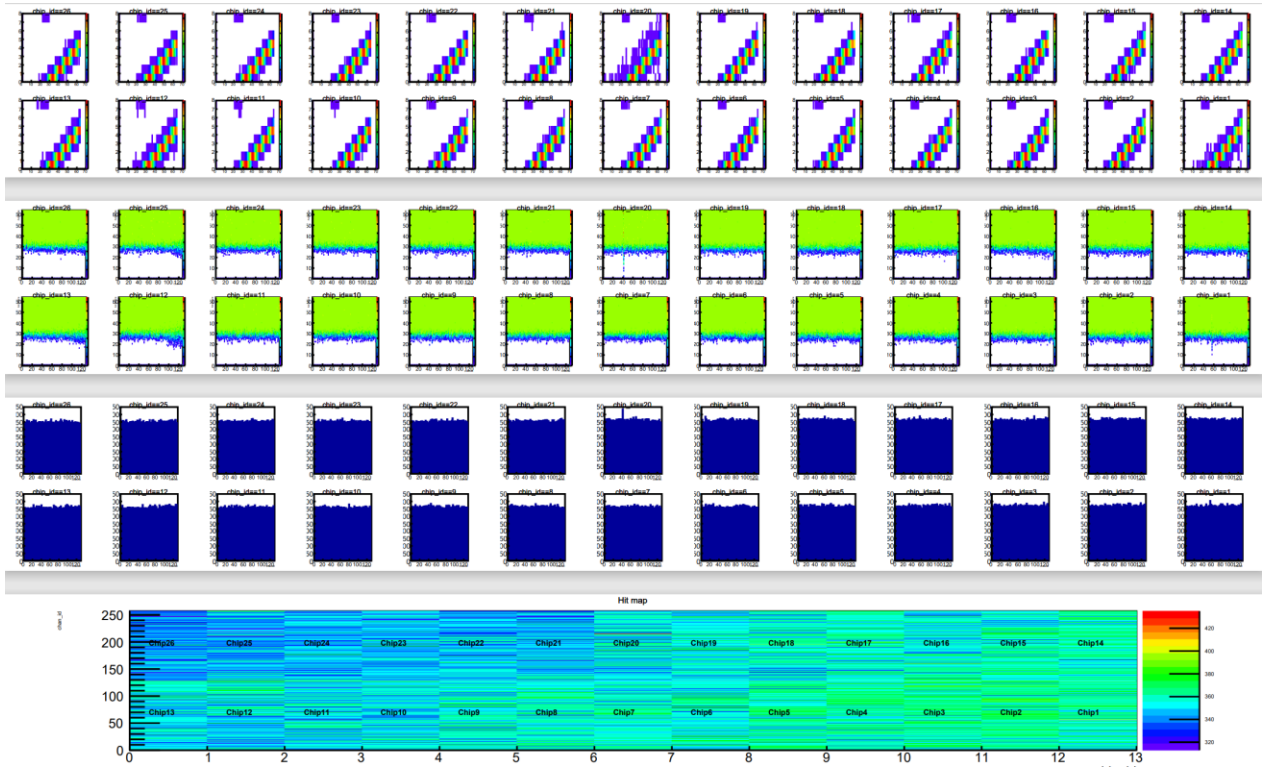
- I was looking for the ROC code that is used now, which is the original code for the modification.

- Status so far

  ・None of the files on the FVTX web site didn't pass the "Verify".
  Verify is passed when the file written in the ROC and
  the file selected on the computer are completely matched.

  ・I have tried actually writing those code in the ROC,
  but could not get them to calibrate.

- So I Verified some files found on a PC that was used in FVTX in the past and found a file that passes Verify.
- After writing this code into the ROC, we were able to successfully calibrate it.
- These gave us the ROC codes currently in use that were needed for the modifications.
- My next plan is to compile this code and test if it works properly. Then I plan to improve this code for Readbacker.



This is a picture of the results of calibration test of the program I found and wrote to ROC

I have put the zip file of this code on the sPHENIX wiki.
sPHENIX wiki →INTT → ROC → ROC FPGA program