

Introduction

Proposed agenda for today:

- (This) introduction
- Update on readout — Jo Schambach
- Update on sensor — Grzegorz Deptuch
- Update on characterization — Nicole Apadula
- AOB

Introduction

We had to skip two of our general EIC silicon consortium meetings in April, so it is timely to share updates as a group,

In the interim, progress from:

- Initial mechanics meeting late March
 - Broader mechanics workshop about to start, c.f. <https://indico.bnl.gov/event/19202/>
- CERN visit by Elke, Rolf, John L., Silvia, Klaus, Laura, Nikki, Grzegorz
 - Multiple take-aways — Grzegorz's talk
 - Small number of EIC LAS variants implies that we need to revisit tiling onto disks, and to a less extent also the outer barrel staves,
- Detector Subsystem Collaboration initiated around a single MAPS-based tracking and vertexing subsystem,

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Detector Subsystem Collaboration for the Silicon Vertex Tracker subsystem

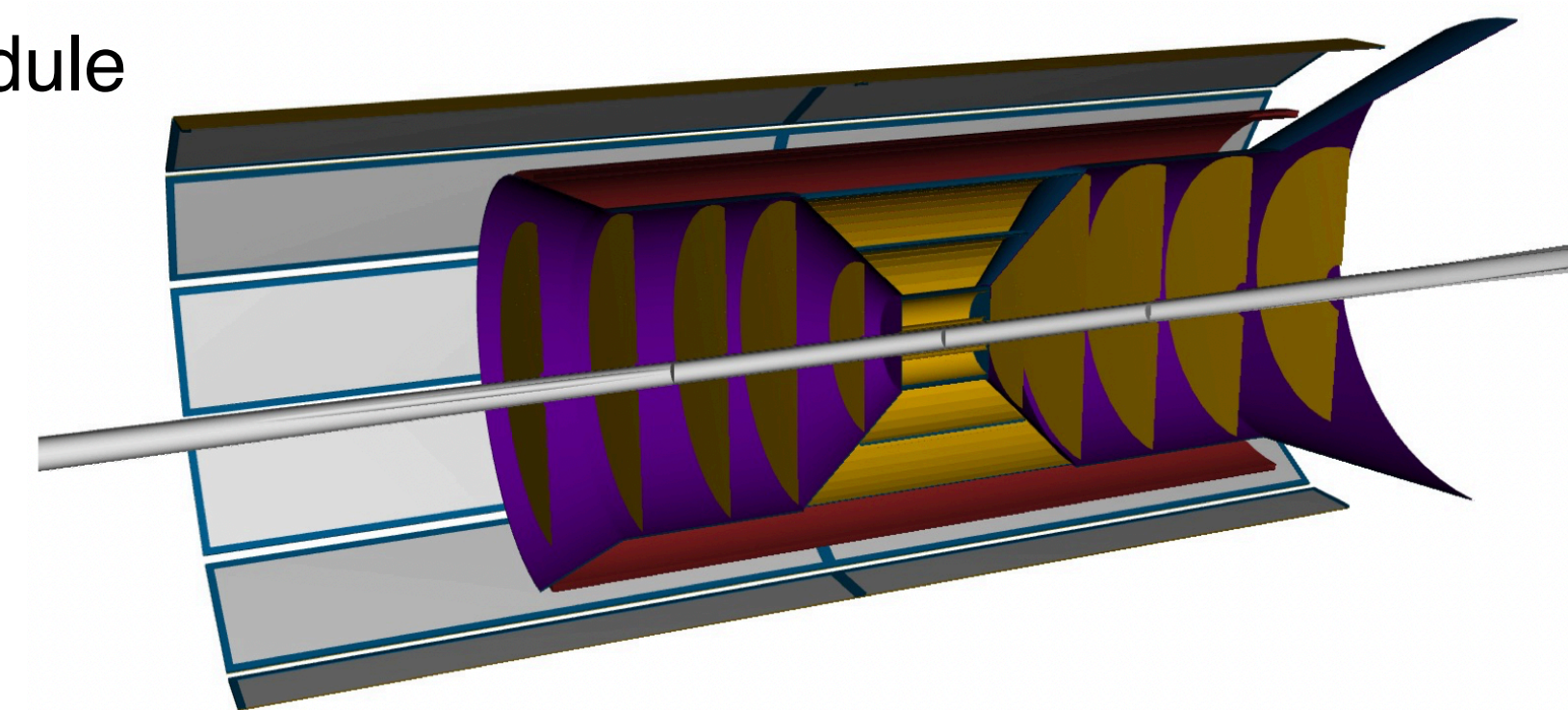
The SVT subsystem is described on the [wiki](#); it is based around a 65nm MAPS sensor and currently consists of five barrel layers (L0—L4), five disks in the hadron-going direction (HD0—HD4), and five disks in the electron-going direction (ED0—ED4).

The **SVT R&D phase** is ongoing. Relevant timelines include:

- EIC vertex sensor qualification in September 2026, concurrent with ALICE-ITS3
- EIC Large Area Sensor production start in February 2027

The **SVT construction phase** will (mostly) follow the R&D phase. Relevant timelines include:

- CD-3, Approve Start of Construction / Execution, is currently anticipated for Spring 2025,
- SVT construction is estimated to take 3—4 years in a technically driven schedule



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Detector Subsystem Collaboration for the Silicon Vertex Tracker subsystem (SVT)

Imperative to advance our earlier initial discussions on:

- Who will participate?
- Who will do what?
- What resources are available to / within the SVT-DSC?
- What is not covered or missing?

grow and integrate the workforce

- Promising developments — e.g. Purdue, MIT, Yonsei (?), ...

organize ourselves in work packages

- Multiple ways to do this; not necessarily the same (emphasis) during the R&D phase and phase construction phase
- Work package roles are key
- Must stay actively engaged in the detector simulation effort, be it inside or outside of the SVT DSC work package structure

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Detector Subsystem Collaboration for the Silicon Vertex Tracker subsystem (SVT)

While work package roles are key, we also need main contacts within the collaboration in the form of the detector subsystem technical coordinator and leader; Laura and Ernst will serve in these roles initially, by consensus.

We propose a half-day meeting (virtual) sometime in the next few weeks to work out the previous who and what questions, the work package structure, and other matters. Input sought and welcome.

The EIC User Group meeting this upcoming July 23-31 in Warsaw, c.f. <https://indico.cern.ch/event/1238718/>, seems a natural place for an in-person follow-up meeting. Is it? Who will participate? What is the best day? Organization would obviously need to start very soon.

What about the EIC silicon consortium?

Probably best to not get too hung up on the difference; most of us are part of ePIC and those of us who are appear so for historic and/or time-sequence reasons — mailing list, wiki, and other logistics can be transferred as needed...

Other news:

Elke, Rolf, and Thomas have just sent a call for progress reports and FY24 project R&D proposals. The deadline will be July 7 (!) with a DAC meeting to follow sometime TBD in the July—August period. They explicitly caution to separate R&D from PED. Take-away: we need to follow up on this soon.