## Y. Kwon, Yonsei Univ.

- Involvement in ITS2/ITS3@ALICE
  - Participation in the design of front end, ALPIDE
  - Chip characterization (probecard production)
  - Production test of ALPIDE (80%)
  - One postdoc in the design team (3 years as of Sep. 2023) and students
- Electron endcap tracker (idea initiated with M. Liu of LANL)
  - 30  $\mu$ -thick Si sensors with pixel size of 18  $\mu$ . Fully digitized and sparcified readout @ 1 Gbps or above, Sensor-only for the active area
  - Operation condition for ePIC will be different (Power/Readout optimization in design?).
  - Very tight schedule for ITS3
- Exclusive electroproduction of J/Ψ off nuclei
- Important decisions (Plan for fiscal year 2024): May in Korea