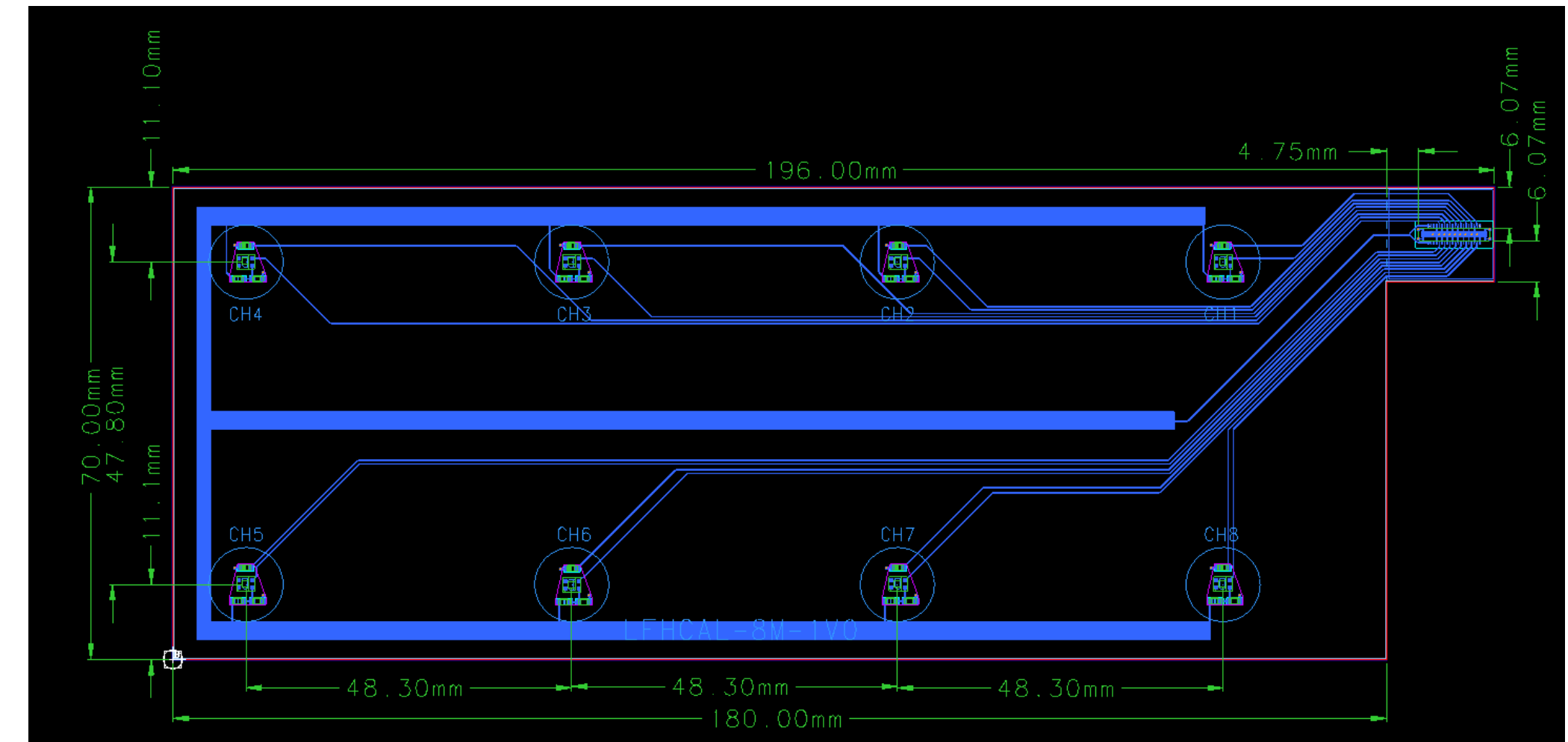


# Readout progress

Norbert

# Plan for the next year

- **Flex board (1-2 weeks)**
  - First design by end of the week (feb 2)
  - Production next week
  - “Long board” with cable connection:
    - PCe type of cable for CAEN/HGCROC board
    - SMA connectors to be compatible with current setup
- **Summing board (March)**
  - Summing signal from multiple SiPM’s
  - Easiest is to use the 4x4 array of 3x3 mm<sup>2</sup> SiPM from Argonne
    - Compact design, replacing the CAEN board
    - Easy to put it in a “dark box” of 10x5cm
- **Long Board (April-May first prototype)**
  - LED drivers via I2C
  - Routing of the signals
  - Prototype in May
  - Real 1.4m one in July

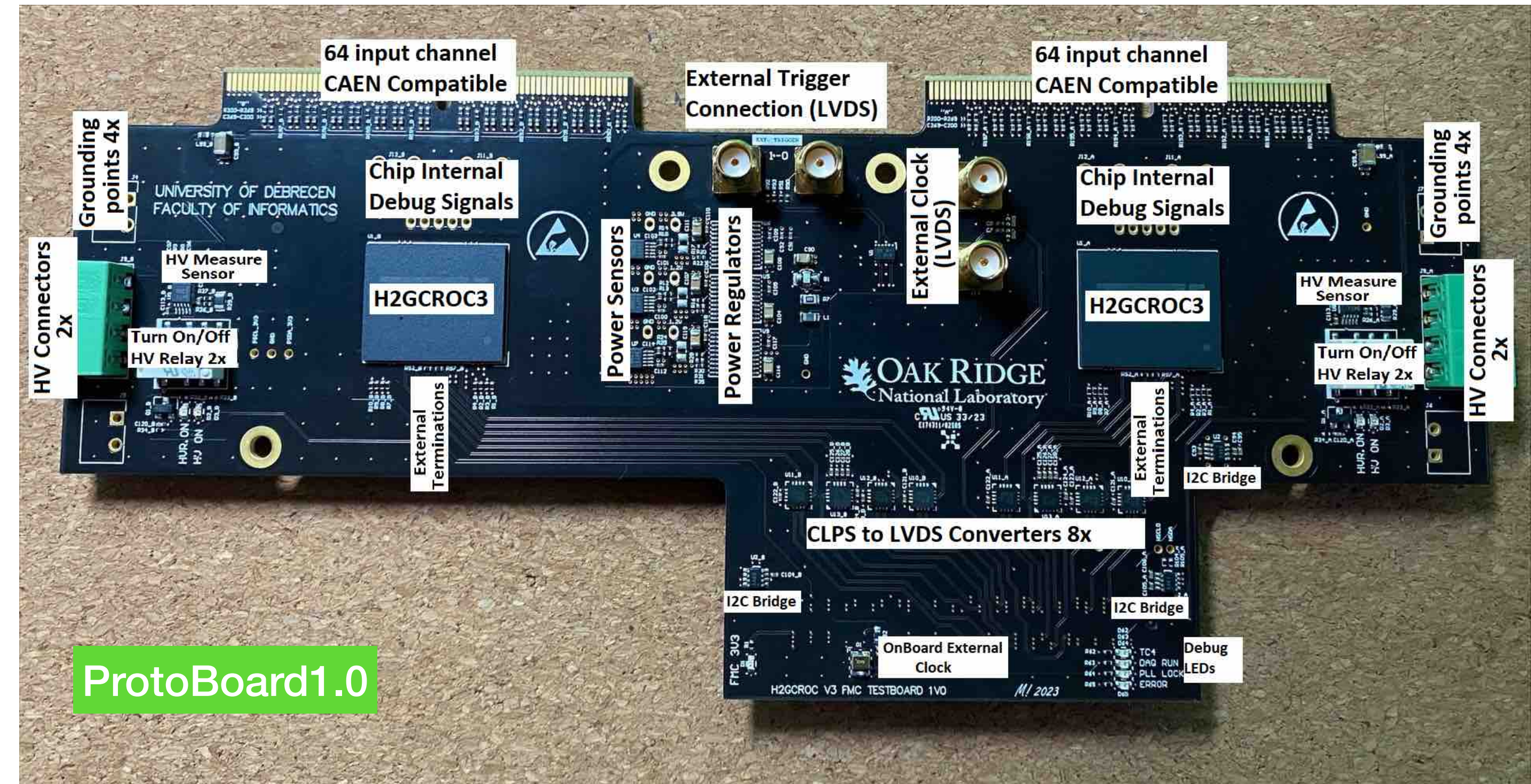




# HGCROC ProtoBoard2.0

## New improvements from ProtoBoard1.0:

- Removing the LVDS drivers (costly, hard to get)
- Adding extra channels on the side (8 extra channels)
- HGCROC3:
  - A-type if there is no B-type available:
    - A has still some bugs from the production, B should have it all fixed
  - Need 6-8 weeks for production, 5+1 boards will be produced
- Adding also the LED pulser SMA connector to it



## Firmware is ready, under testing now

- Readout via Ethernet (1Gbps, can be upgraded to 10Gbps)
- Adding the monitoring: there are temp and HV/power measurement sensors which can monitor the board
- External trigger and clock for synchronization purposes