## Onsite summary and status report

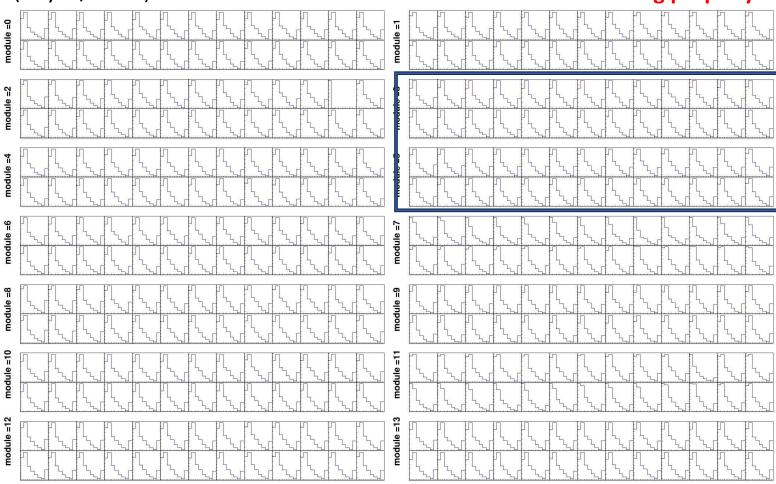
Jaein Hwang

July 6 2023

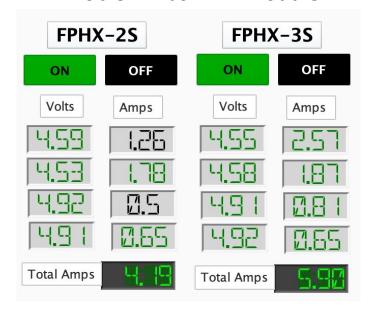
#### INTT1 missing ladders issue (solved)

Run 20703 (July 4 / 2023)





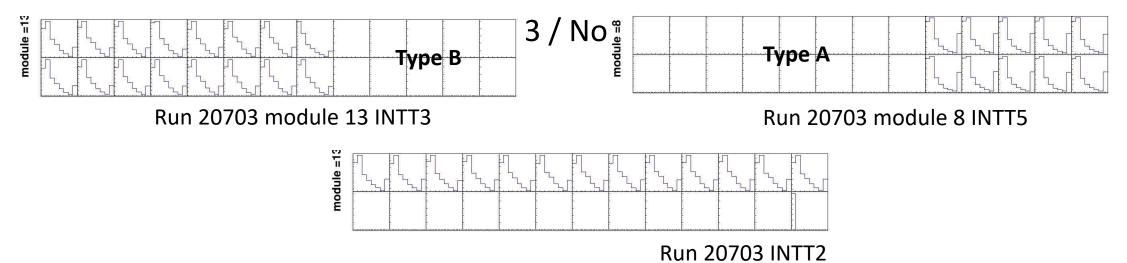
Problem was in LV module.



The ports where the current drop occurred matched the port of the ladders which were empty.

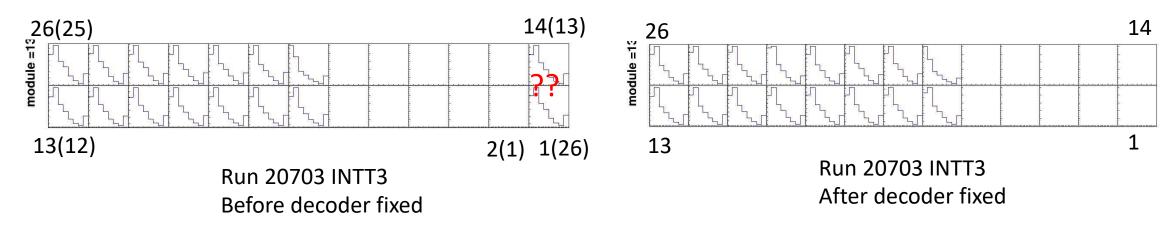
#### Summary of health check (Run 20703)

 After masking Felix slow control to block fake calibration mode hit, all ladders are working properly based on Run 20703. (July 4 / 2023)



### Chip miss-alignment issue(solved)

Type B chips at module 13 in INTT3 have been masked due to no-bias issue



The alignment of the chip was moved one by one.

```
Before After

chip\_id = (chip)\%26+1
case for chip 1^26
chip 25 -> chip 26
chip 26 -> chip 1
chip 1 -> chip 2

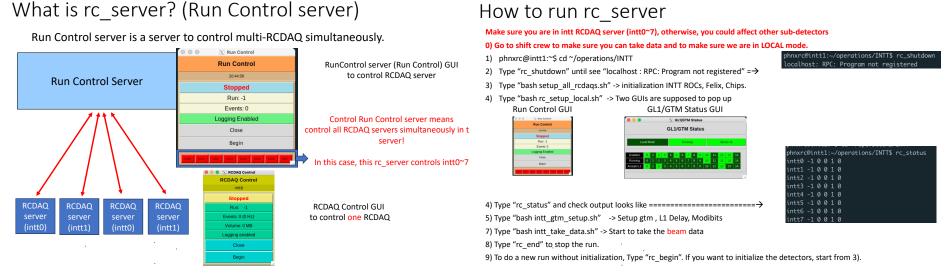
After

chip\_id = (chip-1)\%26+1
case for chip 1^26
chip 25 -> chip 26
chip 26 -> chip 1
chip 1 -> chip 2
```

#### Standalone INTT RonCotrol(rc\_server) on LOCAL

RonControl(rc\_server) is available to take the data with standalone INTT on LOCAL MODE Please go to wiki page and make sure you have latest instructions!

https://wiki.sphenix.bnl.gov/index.php/INTT\_Felix\_DAQ INTT WIKI ->INTT Barrel Operation -> Standalone DAQ This instructions covers : what rc\_server is.. How to use.. How to change INTT configuration..



How to change n\_collision, open\_time

• Make sure you are in OPCO or intt RCDAQ server (intt0~7)
• Open /home/phnxrc/INTT/sphenix\_inttpy/run.py
• Find n\_collisions and open\_time. Change to your setting.

• Make sure you are in OPCO or intt RCDAQ server (intt0~7)
• Open /home/phnxrc/INTT/sphenix\_inttpy/run.py\* at every each intt0~7 servers.

After changing, execute setup\_sill\_rcdags.sh
or do "pyhtons /home/phnxrc/INTT/sphenix\_inttpy/run.py\* at every each intt0~7 servers.

How to change FPHX Parameters(DAC, LVDS...)

• Make sure you are in OPCO or intt RCDAQ server (intt0~7)
• Open /home/phnxrc/INTT/sphenix\_inttpy/run.py
• Find "fphx\_parameters". Change the content inside the fphx\_parameters file.

or do "pyhton3 /home/phnxrc/INTT/sphenix\_inttpy/run.py" at every each intt0~7 servers.

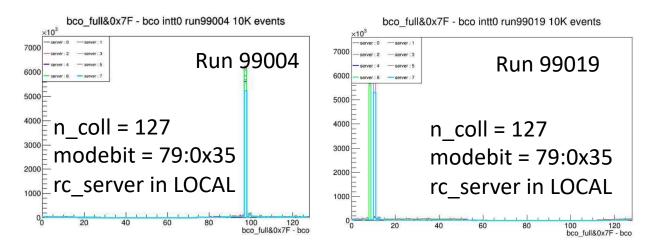
After changing, execute setup\_all\_rcdaqs.sh

Any comments, any questions welcome!

#### BCO miss-alignment issue & short plan for tomorrow

BCO alignment issue may be solved(?)
 Problem was Felix couldn't receive the FPHX reset modebit from GTM in GLOBAL MODE

test run result with new rc\_server on LOCAL MODE (July 1st)



https://sphenix-intra.sdcc.bnl.gov/WWW/elog/INTT/319

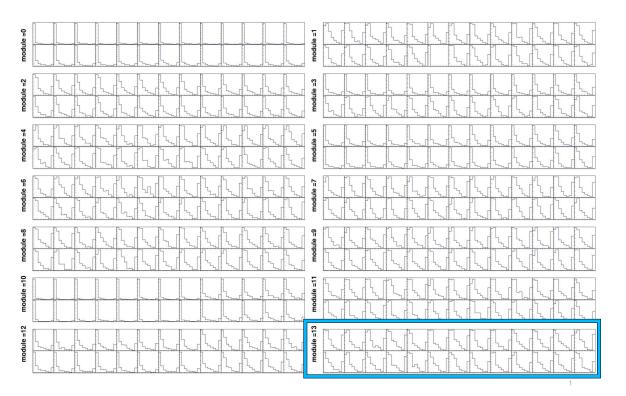
 We modified our scheduler file (configuration file to send modebits signal through GTM) and now we can receive reset signal. (confirmed by oscilloscope and GTM GUI). We have to confirm with real beam data!

Now plan is INTT will take the data tomorrow morning.

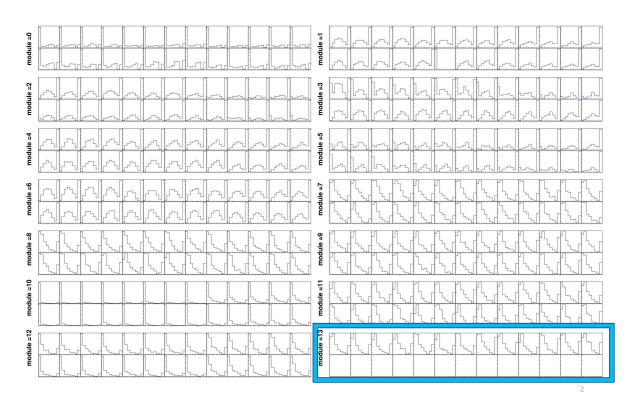
# Back up

### INTT2 empty chips

Run 10483 / June 11 7:20 PM



Run 10493 / June 12 3:40 AM

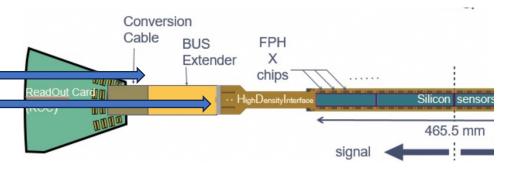


Half of INTT2 began to be empty from the time Run10430 bad connection between

the conversion cable and BUS extender

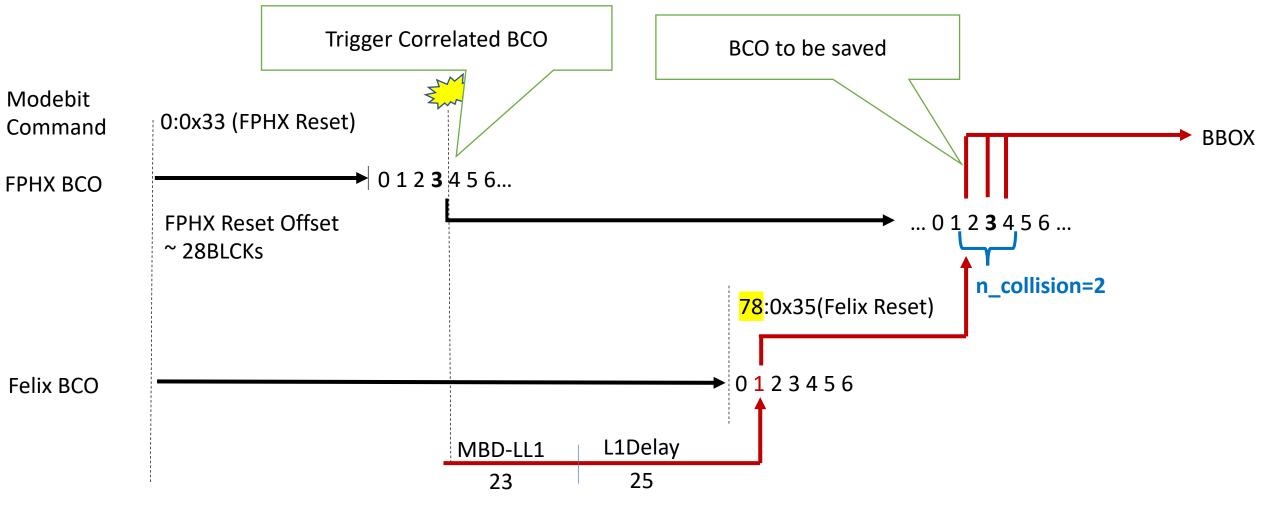
or BUS extender and HDI?

Rachid checked the connection, couldn't find problem.



#### Modebit

INTT (felix-2) was timed-in successfully through the modebit scan. The sweet spot of felix BCO counter reset command issue timing was found at 76 BCLKs after FPHX BCO counter reset issue.



Modebit Felix Reset Delay: 28+23+25=76