# INTT Status

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## To declare physics mode operation of INTT

#### A) 8 Felix servers in operational condition $\checkmark$

- Intt0 and intt1 are under triggerless mode.
- Fallback solution: Disable calibration flag in Felix. No calibration possible for intt0 and intt1.

#### B) Fix timing and online parameters

- Inconsistent time-in condition between 8 felix servers has to be resolved in order to finalize the timing parameters
- All felix servers are to be timed-in within 2 BCLKs (1BCLK by the end of Run23)

#### C) Proof of correlation with other detectors

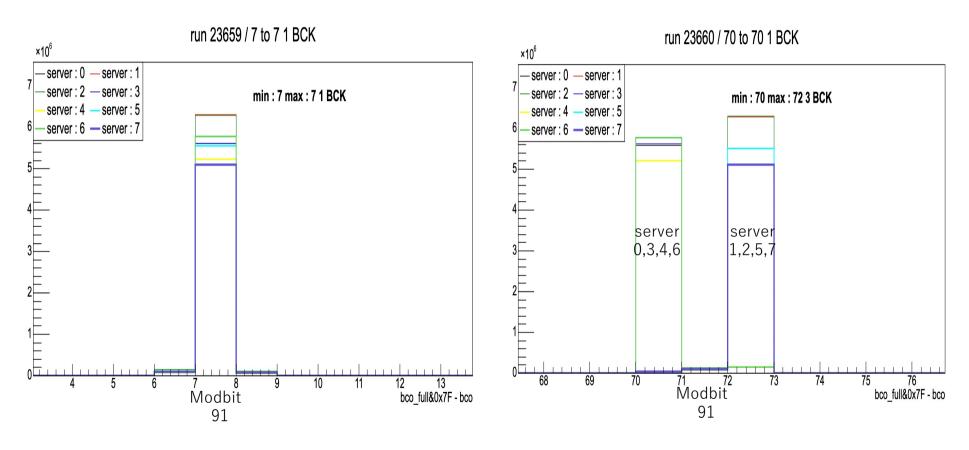
- Attempt to synchronize w/ MBD by event counter after Martin's new decorder was unsuccessful. -> Established MBD & TPOT
- If feasible, MIP observation in conjunction with MBD's z-vertex cut.

#### D) Online Monitor

Need to prove the it displays known dead/hot spots (no-bias silicon region, hot chips, etc)

# B) Timing and Online Parameters

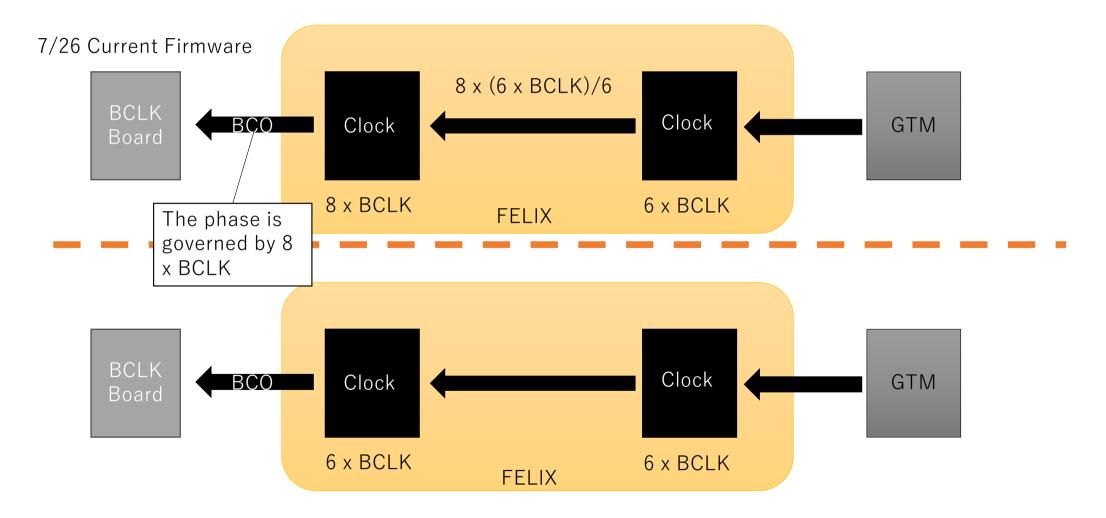
# B) Latest Timing Issue



Plots from Jaein's slide

#### B) Possible Cause?

Raul is skeptical the glitch in the frequency conversion within clock blocks in the felix firmware. The new version avoids the conversion and was tested in intt0 on 7/26.



#### B) BCO Phase Scan

Latest configuration as of 7/24

Fine Delay: 88ps/tic

Maximum =  $88ps \times 255 = 22ns$ 

L1 Coase Delay= L1Delay

Delay Set #	13	14	15	16	17	18	19	20	21	22	23	24
L1 Coase Delay	123	123	124	124	125	125	126	126	127	127	128	128
Fine Delay	10	121	10	121	10	121	10	121	10	121	10	121
Total Delay [BCLK]	20.51	20.59	20.67	20.76	20.84	20.92	21.01	21.09	21.17	21.26	21.34	21.42
Total Delay [ns]	2181.65	2190.53	2199.38	2208.26	2217.11	2225.99	2234.84	2243.72	2252.57	2261.45	2270.30	2279.18

- L1Delay=21
- n\_collision=0
- Modebit 76:0x35
- 60 kEvents (1minute @ 1kHz) x 42 runs ~ 2 hours

#### **Execution and Analysis:**

- Script: Execute ~/operations/INTT//L1FineDelay.sh (need to be tested)
- Time in plots are to be made in felix-by-felix basis: Jaein / Other volunteer?

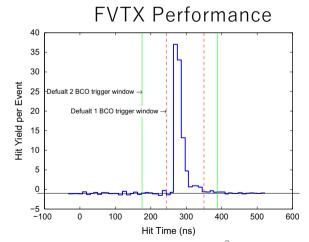
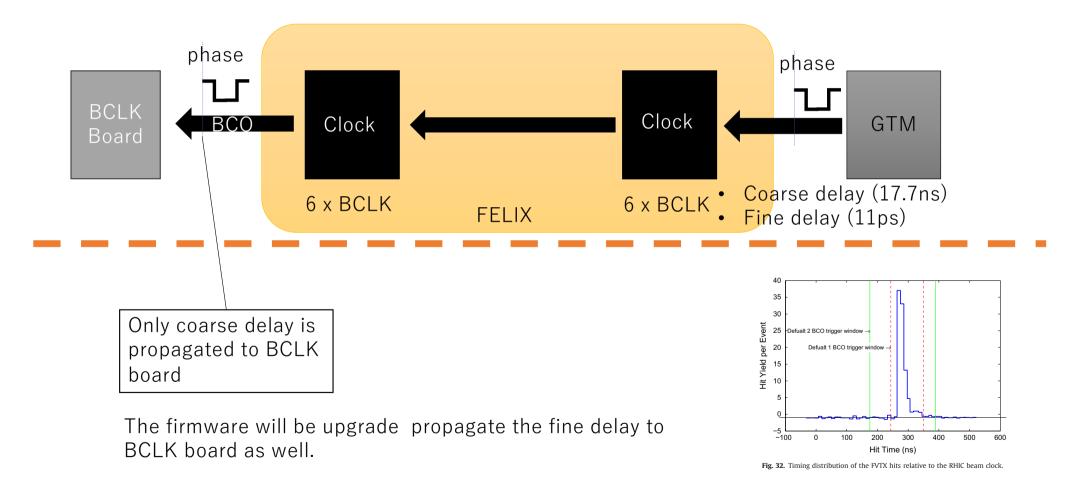


Fig. 32. Timing distribution of the FVTX hits relative to the RHIC beam clock.

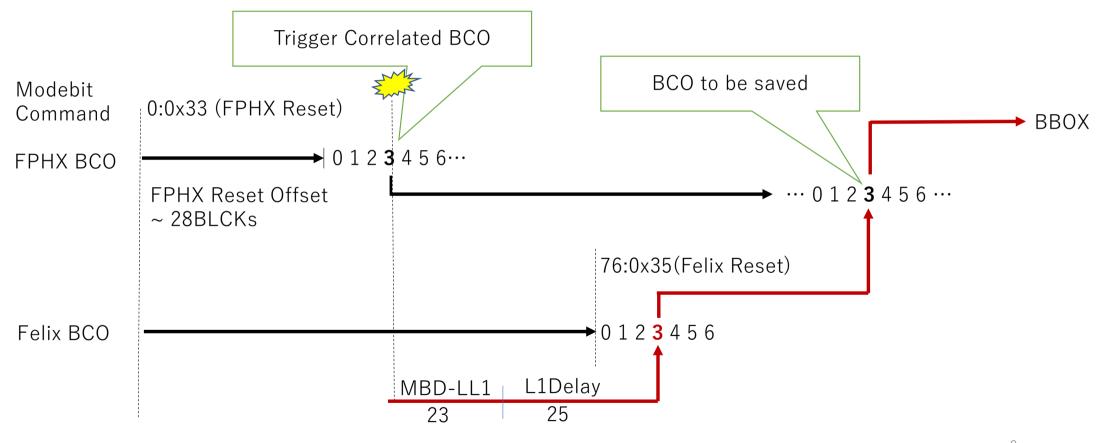
#### BCO Phase Scan Preparation



# History of INTT Timing Tunes

Phase	Date	Run	Comments
I	5/25	7364 ~ 8059	<ul><li>LVL1 Delay Scan</li><li>n_collision Scan</li><li>Open time Scan</li></ul>
П	5/30	8100 ~ 8126	<ul> <li>Modebit delay scan</li> <li>Intt2 timed in at modebit=76:0x35</li> </ul>
П	6/2	9158 ~ 9231	<ul><li>Modebit 78:0x35</li><li>Open up n_collision = 4</li></ul>
IV	6/17	13091 ~ 13127	<ul><li>Modebit delay scan 7 servers</li><li>Inter-felix timing shift observation</li></ul>
V	7/14	21386 ~ 21506	<ul> <li>MBD Trigger 28-&gt;32, L1Delay=25-&gt;21</li> <li>Inter-felix timing shift fixed</li> </ul>
VI	7/25	23642 ~ 23667	<ul> <li>All felix servers timed in at modebit=91:0x35</li> <li>Split timing peak +/-1BCLK alternating runs</li> </ul>

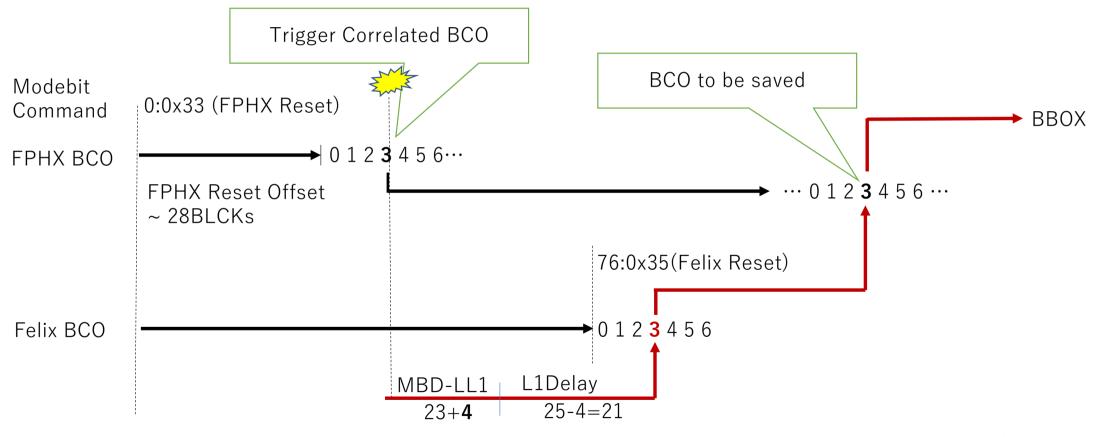
#### Solution: Modebit Offset



Modebit Felix Reset Delay: 28+23+25=76

## Compensation of MBD Timing Tune

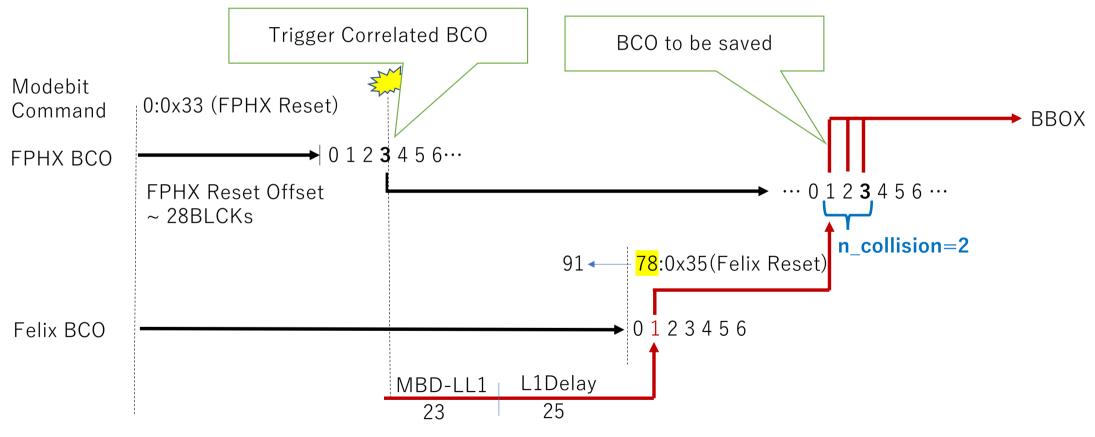
MBD timing change by 4BCLK is compensated by L1Delay setting from 25 -> 21.



Modebit Felix Reset Delay: 28+23+25=76

#### 7/25 Time-In Result

Modebit 76:0x35 -> 91:0x35 picks up earlier hits by 15 BCLKS



Modebit Felix Reset Delay: 28+23+25=76

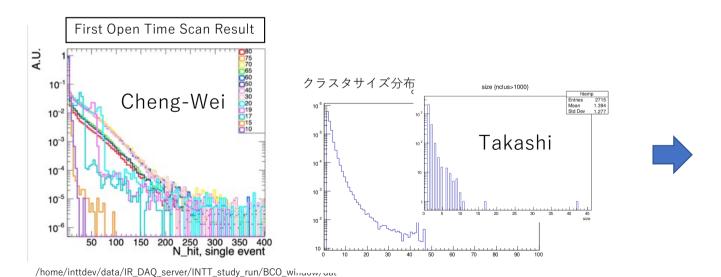
## Remaining Steps to Satisfy Timing Goal

- 1. Fix splitting the timing peak +/-1BCLK issue in the felix firmware (Raul)
- 2. Upgrade firmware to propagate the fine delay to BCLK boards (Raul)
- 3. Execute the phase scan (Onsite crews)

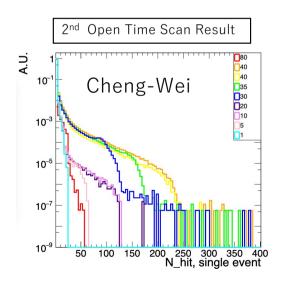
# B) Optimization of Timing Parameters

Cheng-Wei

Index	Date	Run#
I	5/25	8020 ~ 8059
II	6/9	9545 ~ 9553
III	To be done again after	timing issue is resolved



a\_analysis/8020\_Time\_5min\_L1Delay00\_Ncollision127\_Opentime120



#### DACO Scan

Scan	1	2	3	4	5	6	7	8	9	10	11
minutes	5	5	5	5	10	20	60	5	5	5	5
DAC0	17	16	15	18	20	30	40	14	13	12	11
DAC1	44	44	44	44	44	44	44	44	44	44	44
DAC2	48	48	48	48	48	48	48	48	48	48	48
DAC3	52	52	52	52	52	52	52	52	52	52	52
DAC4	56	56	56	56	56	56	56	56	56	56	56
DAC5	60	60	60	60	60	60	60	60	60	60	60
DAC6	64	64	64	64	64	64	64	64	64	64	64
DAC7	68	68	68	68	68	68	68	68	68	68	68

Executed 7/26 Hot channel mask list will be updated (Jaein).

# C) MIP Observation

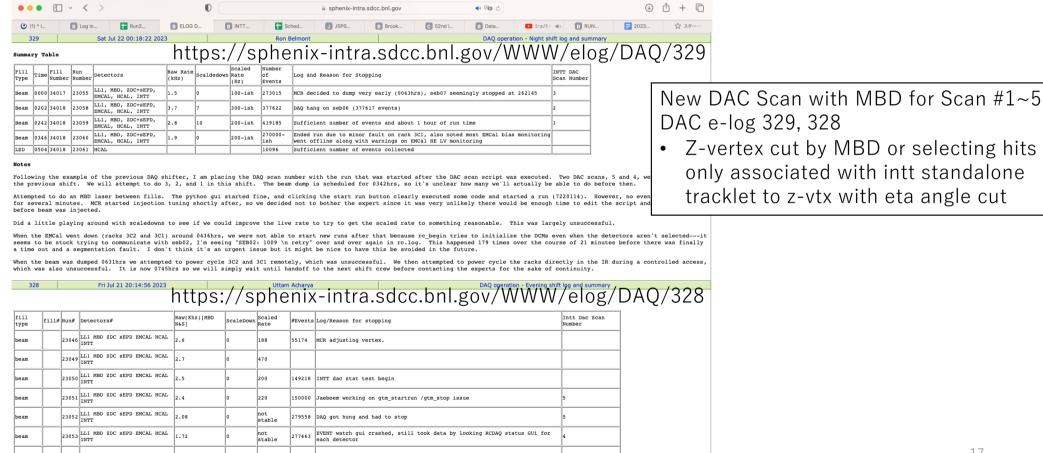
#### Extend to max range

#### DAC Scan

Scan	1	2	3	4	5	6	7	8	9	10	11	12
DAC0	8	28	48	68	88	108	128	148	168	188	212	236
1	12	32	52	72	92	112	132	152	172	192	216	240
2	16	36	56	76	96	116	136	156	176	196	220	244
3	20	40	60	80	100	120	140	160	180	200	224	248
4	24	44	64	84	104	124	144	164	184	204	228	252
5	28	48	68	88	108	128	148	168	188	208	232	255
6	32	52	72	92	112	132	152	172	192	212	236	255
7	36	56	76	96	116	136	156	176	196	216	240	255

- BigPartition together with MBD (Must) no need to be a dedicated run
- Can be done with n\_collision=127 (w/o waiting for asynchronous timing issue btwn intt servers.
- 12 settings
- ~2M events (30 minutes @ ~1kHz)
- 6 hours total
- If the series of data are interrupted by the beam dump, repeat the same setting as the last run at the last store.

#### Towards MIP Peak Observation



DAC e-log 329, 328

 Z-vertex cut by MBD or selecting hits only associated with intt standalone tracklet to z-vtx with eta angle cut

# D) Online Monitor

