Time over Threshold Studies for DPTS

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What it Time over Threshold?

- When a pixel activates, it is the time it spends over the threshold.
- Important because it tells us injectected charge.

$$ToT = aQ_{inj} + b - \frac{c}{Q_{inj} - d}$$



Different voltages and currents we measured:

- Nominal
- V_CASB: 150mV, 200mV, 225mV, 250mV, 275mV, 350mV
- I_RESET: 5pA, 15pA, 25pA, 40pA, 60pA
- V_CASN: 200mV, 400mV, 500mV, 600mV
- I_DB: 50nA, 200nA, 300nA, 400nA, 500nA
- I_BIAS: 50nA, 200nA, 300nA, 400nA, 500nA

What the nominal settings are:

- V_CASB = 300 mV
- V_CASN = 300 mV
- I_reset = 10 pA
- I_DB = 100 nA
- I_BIAS = 100 nA
- V_H = 600 mV
- V_SUB = -1.2 V

Nominal Settings Studies





Nominal Settings Studies



I_BIAS Studies

LBNL Setup







Paper Findings

https://arxiv.org/pdf/2212.08621.pdf7

I_BIAS Studies



I_DB Studies





Paper Findings

LBNL Setup

I_DB Studies



I_Reset Studies





Paper Findings

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I_Reset Studies



V_CASB Studies

LBNL Setup





Paper Findings

https://arxiv.org/pdf/2212.08621.pdf

V_CASB Studies



V_CASN Studies





Paper Findings

LBNL Setup

V_CASN Studies



Future Directions and Conclusion

- Our ITS3 chip follows the same trends as measured in the ALICE paper.
- Relative mean/RMS has a discernible trend, but is still relatively constant.
- Test detector with source
- Look into pixel to pixel ToT differences to find specific patterns.





