

# AstroPix: End of Stave Discussion

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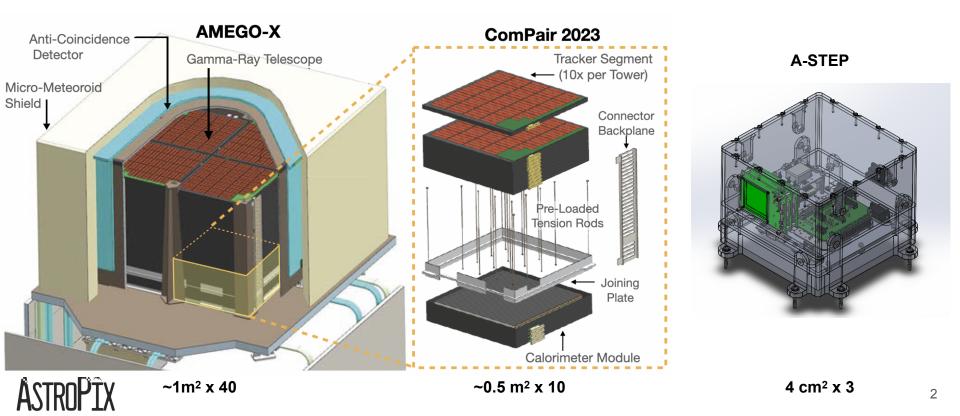
January 11, 2023







#### What have we been talking about?



#### Tracker

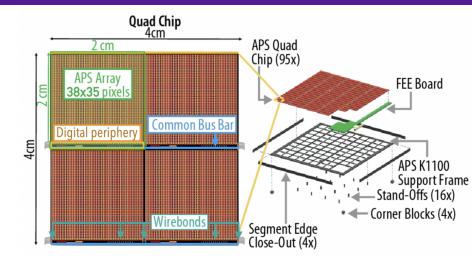
Plan: 10 identical stacked Segments of AstroPix v5\*\* detectors

95 Quad Chips / layer = 1260 cm<sup>2</sup> active area

Segment mechanical structure and FEE designed and fab'd @ GSFC

Integration and testing @ ANL

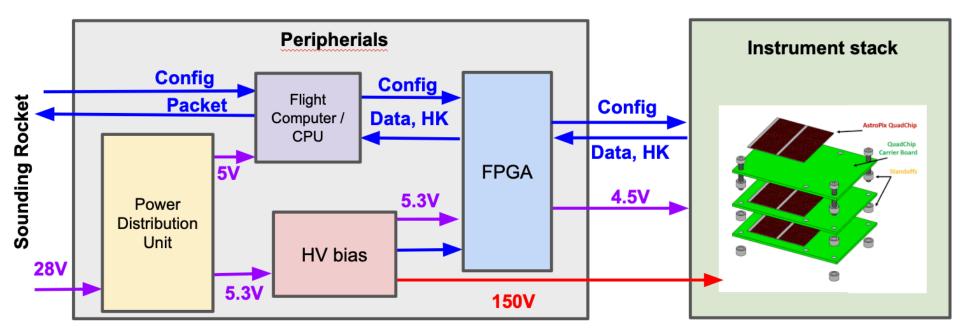
- Pick-and-place machine and wire-bonding
- Fully integrated Tracker segments transported to GSFC for full Tracker integration



Parameter	ComPair 2015	ComPair 2023	AMEGO-X
# of detectors	10	950	15200
Detector tech.	DSSD	AstroPix	AstroPix
Energy res. $(\sigma)$	14  keV	$5 { m keV}$	$5 {\rm ~keV}$
Position res.	$500~\mu{ m m}$	$500~\mu{ m m}$	$500~\mu{ m m}$
Time res.	$10 \ \mu s$	$1 \ \mu s$	$1 \ \mu s$
Dynamic range	50-700  keV	25-700  keV	$25-700 \text{ keV}_3$



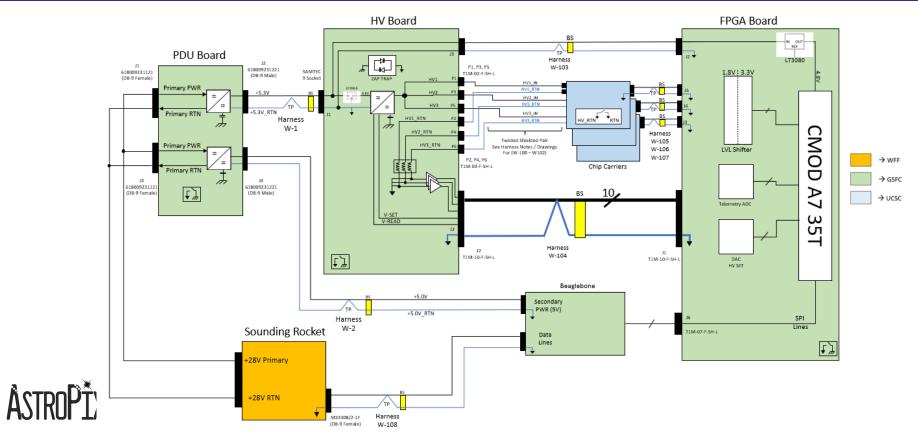
## Block diagram for A-STEP



Power rail, HV, data

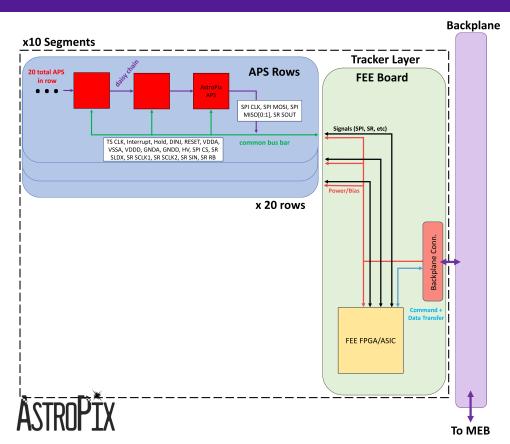
Power conditioning on every board individually, as necessary

## Block diagram for A-STEP



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## Block diagram for ComPair 2



Trade studies on FPGA, whether we can use an ASIC instead, interaction with the backplane and trigger