Digitization in ePIC: Lessons from AC-LGAD subsystems



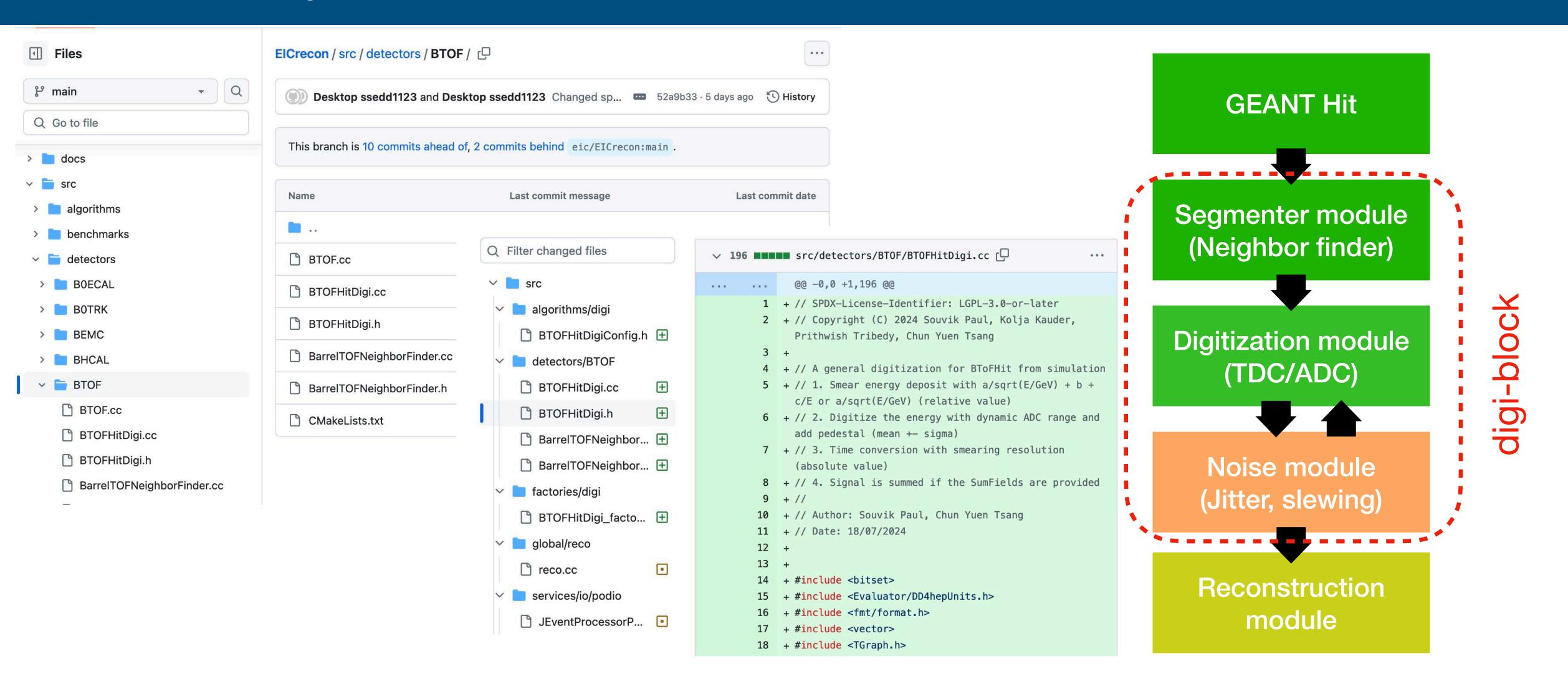


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Join EICUG & ePIC collaboration meeting, Lehigh University, Jul 22-27, 2024

Quick summary of the current status



Recently we finished first implementation of the digitization model for AC-LGAD-based BTOF

— work on other subsystems, FTOF and B0, is ongoing.

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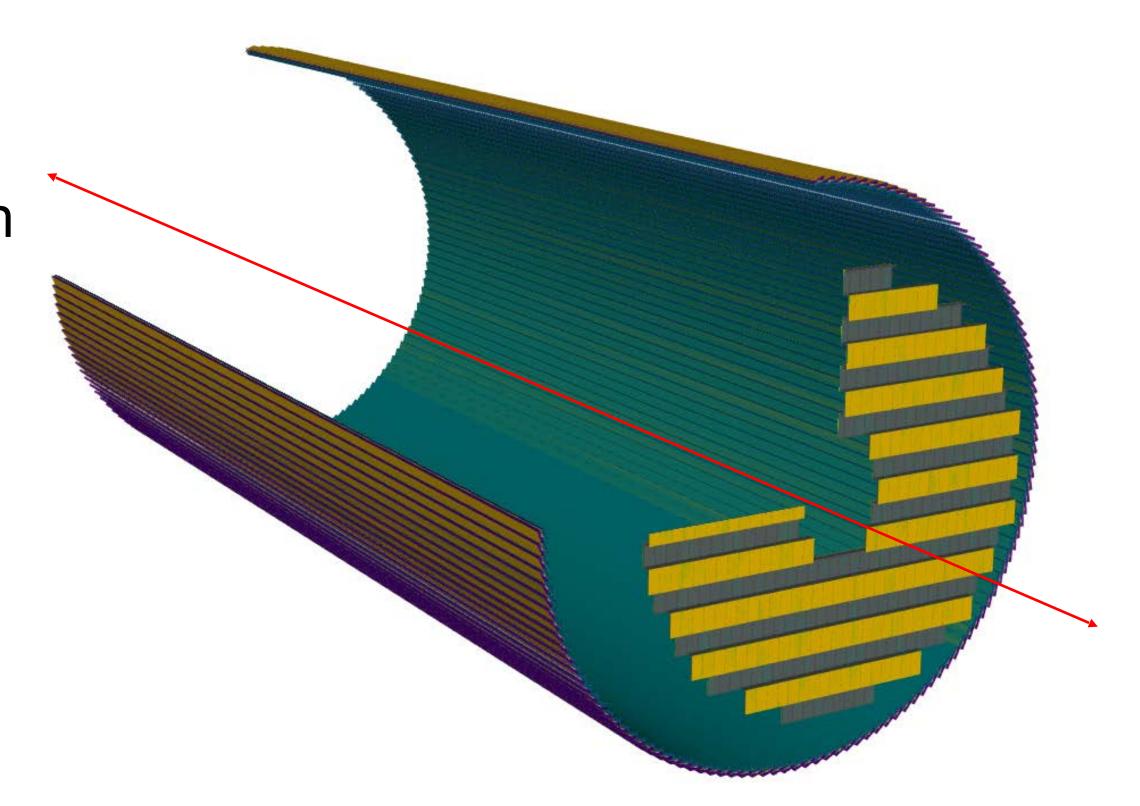
Overview

The digitization model for AC-LGAD system has step:

- Conversion of GEANT deposited energy into ADC/TDC
- Implementation of a charge-sharing
- Incorporating noise due to full readout chain

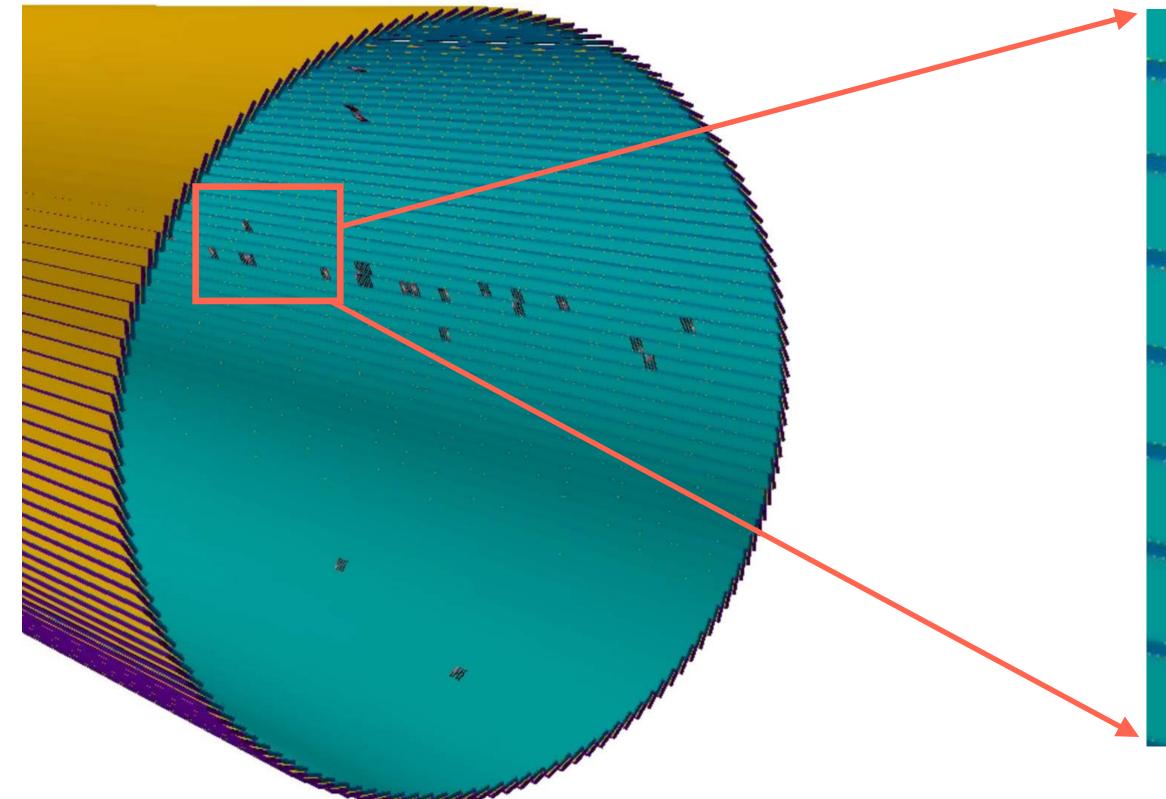
In ElCrecon has two modules:

- 1) Segmentation/neighbor finder,
- 2) Digitization/noise



Using BTOF as the reference example for digitization of AC-LGAD systems

Task at hand

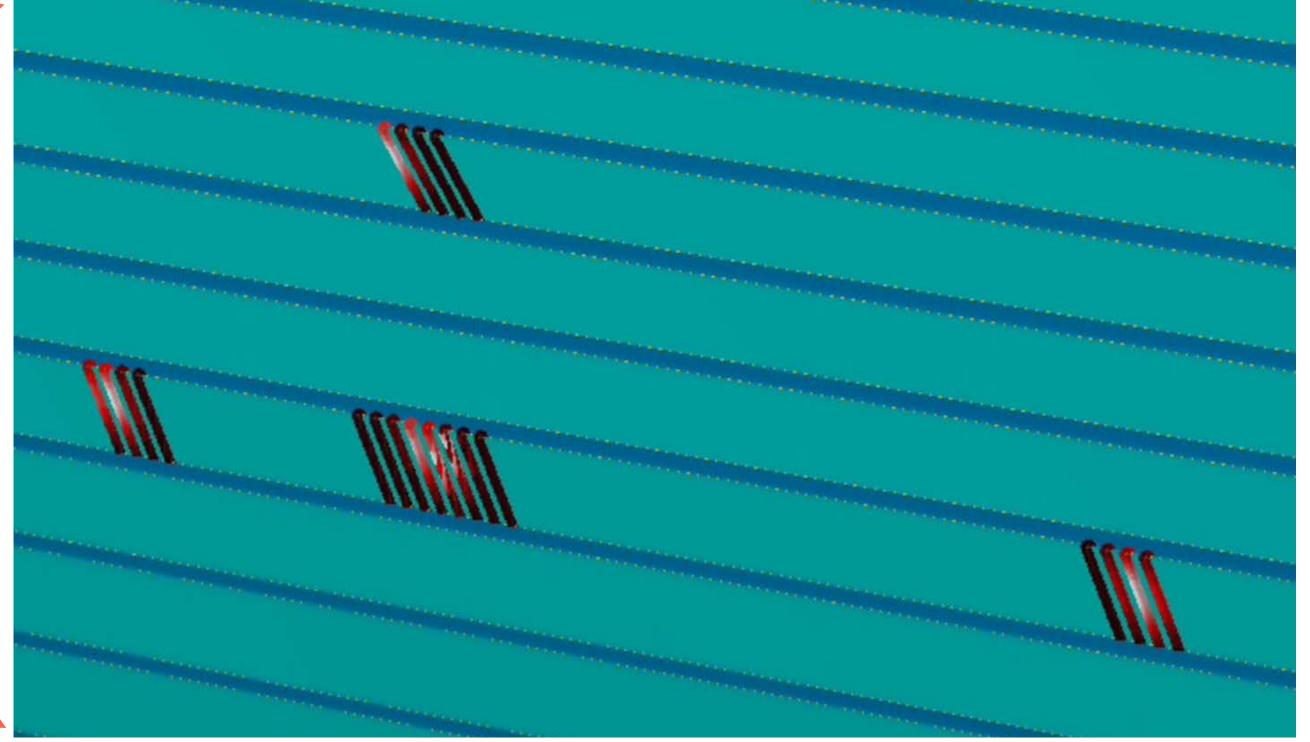




TOFBarrelRecHit.cellID = 18425070710973014364
TOFBarrelRecHit.position.x = 636.281799
TOFBarrelRecHit.position.y = 32.092369
TOFBarrelRecHit.position.z = 1077.500000

TOFBarrelRecHit.time = 4.227000

TOFBarrelRecHit.edep = 0.000184



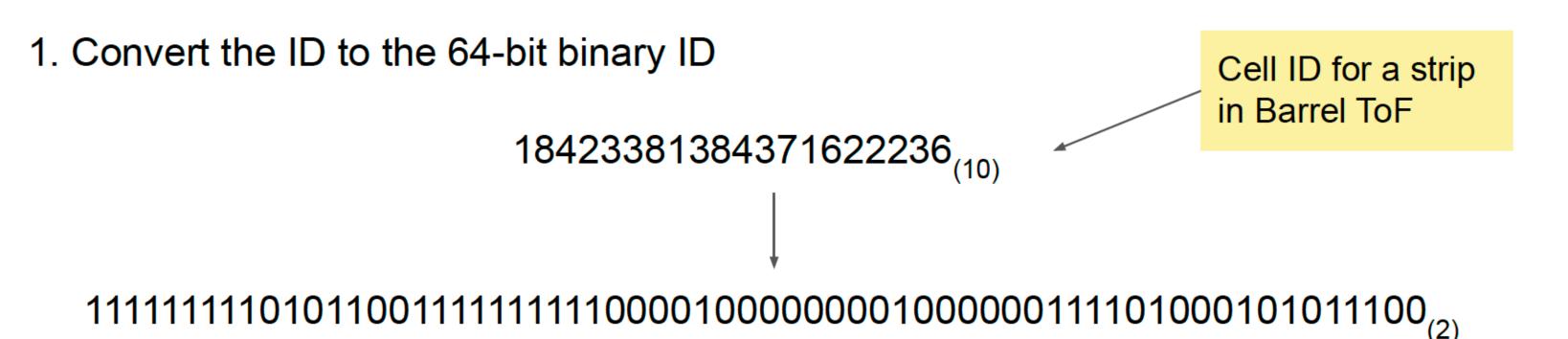
Digitized hit with charge sharing

TOFBarrelADCTDC.cellID = 18425352185949725020, 18425070710973014364, 18424789235996303708, 18424507761019593052, 18424507782494429532...

TOFBarrelADCTDC.charge = 359, 785, 359, 34, 34 ...
TOFBarrelADCTDC.timeStamp = 197, 194, 197, 215, 215 ...

First challenge: Deciphering the cell ID

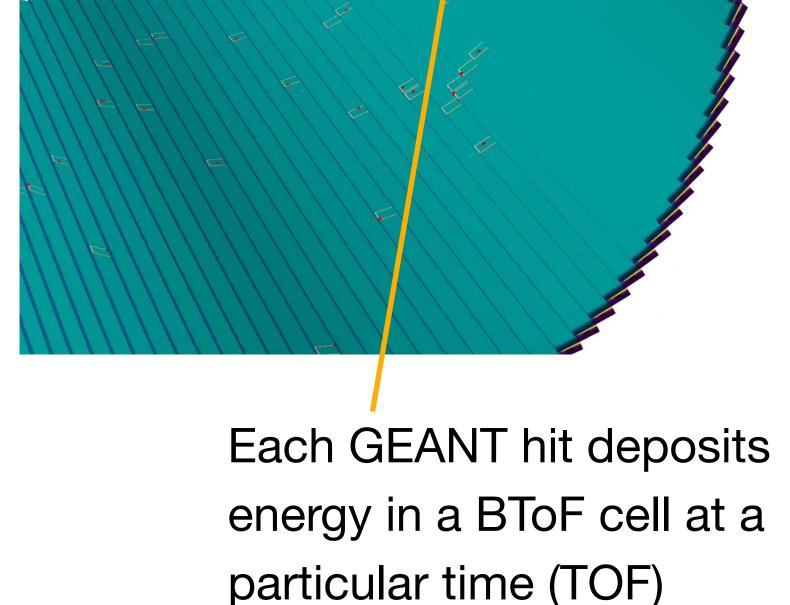
Cell ID helps to go to local co-ordinates & group cells together for signal sharing



2. Split the binary ID based on the identifier in the xml code. For Barrel ToF, the identifier is:

<id>system:8,layer:4,module:12,sensor:2,x:32:-16,y:-16</id>

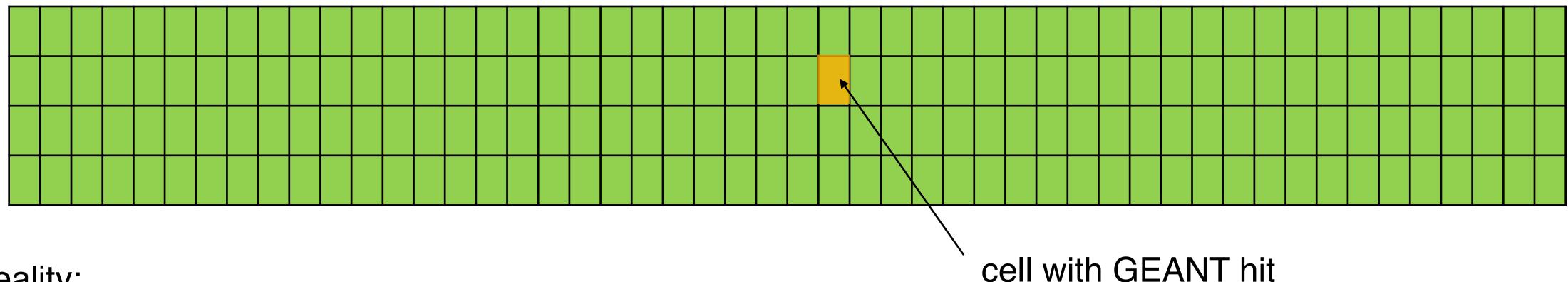
y Sensor Module Layer System



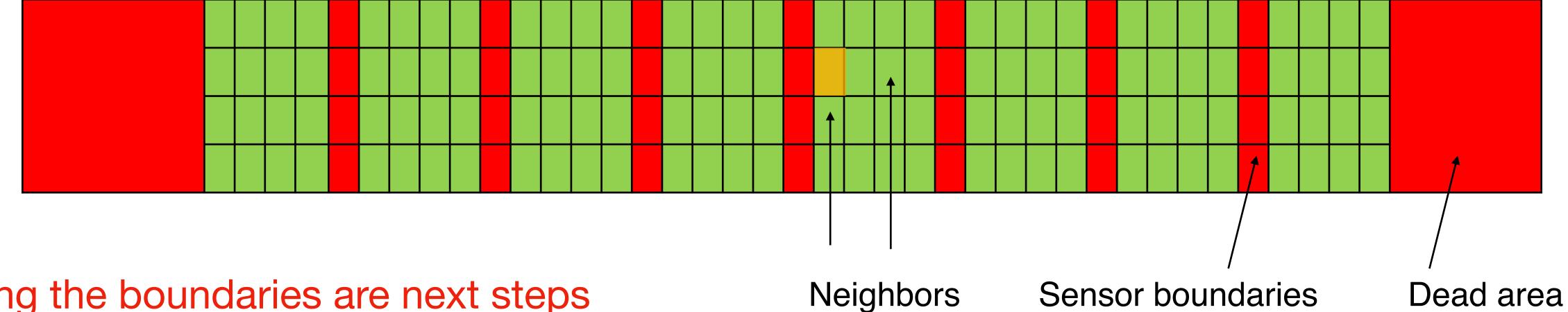
Deciphering the cell-id is the first step & was our major challenge

Put cell in local co-ordinate, find edges & neighbors

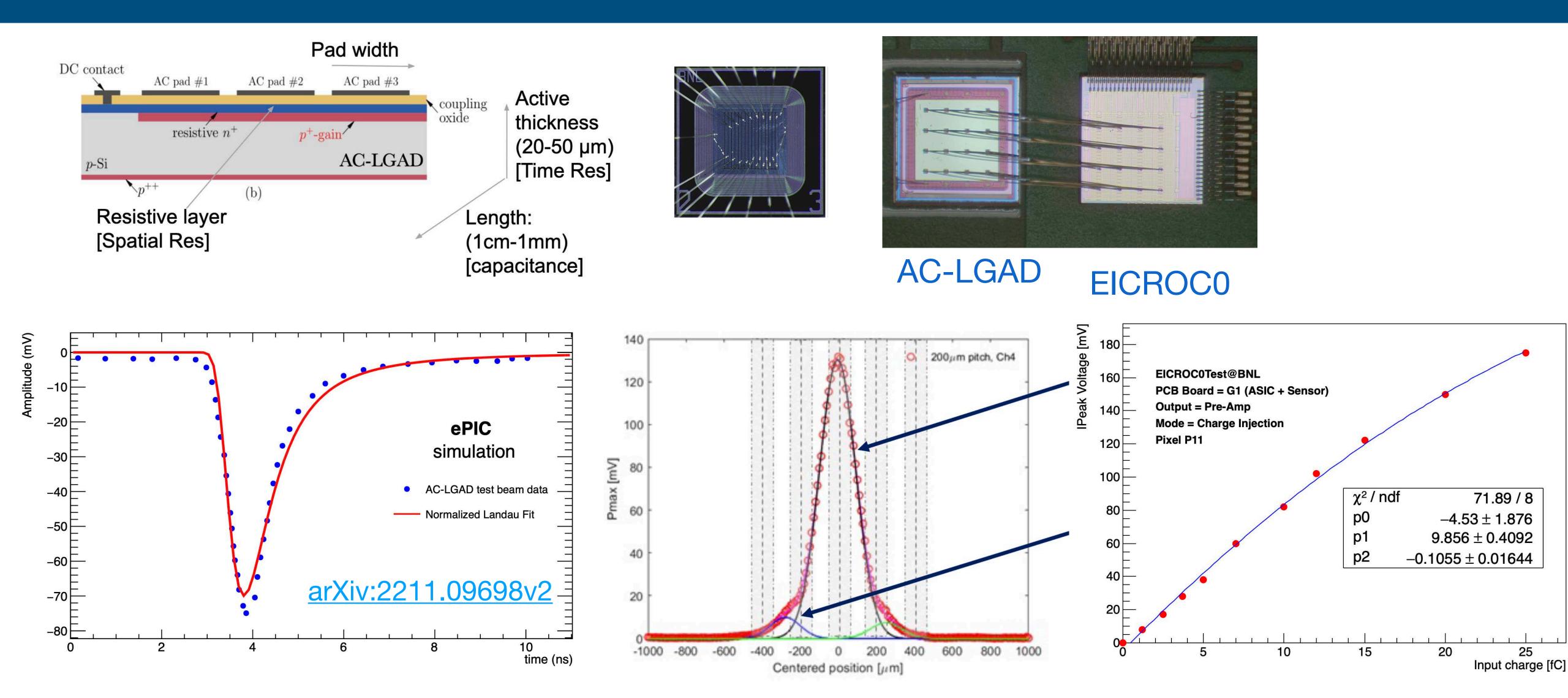
Current ElCrecon simulation: Pixels (cells) are populated in a regular grid from left edge to the right edge.



- Reality:
- Dead space in the left and right edges.
- Dead space between every nth cell.



Input from sensor+asic+RDO

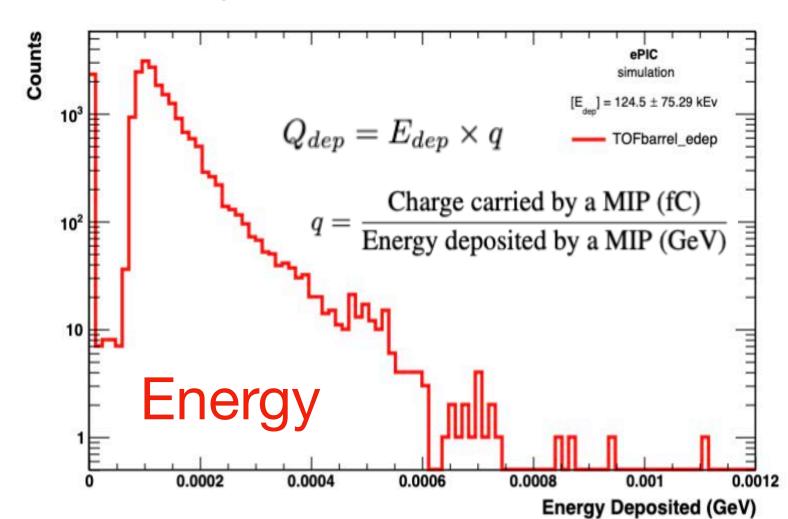


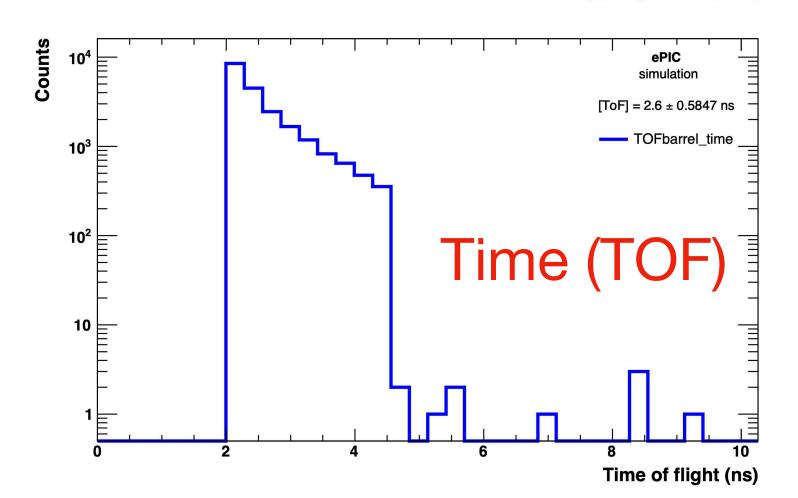
Input from hardware on signal shape, charge spread, pre-Amp output

Energy & time to Peak (ADC) & TOA (TDC)

Event Generation & Transport:

- 250k μ– particles
- $0 \text{ GeV} \le p \le 30 \text{ GeV}$
- $0^{\circ} \le \Theta \le 180^{\circ}$





GEANT input:

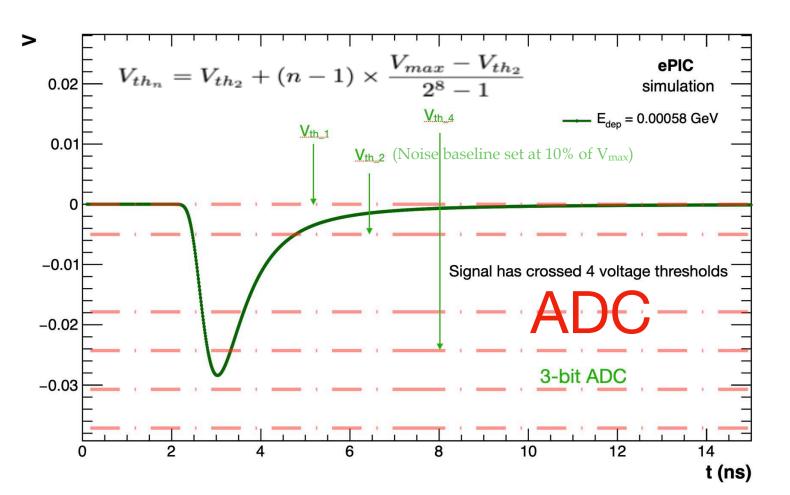
- Q_{dep}: Area under analog signal
- Analog signals placed at ToF

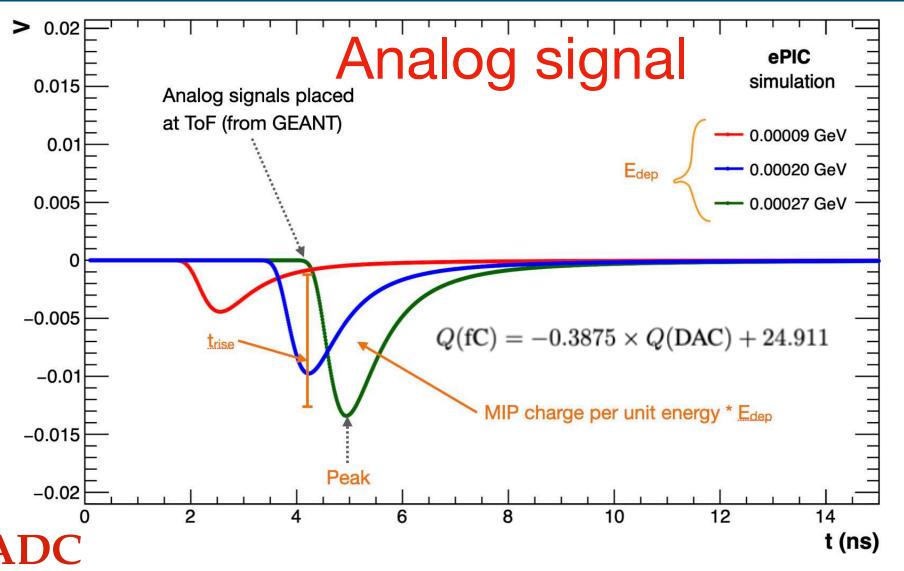
Data-driven input:

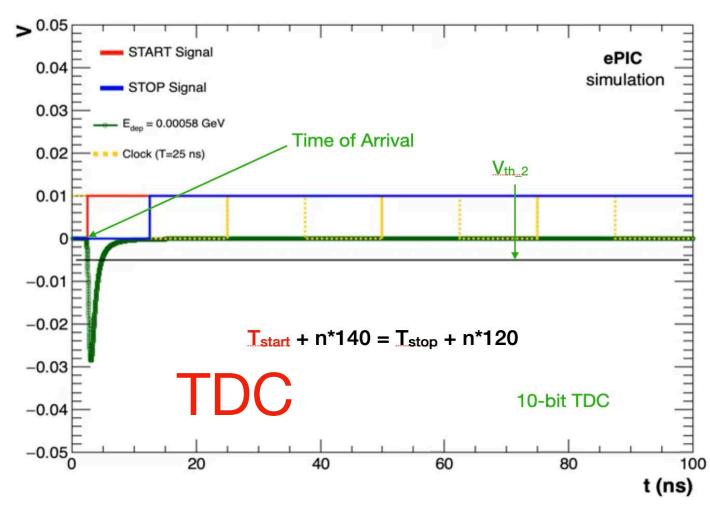
- Analog signal parameterized by Landau distribution
- Risetime ~ 450 ps
- Shape width ~294 ps.

Voltage thresholds —— 8-bit ADC

CLK + START/STOP → 10-bit TDC

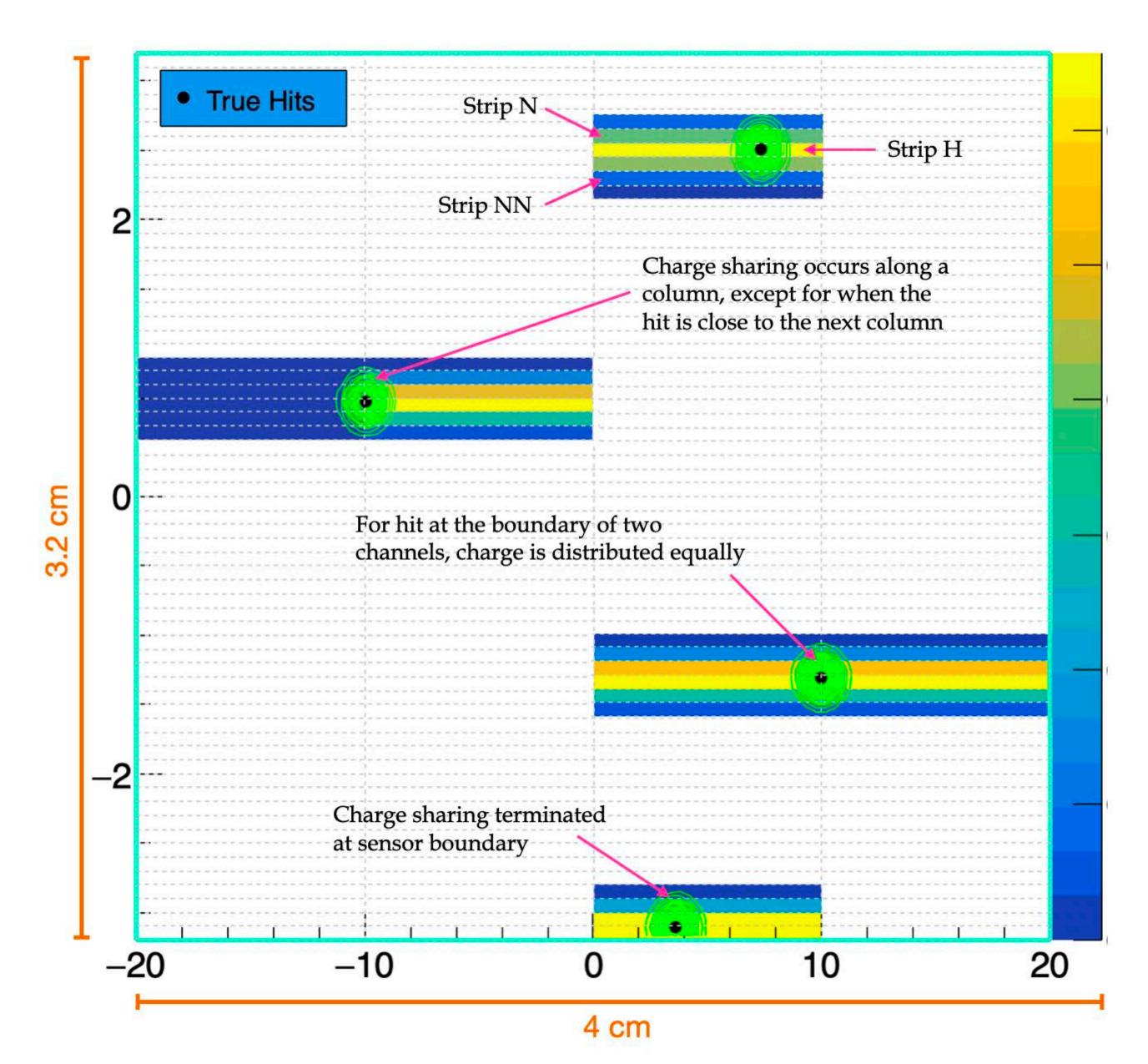






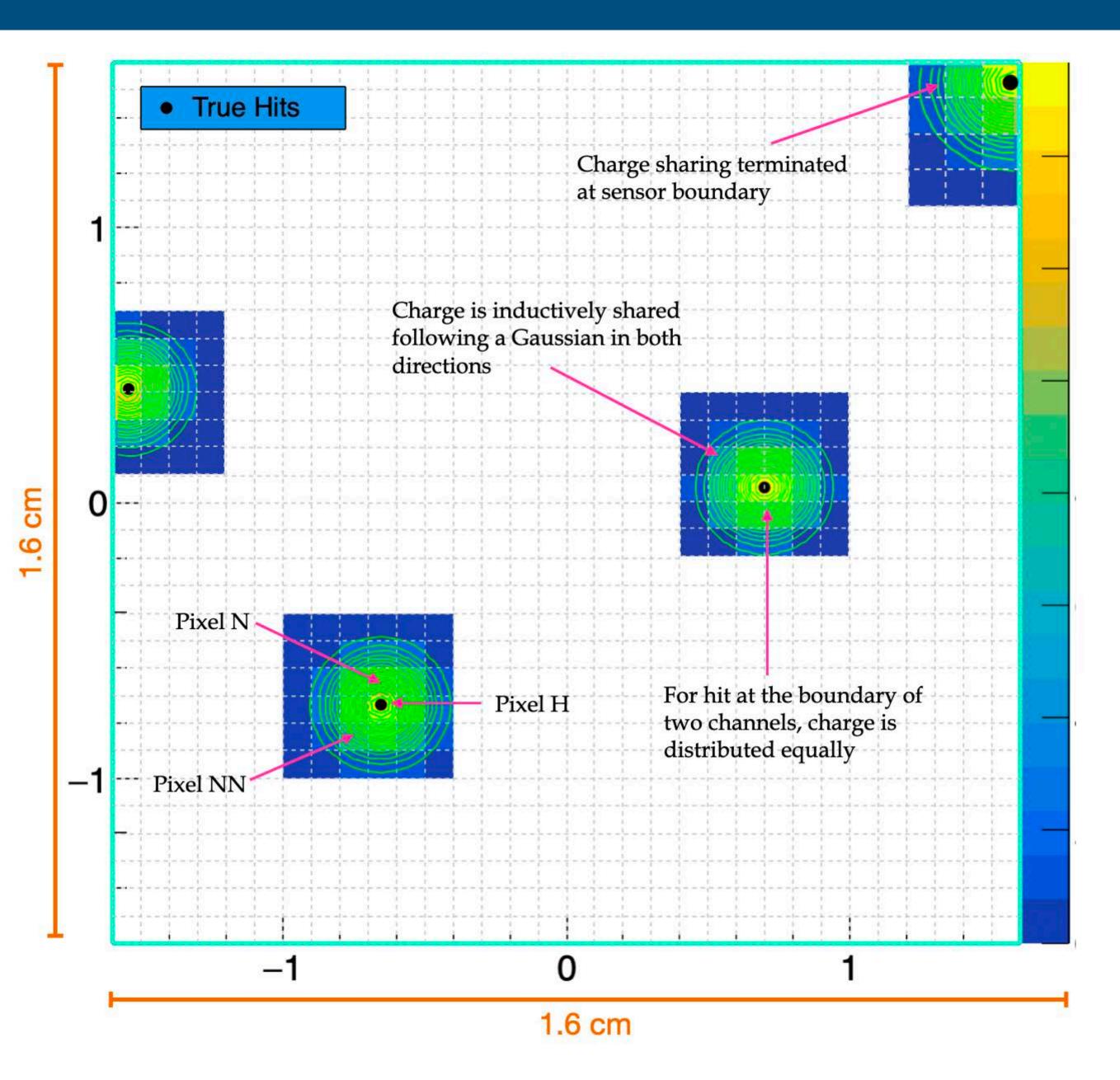
Energy Deposited → Charge → Peak of Signal → ADC
Time of Flight • Rise Time → Time of Arrival → TDC

Charge sharing: Strip geometry



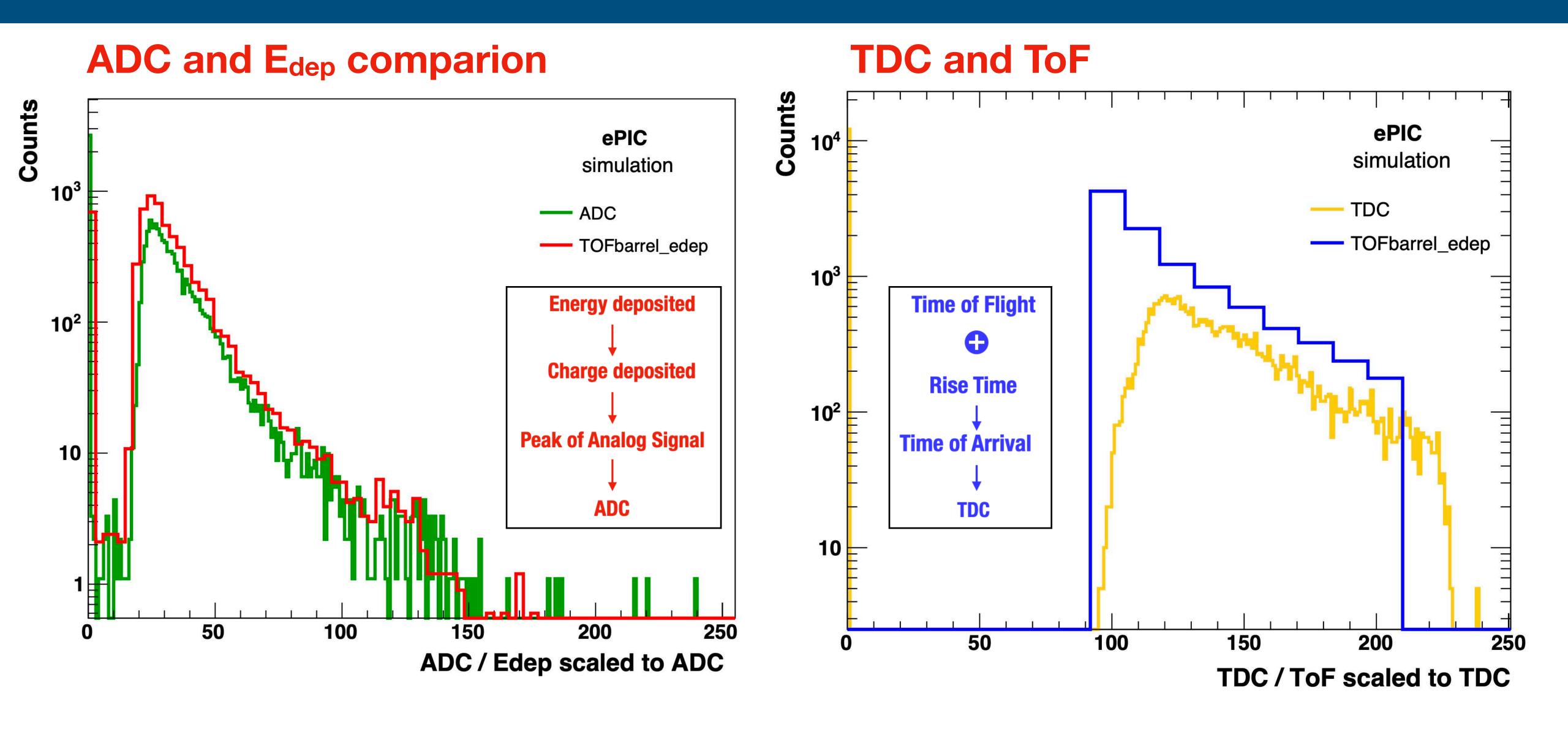
- A hit in Strip H has a Gaussian-like distribution of charge vs distance (Charge shared inductively in sensor).
- The Gaussian peaks at the center of Strip H, and has a standard deviation in X and Y, that can be tuned (Property of AC-LGAD).
- The maximum distance to which Pixel H can induce charge can also be optimized.

Charge sharing: Pixel geometry



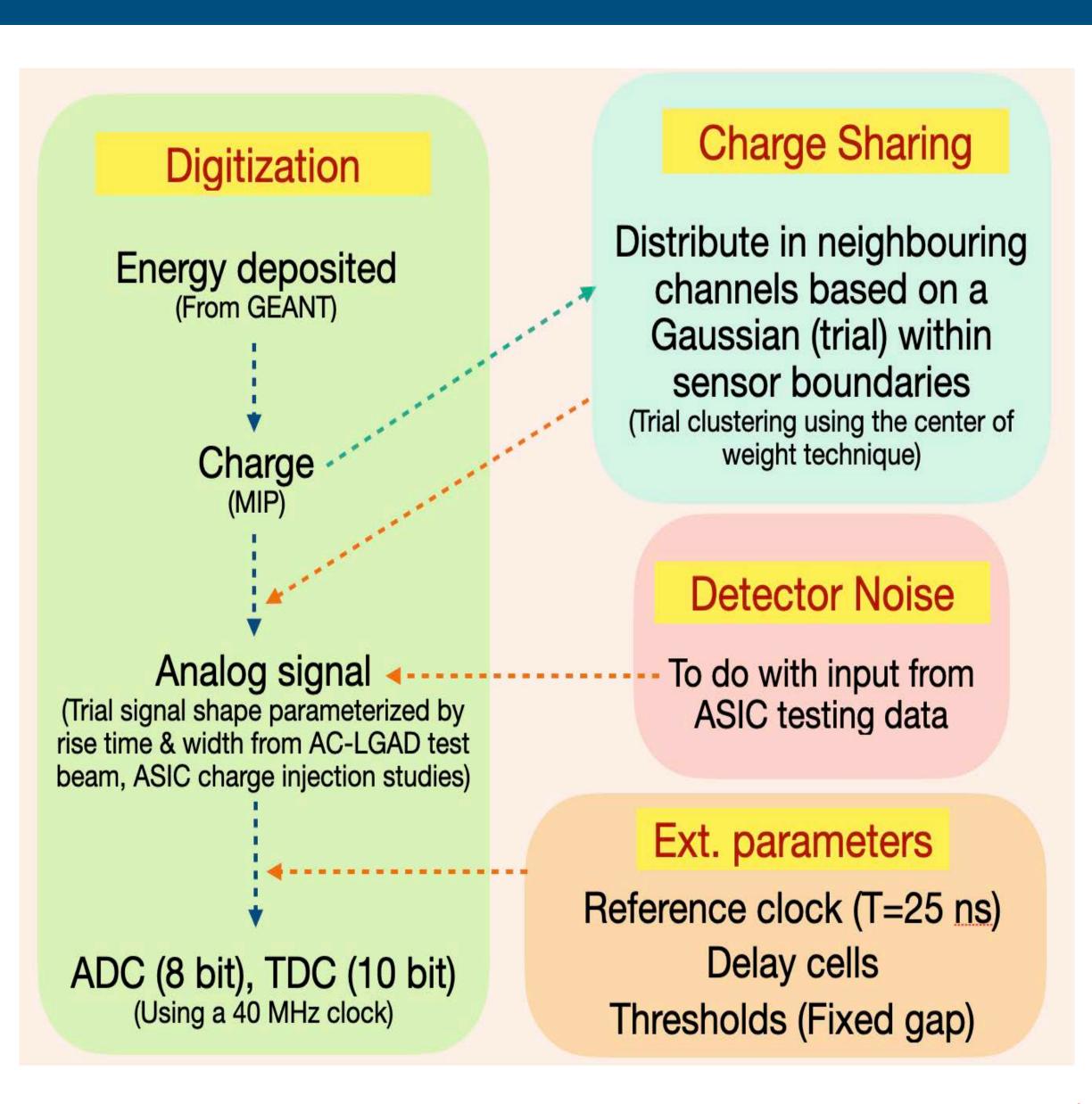
- A hit in Pixel H has a Gaussian-like distribution of charge vs distance (Charge shared inductively in sensor).
- The Gaussian peaks at the center of Pixel H, and has a standard deviation in X and Y, that can be tuned (Property of AC-LGAD).
- The maximum distance to which Pixel H can induce charge can also be optimized.

Energy/ADC & time/TDC comparison



ADC and TDC distribution are final output consistent with GEANT input

Summary



https://github.com/ssedd1123/EICrecon

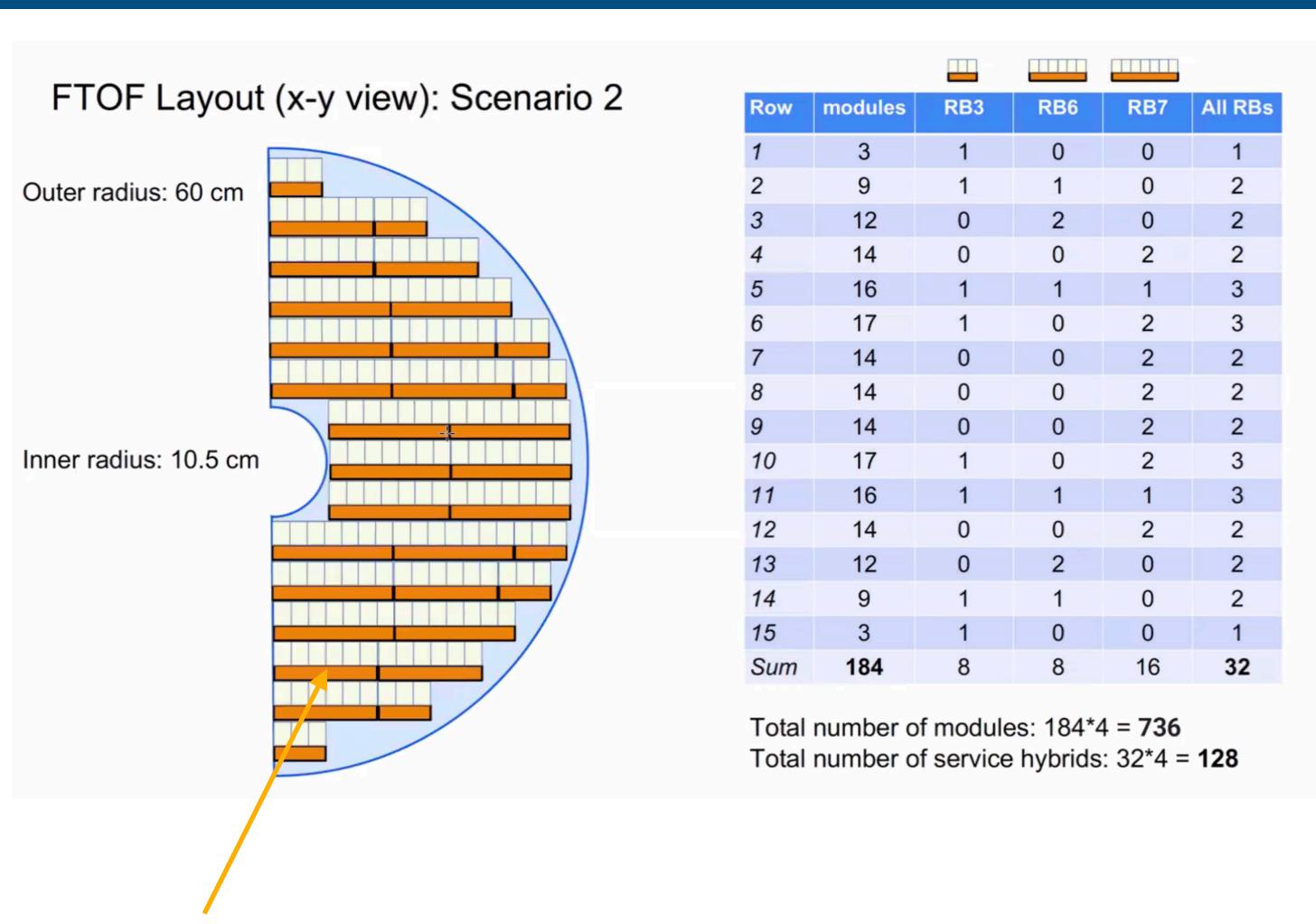
BTOFHitDigi.cc, BTOFHitDigi.h, BarrelTOFNeighborFinder.cc, BarrelTOFNeighborFinder.h in EICRecon/src/detectors/BTOF

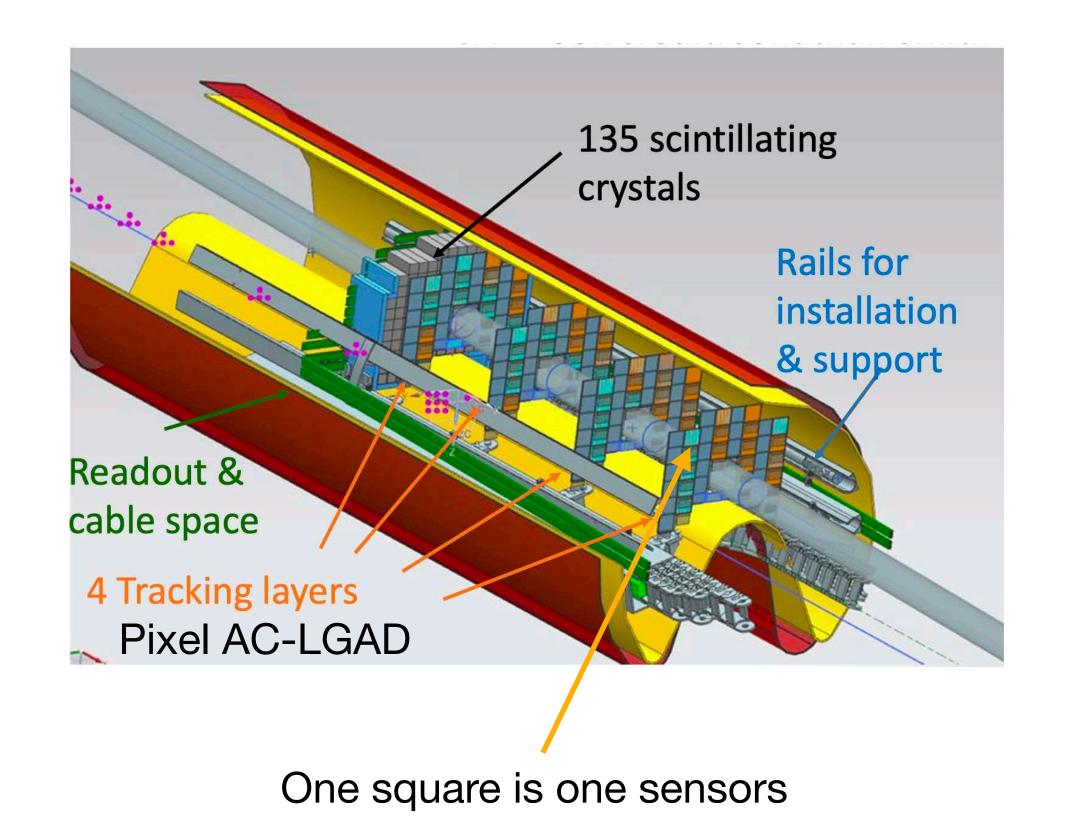
BTOFHitDigi_factory.h in EICrecon/src/factories/digi

BTOFHitDigiConfig.h in EICrecon/src/algorithms/digi

Parameter	Value
Rise time (Landau MPV)	0.45 ns
Shape Width (Landau) = FWHM/2	0.293951 ns
Amplitude (Landau)	-113.766 V
MIP charge/energy	190000 fC.GeV ⁻¹
Time period (Reference clock)	25 ns
Std. Dev. in X, Y (Gaussian for charge sharing)	0.5 mm, 0.5 mm
ToF Quantization time	0.02 ns

Other AC-LGAD systems



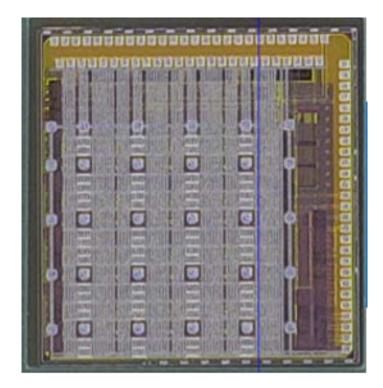


One square is four sensors
Pixel AC-LGAD

FTOF and B0 digitization in progress

Noise implementation

EICROCO ASIC



AC-LGAD ppRDO



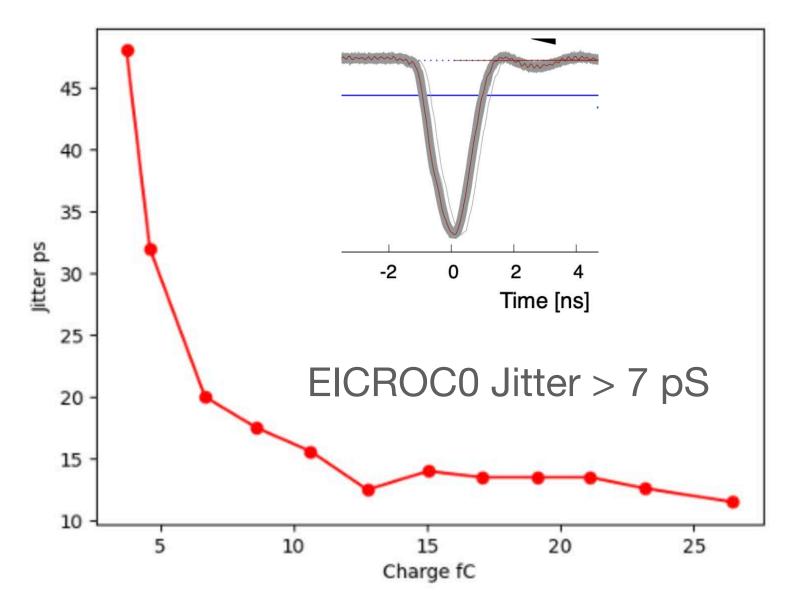
EICROCoTest@BNL
PCB Board = G1 (ASIC + Sensor)
Output = Pre-Amp
Mode = Charge Injection
Input charge 64 [DAC unit]

-5

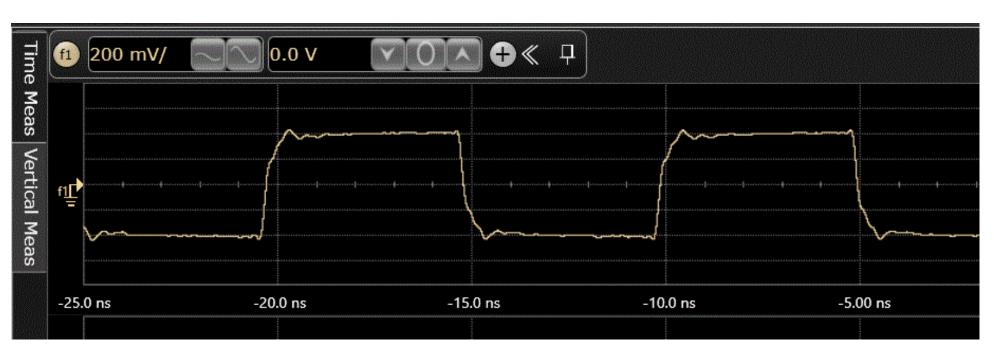
Noise @ input charge = 63 DAC = 0.5 fC

-20

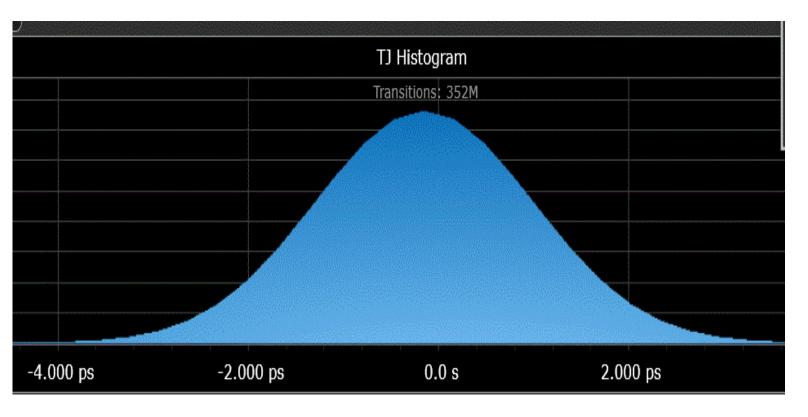
Time [ns]



PLL output 98.5 MHz clock distribution

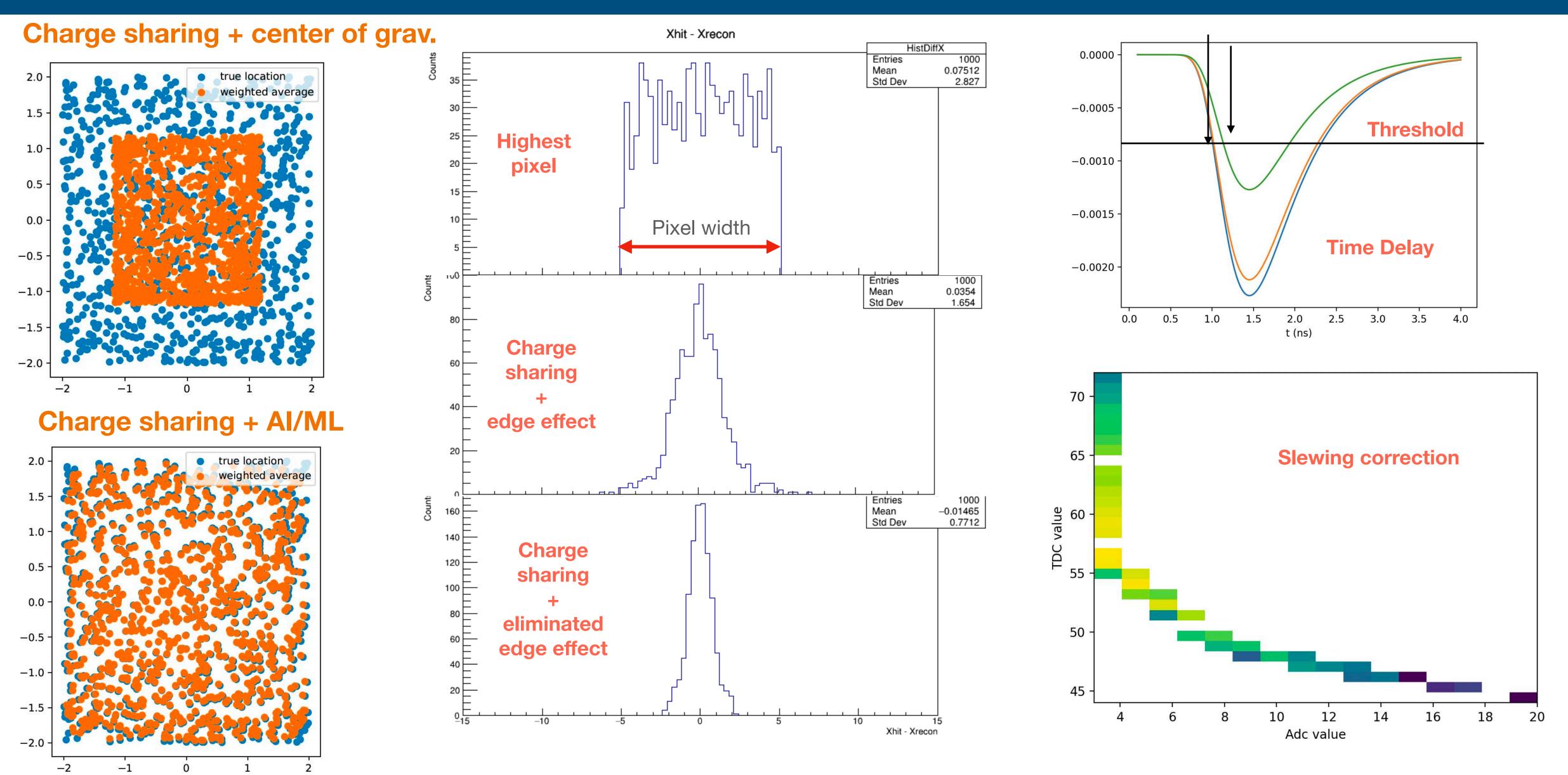


Clock jitter ~1.12 pS



Noise implementation in progress...

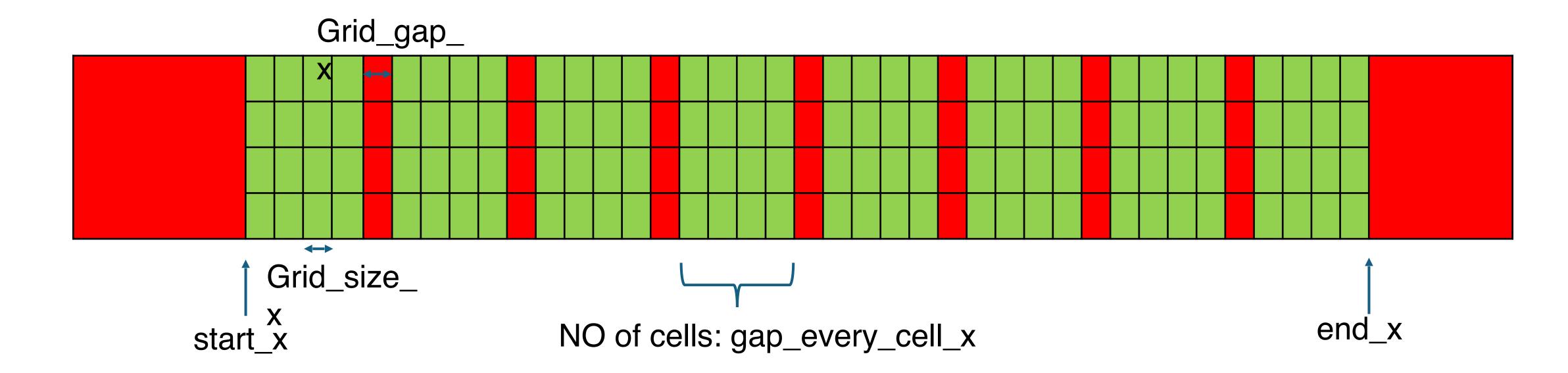
What's after digitization: reconstruction



Reconstruction with digitization in progress...

Thank you

Put pixels in the right place:



Work done: Wrote an "UnevenCartesianGridXY" class to put dead space and pixels in the right place.

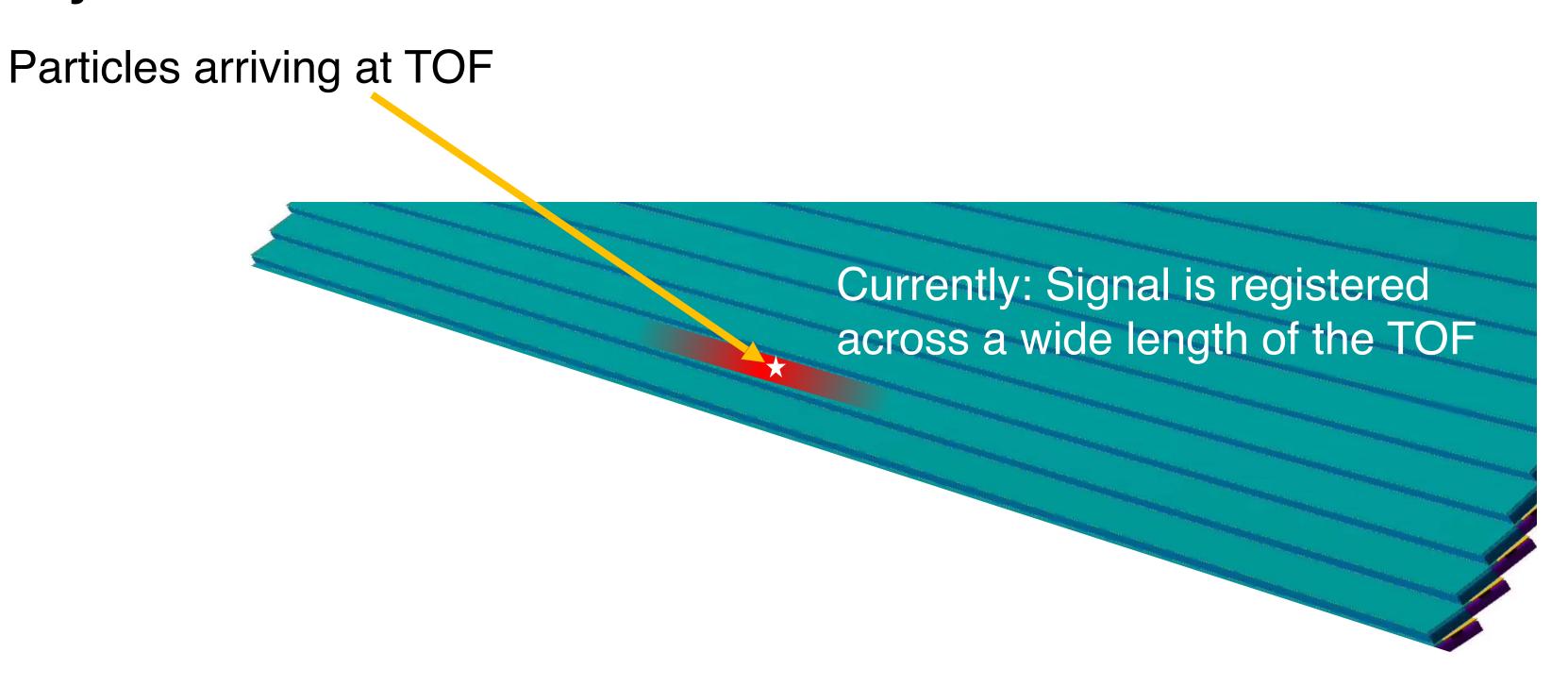
New "tof_barrel.xml":

```
</readouts>
</readout name="TOFBarrelHits">
</readout name="TOFBarrelHits">
</segmentation type="UnevenCartesianGridXY" grid_size_x="0.1*mm" grid_size_y="1*cm" grid_gap_x="0.01*mm" grid_gap_y="0.5*cm" start_x="-1.8*cm" start_y="-128*cm" end_x="1.8*cm" end_y="128*cm" gap_every_cell_x="64" gap_every_cell_y="4"/>
</id>
</readout>
</readout>
</readout>
</readout>
</readout>
</readout>
</readout>
```

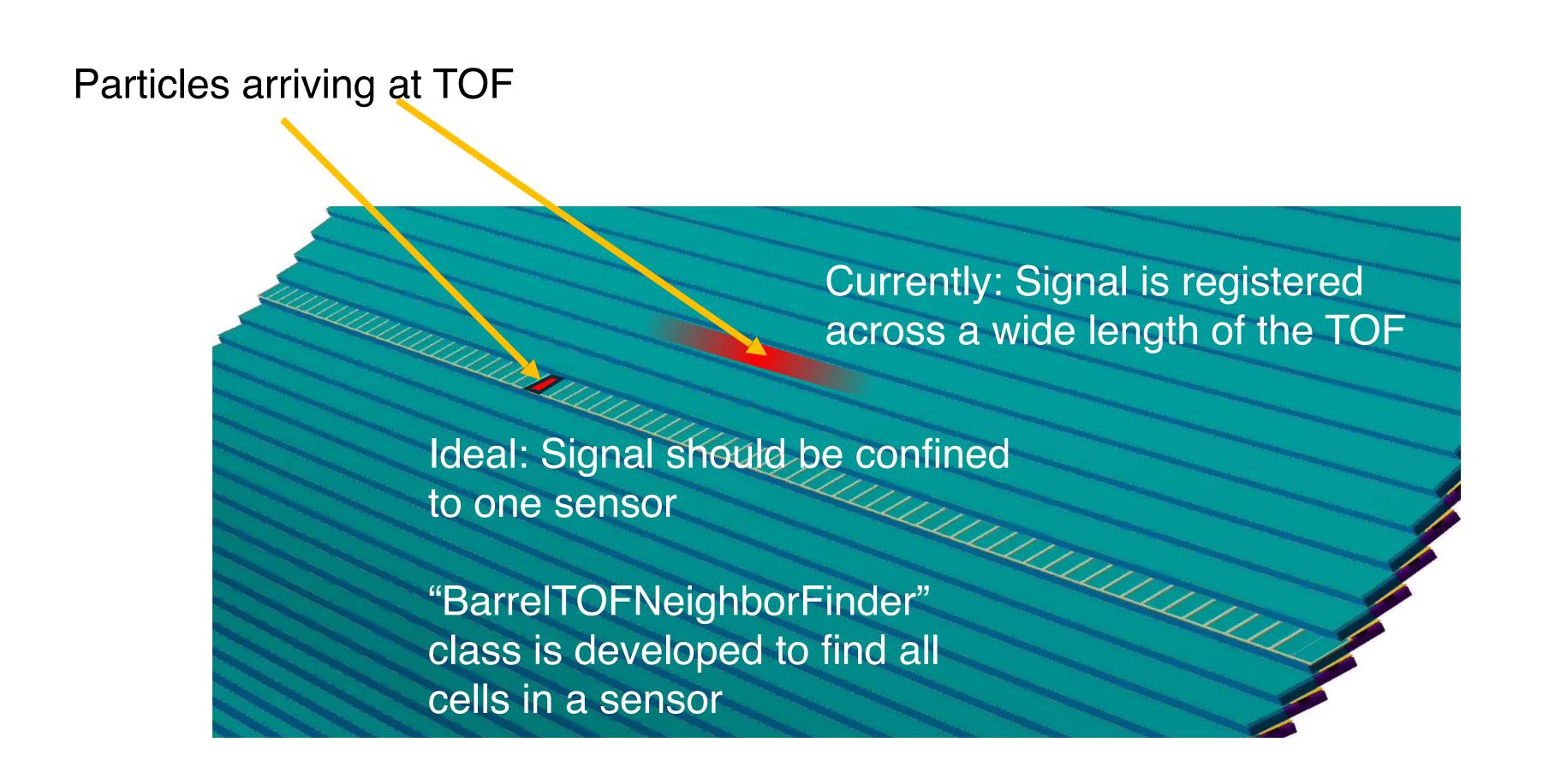
Don't have the accurate numbers, but they are free parameters. Can be adjusted easily.

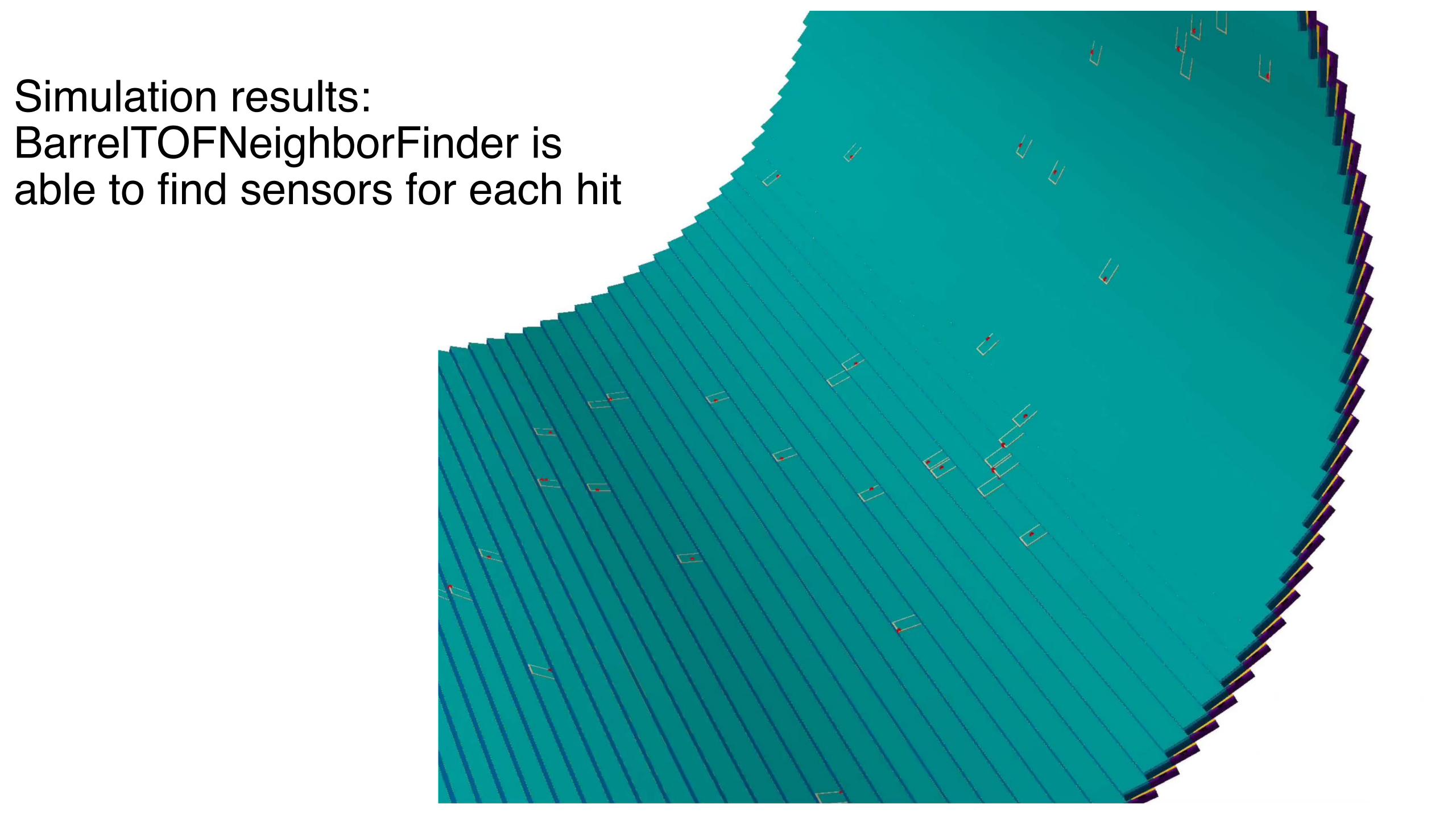
Calculate energy for each pixel.

Currently in GEANT, each stave of TOF is one entire unit.



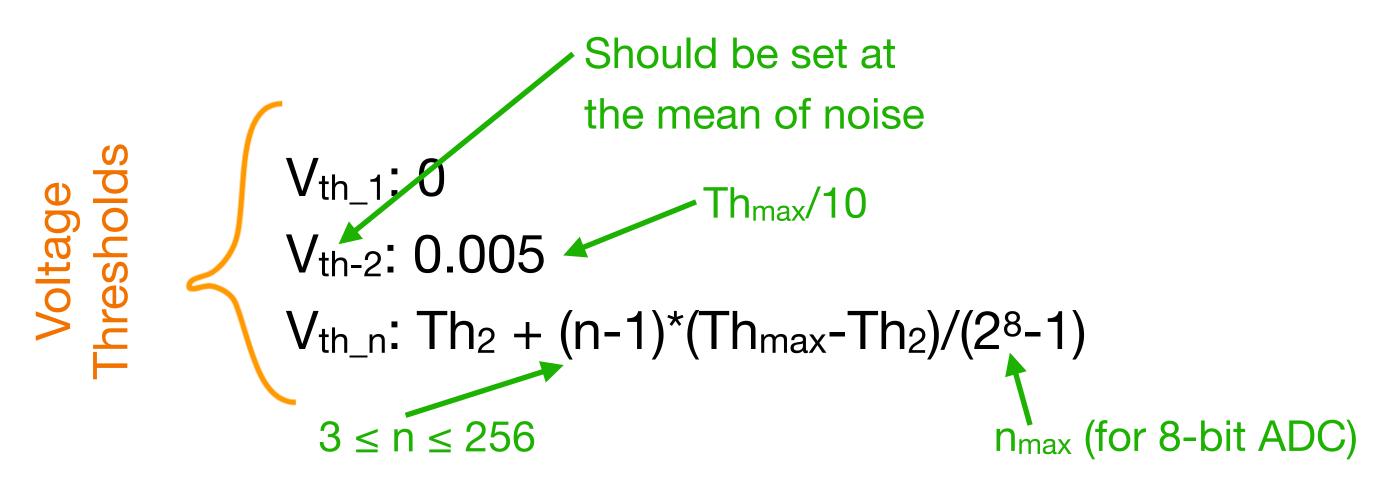
Solution: Write a class that interface with UnevenCartesianGridXY to get cell boundaries



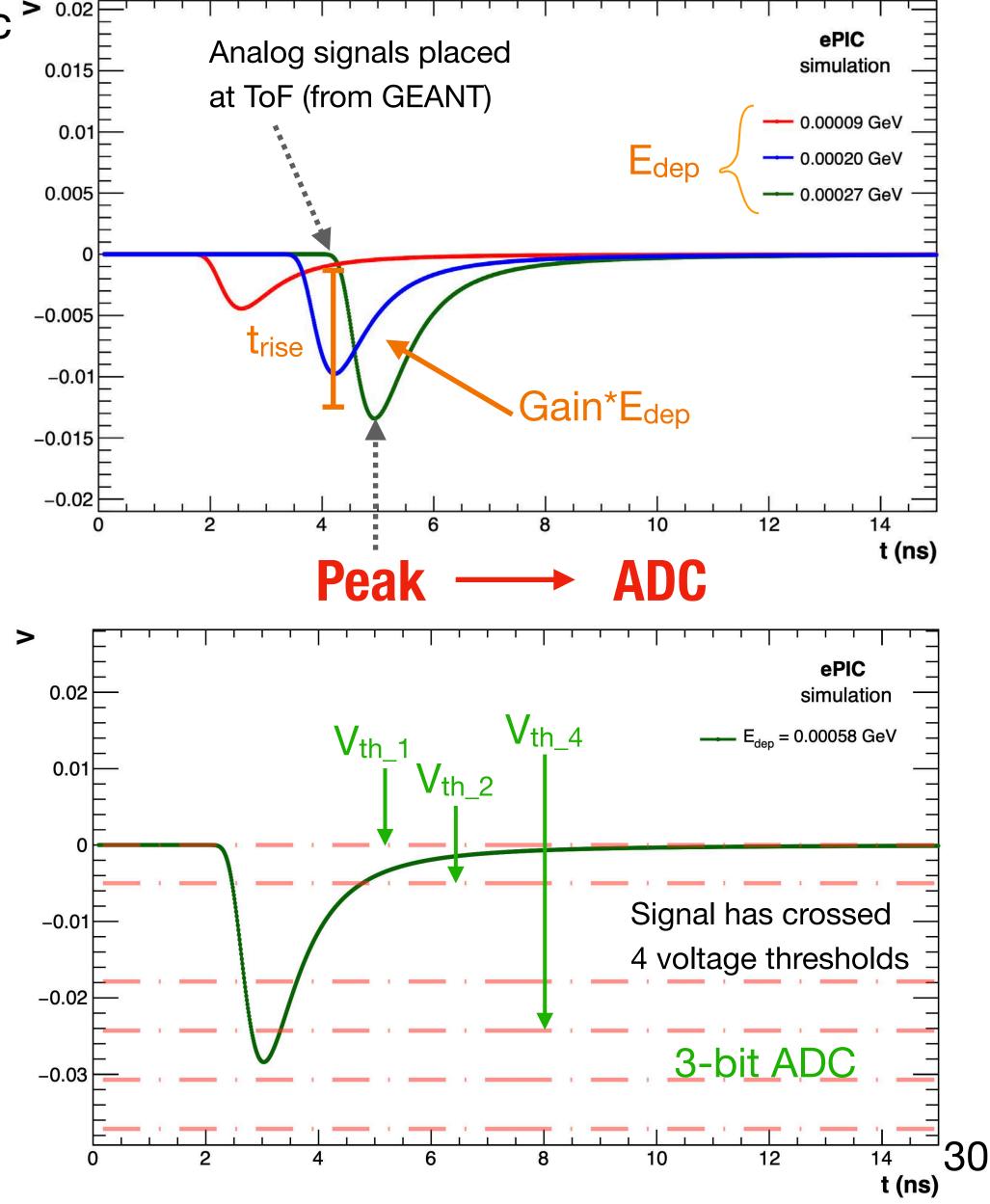


Analog Signal, Voltage Threshold & ADC

- Energy deposited (**Edep** from GEANT) is multiplied by the realistic AC-LGAD **gain** (~80) to give the **area** of the analog signal.
- The t_{rise}(~450 ps) and the standard deviation(~294 ps) of a real AC-LGAD signal (obtained from ASIC charge injection studies), and the area (calculated from E_{dep}) are used to parameterize a Landau-like analog signal.



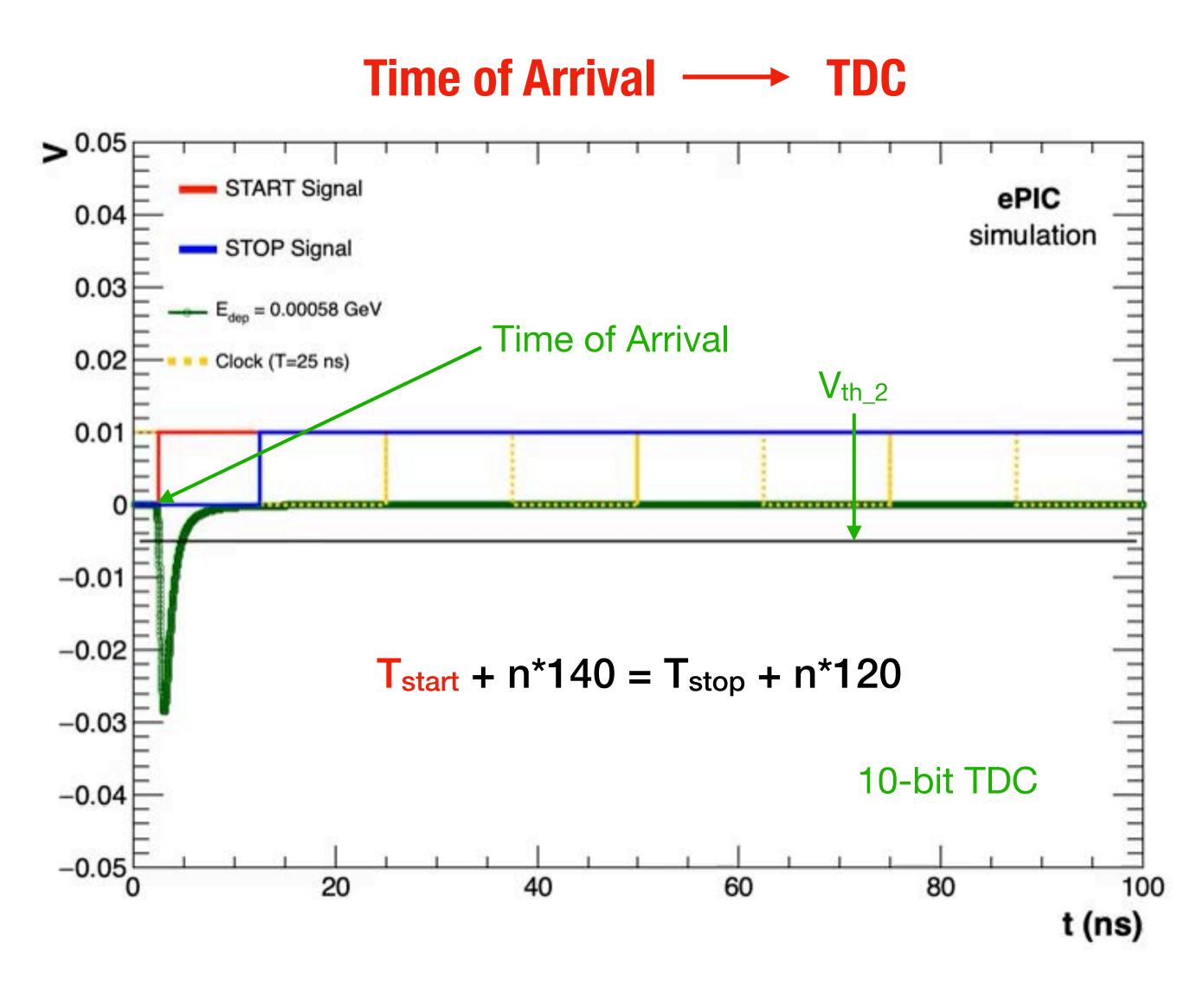
- The number of voltage thresholds crossed by the analog signal (1 ≤ n ≤ 256) is converted to a 8-bit ADC code.
- Will be updated according to latest EICROC results.



Analog Signal & TDC

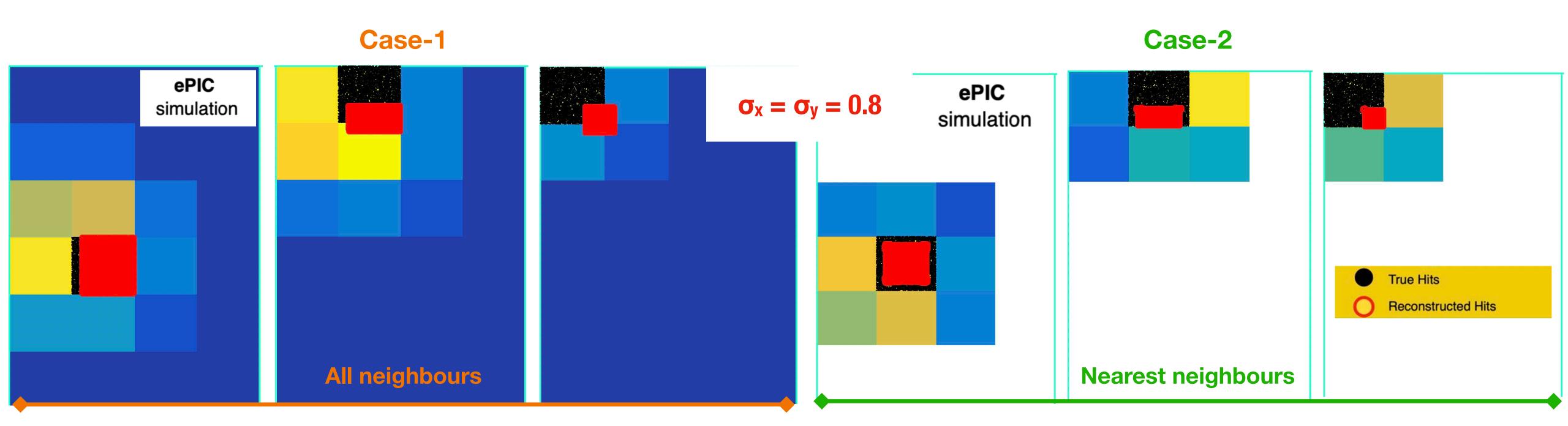
- Almost all hits occur in the 1st half-period of the clock(f = 40MHz). When the analog signal crosses V_{th_2} (Time of Arrival), the START signal flips from 0 to 1. When the clock cycle flips from the 1st to the 2nd half-period, the STOP signal flips from 0 to 1.
- Consecutive delay cells propagate the START signal (140 ps delay) and the STOP signal (120 ps delay) in parallel until the START signal crosses the STOP signal (Mathematically, |START-STOP| < 20 ps).





• The number of times the signals move (1 \leq n \leq 1024) is converted to a 10-bit TDC code.

Charge Sharing (Geometric effects)



- The position of the hit pixel and the number of charge-sharing neighbours has an effect on the reconstruction accuracy of the hits.
- Reconstruction accuracy decreases as the pixel hit position changes from central to corner.
- Central pixel has 8 nearest neighbours, edge pixel has 5 nearest neighbours and corner pixel has 3 nearest neighbours.
- Reconstruction accuracy for Case-2 is greater than that for Case-1.

Charge Sharing (More geometric effects)

