Backward ECAL: plans for beam tests

- > Timeframe: summer/fall 2024
- ➤ Goal: test read-out electronics (both discreet and ASIC), linearity, energy resolution
- > Anticipated system: 5x5 prototype equipped with 4 SiPM per crystal (10um pitch)
- ➤ Possible facilities: JLab Hall D, DESY (GeV-scale electron facilities)