# Preparation for calibration measurements

Genki Nukazuka (RIKEN)

## What is needed for the ladder tests?

#### Hardware

- Cable connections
  - conversion cables (by INTT crews)
  - bias (D. Cacace et al.)
  - FPHX power (D. Cacace et al.)
  - ► ROC power (D. Cacace et al.)
  - data fibers (D. Cacace et al.)
  - slow control fiber (D. Cacace et al.)
- ROC cooling connections, cleaning, and tests (Rob)
- Power for our power racks

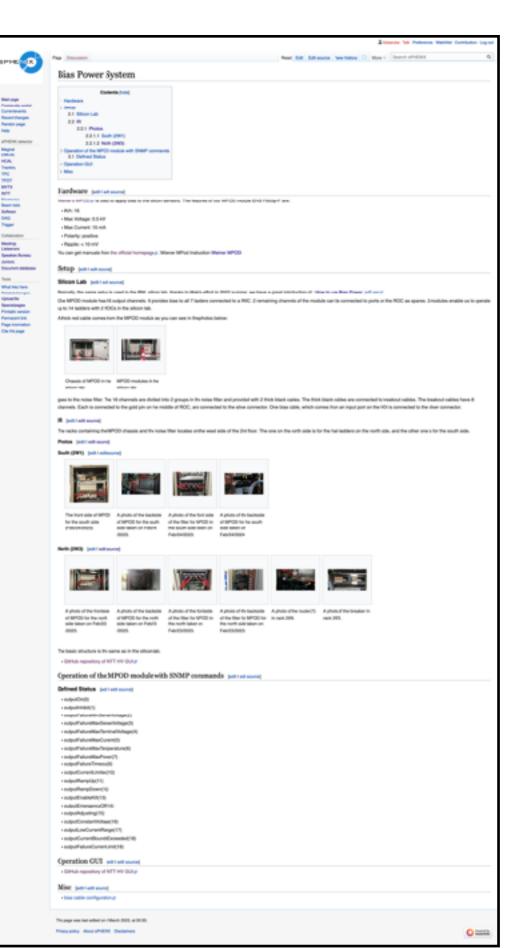
#### Software

- LV GUI (Maya): Ready
- HV GUI (Joseph): we need individual control and current/voltage monitor. Where is that version?
- Calibration feature in the FELIX firmware (Raul)
- Analysis software: ongoing
- Data conservation/management (?):

# What is needed for the ladder tests?: HV GUI



- The shifter's GUI doesn't have many features:
  - Individual channel operation
  - Current/Voltage monitor
- Where is our expert's GUI?
- Instruction has to be documented in the sPHENIX wiki, by the way.





### FELIX

- Raul has been implementing FELIX firmware for calibration measurements.
- Now, he is in the phase of test measurements: measurements with a temporary setup were started.
- A short meeting was held among Raul, Jaein, Akitomo, and me to know the status.
- The idea of the calibration mode is quite simple: running INTT in the streaming readout mode

(n\_collisions = 127, open\_time = 127) with a specific GTM scheduler, and taking data with RCDAQ (not big-partition but small-partition).

- What needs to be implemented/modified:
  - ~/INTT/run.py in the sPHENIX common directory at 1008:
     Minor modification to change the flag dam::sc\_target, which controls whether ROC(s) accept slow control or not, to be enabled during the measurement.
  - ~/operations/INTT/modebits.sh in the sPHENIX common directory at 1008: The scheduler to be loaded needs to be changed.
  - ~/INTT/sphenix\_inttpy/run\_scripts/fphx\_parameters\_calib.txt: Let's check →
     I made another file for the original calibration parameters (fphx\_parameters\_calib\_no\_bex.txt) just in case.
  - Channel mask: Noisy chips are masked ch by ch, so it takes time. It needs to be updated (low priority).

DAC parameters were optimized by Itaru for the setup with a bus-extender.

#DAC0

DAC0

#DAC1

DAC<sub>2</sub>

DAC3

DAC4

DAC5

DAC6

N2sel

P3sel

P2sel

Gsel

BWsel

P1sel

LVDS

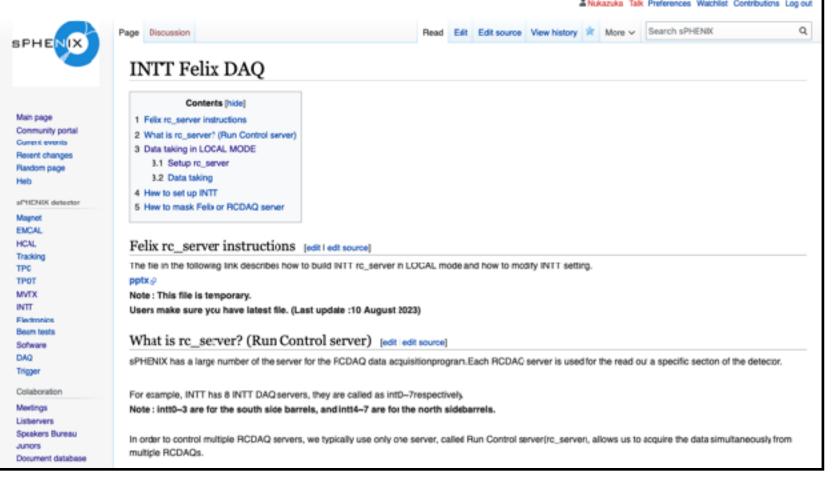
Injsel

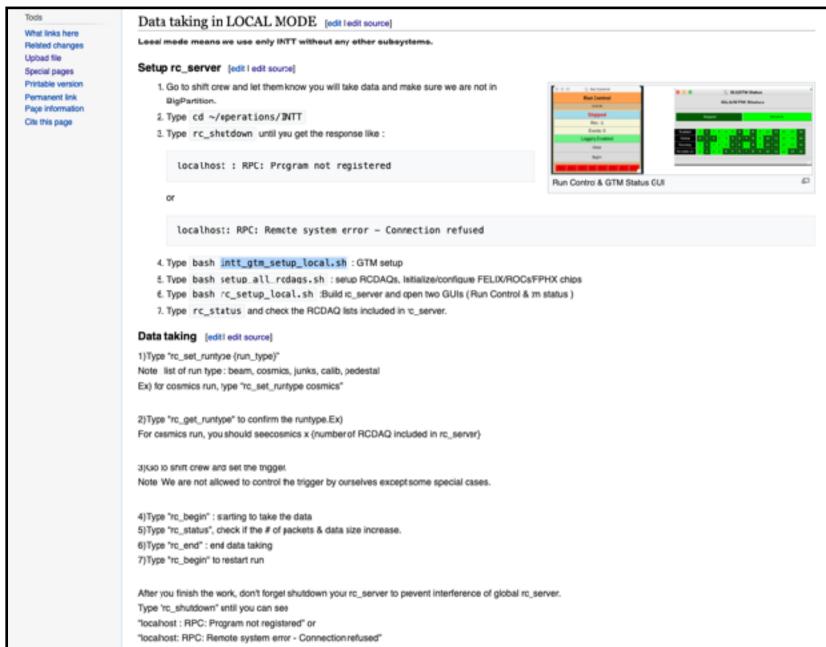
FB1sel 4

31

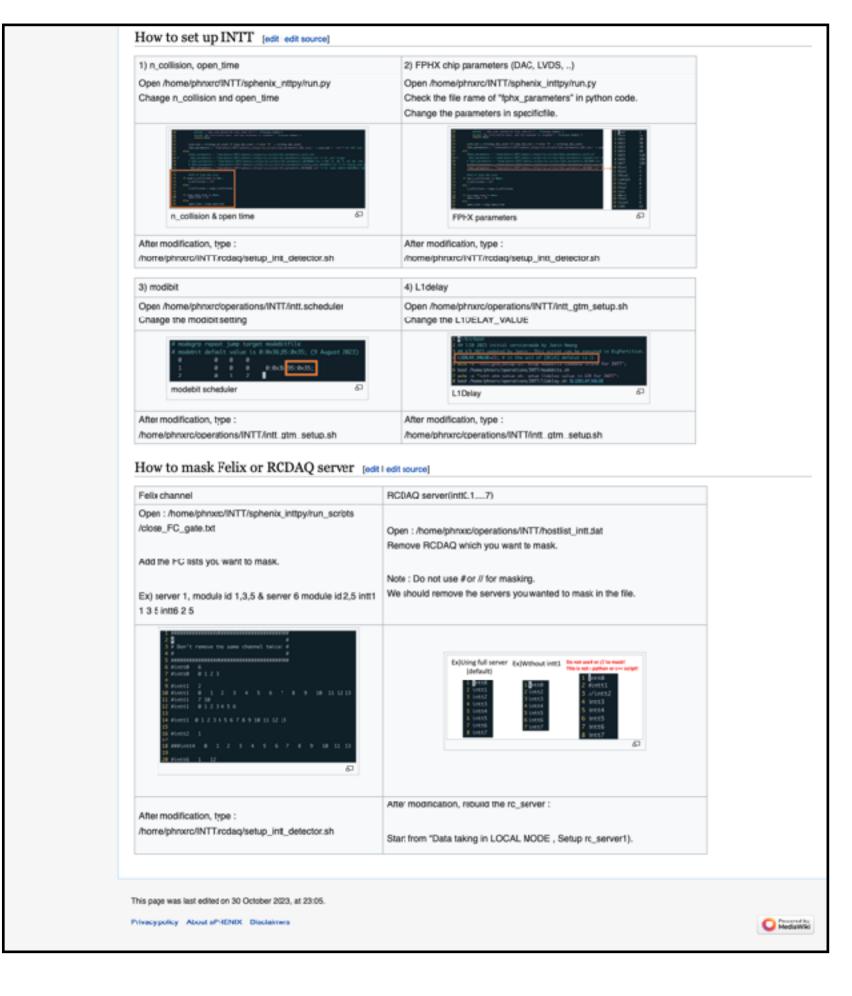
# If you don't remember how to run INTT in local mode...

Jaein prepared a nice manual for YOU!
 https://wiki.sphenix.bnl.gov/index.php/INTT\_Felix\_DAQ



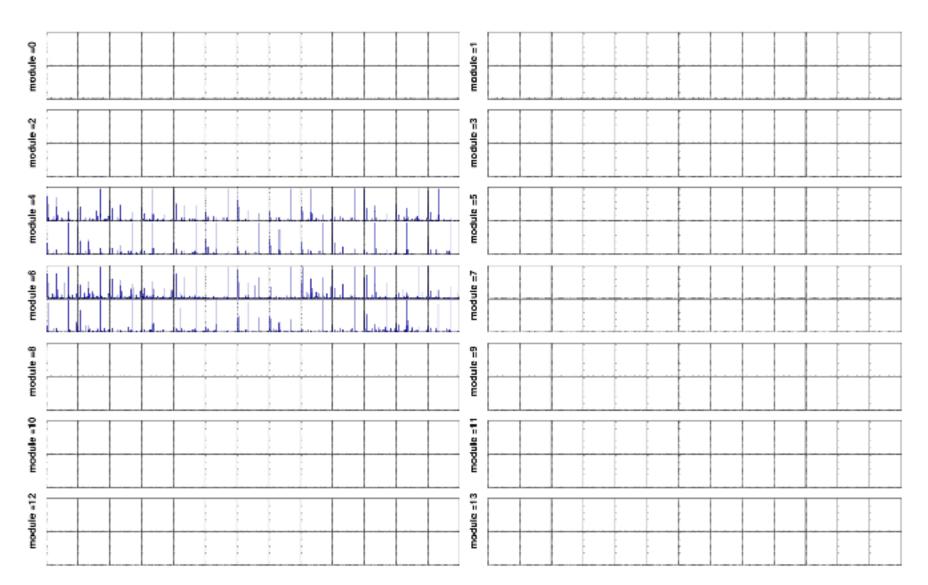


We can follow those step for calibration measurements!



## Software status

- **Decoder**: I modified Takashi's event-base decoder to output the hit-base decoder so that we get all hits without any loss. I don't use Fun4All decoder. What we want is DST generated in a proper manner but just quick results.
- FelixQuickViewer: It's working well. I'm optimizing it.
- Ladder map:
  - I cleaned the directories. The latest version in 2023 was copied to the 2024 directory. Symbolic links to them were made under map\_ladder. Those symbolics are used by other applications. There are some map\_ladder directories which are NOT synchronized...
  - ~/INTT/map\_ladder at 1008
  - /sphenix/tg/tg01/commissioning/INTT/map\_ladder in SDCC
  - /home/inttdev/INTT/map\_ladder in inttdev@inttdaq Migration to PostgreSQL was done, but users (apps) need to be modified.



intt\_intt1-00014056-0004 (junk data) FelixQuickViewer itself is working well.

```
[nukazuka@sphnx03 08:01:21 INTT] $ tre -d map_ladder/
map_ladder/
|---- README.md
|---- intt0_map.txt -> 2024/intt0_map.txt
|---- intt1_map.txt -> 2024/intt1_map.txt
|---- intt2_map.txt -> 2024/intt3_map.txt
|---- intt3_map.txt -> 2024/intt3_map.txt
|---- intt4_map.txt -> 2024/intt5_map.txt
|---- intt5_map.txt -> 2024/intt5_map.txt
|---- intt6_map.txt -> 2024/intt6_map.txt
|---- intt7_map.txt -> 2024/intt7_map.txt
|---- 2023
|---- silicon_lab
|---- IR
|---- 2024
|---- 2025
```

Directory structure under map\_ladder.