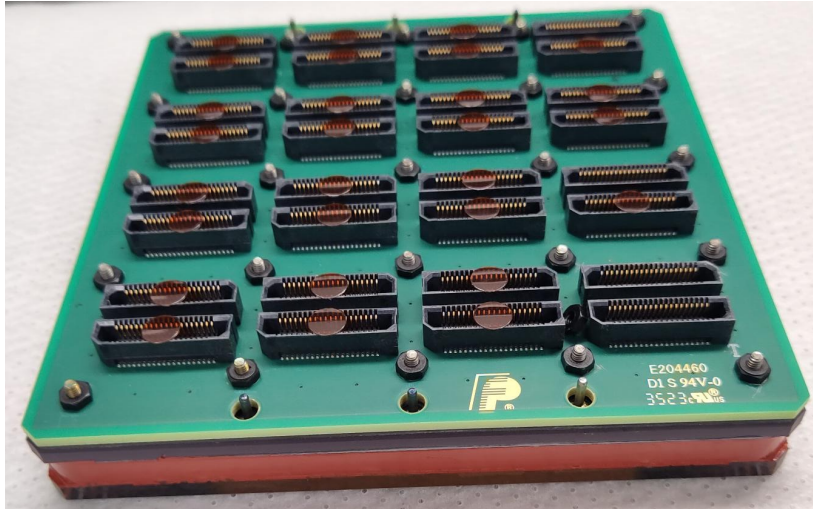
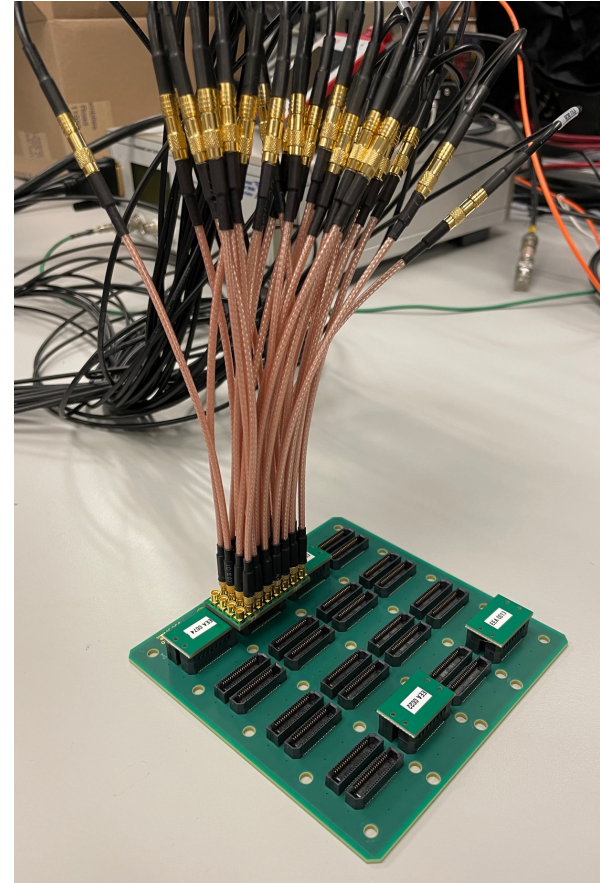


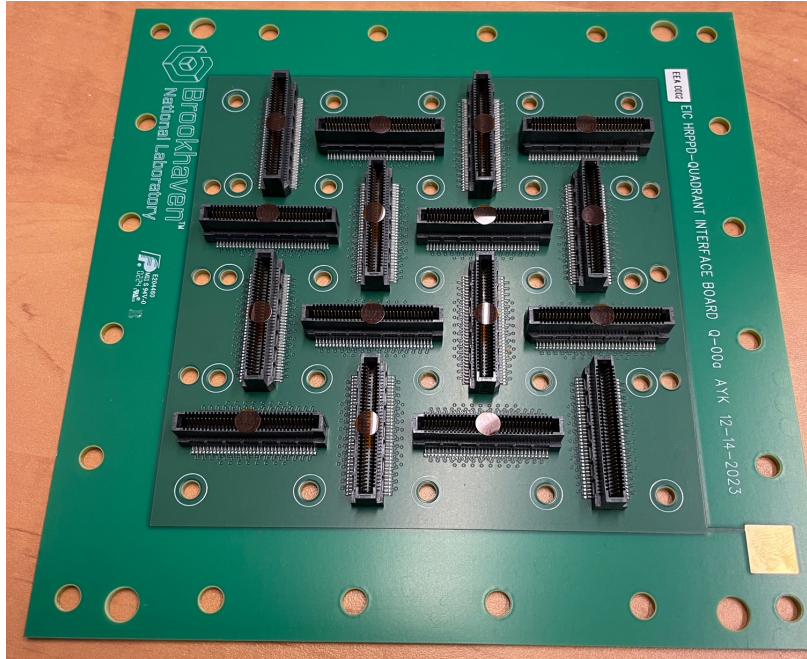
# HRPPDs #15,16 (EIC HRPPDs #1,2) status update



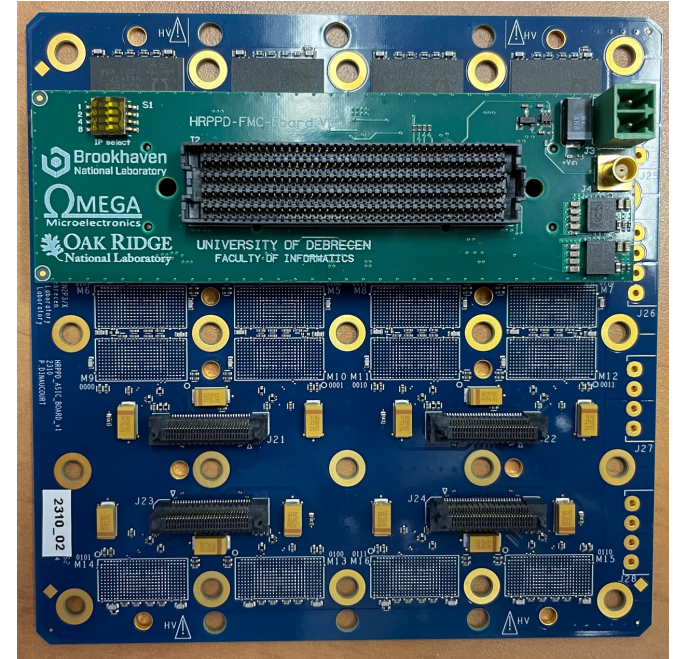
- HRPPD #16 was shipped to JLab yesterday
  - Performance very similar to #15
  - (All mechanical screws are brazed rather than glued)
  - A differential QE( $\lambda$ ) curve was taken in five spots
- All missing PCB interfaces received at BNL
  - Will be set out to Incom & JLab today



# HRPPD backplane(s)



Passive interface for the test stand at BNL



HGCROC3 backplane + interface to an FPGA kit

- All parts received at BNL
- ASIC board + FMC (passive) board received at Debrecen a week ago
- Next step is the V0 validation (without FPGA boards) & a Linux driver development