

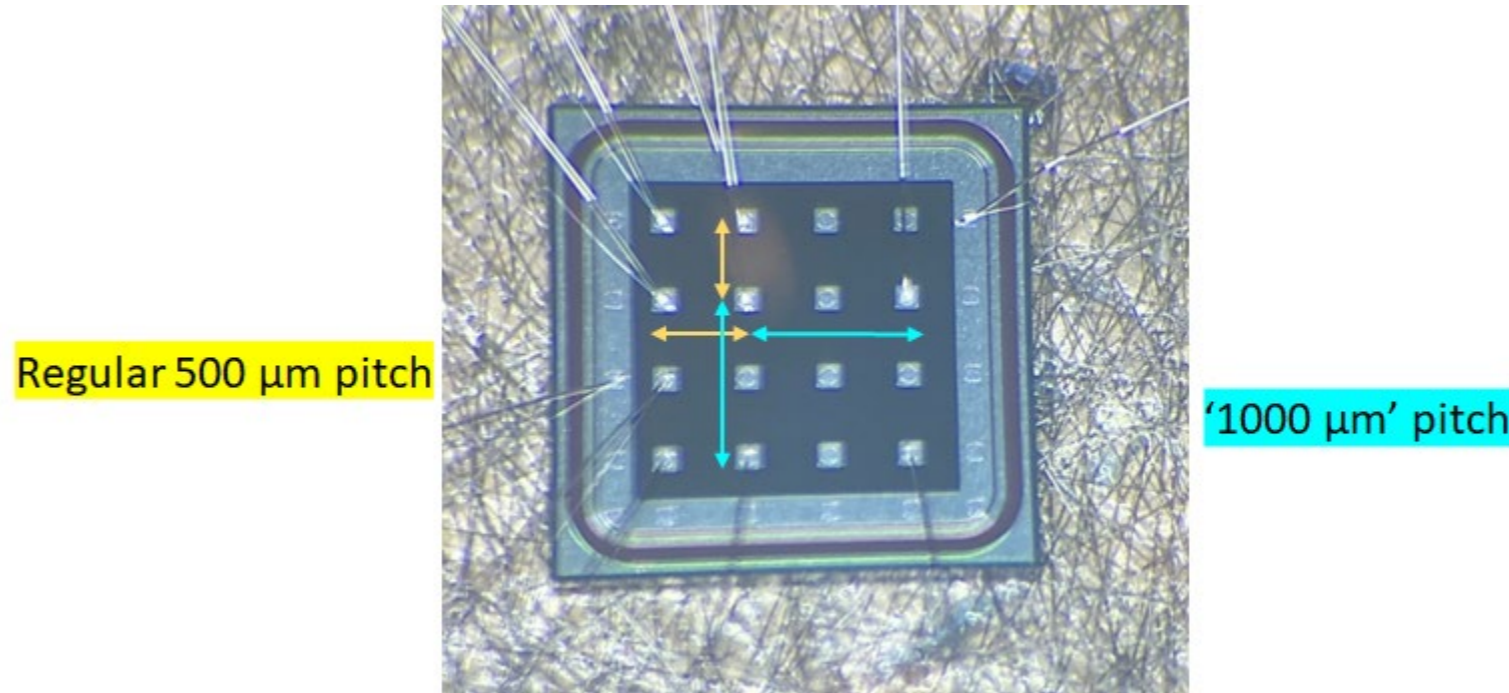


# Evaluation of different AC-LGAD geometries

**Dr. Simone M. Mazza (UCSC) for the SCIPP team**

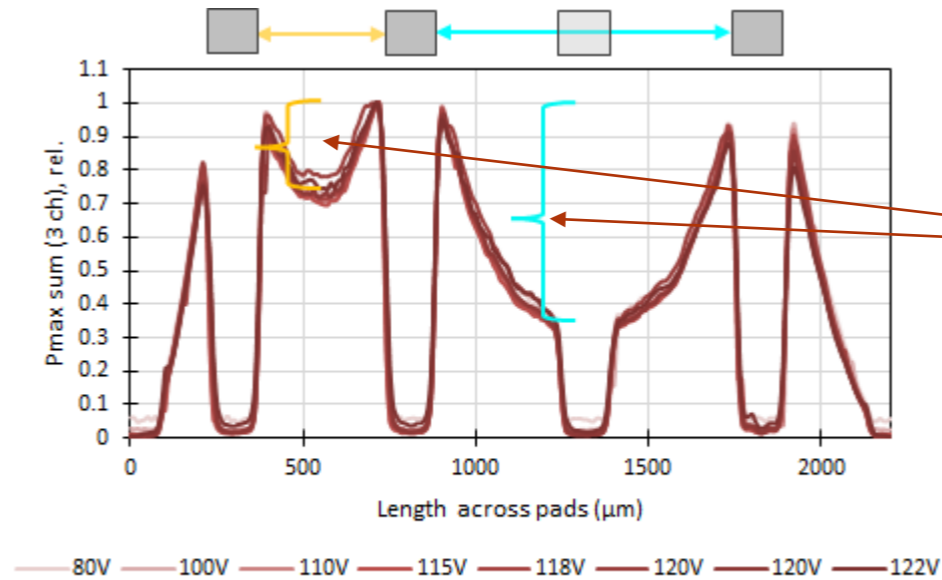
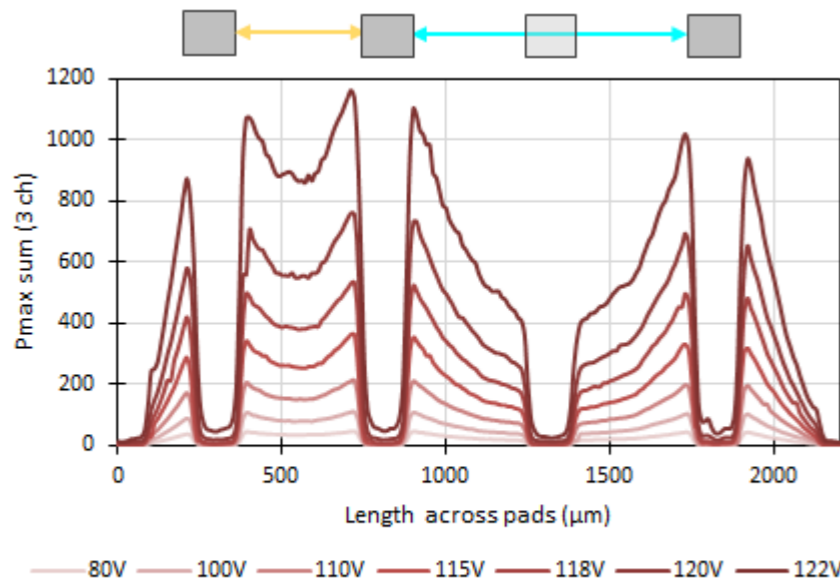
# Standard square pad AC-LGAD

- Presented at TOF-PID meeting a few weeks ago
- Approach: leave some pads in a 4x4 pad array with 500  $\mu\text{m}$  pitch unbonded and floating to mimic 1000  $\mu\text{m}$  pitch, monitor pulse maximum as function of distance
- Using smallest currently available pad size in the HPK production: 150x150  $\mu\text{m}$ . Here, a C600 sensor (more signal sharing) with bulk thickness 20  $\mu\text{m}$  (faster rise time)

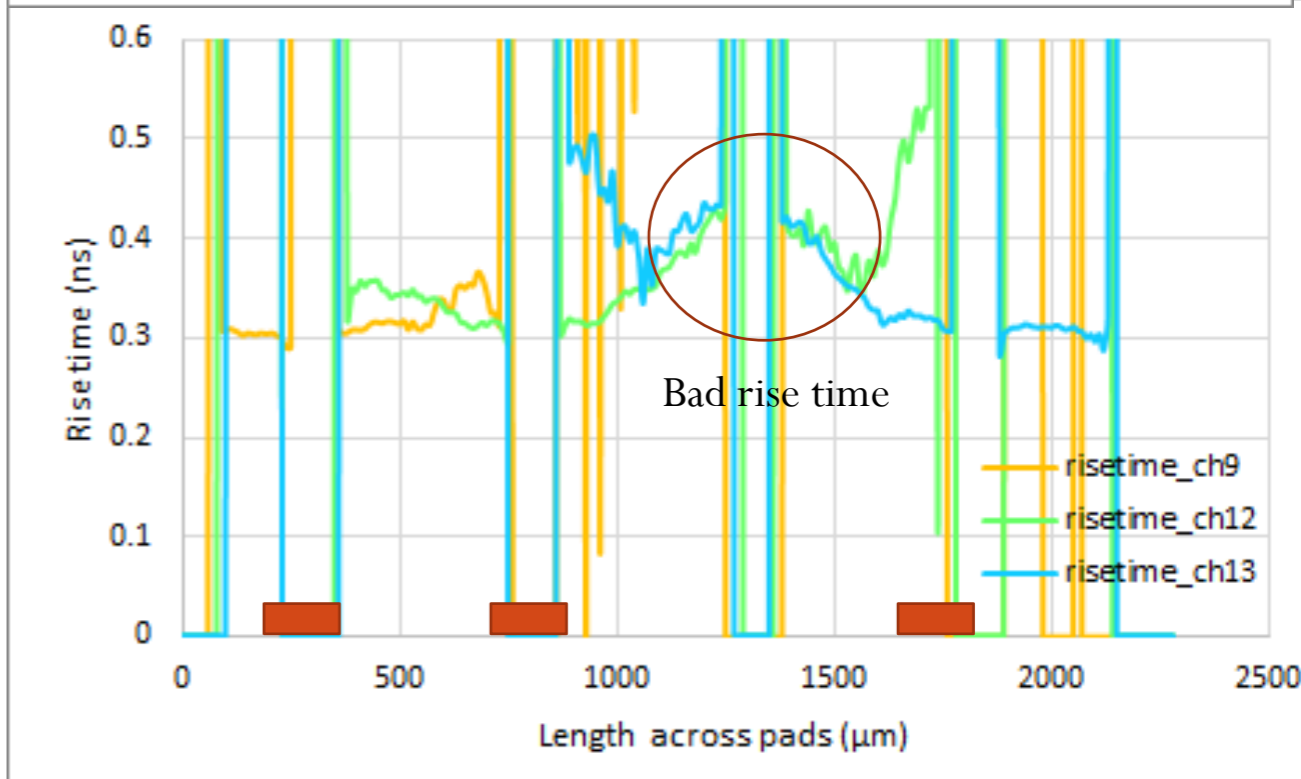
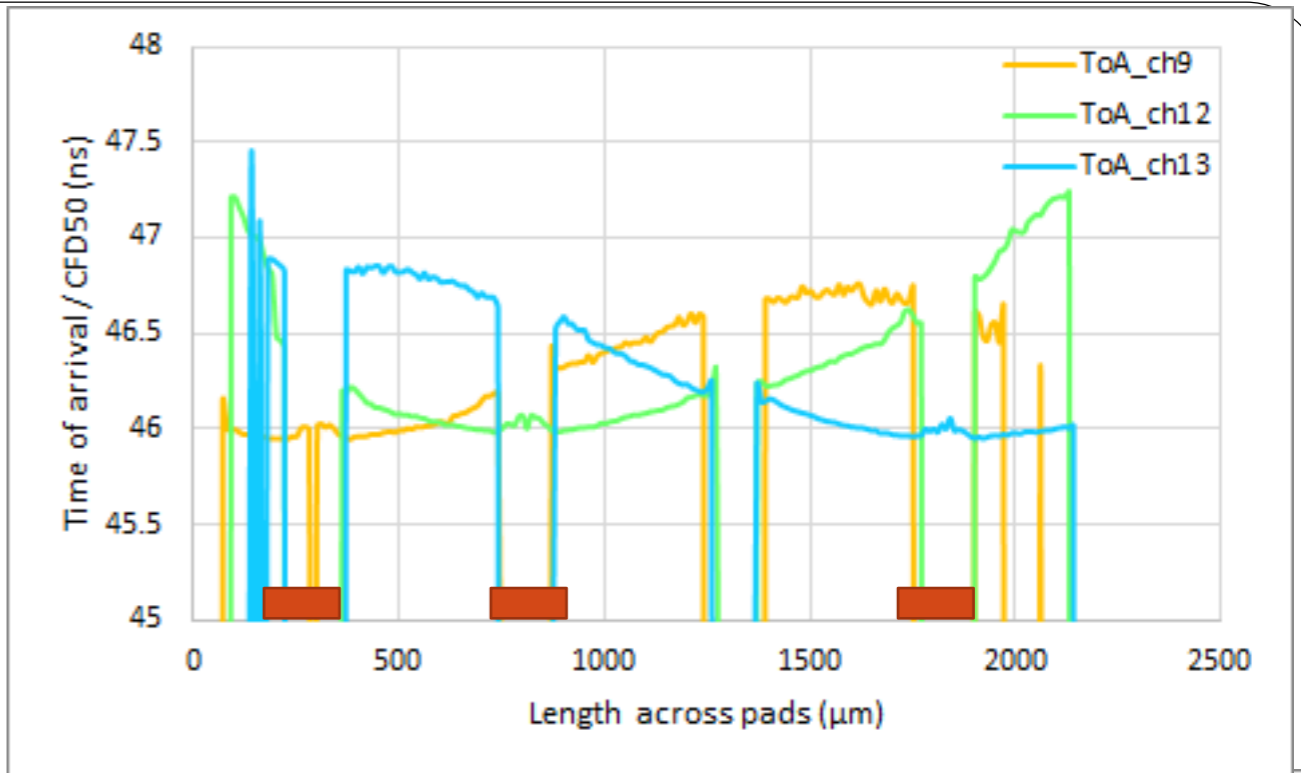
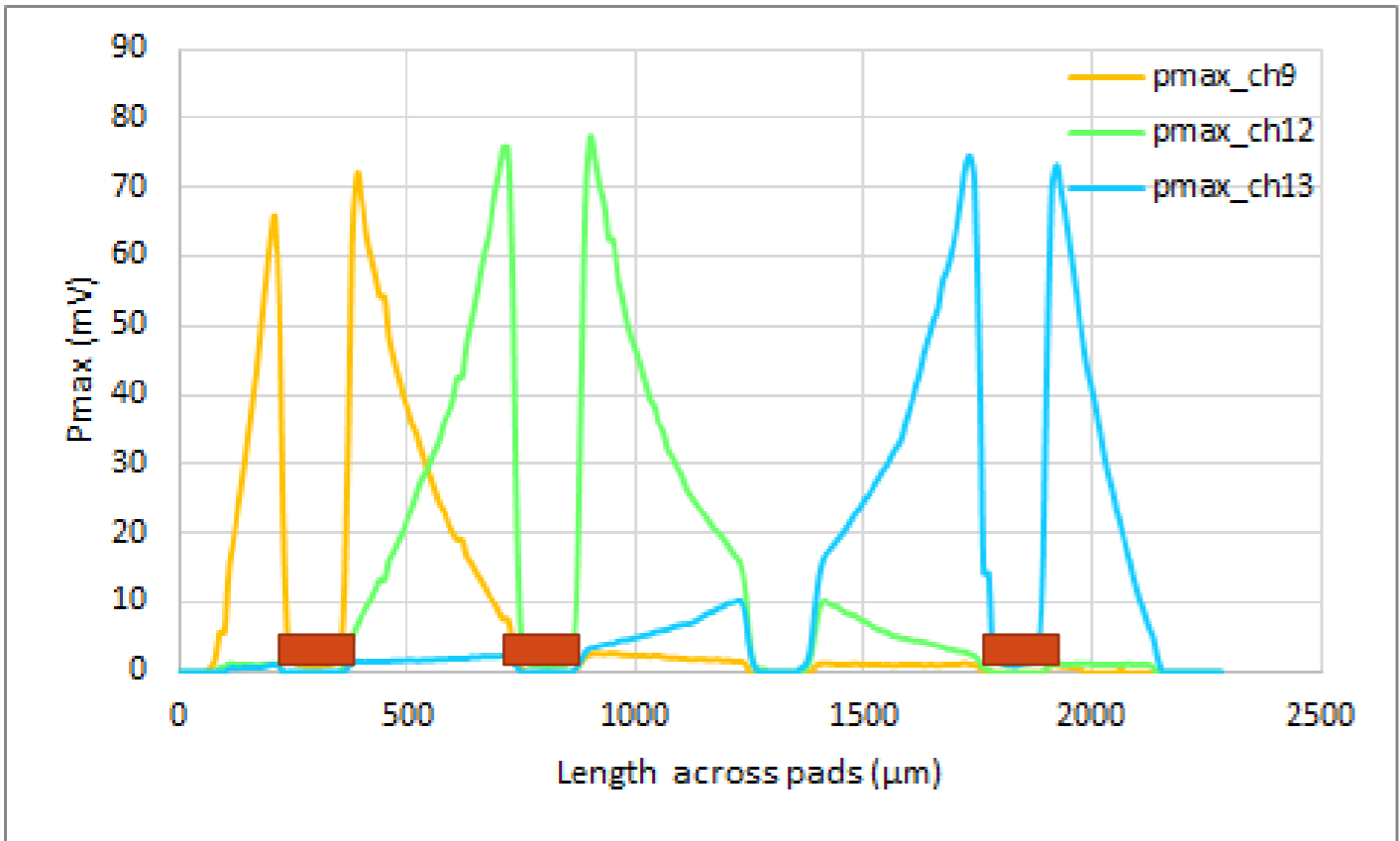


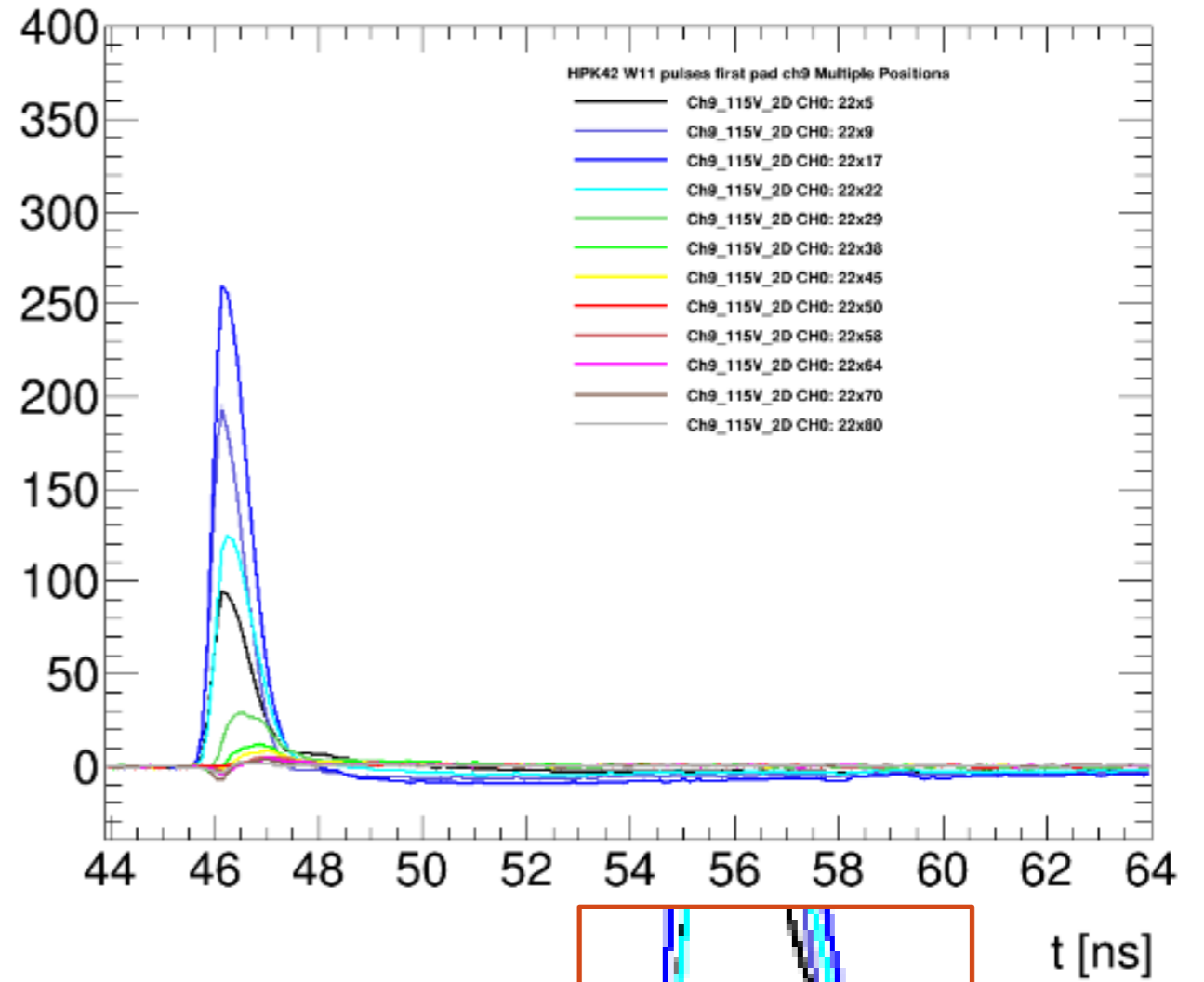
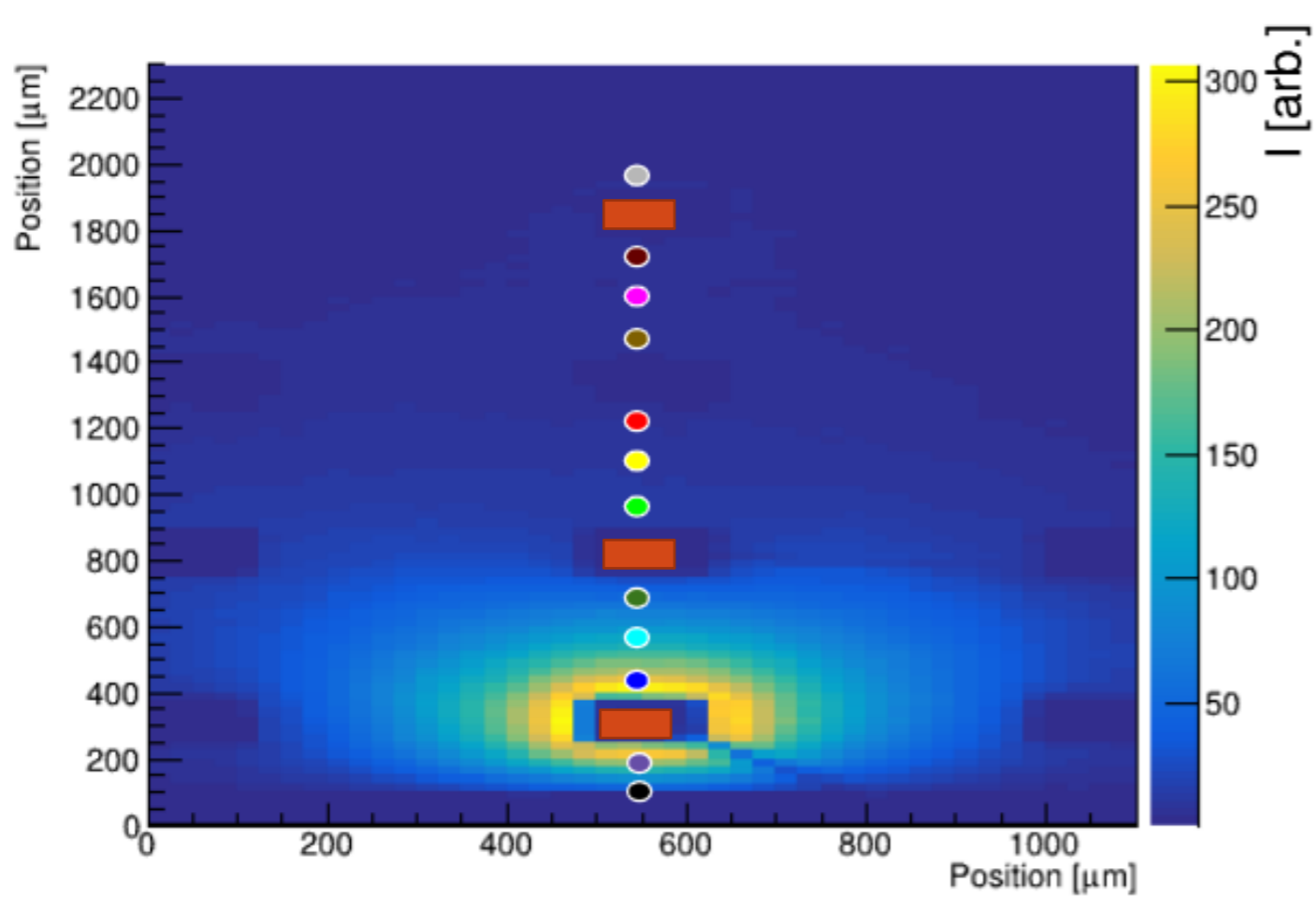
# Standard square pad AC-LGAD

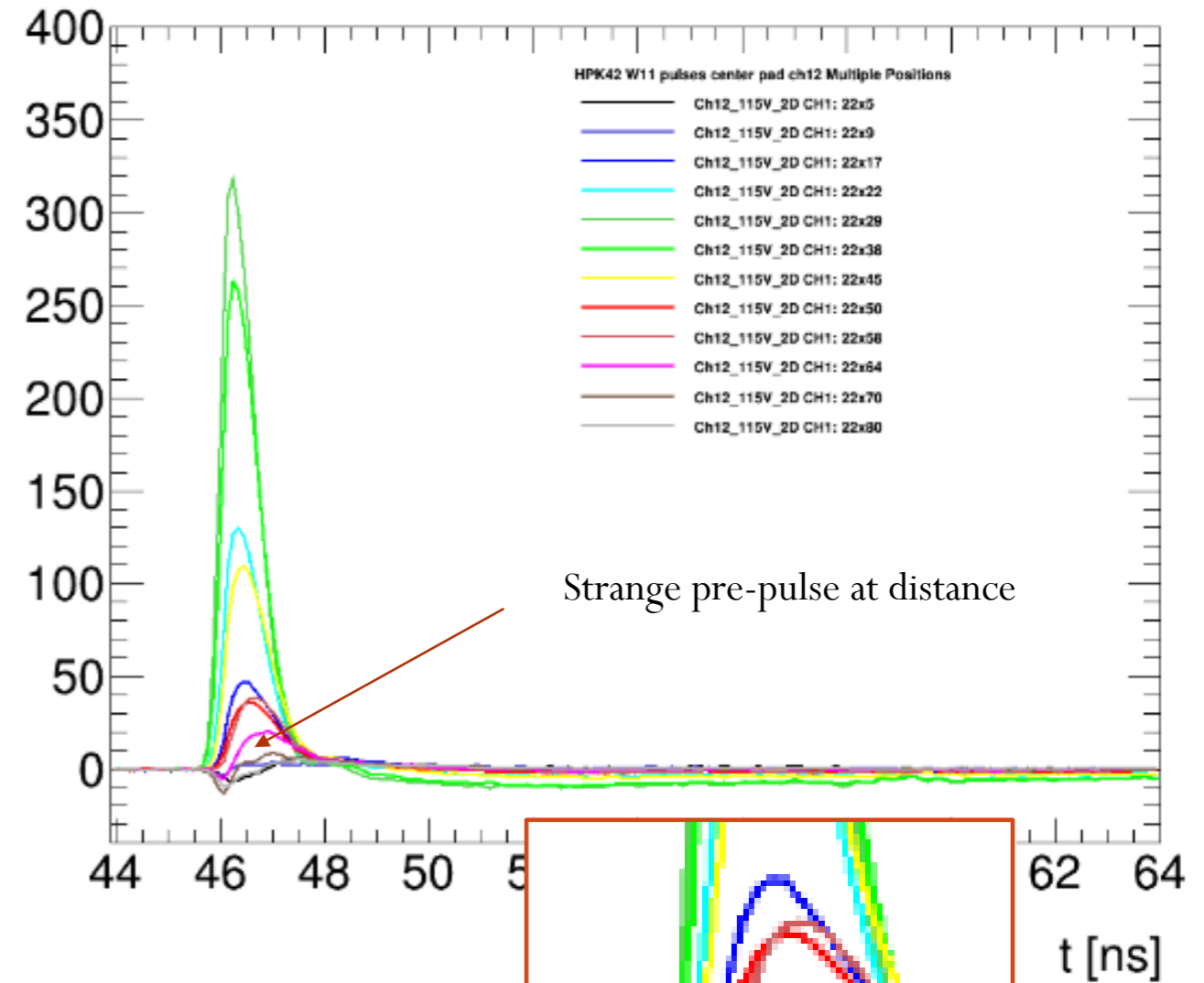
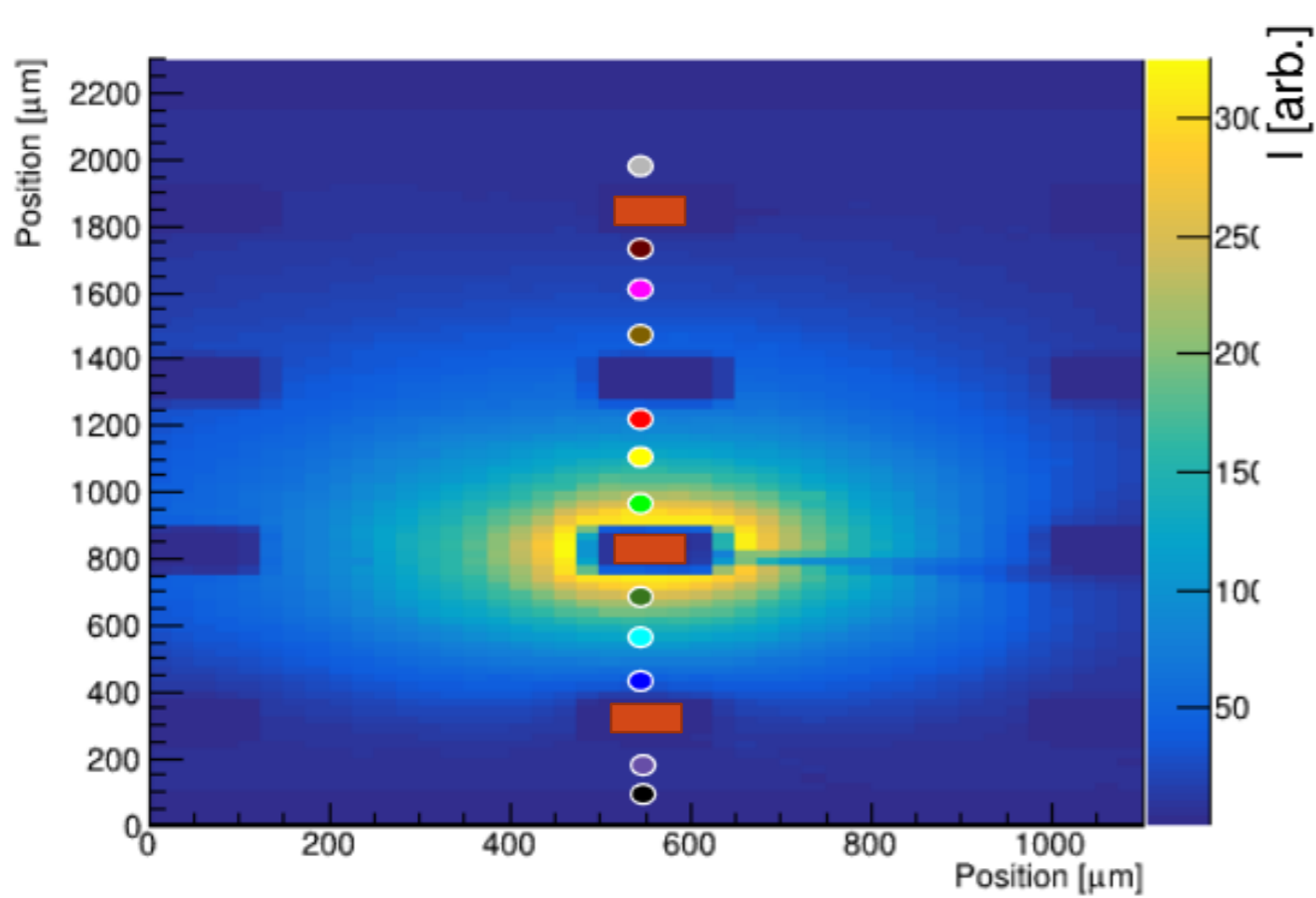
- Significant **loss of signal amplitude** between pads at 500  $\mu\text{m}$ , more pronounced for the double distance: in this sensor, ca. 27% at the center point between adjacent pads, **ca. 65% for '1000  $\mu\text{m}$ ' pitch**
- The effect of the bias voltage on relative signal sharing is minimal (observed throughout this production)
- Whether smaller signal, worse SNR and jitter are acceptable depends on what gain the sensor is operated at = what absolute signal remains, and how critical the reduction of the metal size or channel count is finally determined to be

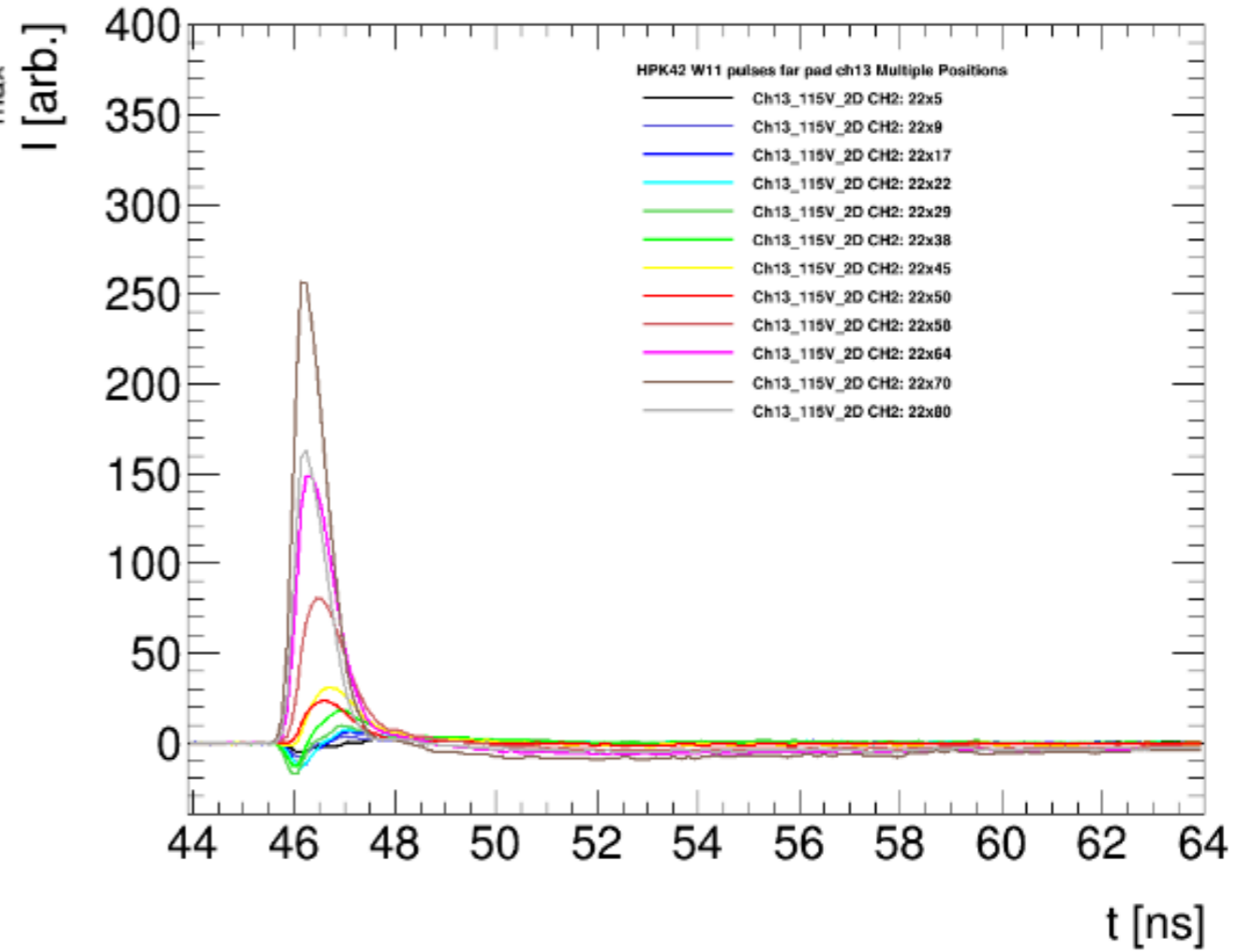
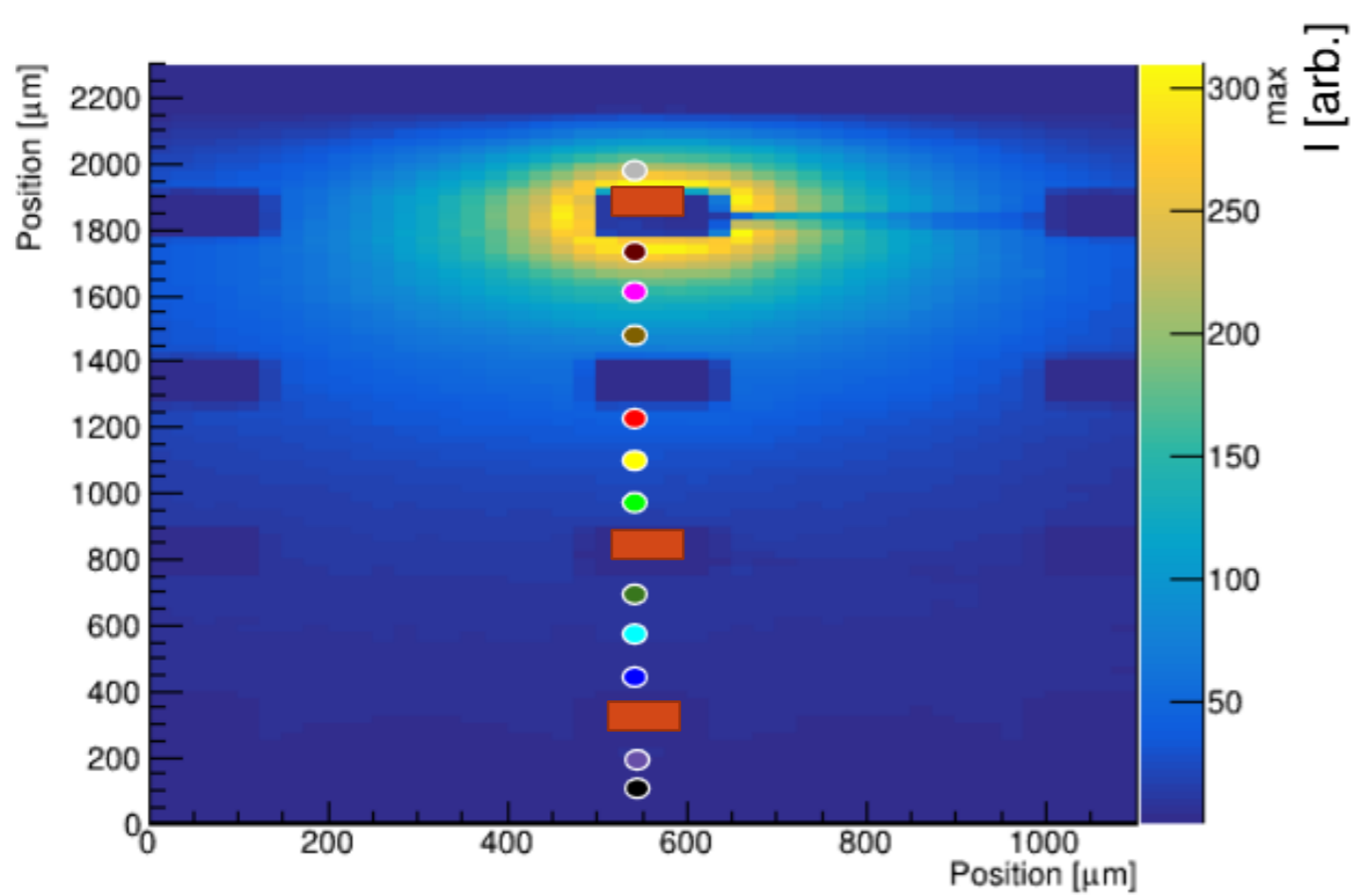


How to minimize the 'dip' in between pads?









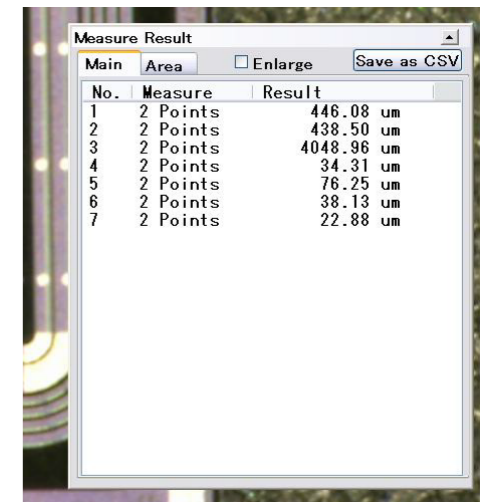
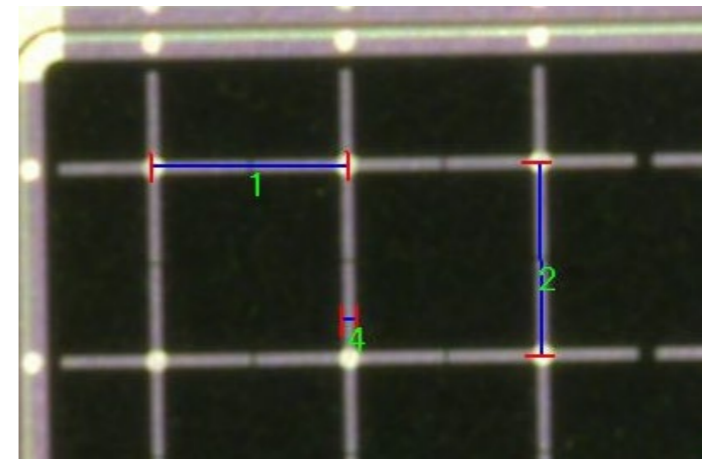
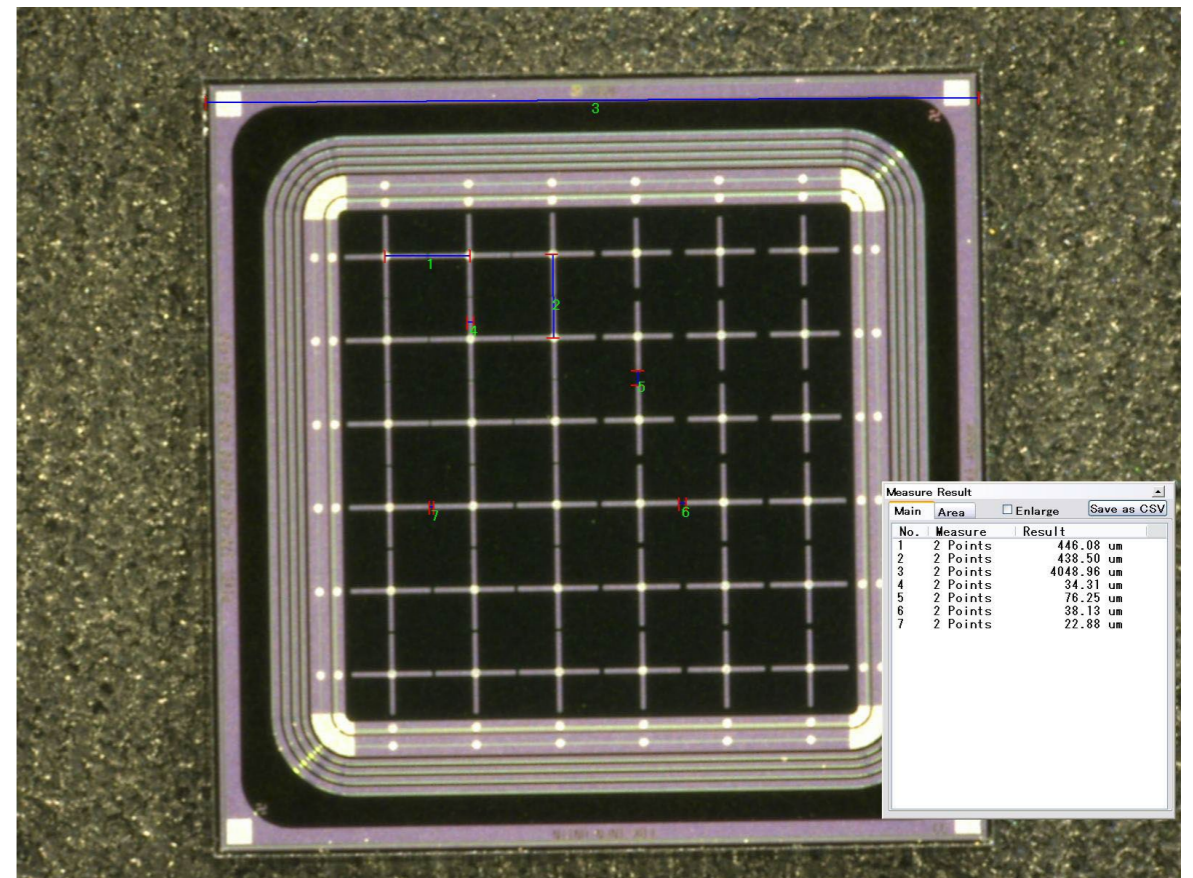
# Crosses

---

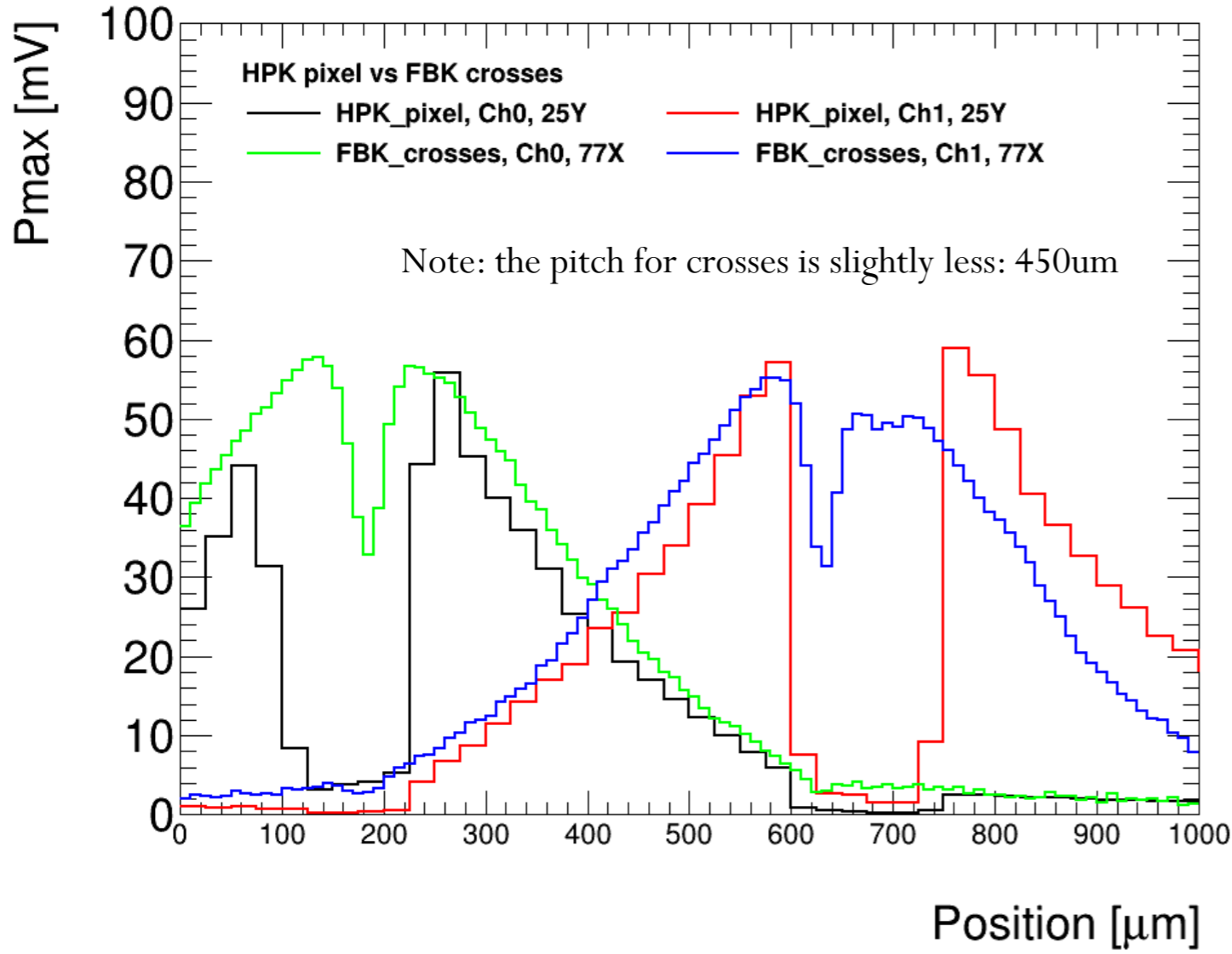


# New geometries - crosses

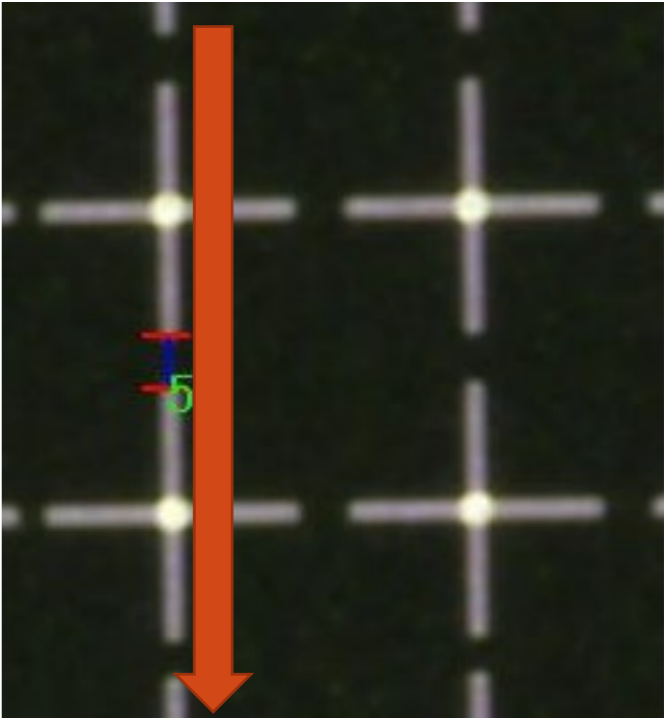
- FBK sensor from RSD2 production
  - Pitch 450um
  - Bond pad size  $\sim 50 \times 50 \mu\text{m}$
  - Metal arm width  $\sim 35 \mu\text{m}$
- Using metal 'arms' to collect the charge in a cross-like pattern
- Reduce the signal reduction in between thanks to the more efficient charge collection
  - Tested with laser TCT and compared with standard square pad sensor
- Only relative for now, no absolute comparison



# New geometries - crosses

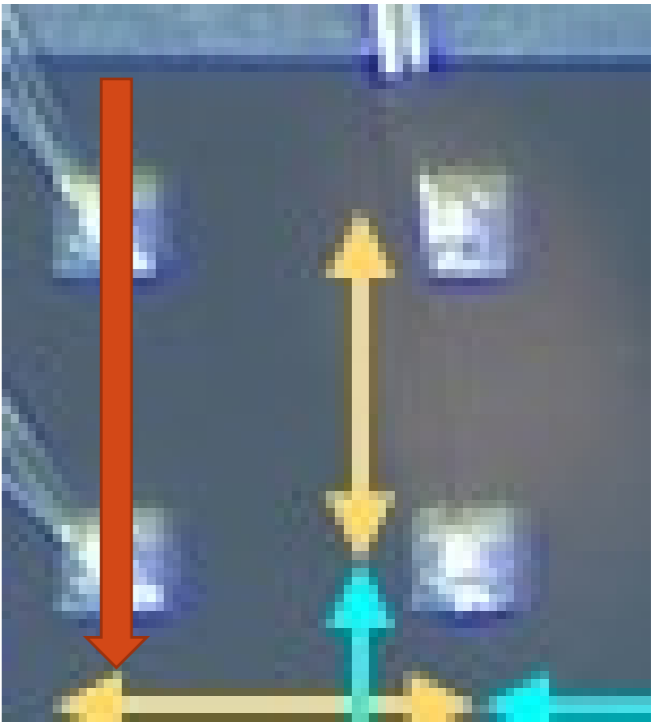


FBK crosses

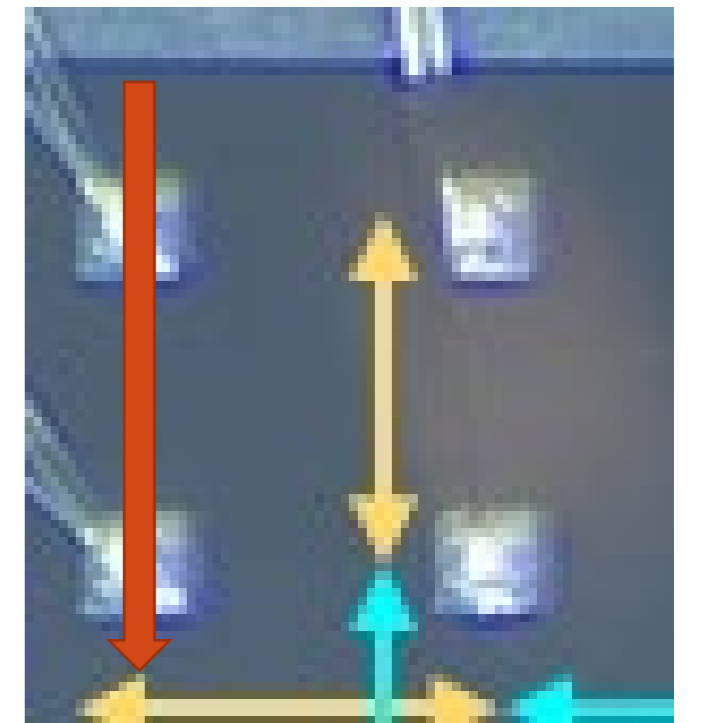
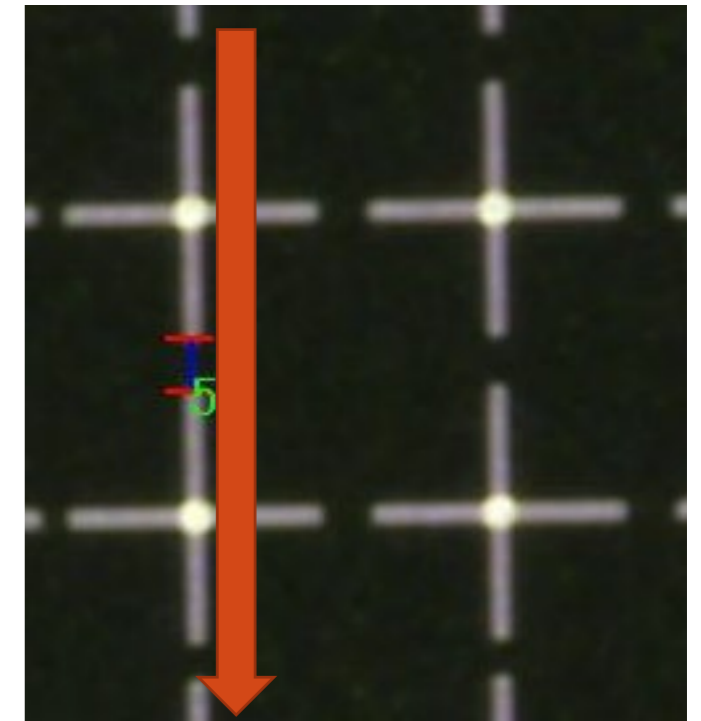
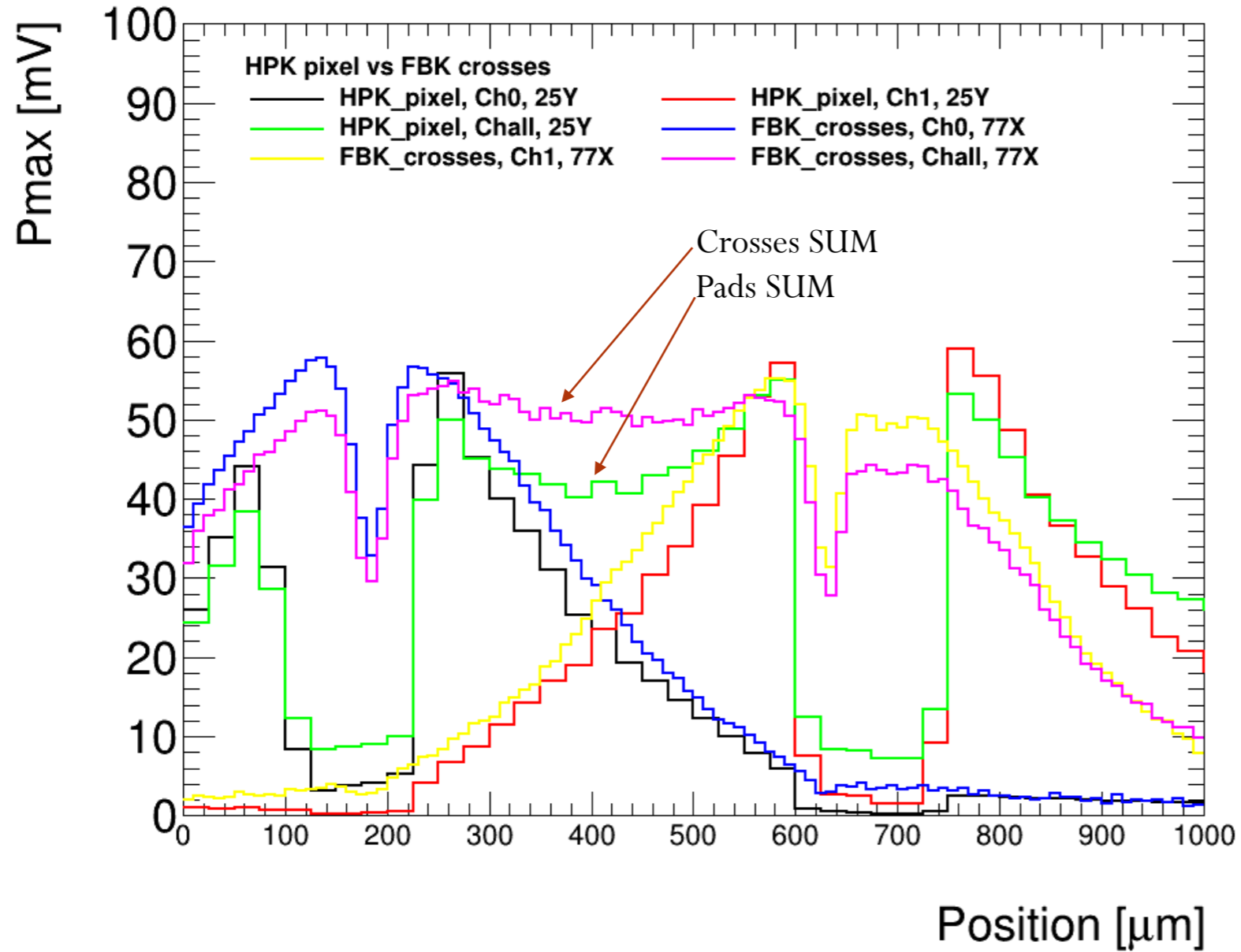


Scans are off-center  
(to avoid arms)  
But in the same positions

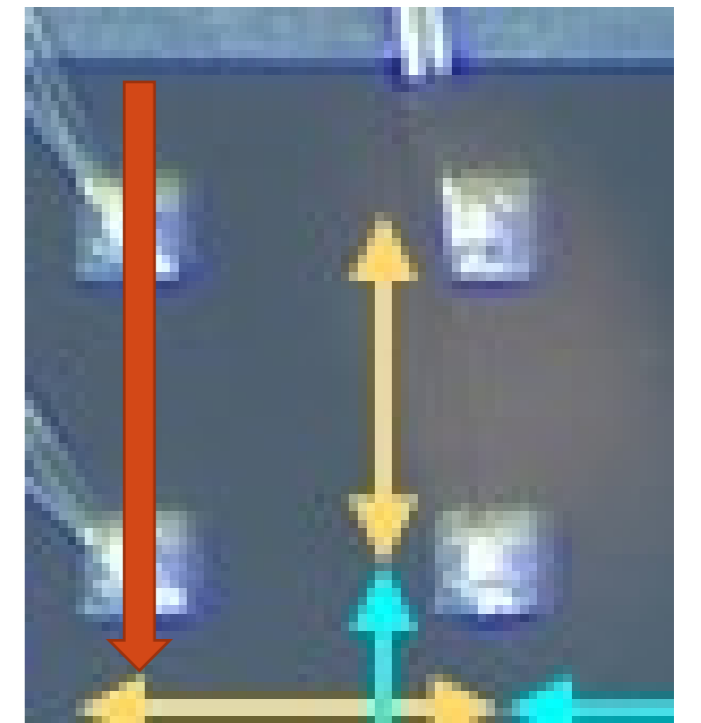
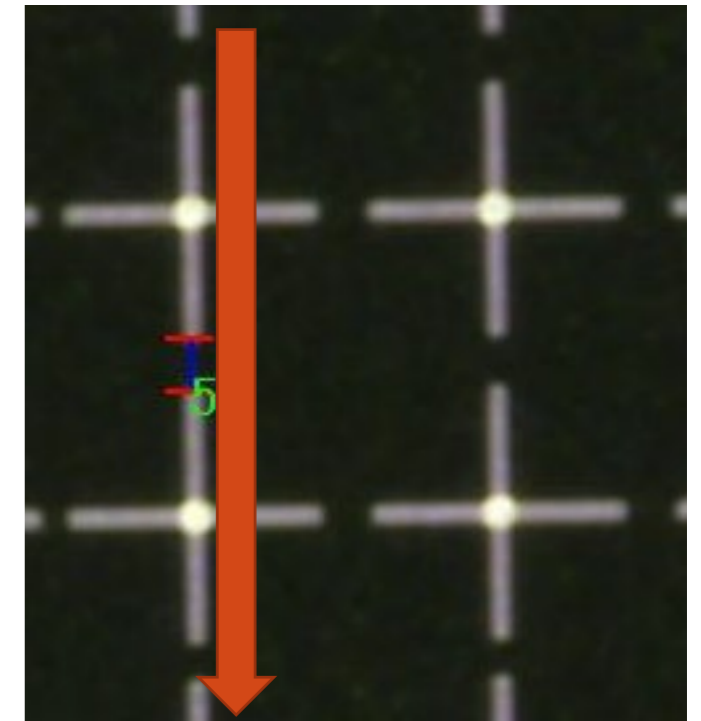
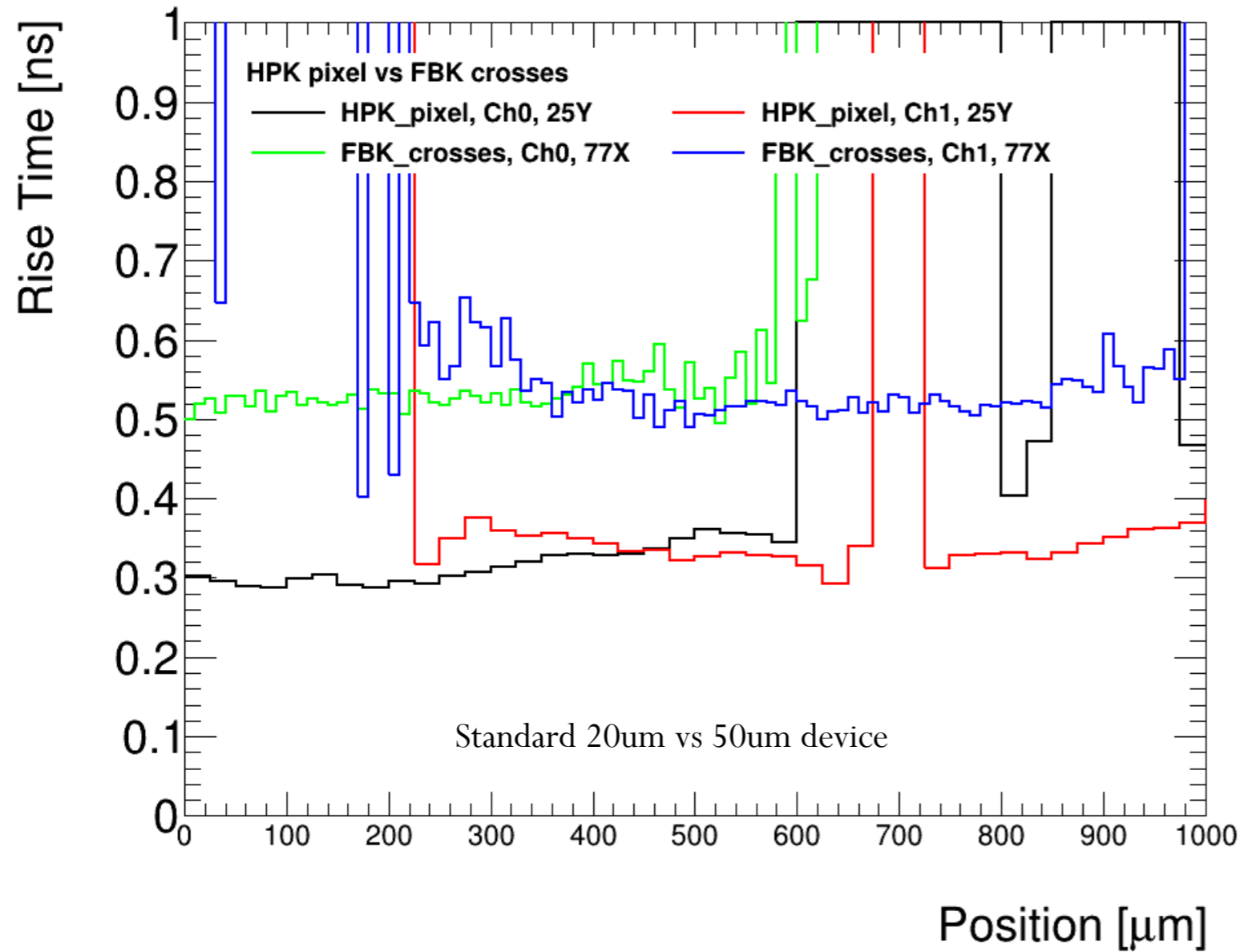
HPK pads



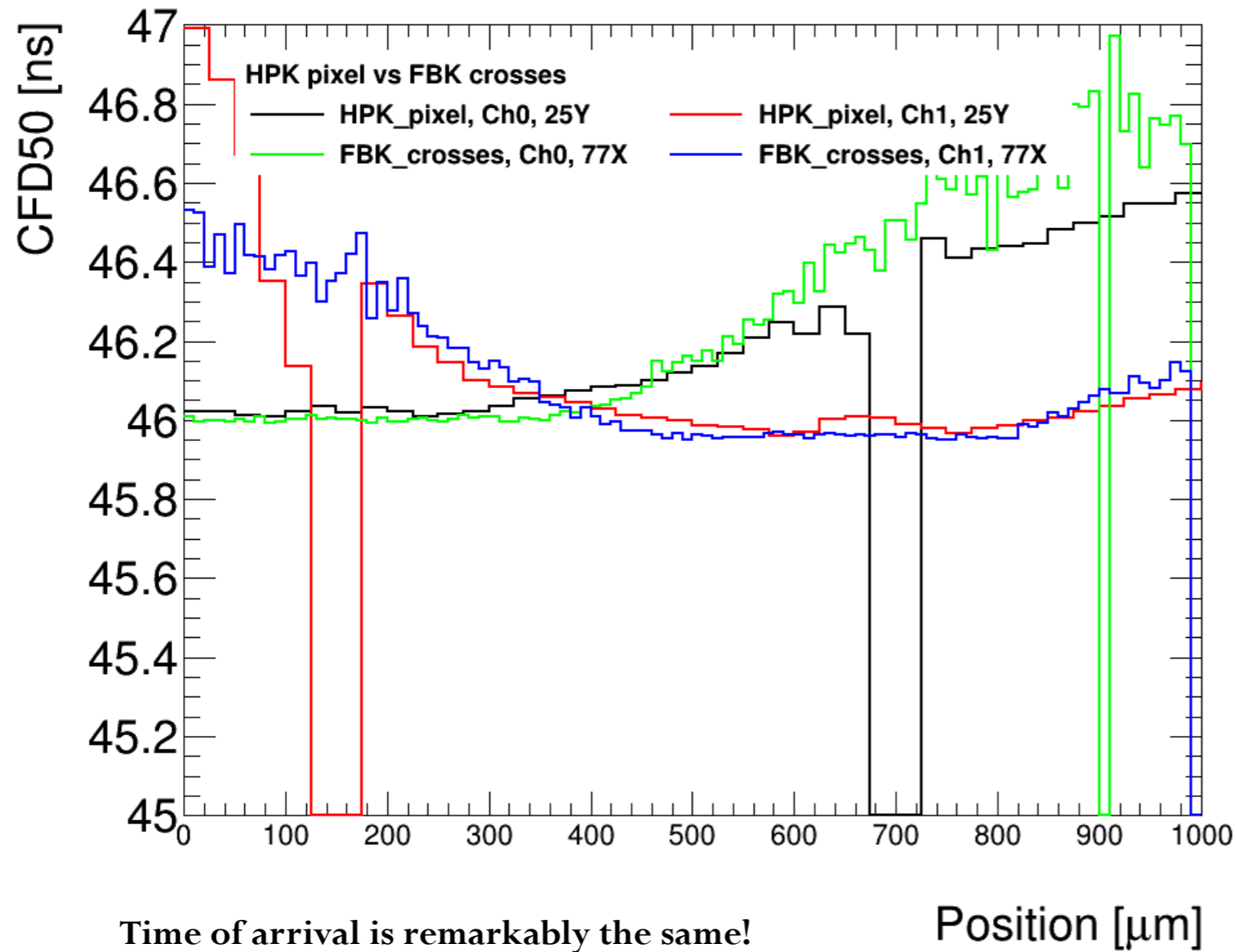
# New geometries - crosses



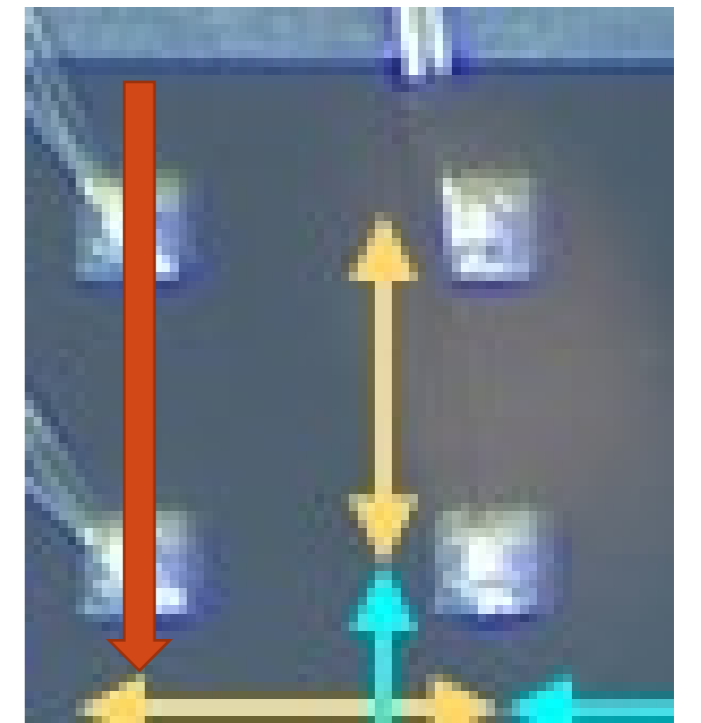
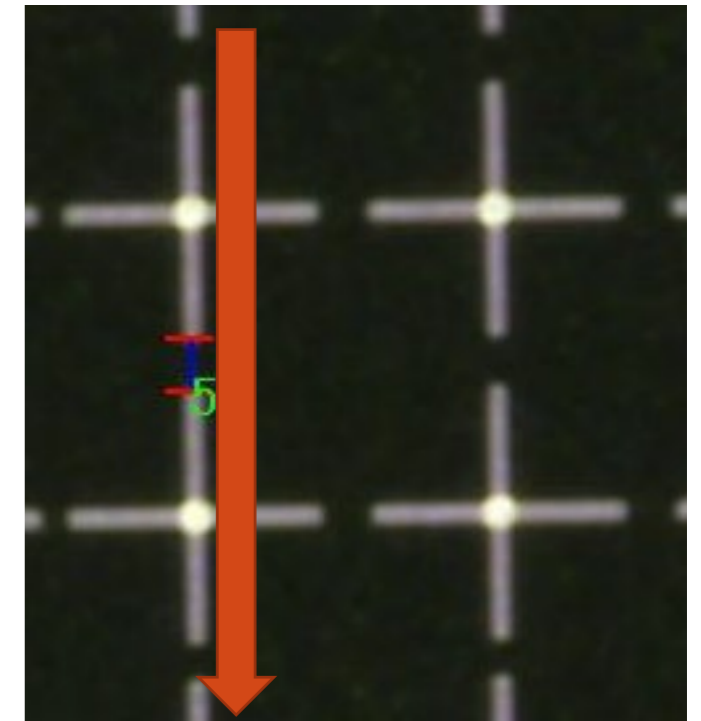
# New geometries - crosses

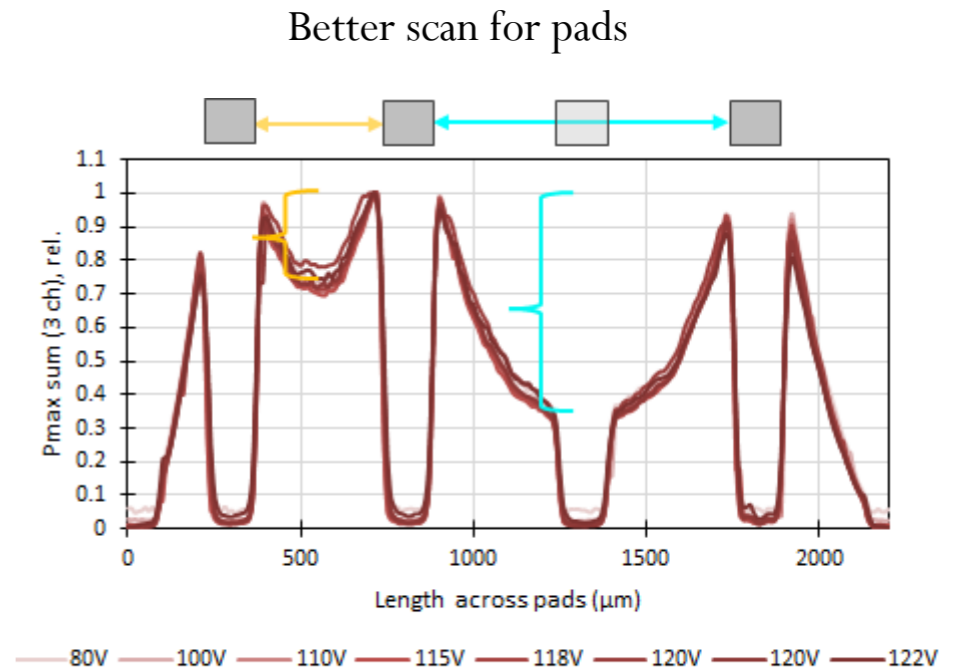
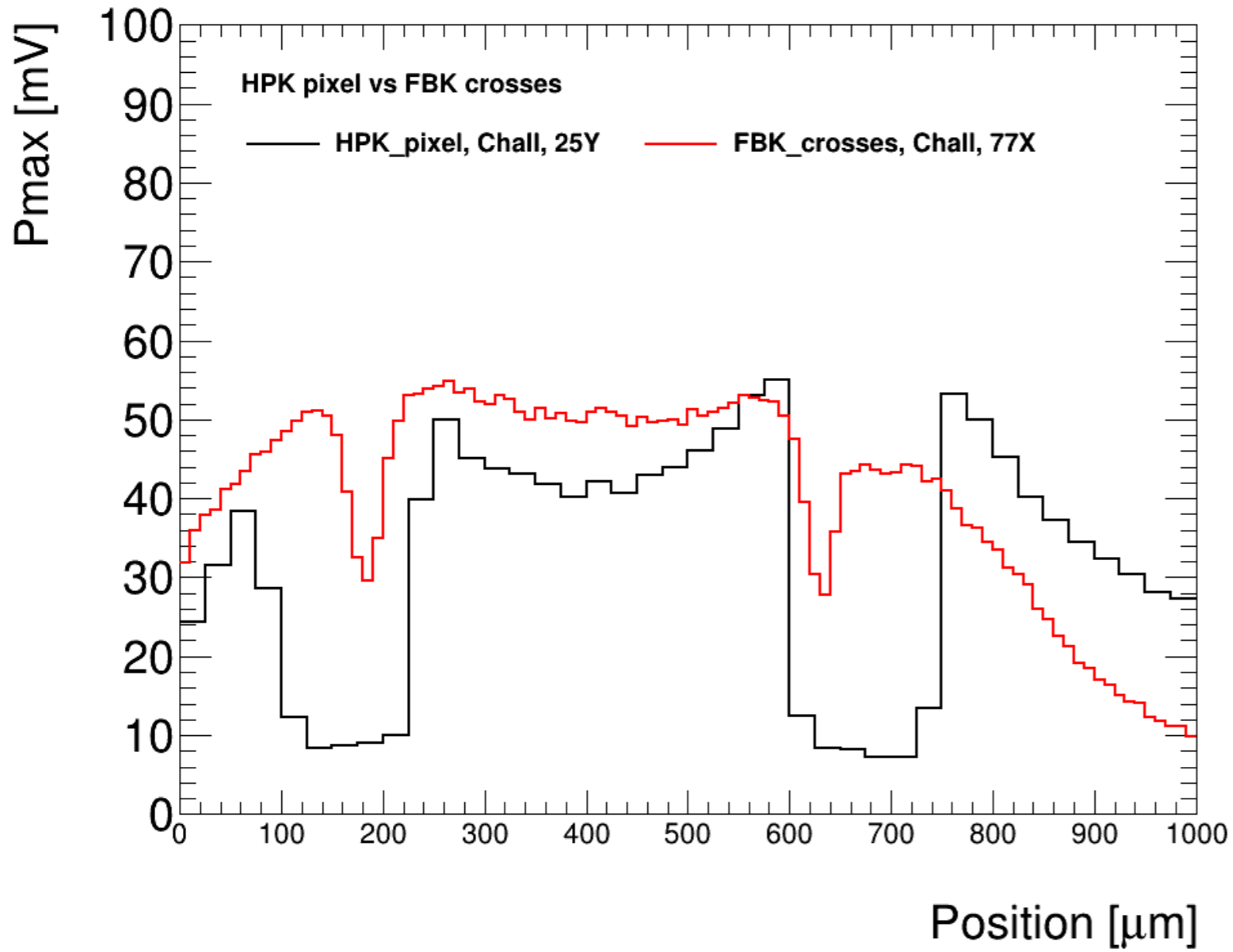


# New geometries - crosses



Time of arrival is remarkably the same!

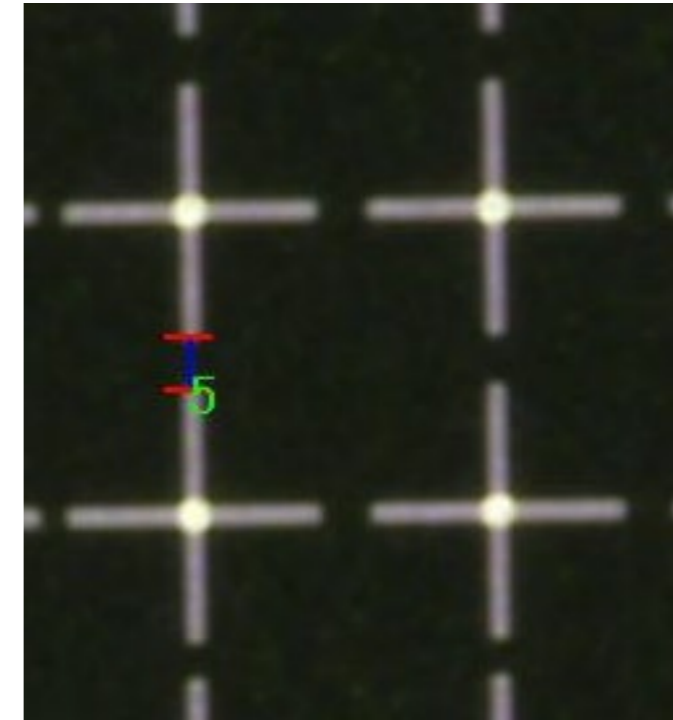
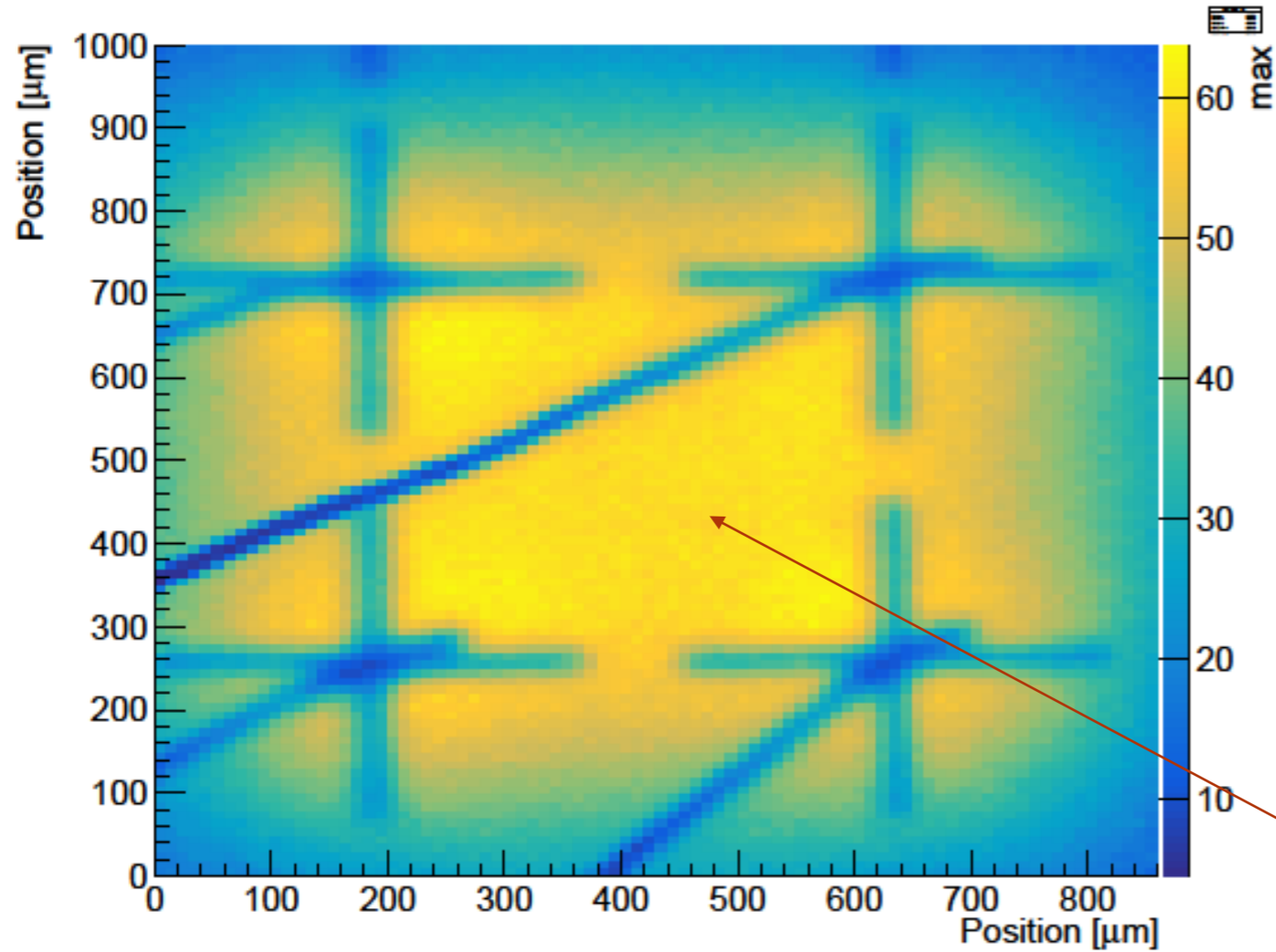




21

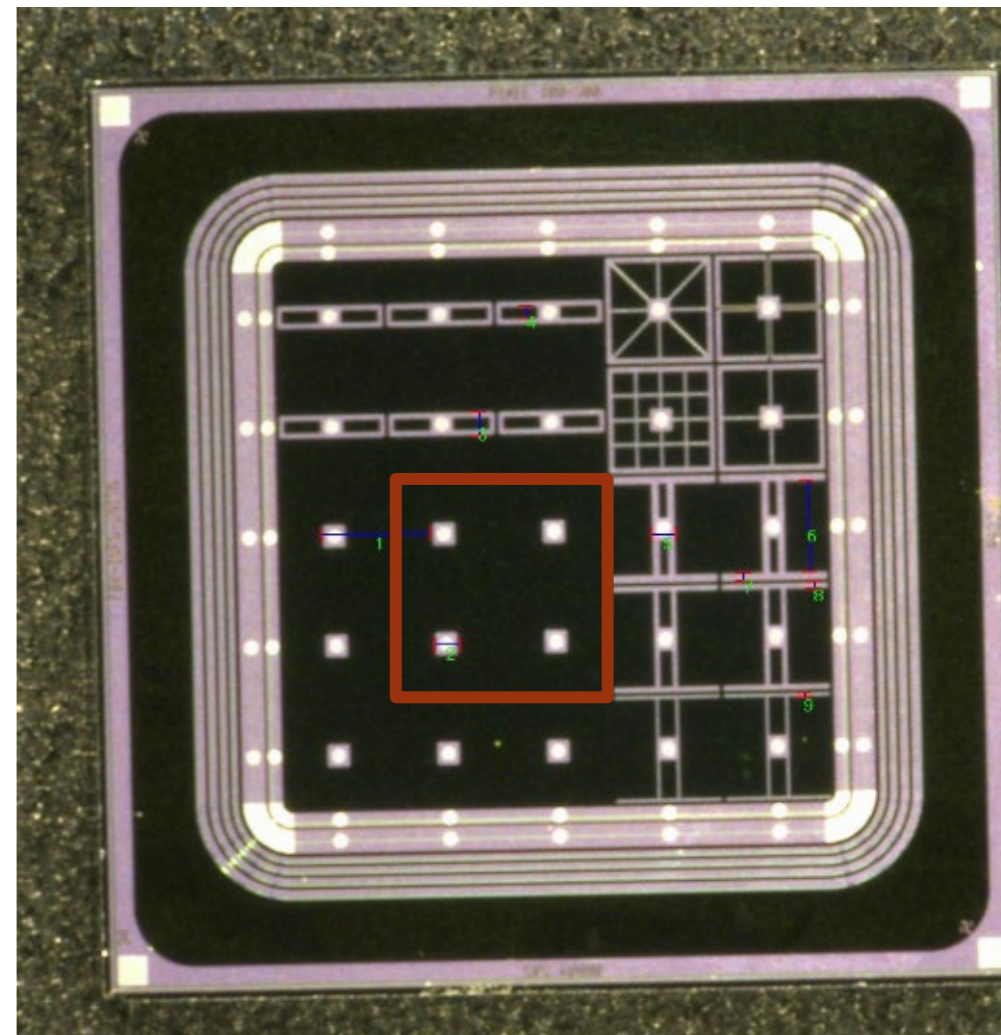
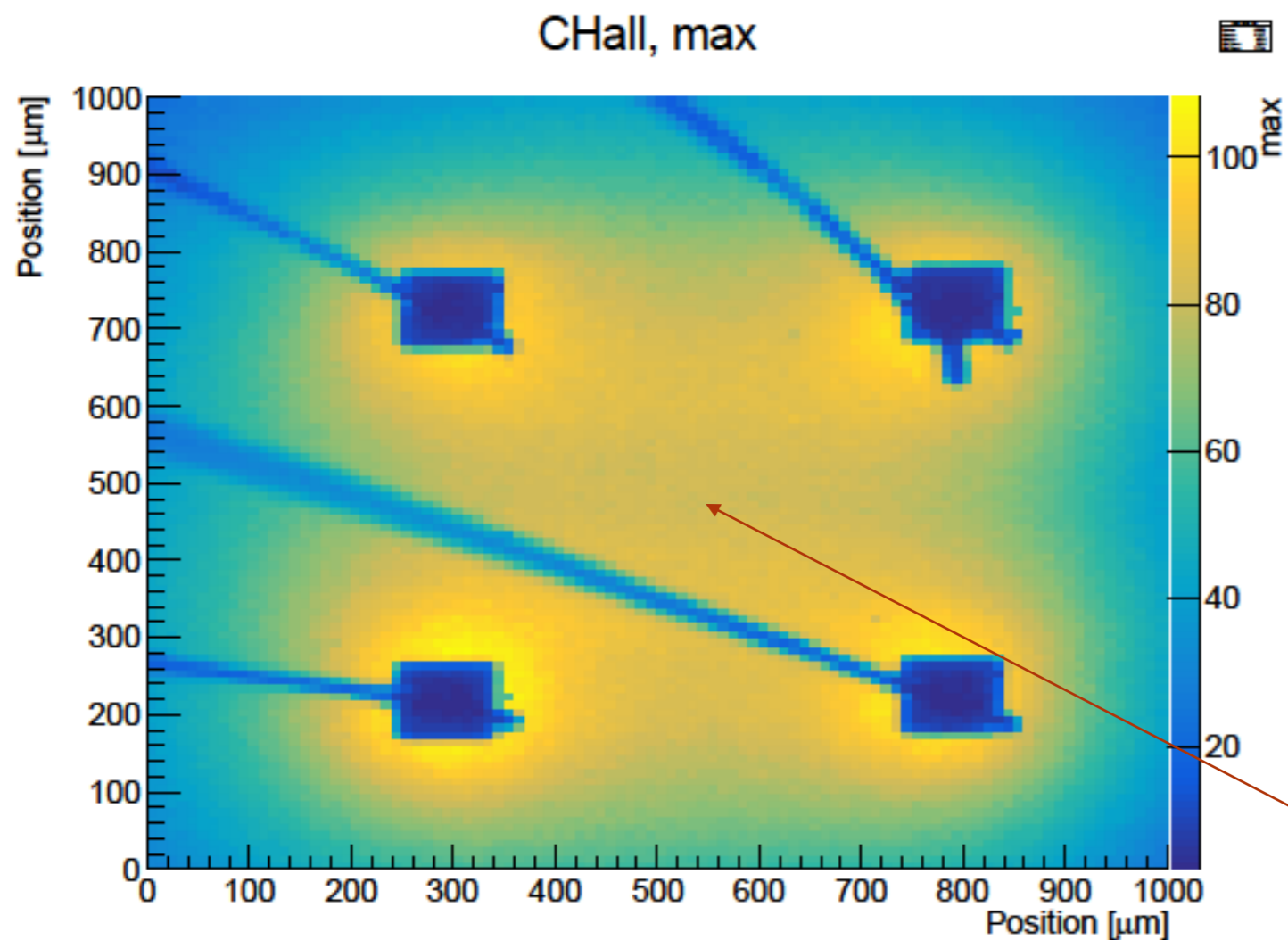
# New geometries - crosses

CHall, max



Sum of all signals pretty constant even in the center

# FBK pads sensor

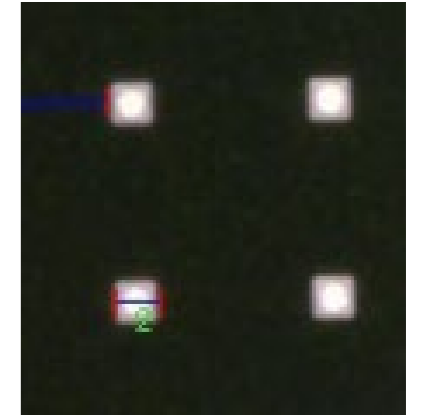
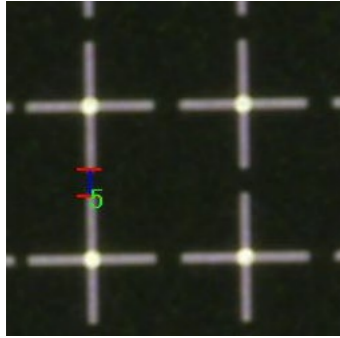


Signal 'dips' even more in the center

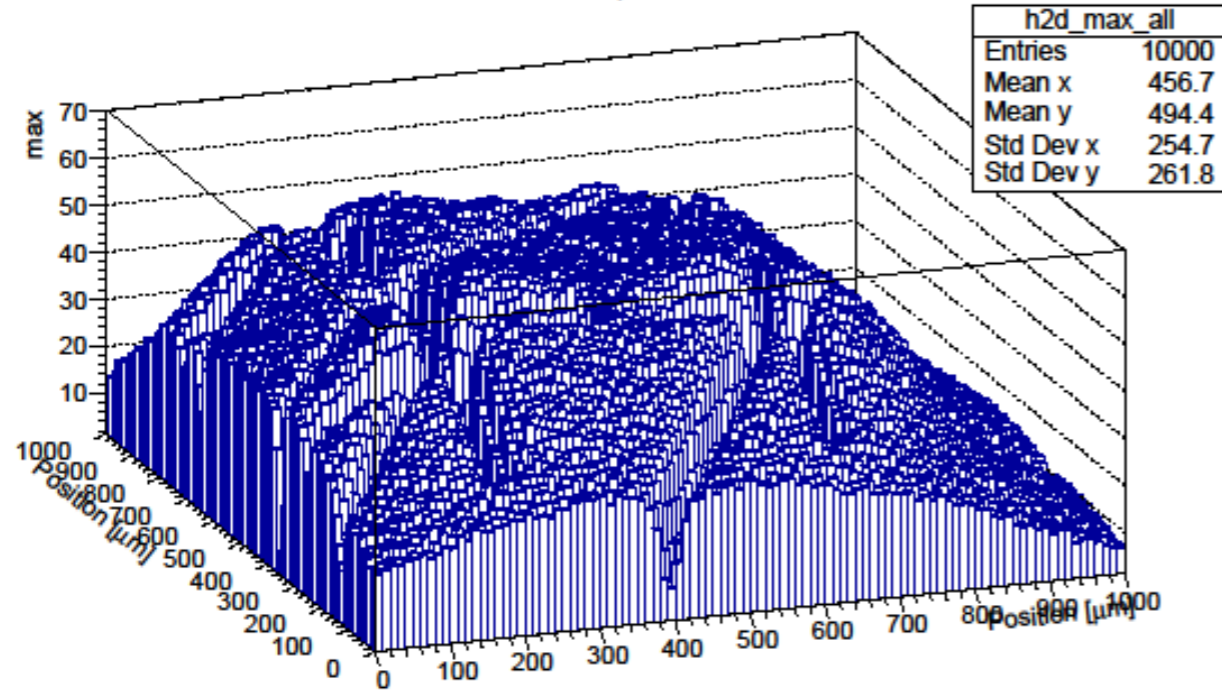
Unfortunately, no data on the HPK sensor like this for now  
→ Will do next



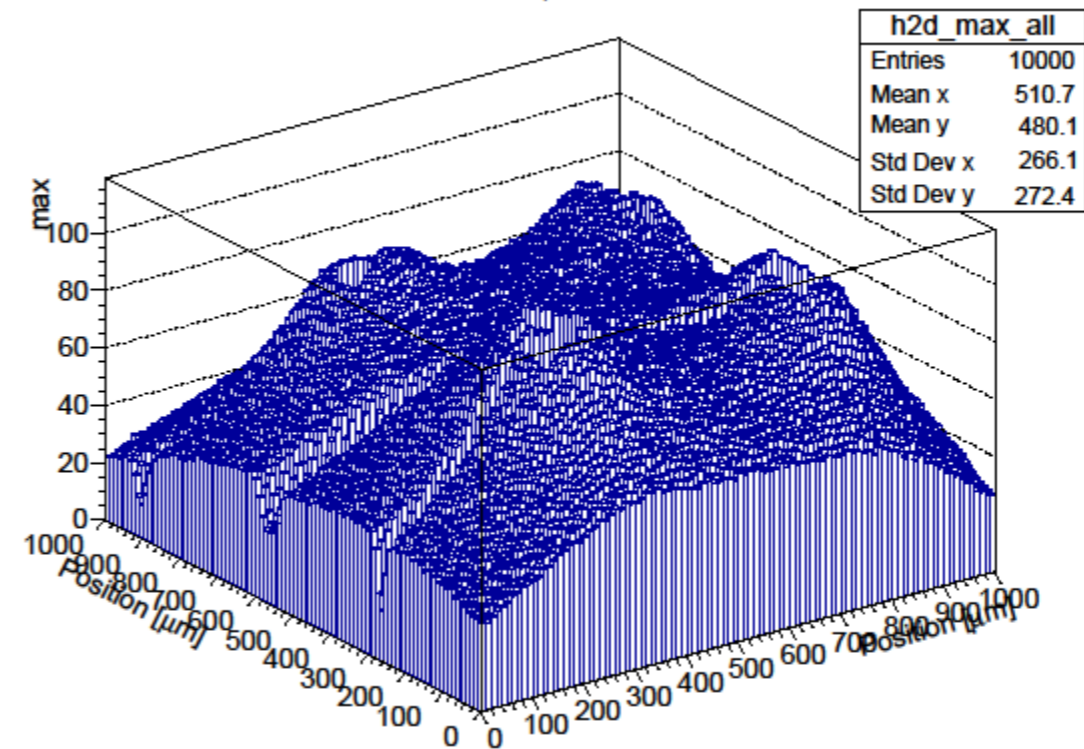
# New geometries - crosses



CHall, max

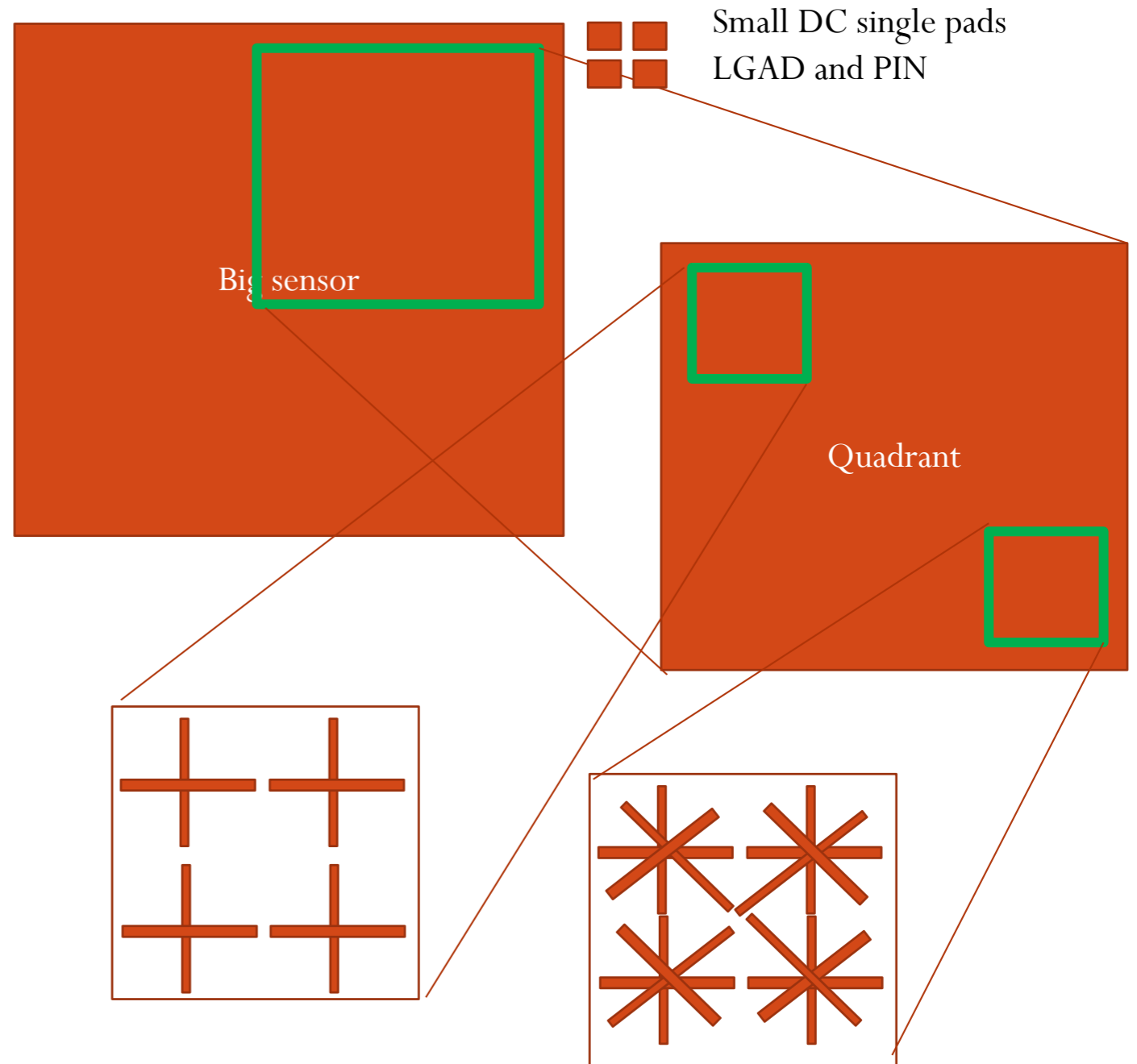


CHall, max

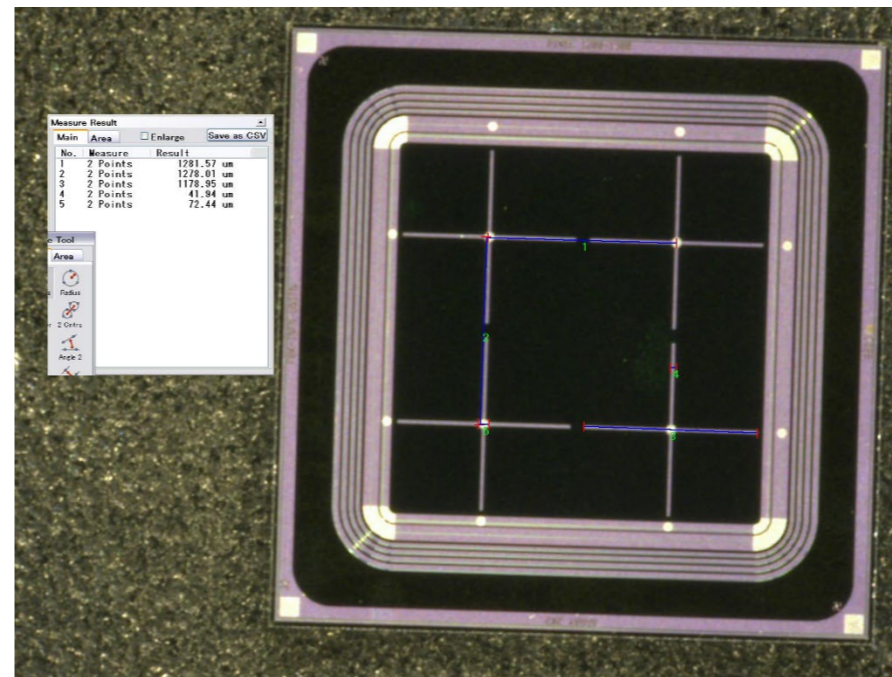
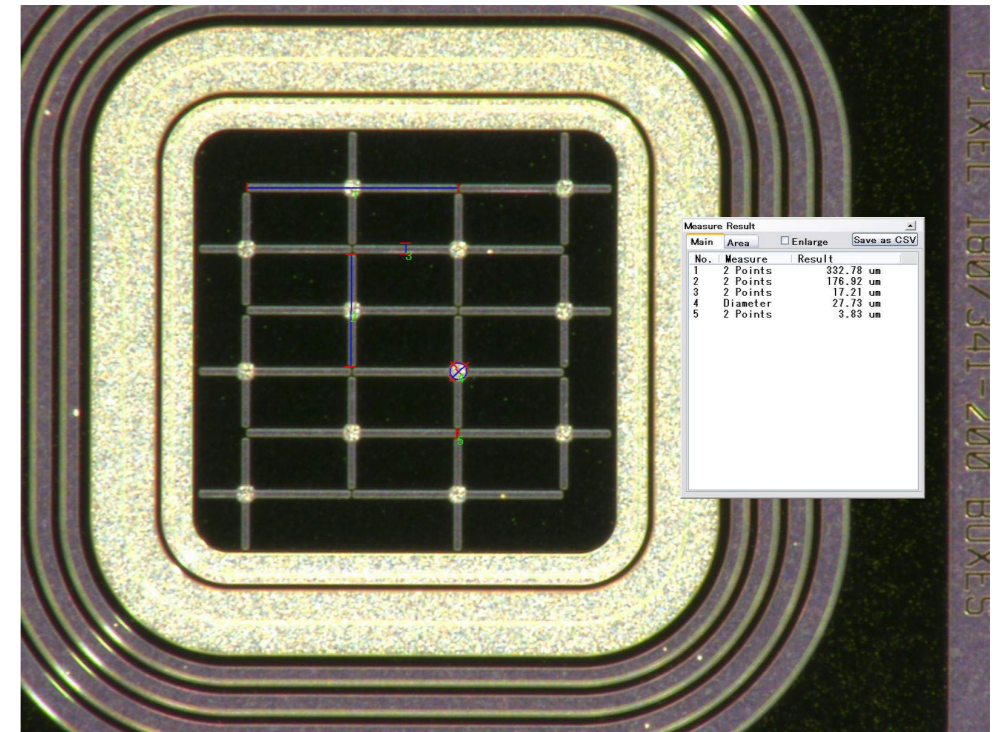
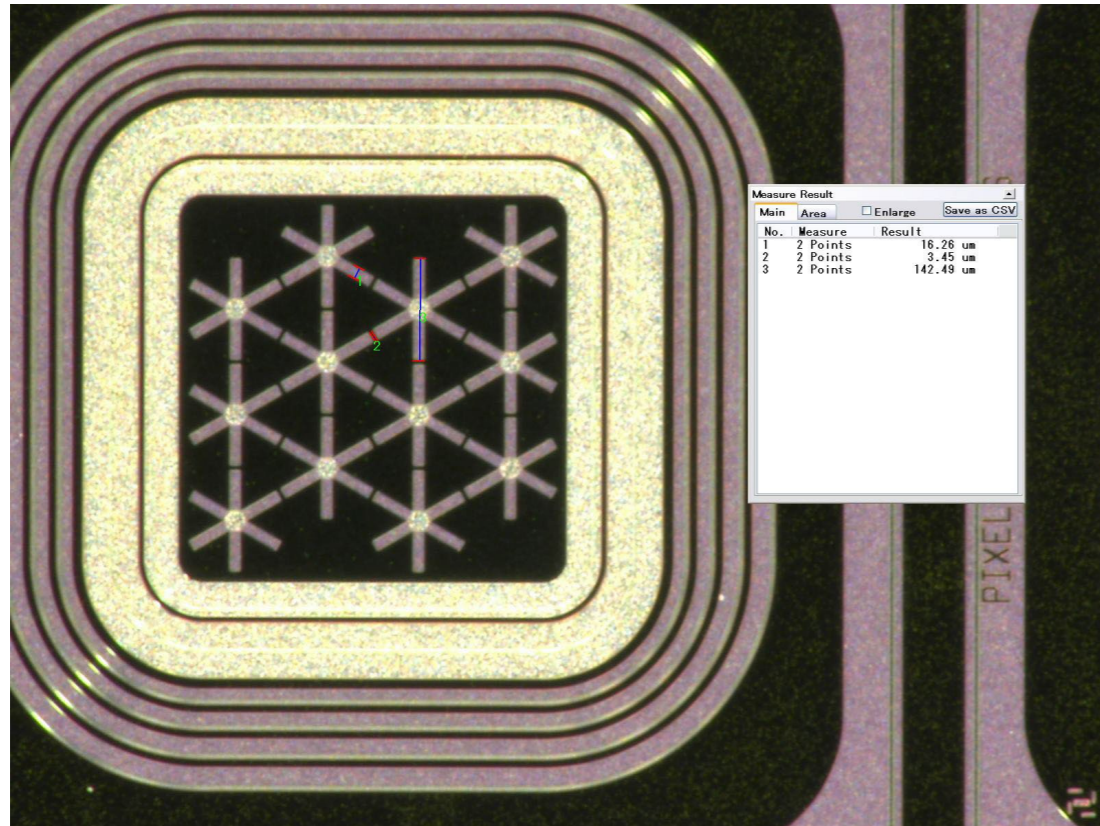


# Proposal for HPK run

- It's clear that a 'cross-like' geometry can help in maintaining the S/N constant across the entire sensor area
- We can try in a subset of the large detectors the following geometries
  - Crosses
  - Snowflakes
- Can be clusters of 2x2 or even 3x3
- In addition **some single pad DC devices ~mm PIN and LGAD at the side of the wafer**
  - Invaluable to test the absolute charge collection and best possible time resolution!
  - Useful to study the interplay between N+ and gain layer doping
  - Different sizes → can test the effect of input capacitance on readout chip

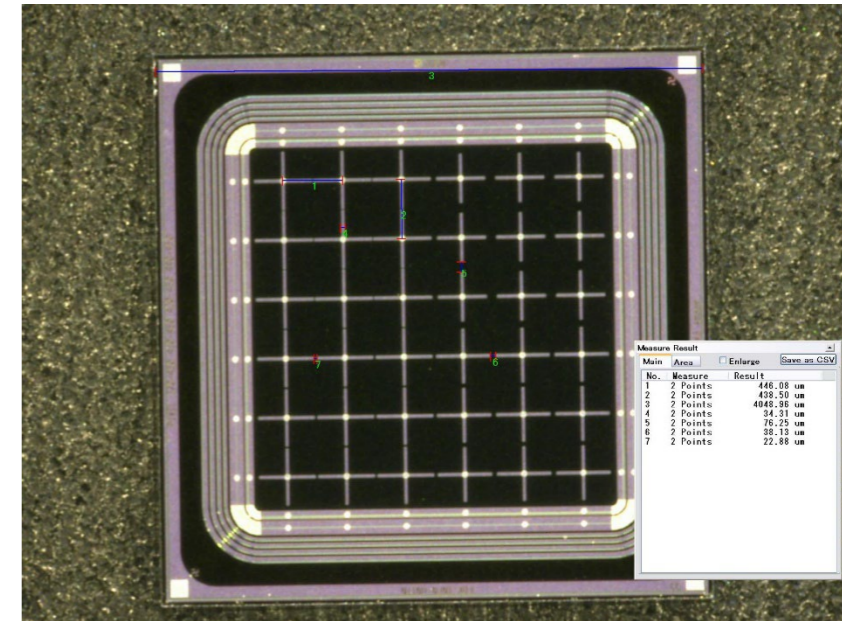


# Other available geometries...



# Conclusions

- Signal 'reduction' in between metal electrodes
  - This effect is increased with larger pitches
- An effective way to reduce signal reduction in between electrodes is using metal structure to increase the charge collection efficiency
  - Crosses seem to be a good working candidate
- Propose to put a few of these geometries (including 'snowflake') in some of the full size sensors from HPK
  - Can be just clusters of 4 or 9 pads at the corner for each quadrant
- Proposal to add single pad devices (LGAD, PIN) at the side of the wafer
  - Test absolute gain of the wafer and best time resolution achievable
  - With dimension 0.2mm, 0.5mm, 1mm (2mm?) to check the effect input capacitance on the readout chip

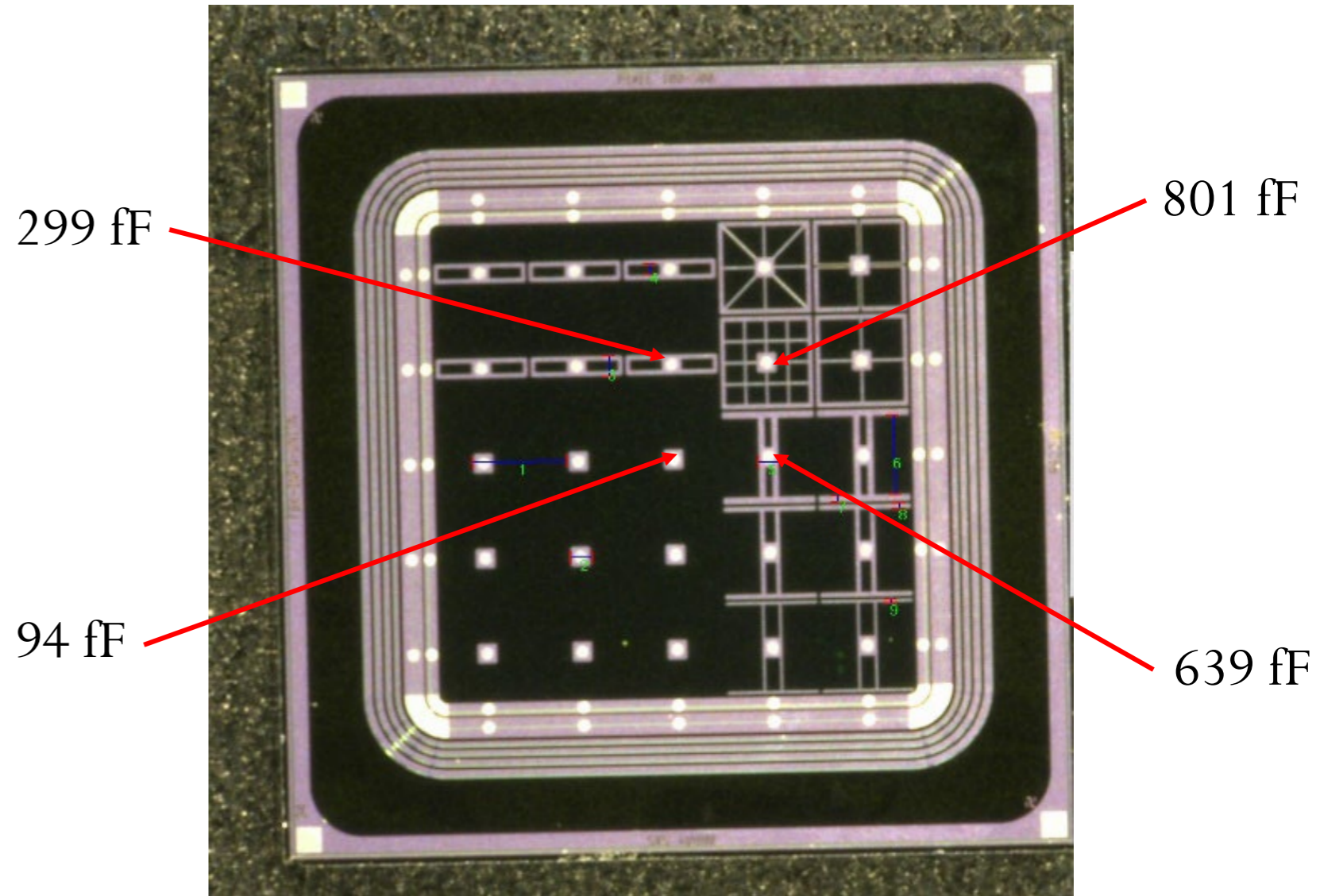
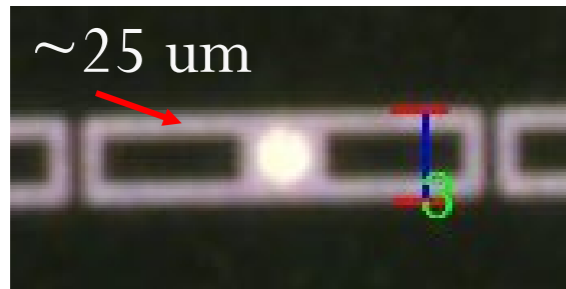


# Backup

---

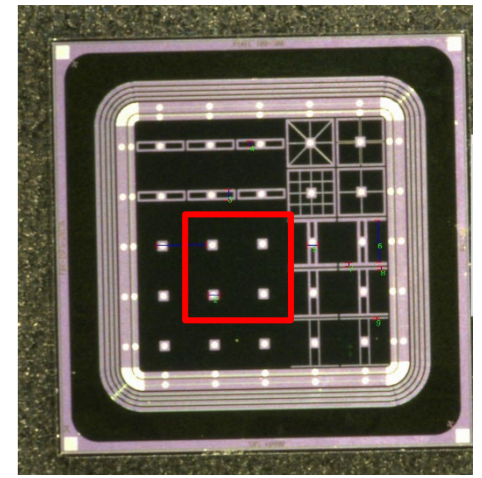
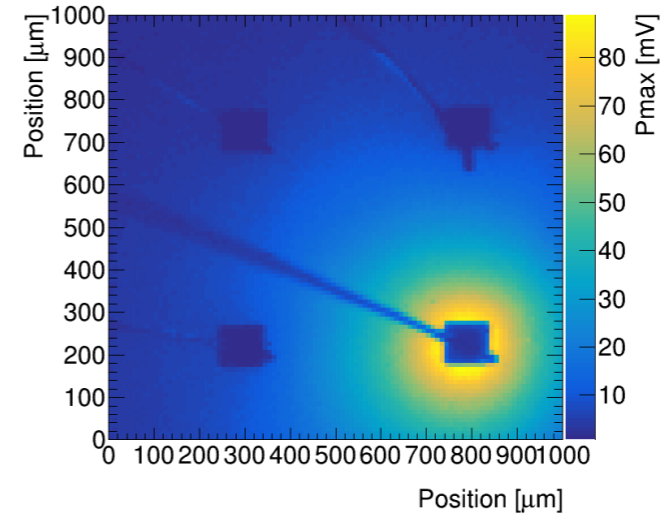
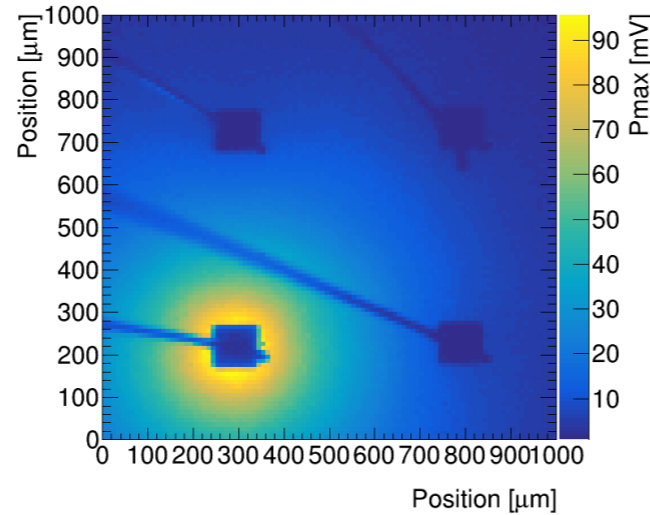
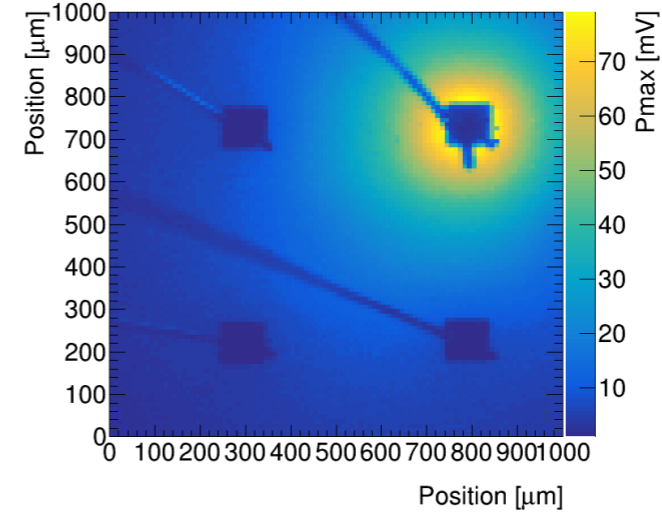
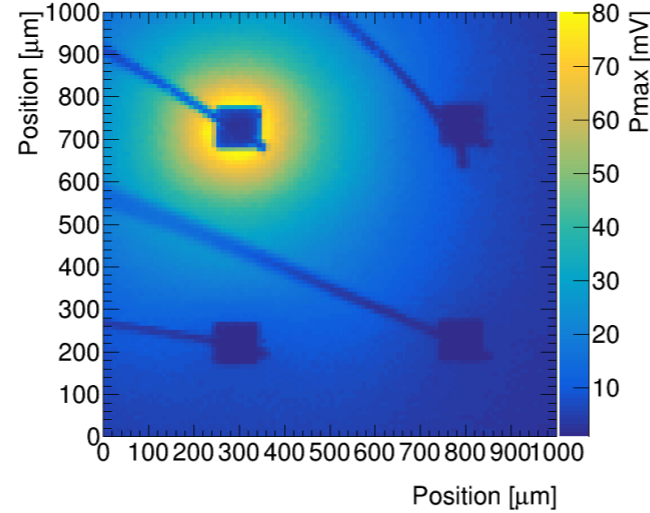
# AC-LGAD AC-capacitances

- Measurement of the pad capacitance for each different type of pad
  - HV from the backside, N+ and guard ring grounded, capacimeter connected to top metal
  - The rest of the metal pads around it are floating
- Pad's capacitance scales with amount of metal coverage on top as expected
- **Opening in the metal does reduce the capacitance**
  - Micro-strips are  $\sim 100 \times 500$   $\mu\text{m}$  but the capacitance is not 5 times the one of  $100 \times 100$   $\mu\text{m}$  pads
  - Capacitance is only  $\sim 3$  times
  - Scales with the (2x)  $175 \times 50$   $\mu\text{m}$  area of the opening
  - H-pad measured has thicker arms so the capacitance is significantly higher



# AC-LGAD 100x100 $\mu\text{m}$ pads

- Pro
  - Homogeneous in X-Y
  - Likely good reconstruction in the region in between pads
  - Small input capacitance
- Cons
  - Smaller signal



# Comparison of 1D profiles, all pads - X

