

INTT Team Commissioning North Side

Genki N., Akitomo E., Raul C., Maya S., Jeffrey H., Daniel C., Robert P., Stephen B., and Rachid N.
March 11th, 2024 (completed):

- ❖ All eight ROCs were installed and tested (pedestal data), see next slides for results,
→ after debugging, this year (Run-24), we lost one sensor compared to Run-23 (98.2% live chips in total of the INTT northside).



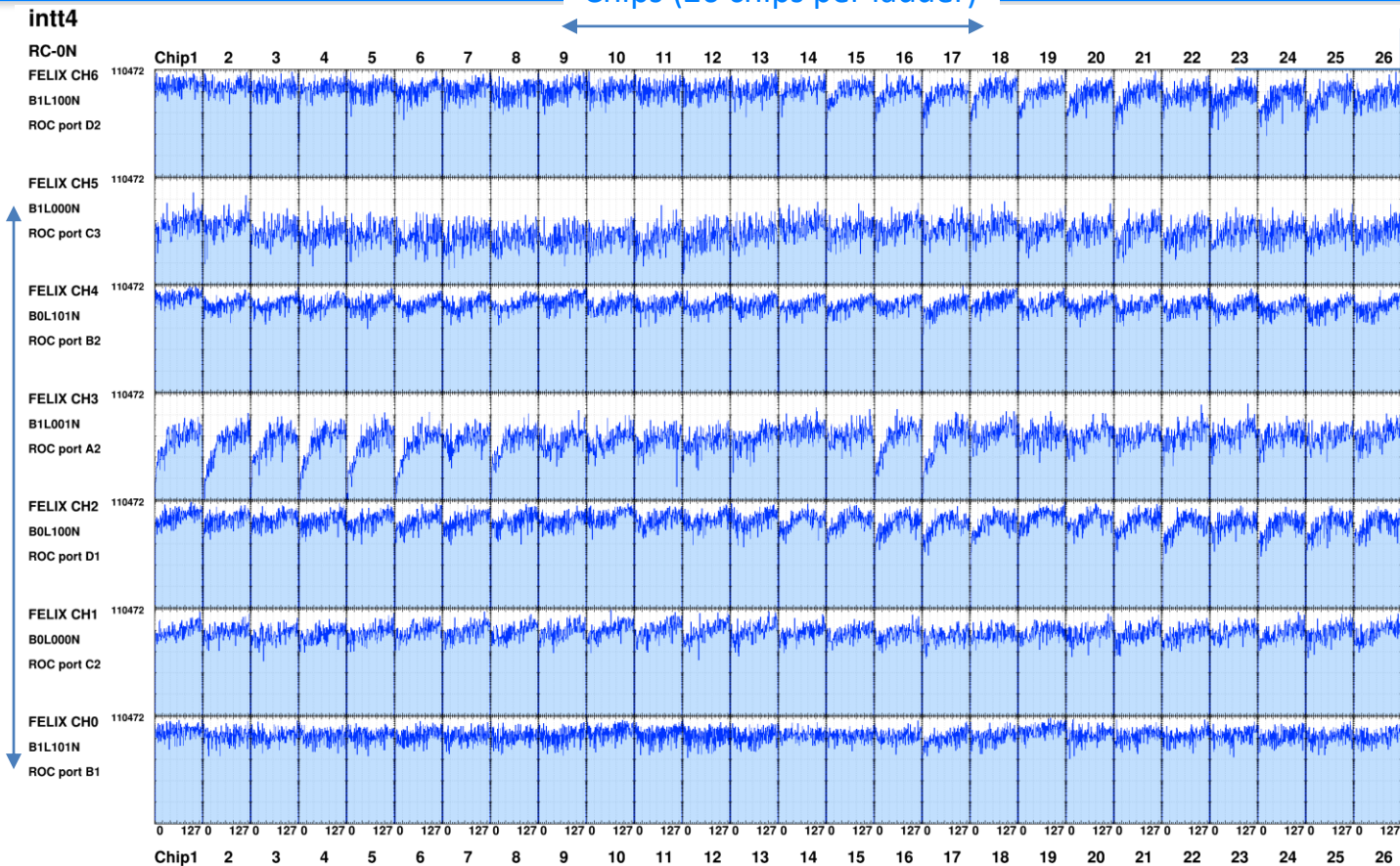
Status: ROC-0N: same as Run-23



Chips (26 chips per ladder)

ROC-0N
(intt4-Felix)
Pedestal Data
Run#-00032376
(DAC0=10)

Ladder# ID
(7 ladders per ROC)



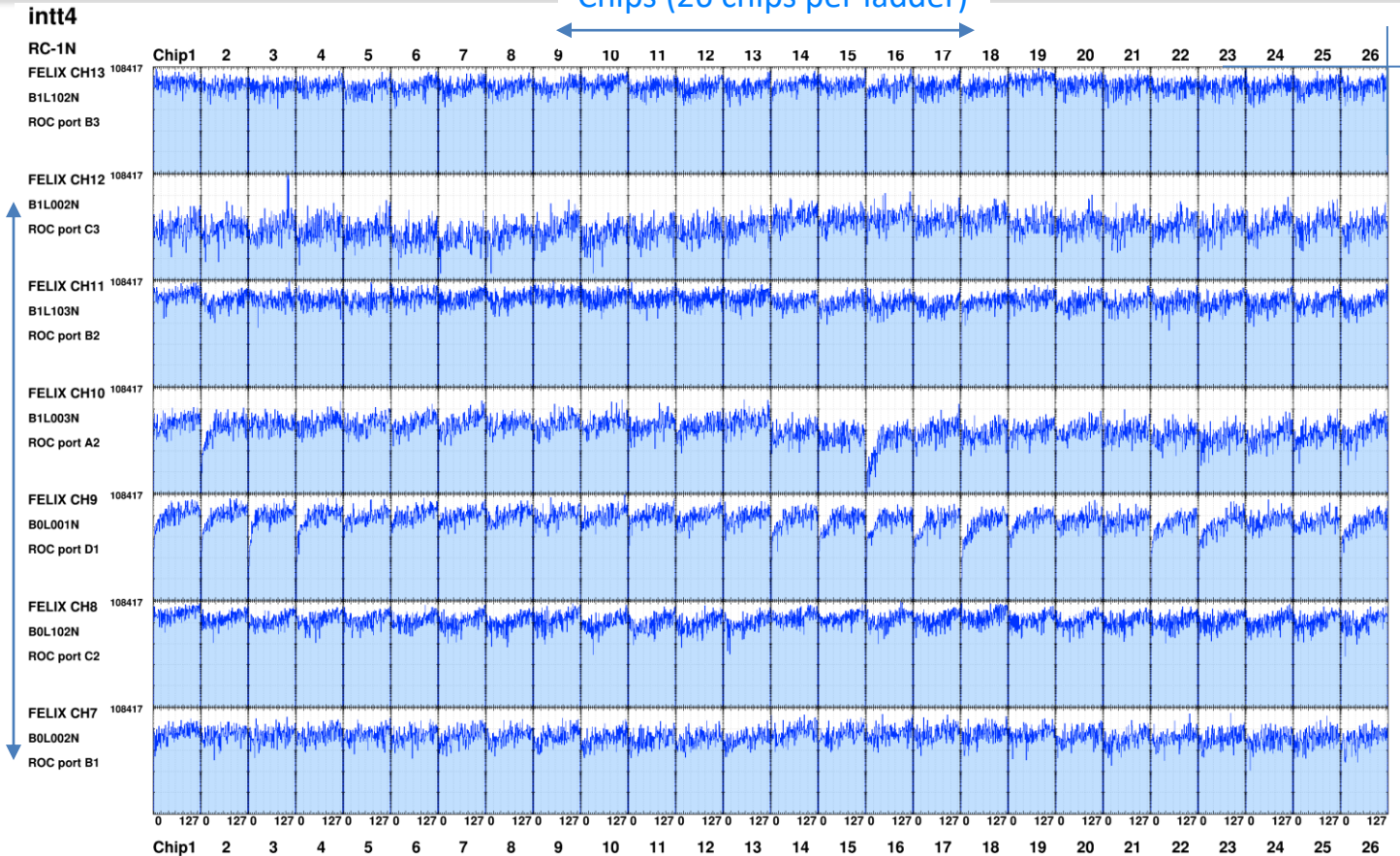
Status: ROC-1N: same as Run-23



Chips (26 chips per ladder)

ROC-1N
(intt4-Felix)
Pedestal Data
Run#-00032376
(DAC0=10)

Ladder# ID
(7 ladders per ROC)



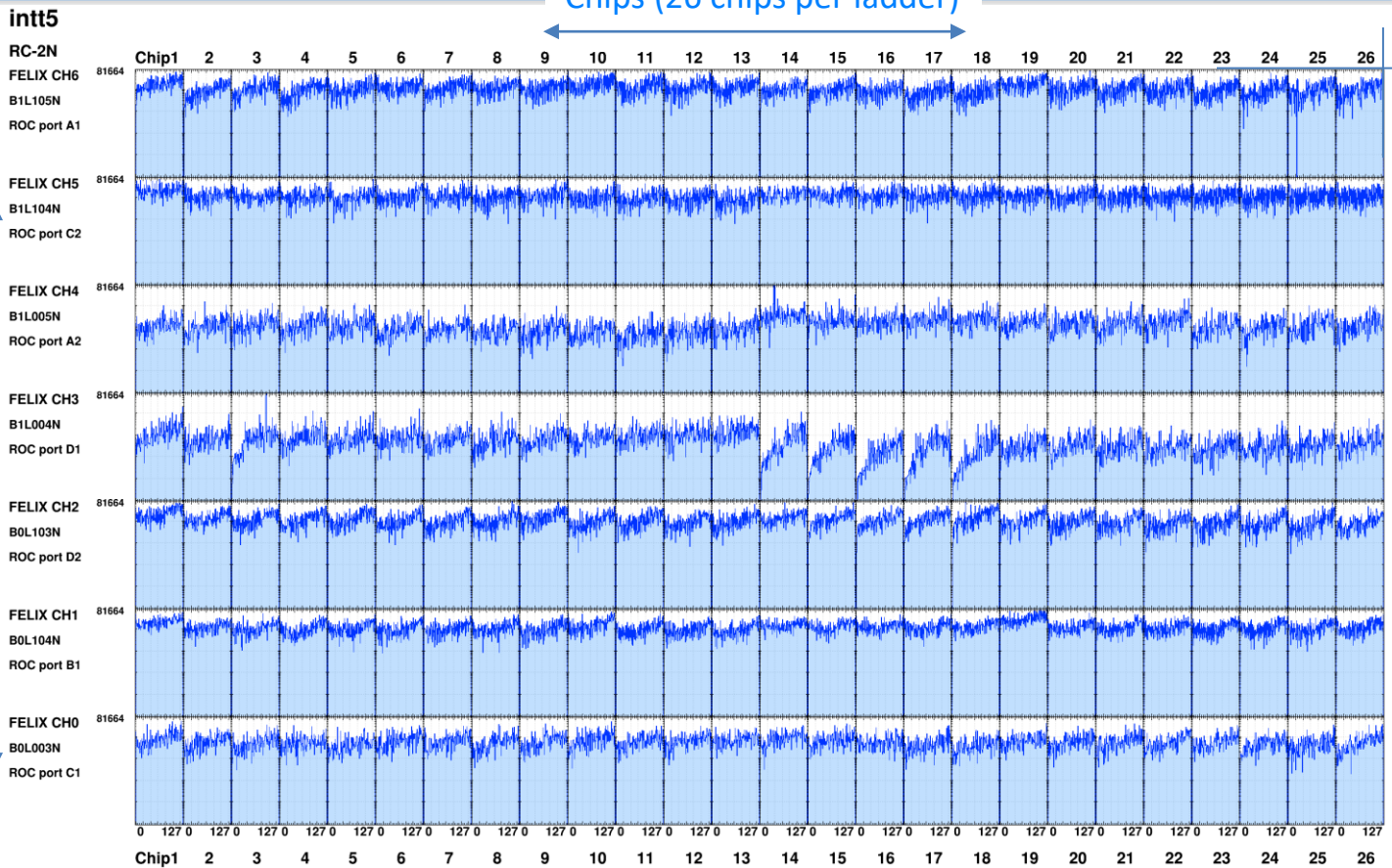
Status: ROC-2N: same as Run-23



Chips (26 chips per ladder)

ROC-2N
(intt5-Felix)
Pedestal Data
Run#-00032376
(DAC0=10)

Ladder# ID
(7 ladders per ROC)



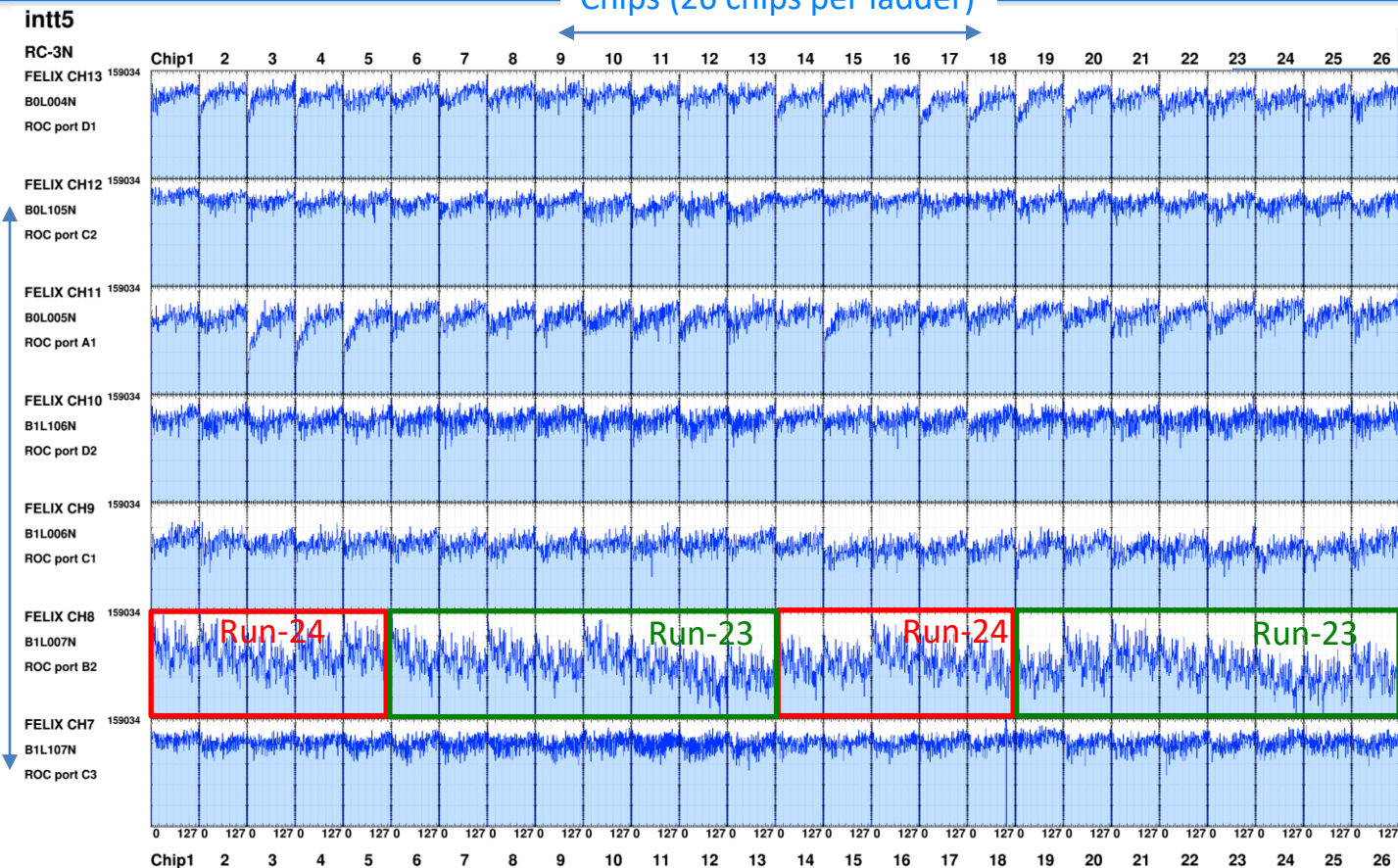
Status: ROC-3N: We lost one Sensor (Type-B 10 chips)



Chips (26 chips per ladder)

ROC-3N
(intt5-Felix)
Pedestal Data
Run#-00032376
(DAC0=10)

Ladder# ID
(7 ladders per ROC)



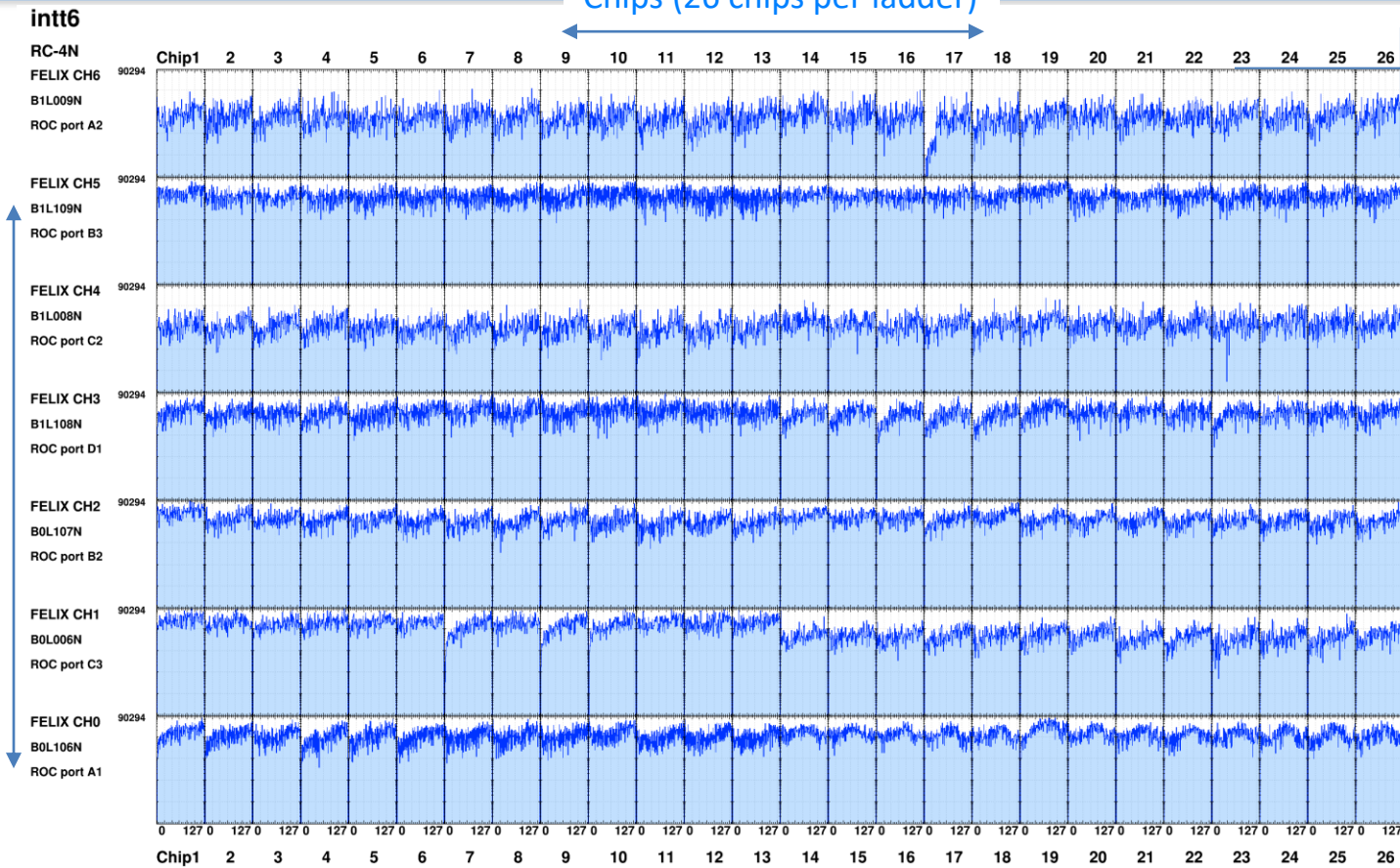
Status: ROC-4N: better than Run-23



Chips (26 chips per ladder)

ROC-4N
(intt6-Felix)
Pedestal Data
Run#-00032376
(DAC0=10)

Ladder# ID
(7 ladders per ROC)



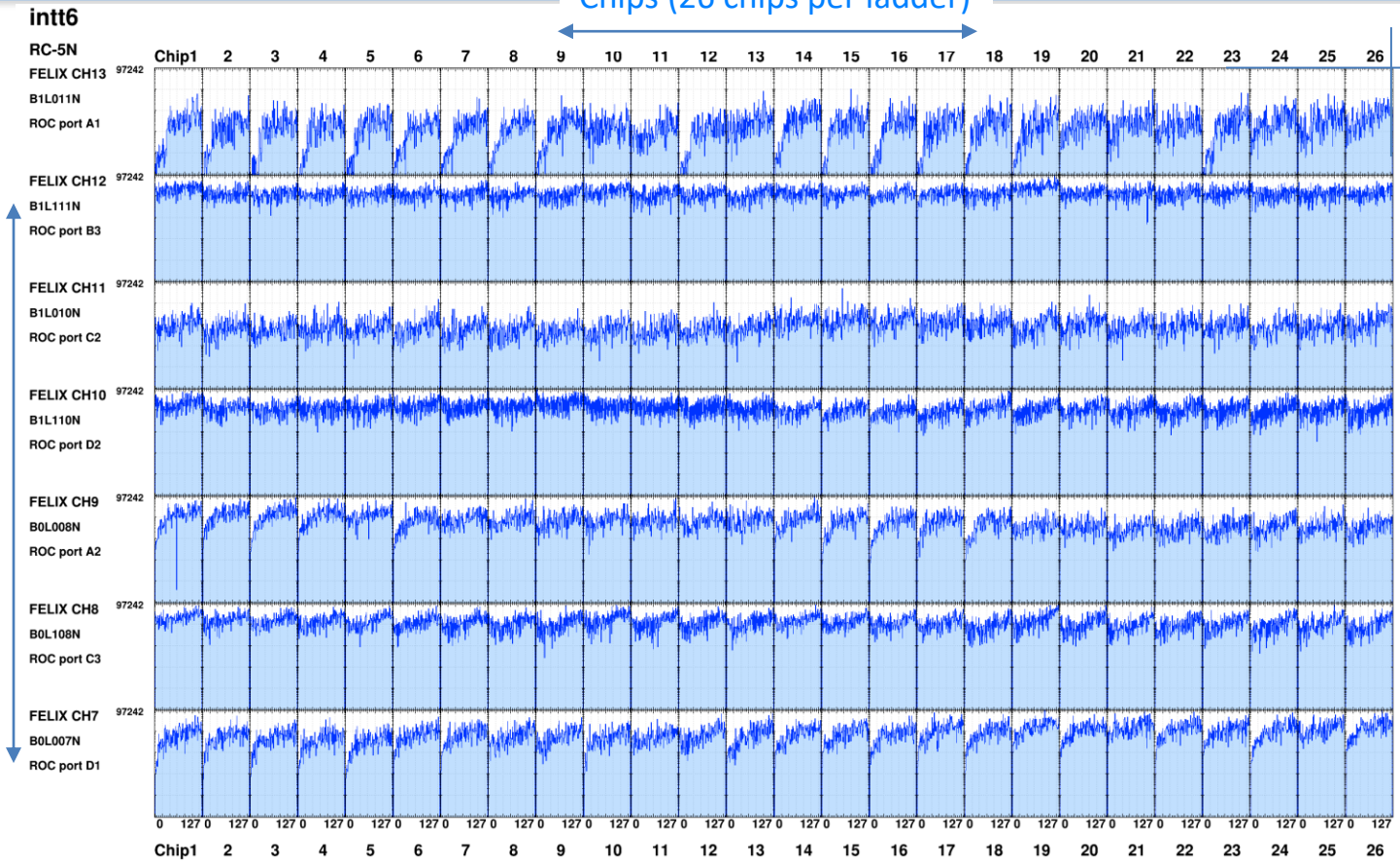
Status: ROC-5N: same as Run-23



Chips (26 chips per ladder)

ROC-5N
(intt6-Felix)
Pedestal Data
Run#-00032376
(DAC0=10)

Ladder# ID
(7 ladders per ROC)



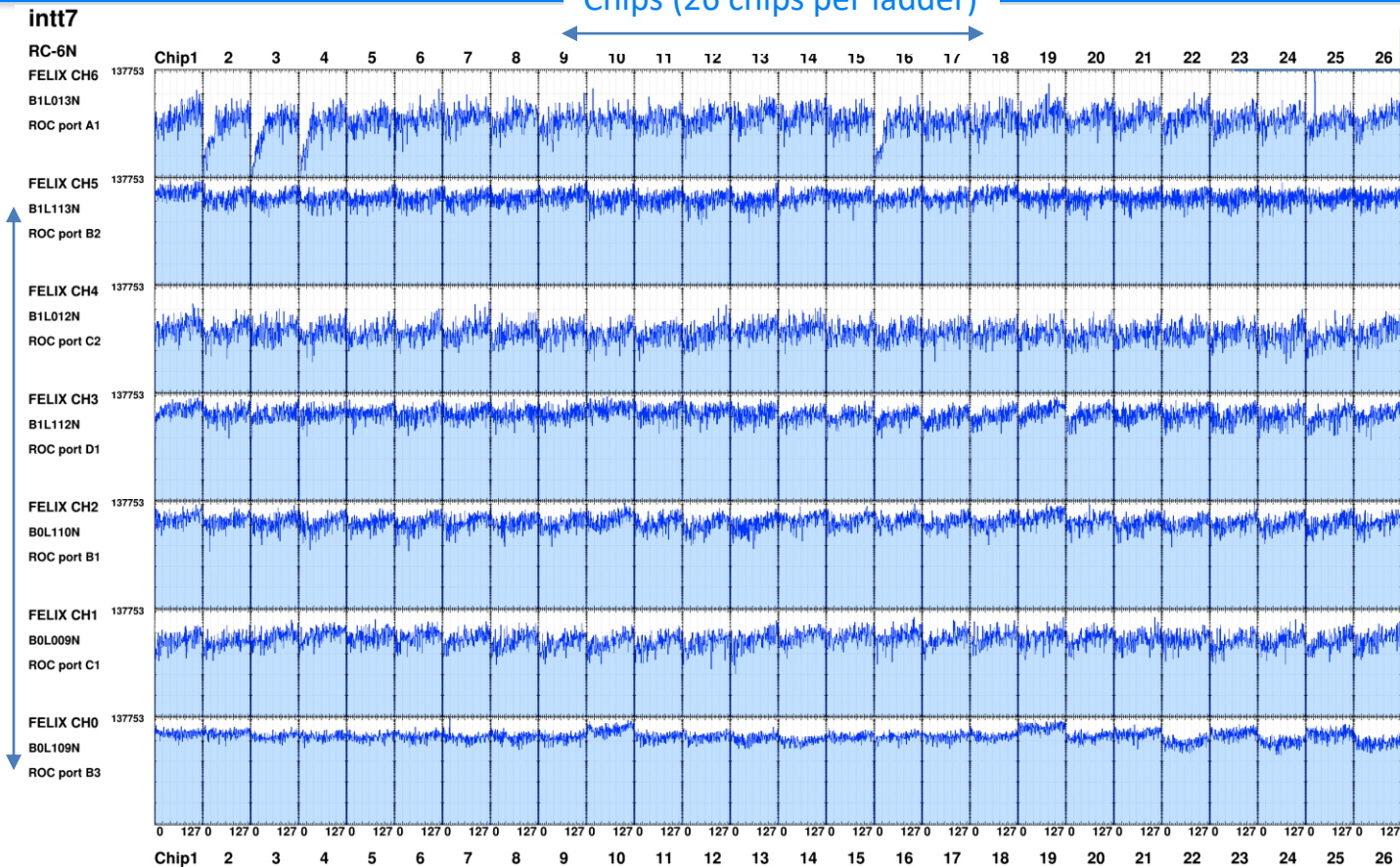
Status: ROC-6N: same as Run-23



Chips (26 chips per ladder)

ROC-6N
(intt7-Felix)
Pedestal Data
Run#-00032376
(DAC0=10)

Ladder# ID
(7 ladders per ROC)

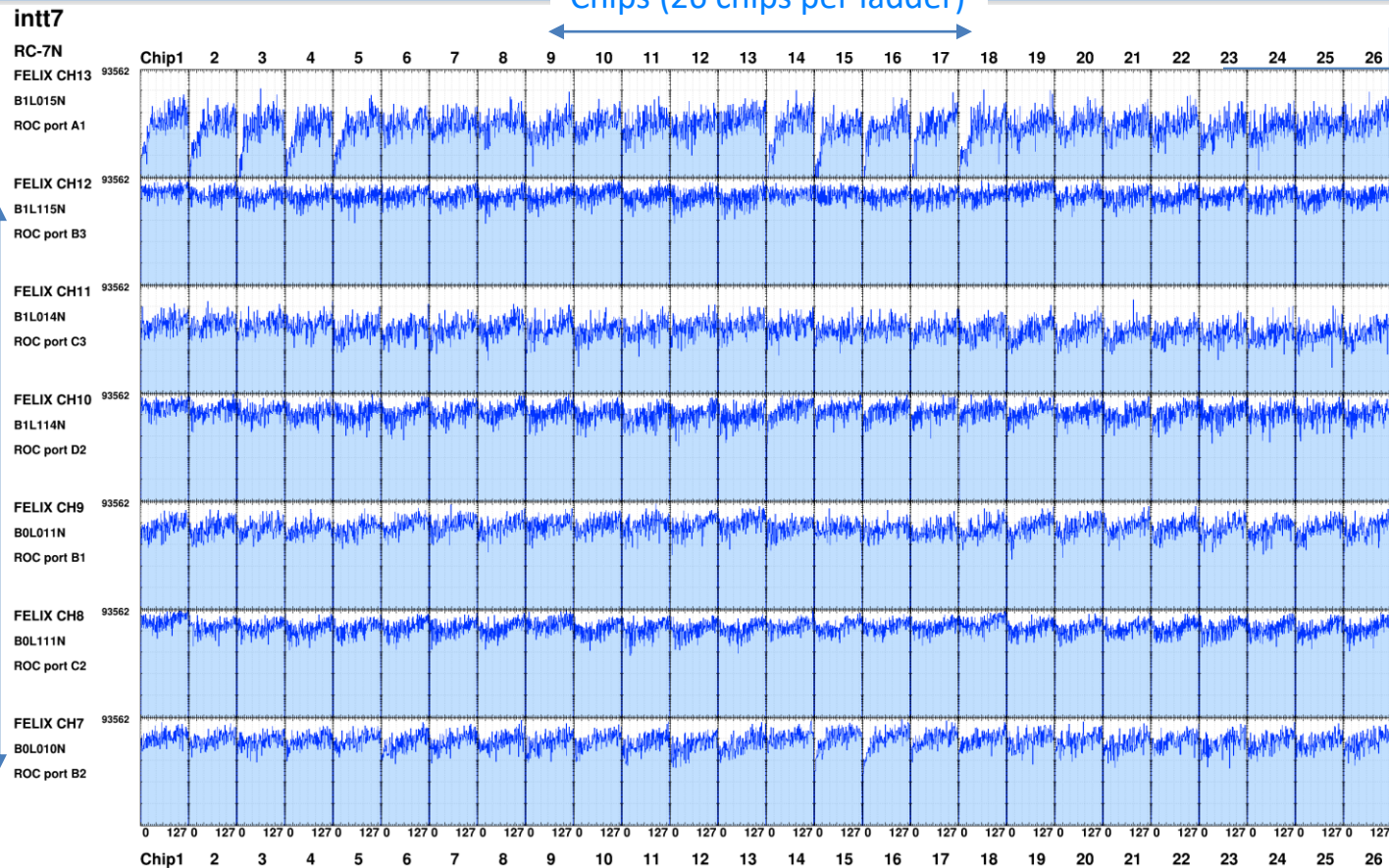


Status: ROC-7N: same as Run-23

Chips (26 chips per ladder)

ROC-7N
(intt7-Felix)
Pedestal Data
Run#-00032376
(DAC0=10)

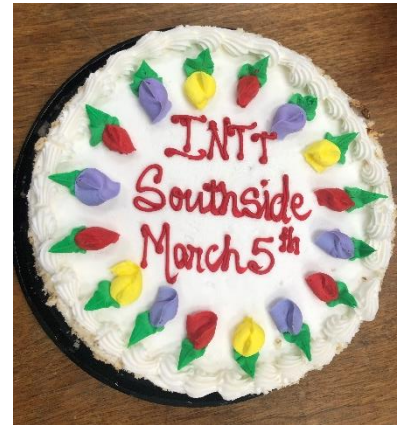
Ladder# ID
(7 ladders per ROC)



INTT Team Commissioning South Side

Kazuma F., Takahiro K., Jaein H., Raul C., Maya S., Akitomo E., Jeffrey H., Daniel C., Robert P., Stephen B., Genki N. and Rachid N.
March 4th, 2024 (completed):

- ❖ All eight ROCs installed and tested (pedestal data), see next slides for results,
→ after debugging, all ladders show identical results as Run-23 (99.1% live chips in total of the INTT southside).



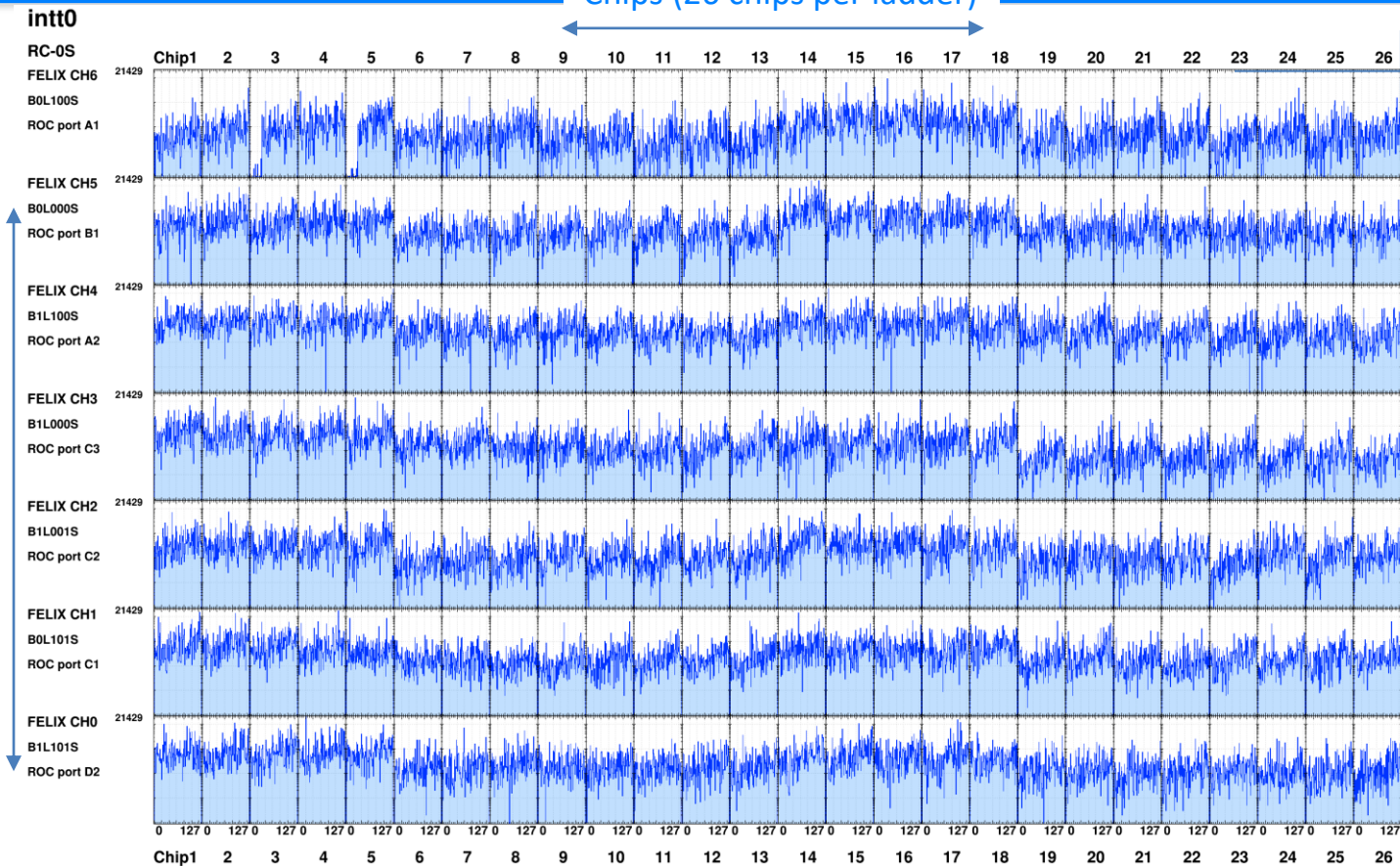
Status: ROC-0S: same as Run-23



Chips (26 chips per ladder)

ROC-0S
(intt0-Felix)
Pedestal Data
Run#-00031686
(DAC0=10)

Ladder# ID
(7 ladders per ROC)



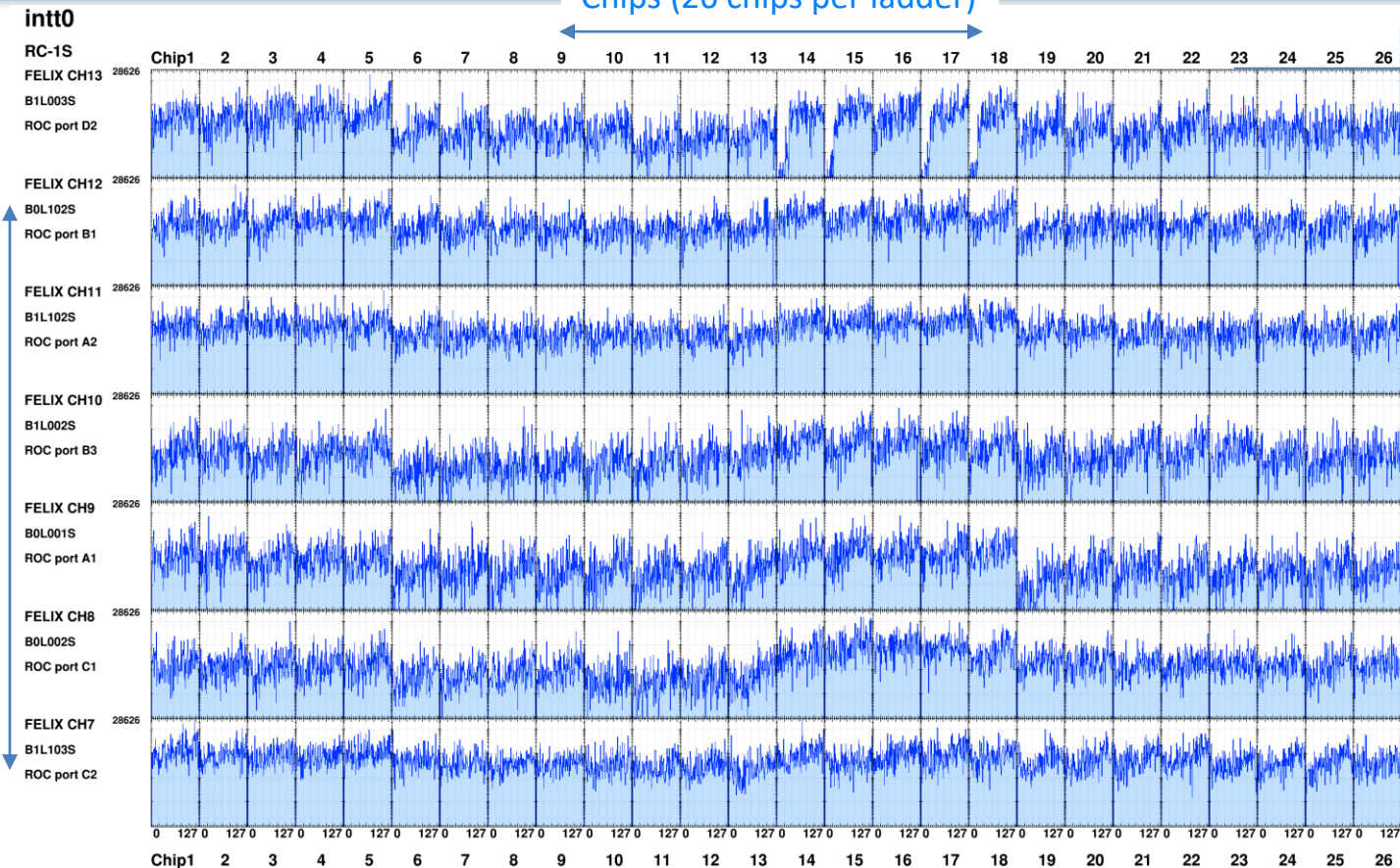
Status: ROC-1S: same as Run-23



Chips (26 chips per ladder)

ROC-1S
(intt0-Felix)
Pedestal Data
Run#-00031686
(DAC0=10)

Ladder# ID
(7 ladders per ROC)



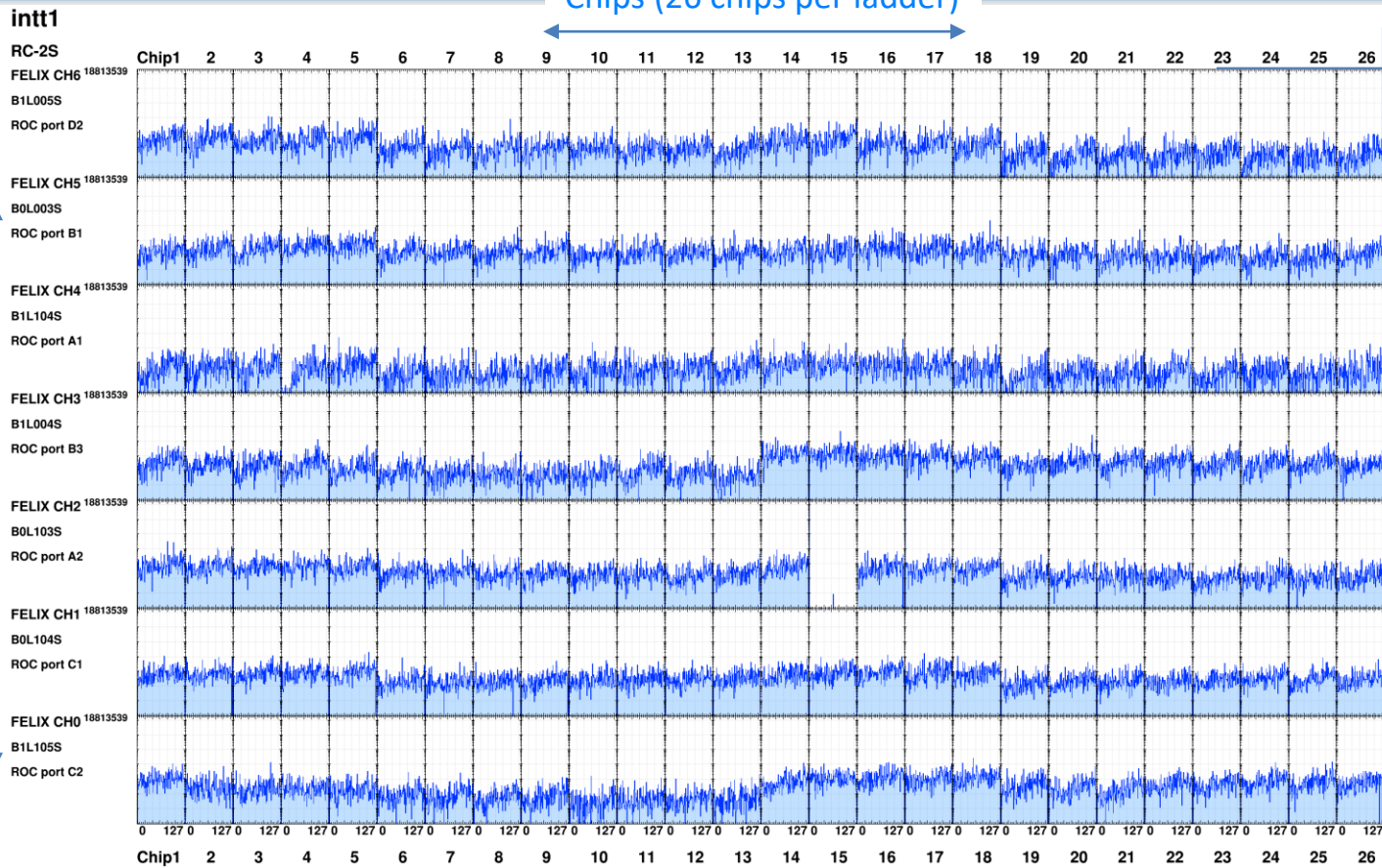
Status: ROC-2S: same as Run-23



Chips (26 chips per ladder)

ROC-2S
(intt1-Felix)
Pedestal Data
Run#-00031686
(DAC0=10)

Ladder# ID
(7 ladders per ROC)

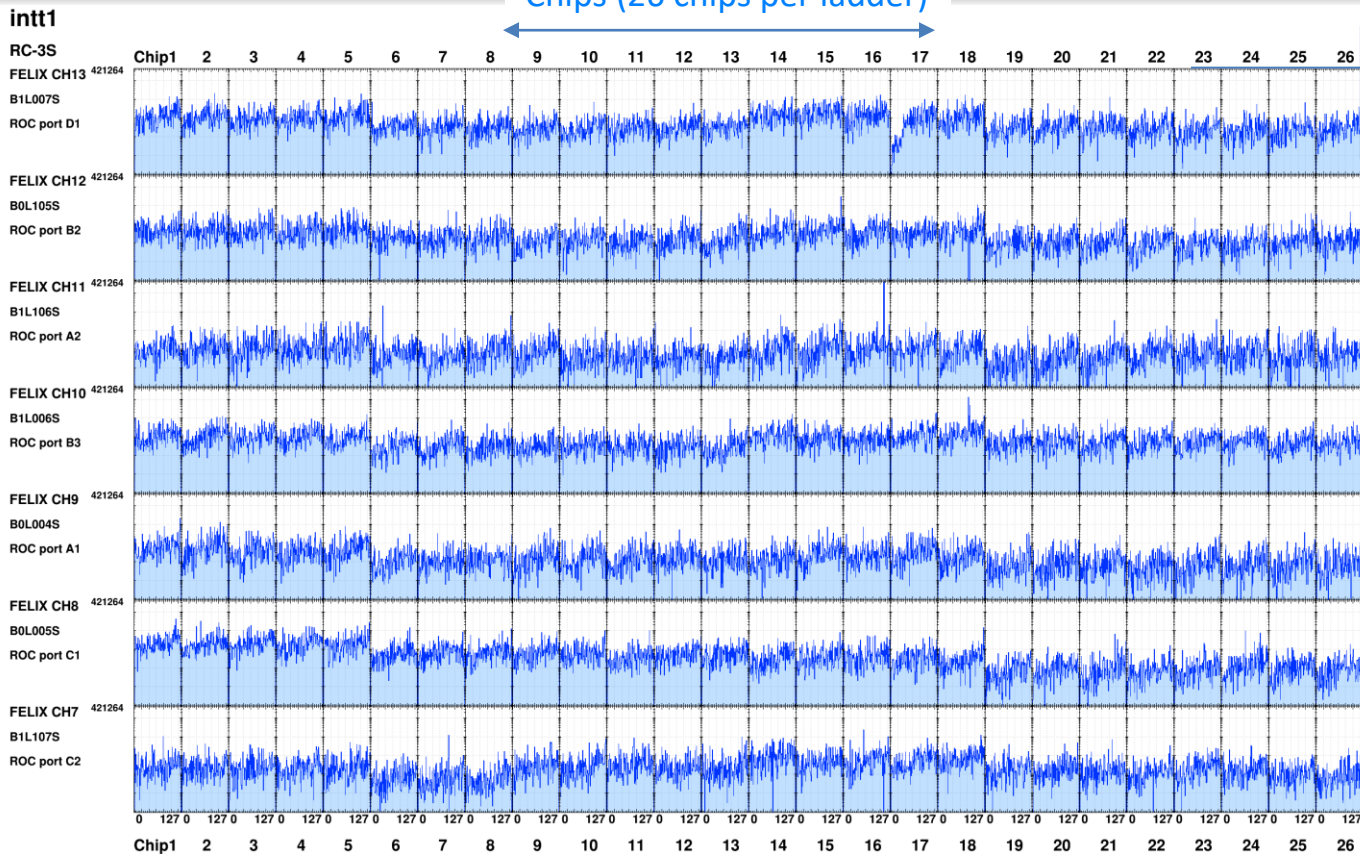


Status: ROC-3S: same as Run-23



Chips (26 chips per ladder)

ROC-3S
(intt1-Felix)
Pedestal Data
Run#-00031686
(DAC0=10)



Ladder# ID
(7 ladders per ROC)

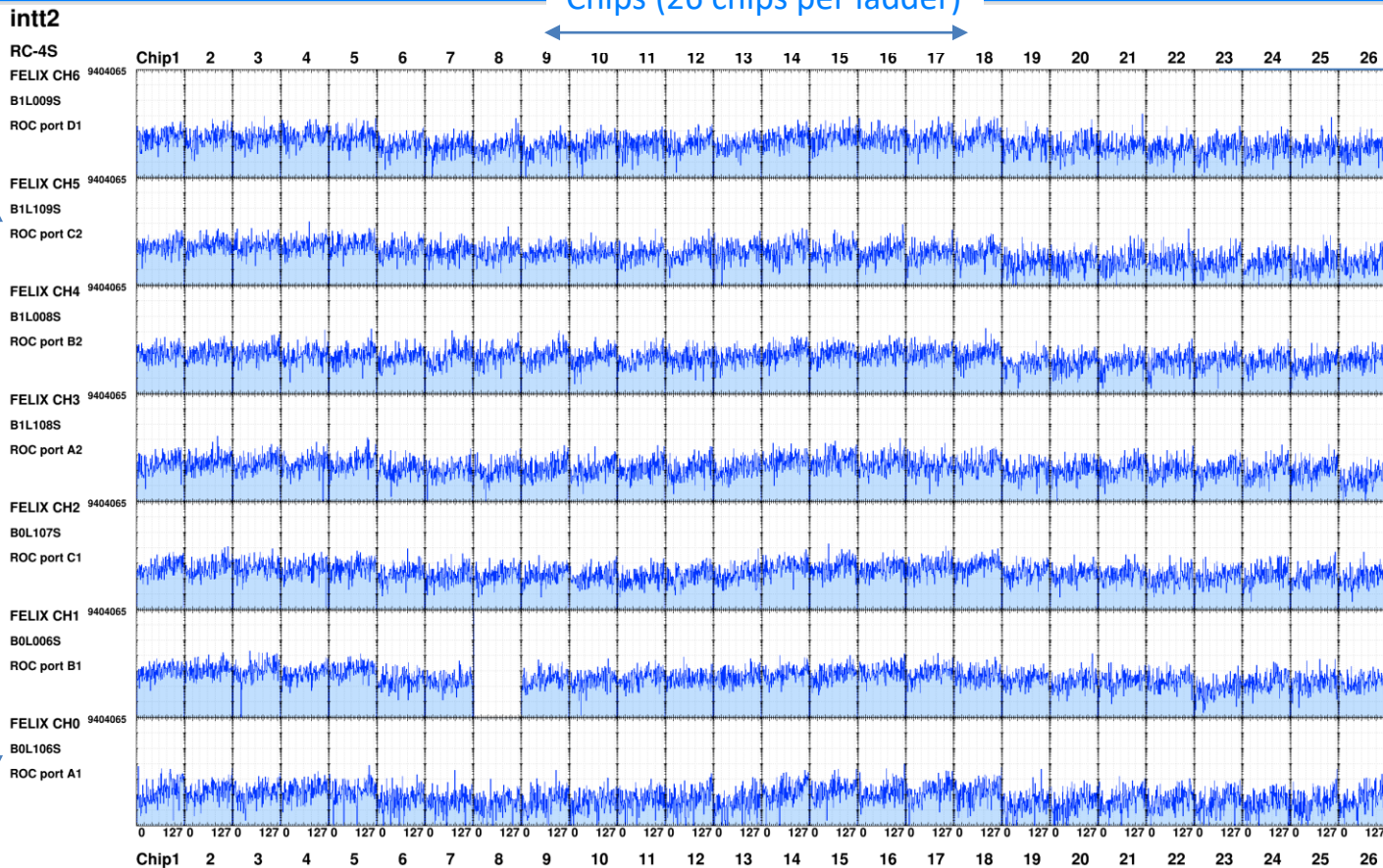
Status: ROC-4S: same as Run-23



Chips (26 chips per ladder)

ROC-4S
(intt2-Felix)
Pedestal Data
Run#-00031686
(DAC0=10)

Ladder# ID
(7 ladders per ROC)



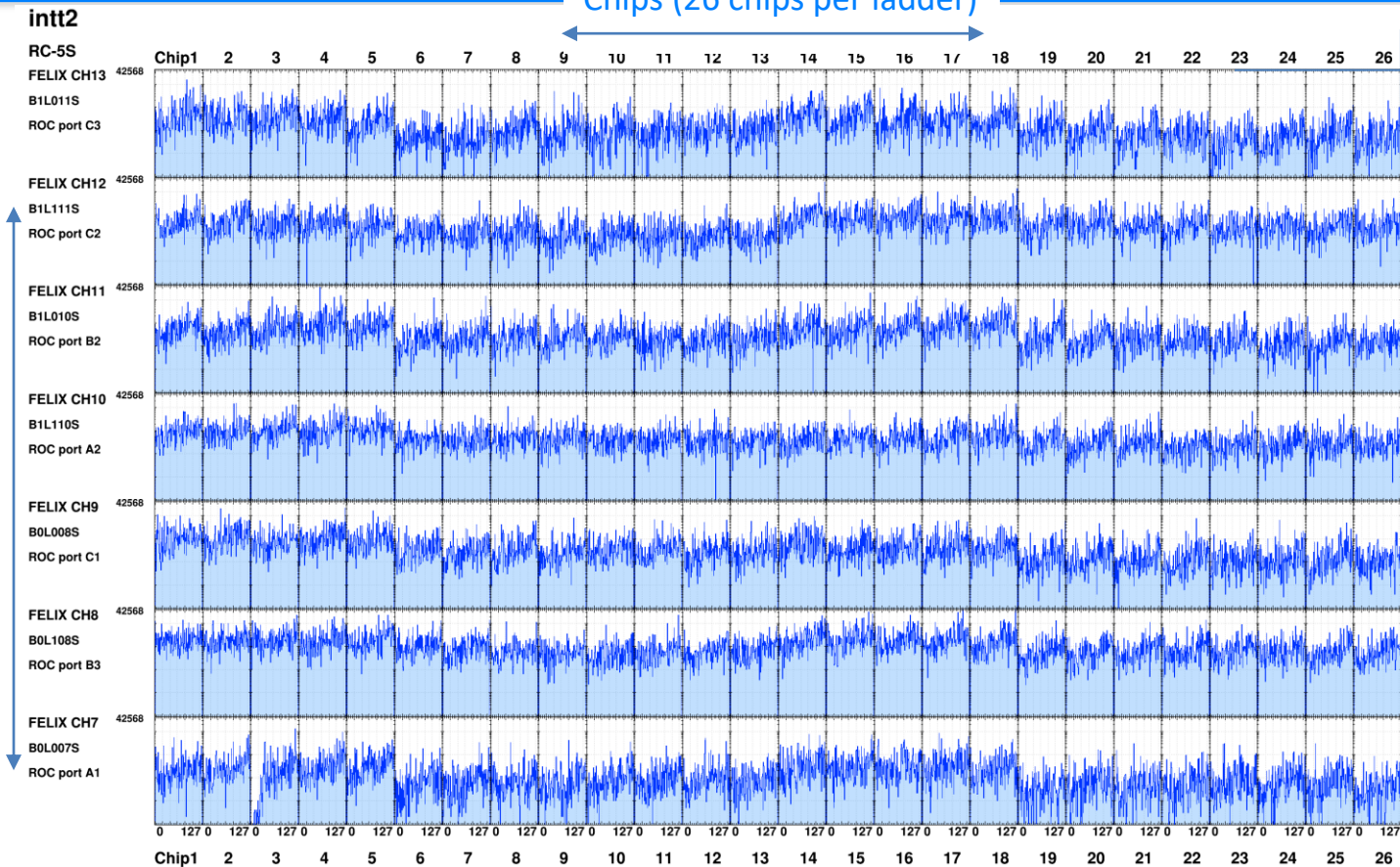
Status: ROC-5S: same as Run-23



Chips (26 chips per ladder)

ROC-5S
(intt2-Felix)
Pedestal Data
Run#-00031686
(DAC0=10)

Ladder# ID
(7 ladders per ROC)

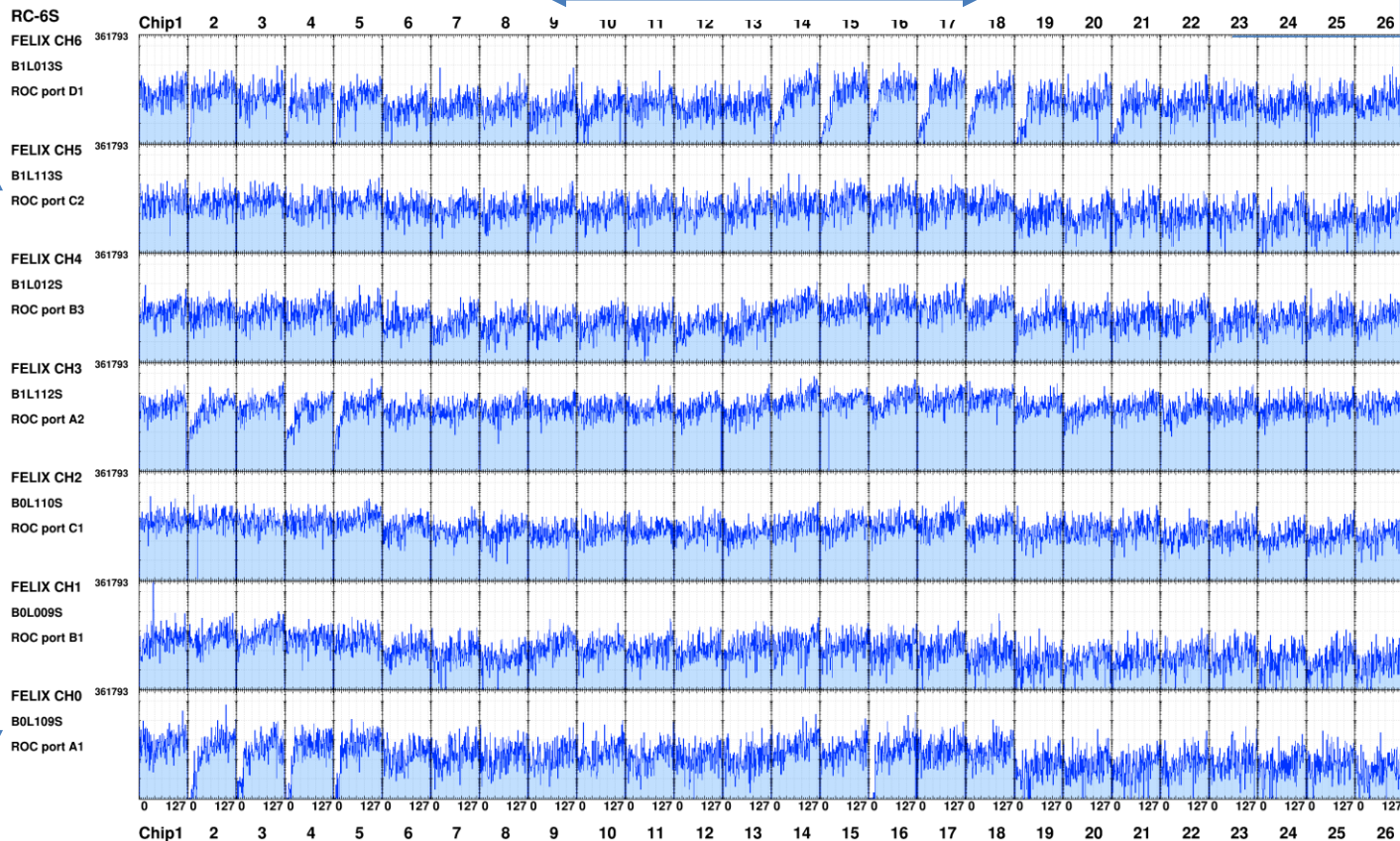


Status: ROC-6S: same as Run-23



Chips (26 chips per ladder)

intt3



ROC-6S
(intt3-Felix)
Pedestal Data
Run#-00031686
(DAC0=10)

Ladder# ID
(7 ladders per ROC)

Status: ROC-7S: same as Run-23

Chips (26 chips per ladder)

ROC-7S
(intt3-Felix)
Pedestal Data
Run#-00031686
(DAC0=10)

Ladder# ID
(7 ladders per ROC)

