

Status of the eRD109 “RDO/Timing/Service Hybrid”

EPIC DAQ Meeting, 04-Apr-2024

Wei Li, Mike Matveev, Tonko Ljubicic (Rice University)

William Gu (Jlab), as an external contributor and interested party

Zhenyu Ye (LBL)

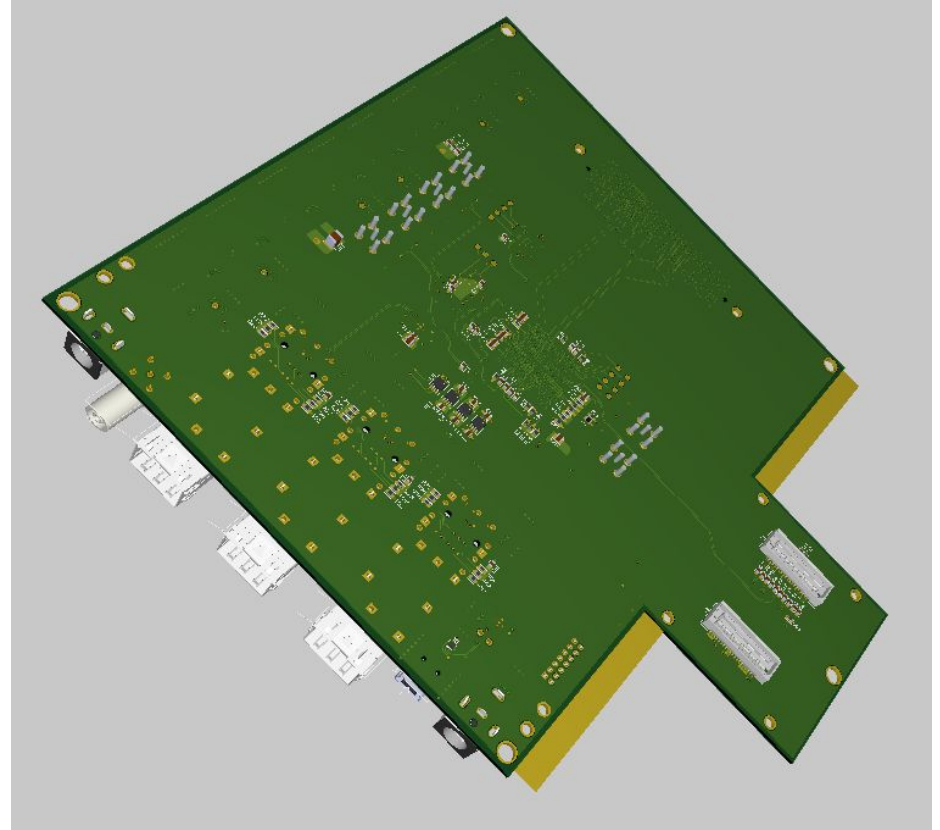
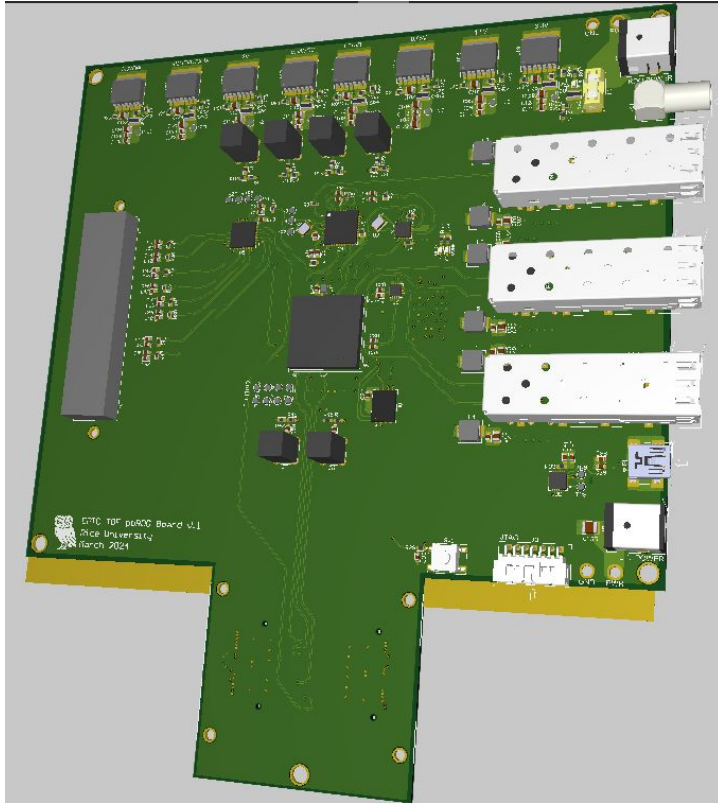
Prithwish Tribedy, Prashanth Shanmuganathan (BNL)

Status as of today

	Feb 1, 2024	Mar 7, 2024	Apr 4, 2024 (today)	In Proposal
Schematics	90%	100%	100%	Jun 1
0th firmware	90%	100%	100%	–
Purchase long-lead items	0%	100% FPGA 90% others	100%	Jun 1
Board layout	50%	90%	100%	–
PCB design	0%	50% (external vendor)	100%	Aug 1
Boards complete	–	~end April	Apr 18 (expected) 6 boards	Sep 31

⇒ comfortably ahead of schedule

ppRDO



Conclusion

- ahead of production schedule
- upcoming efforts
 - setting up firmware & development
 - William/JLab, Zhenyu/LBL, TL/Rice
 - setting up for Q&A, testing & timing measurements
 - Mike@Rice, William@JLab, Prithwish@BNL (but waiting for scope)
 - radiation tests down the road
 - Zhenyu/LBL, others TBD?