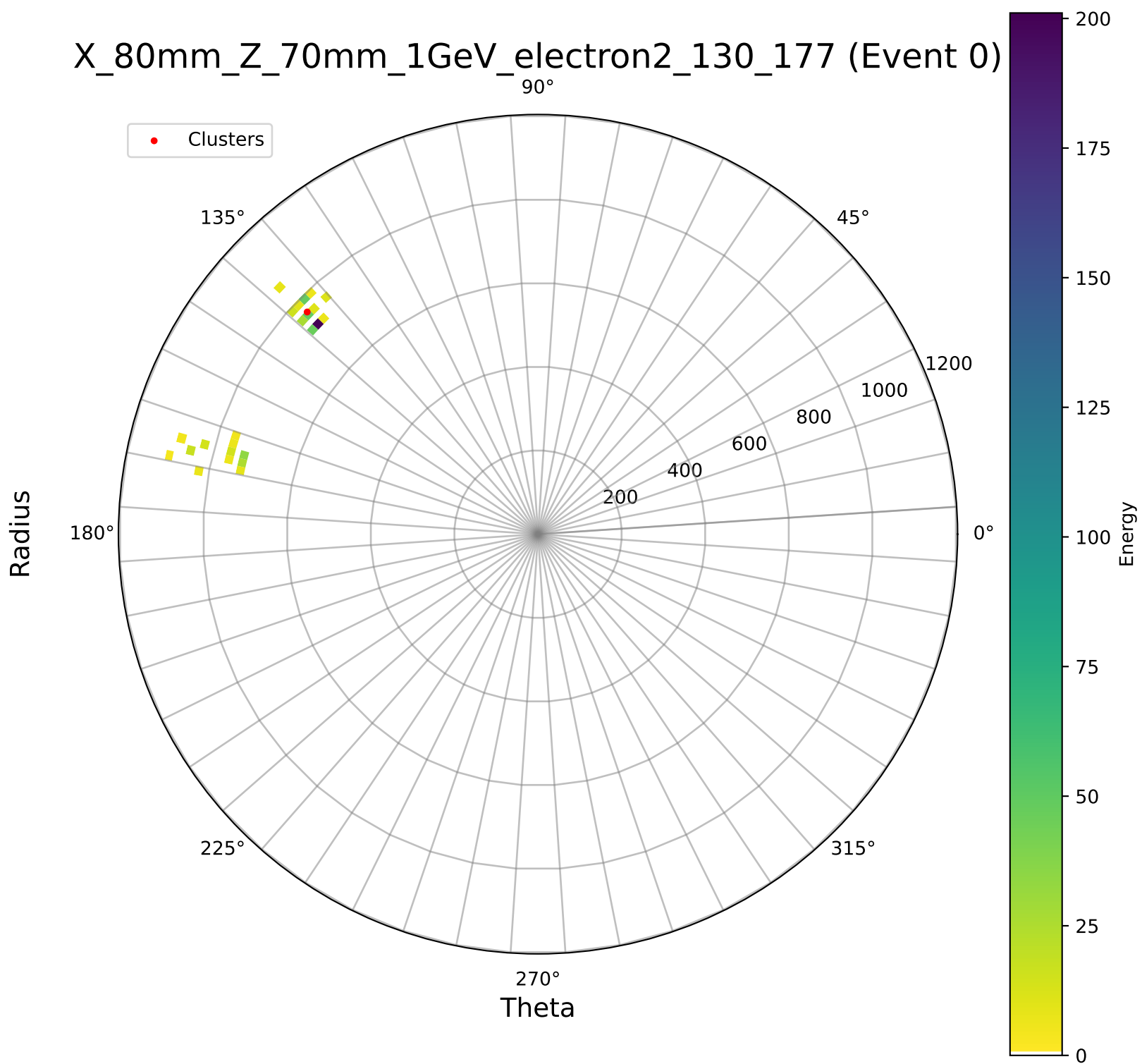
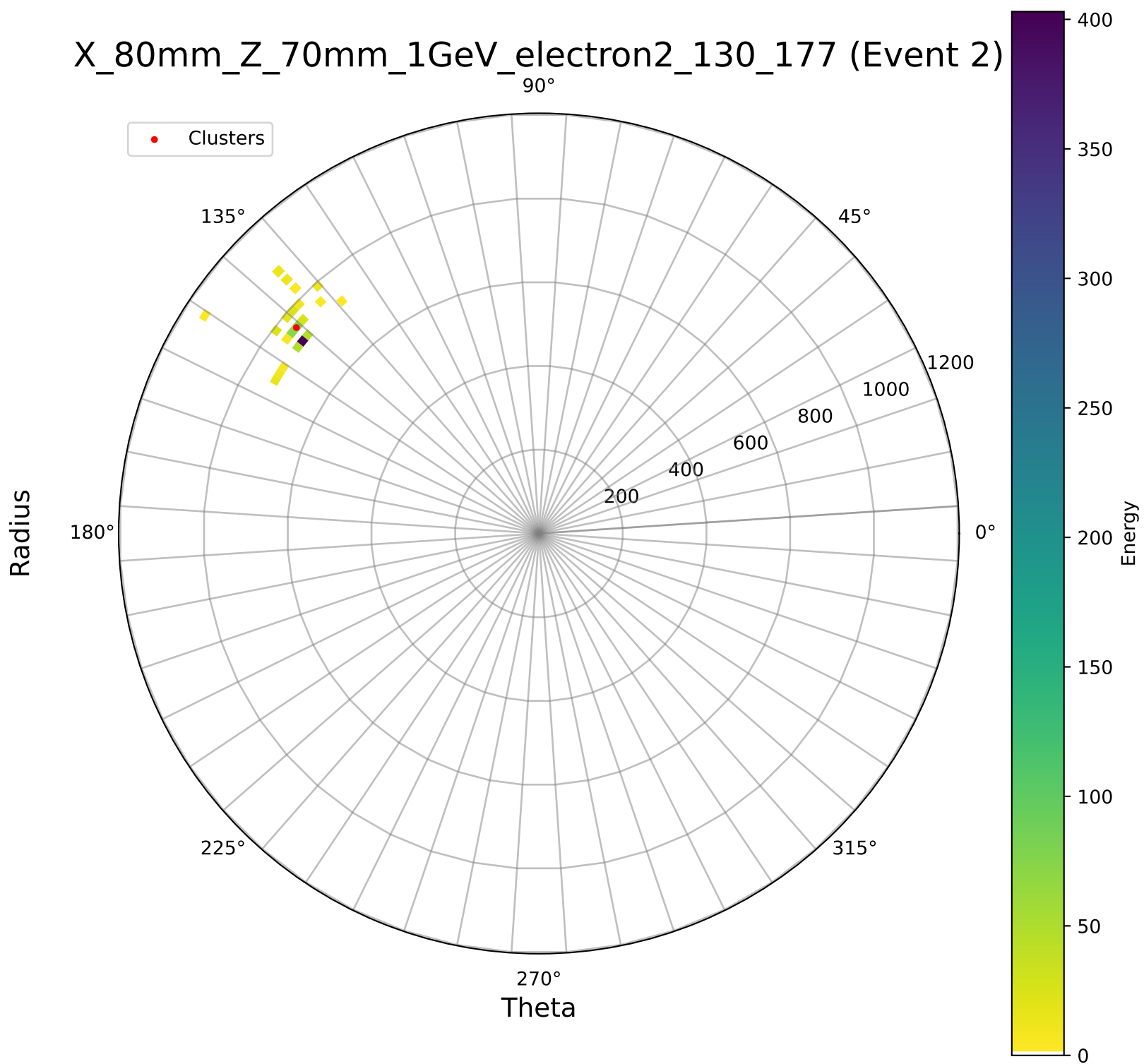


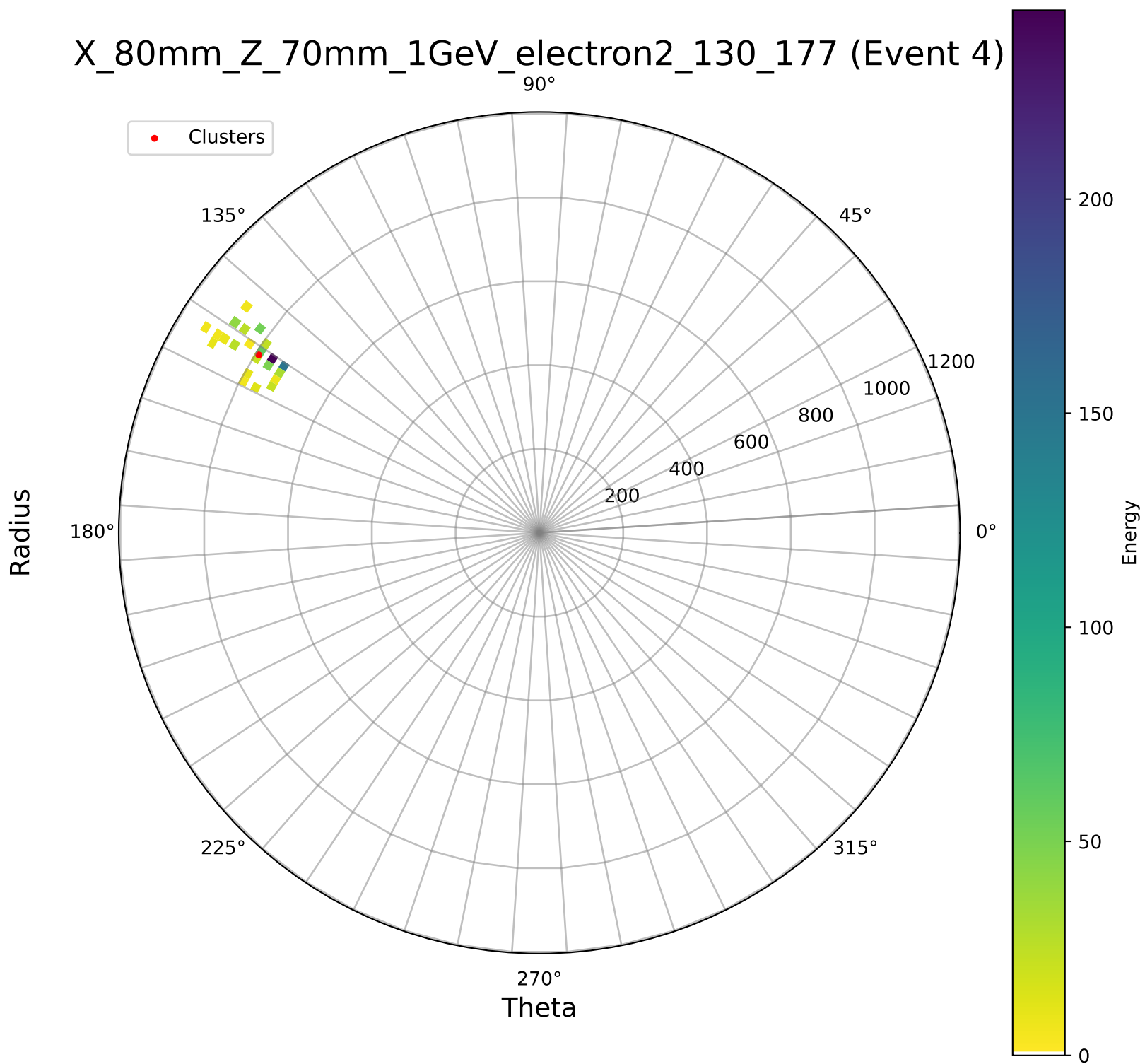
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 0)



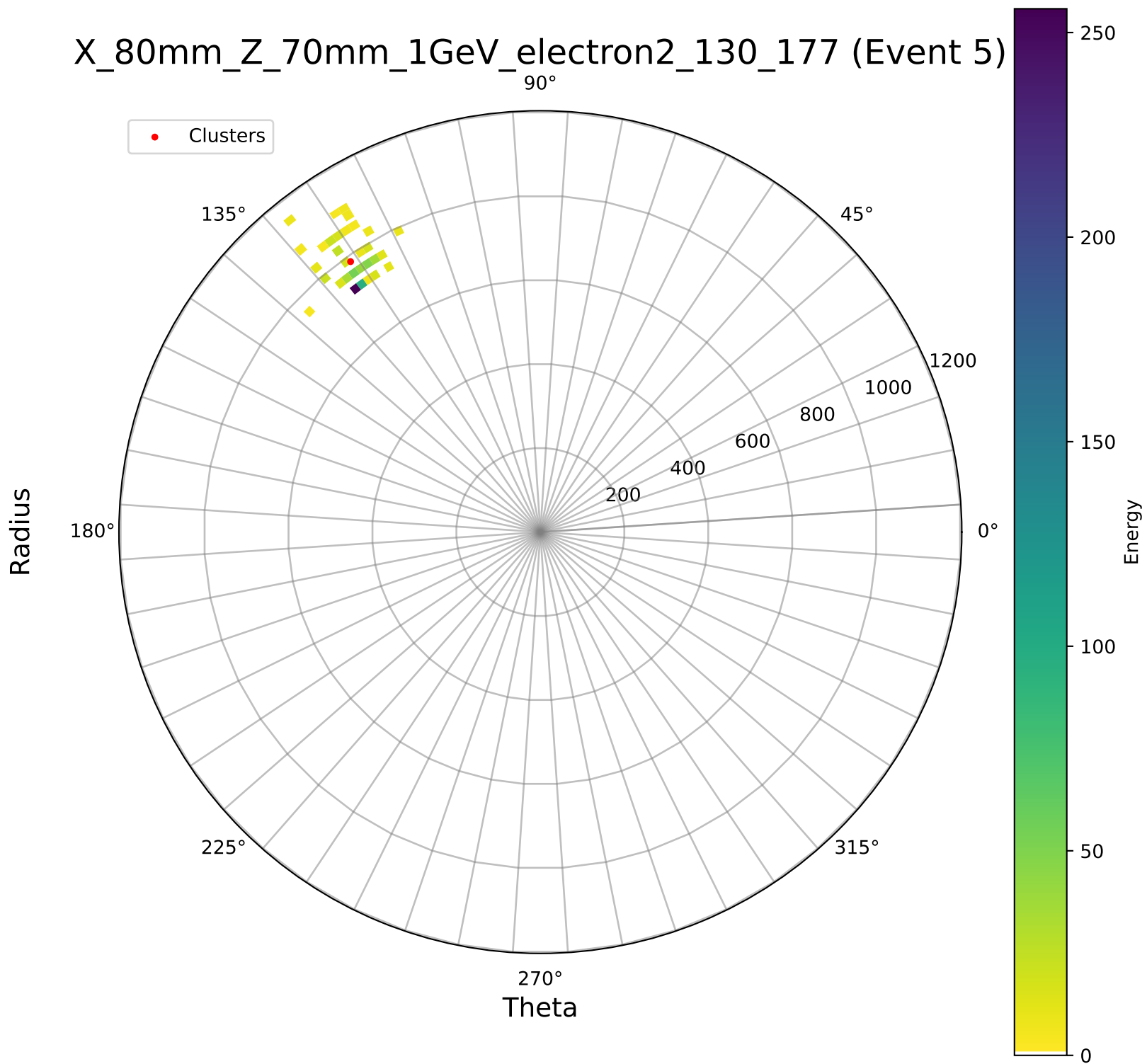
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 2)



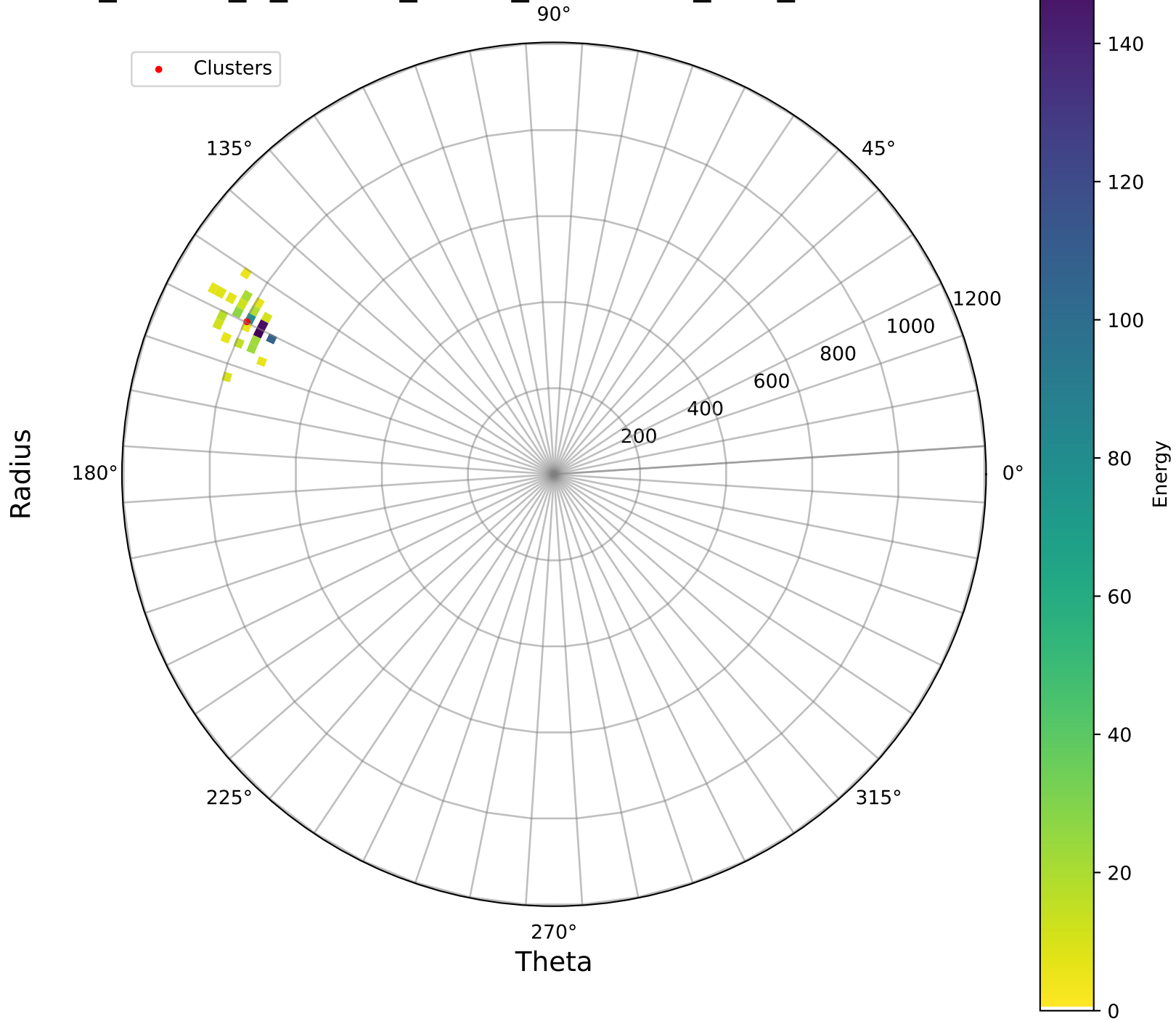
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 4)



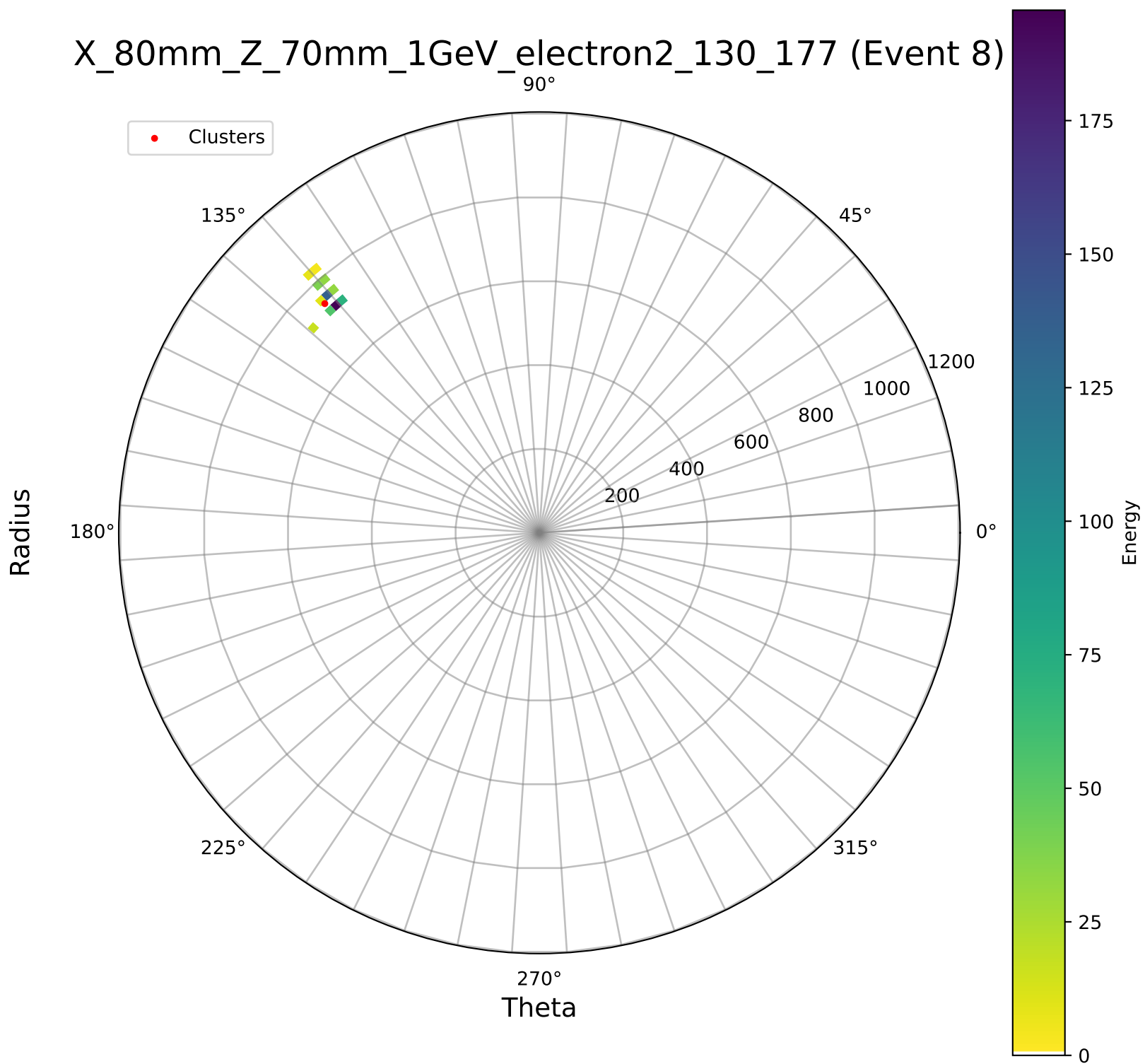
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 5)



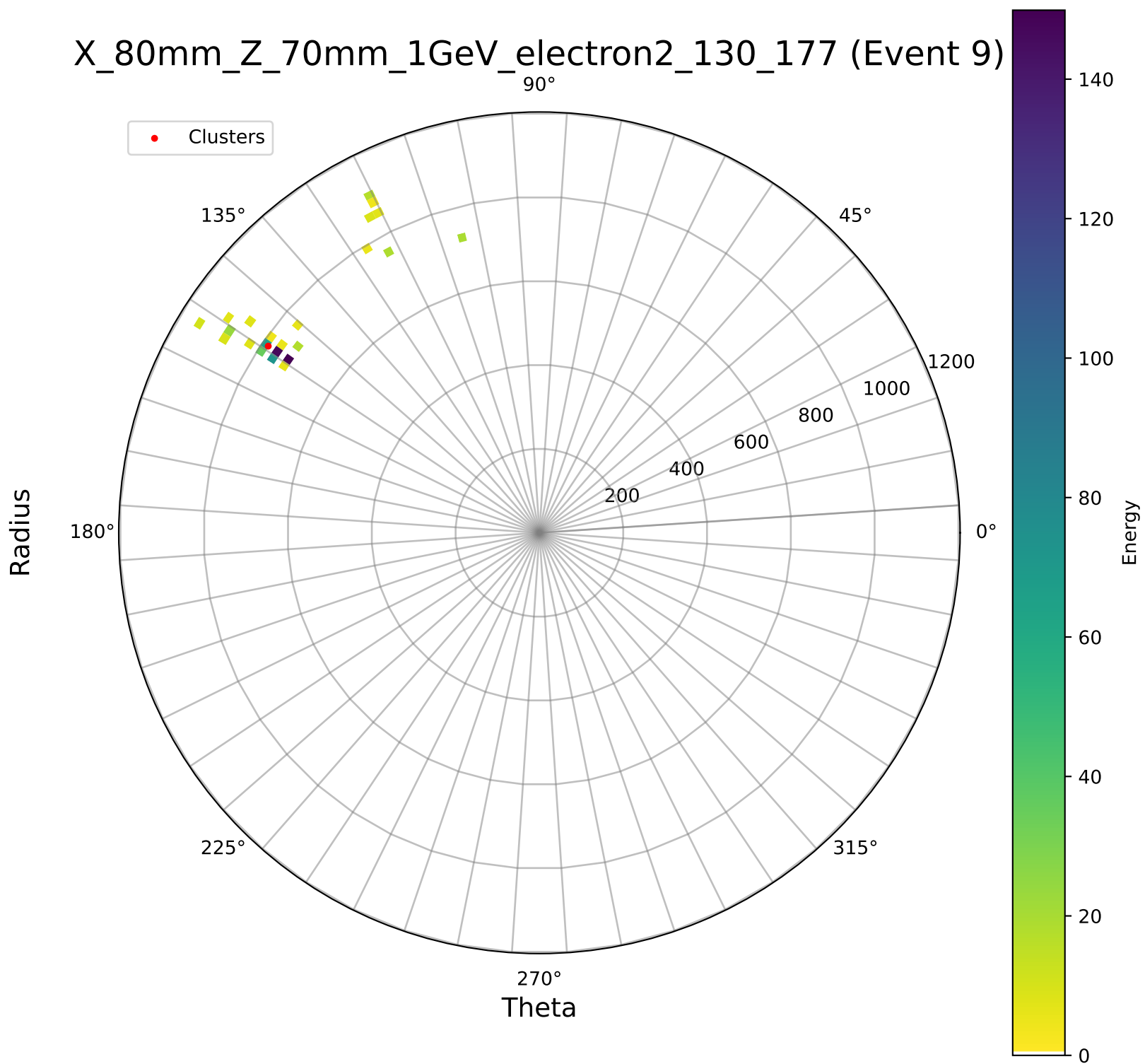
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 7)



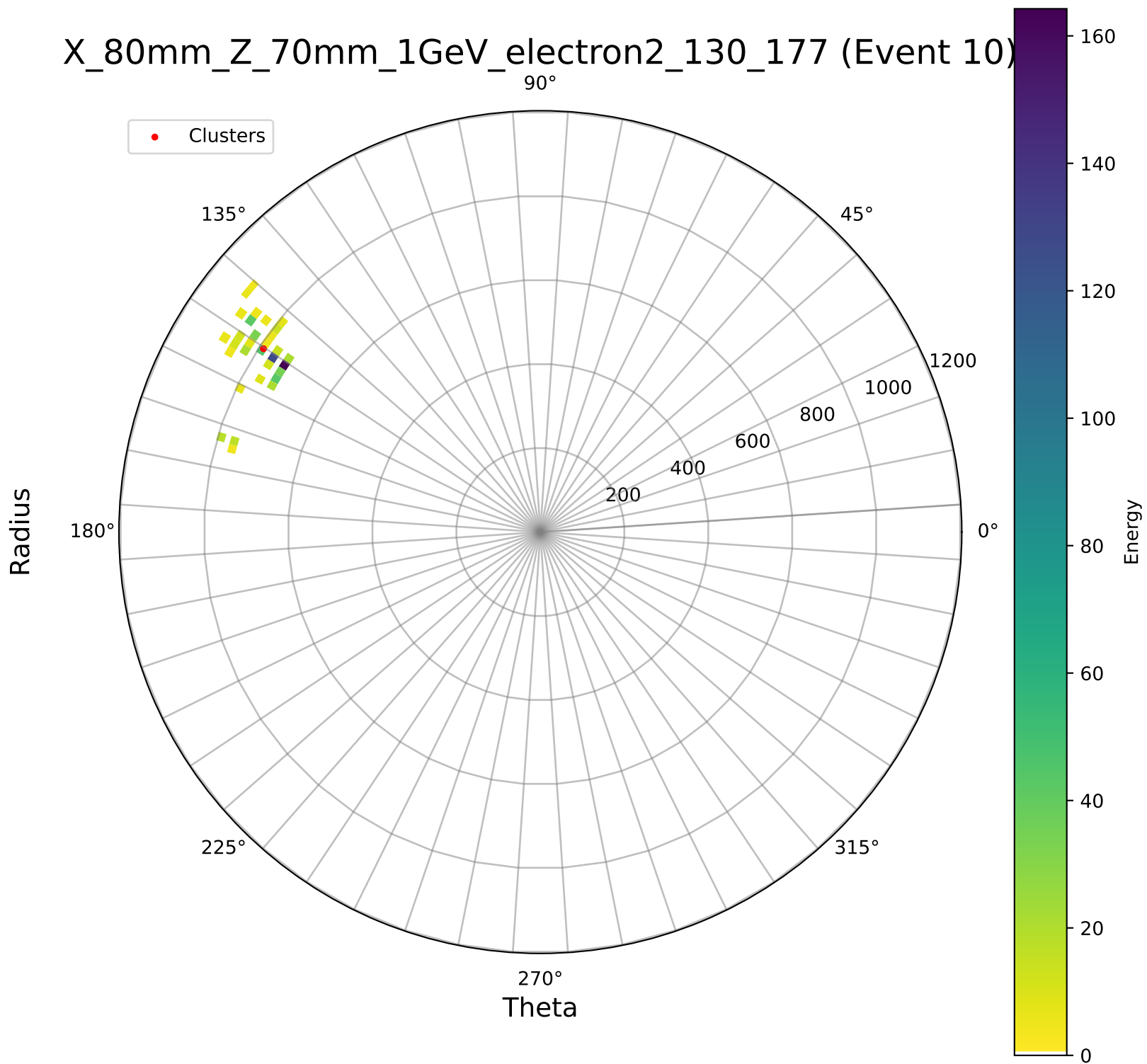
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 8)



# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 9)

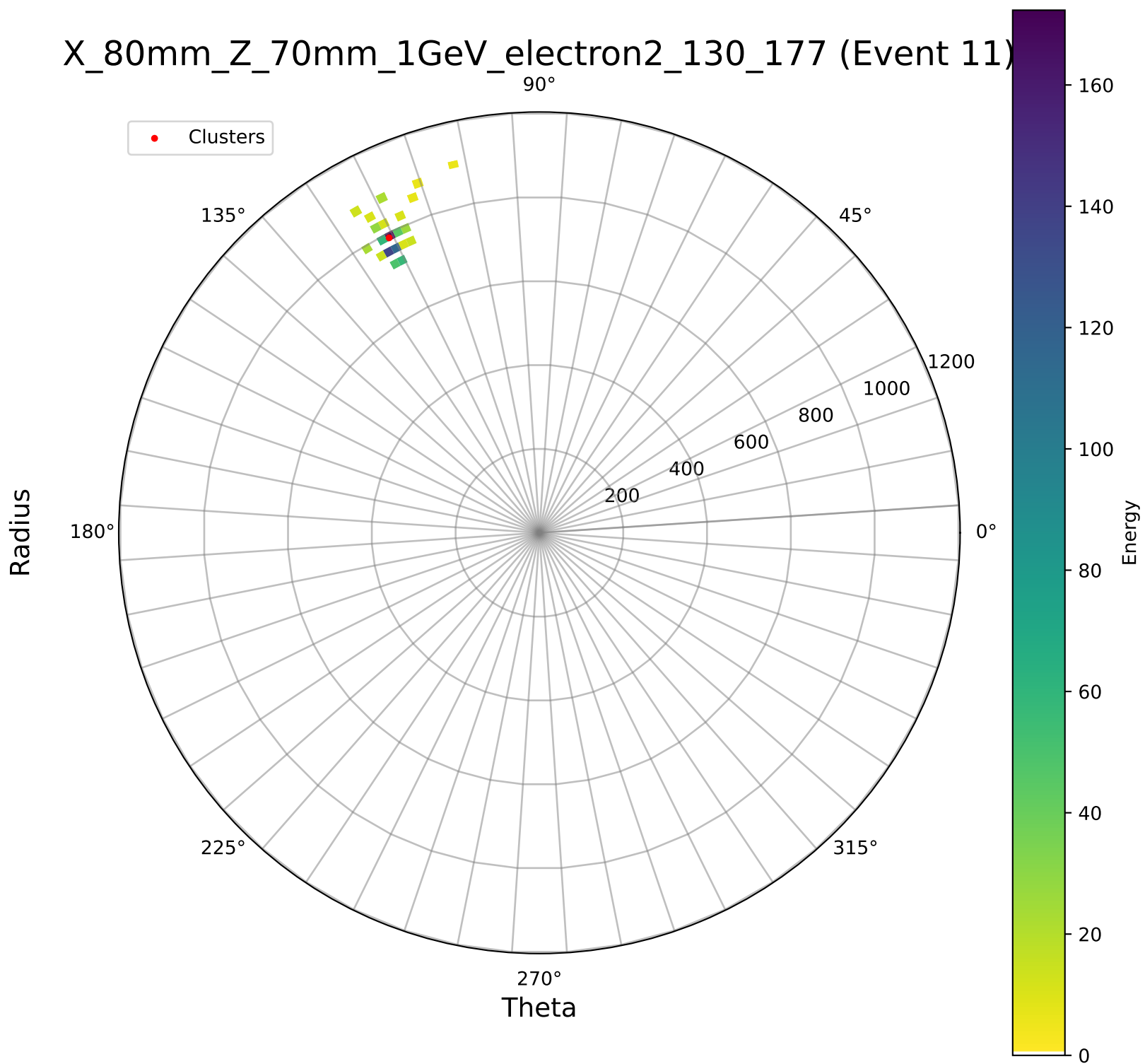


X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 10)

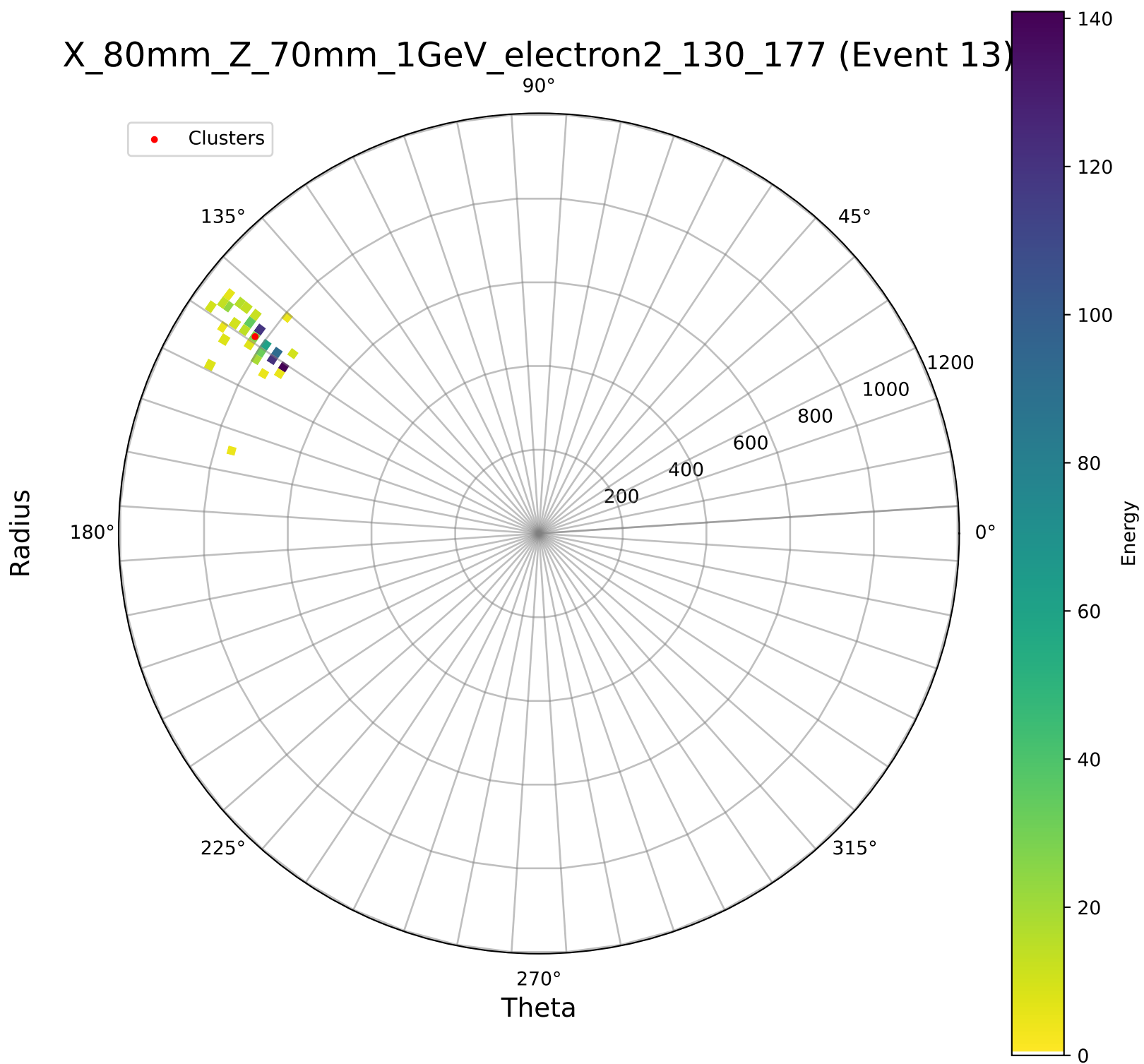




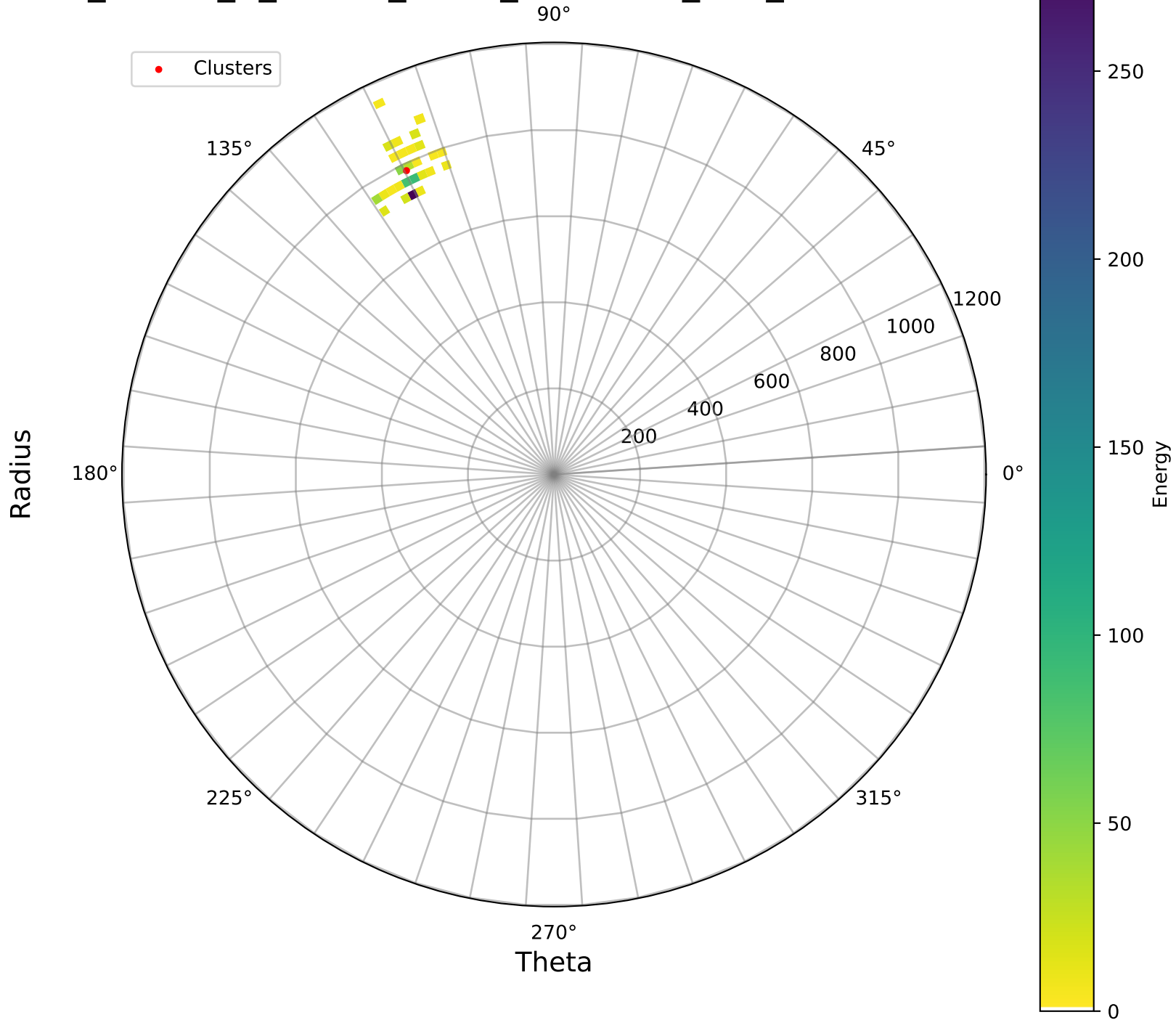
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 11)



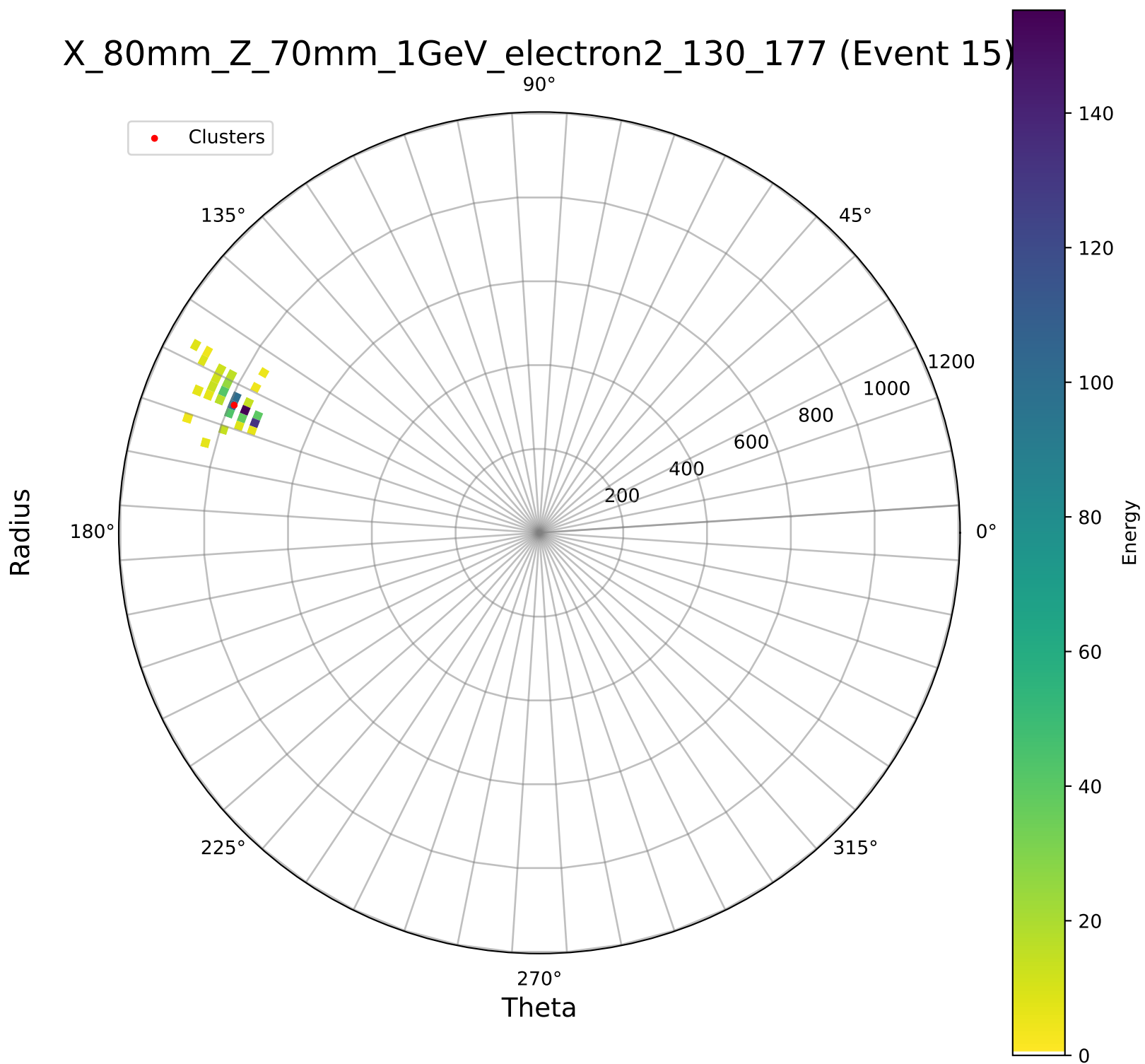
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 13)



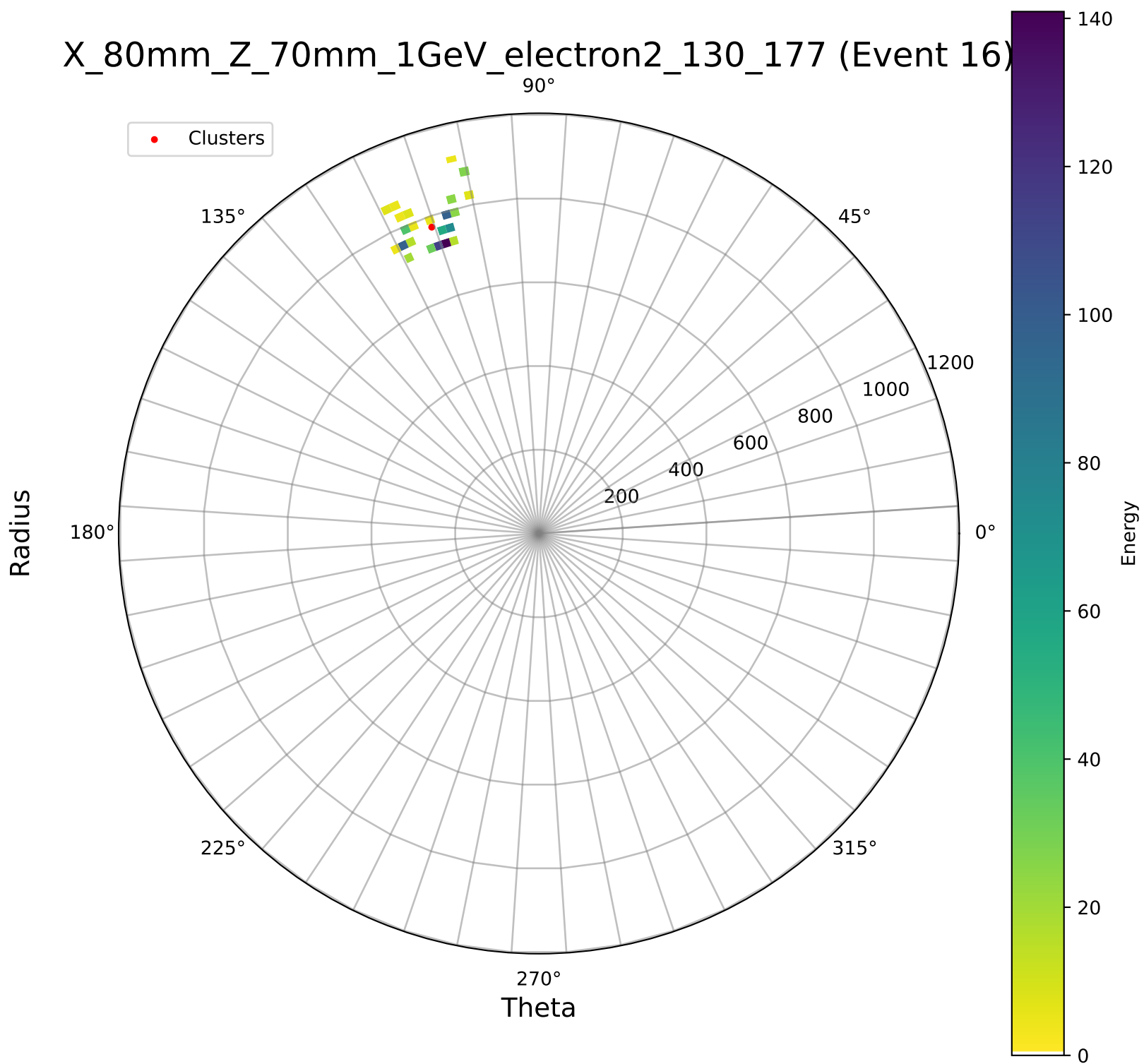
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 14)



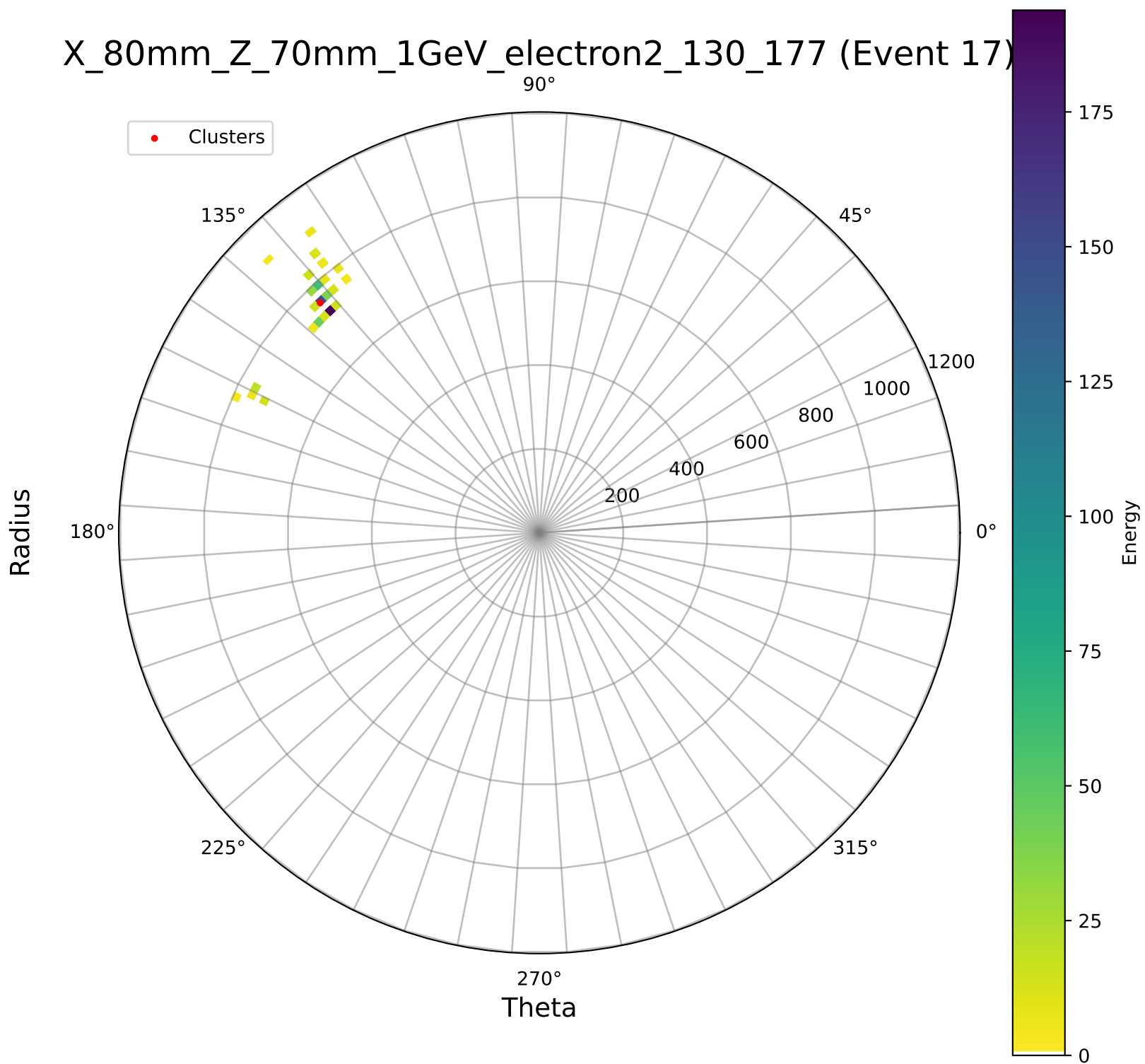
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 15)



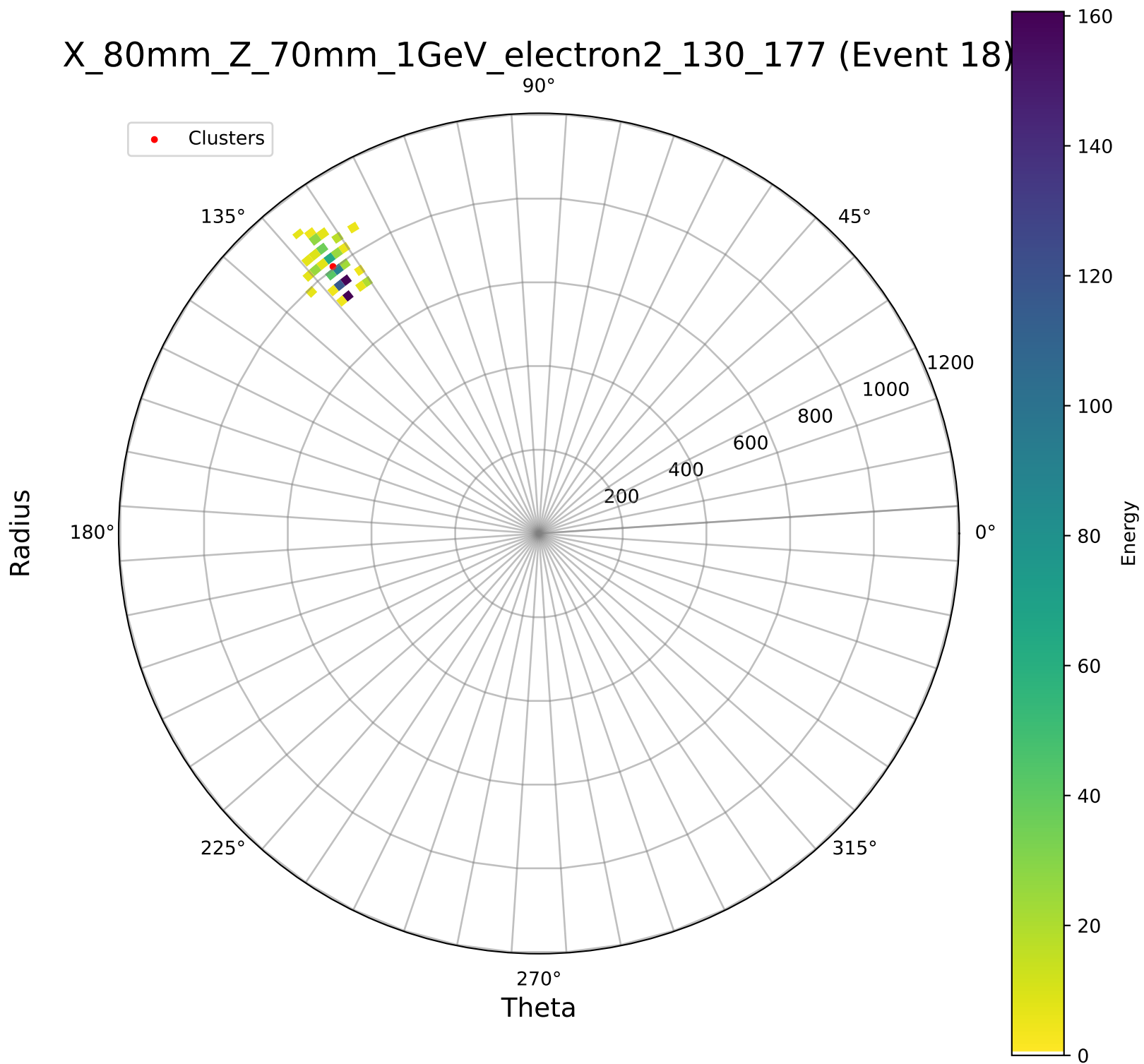
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 16)



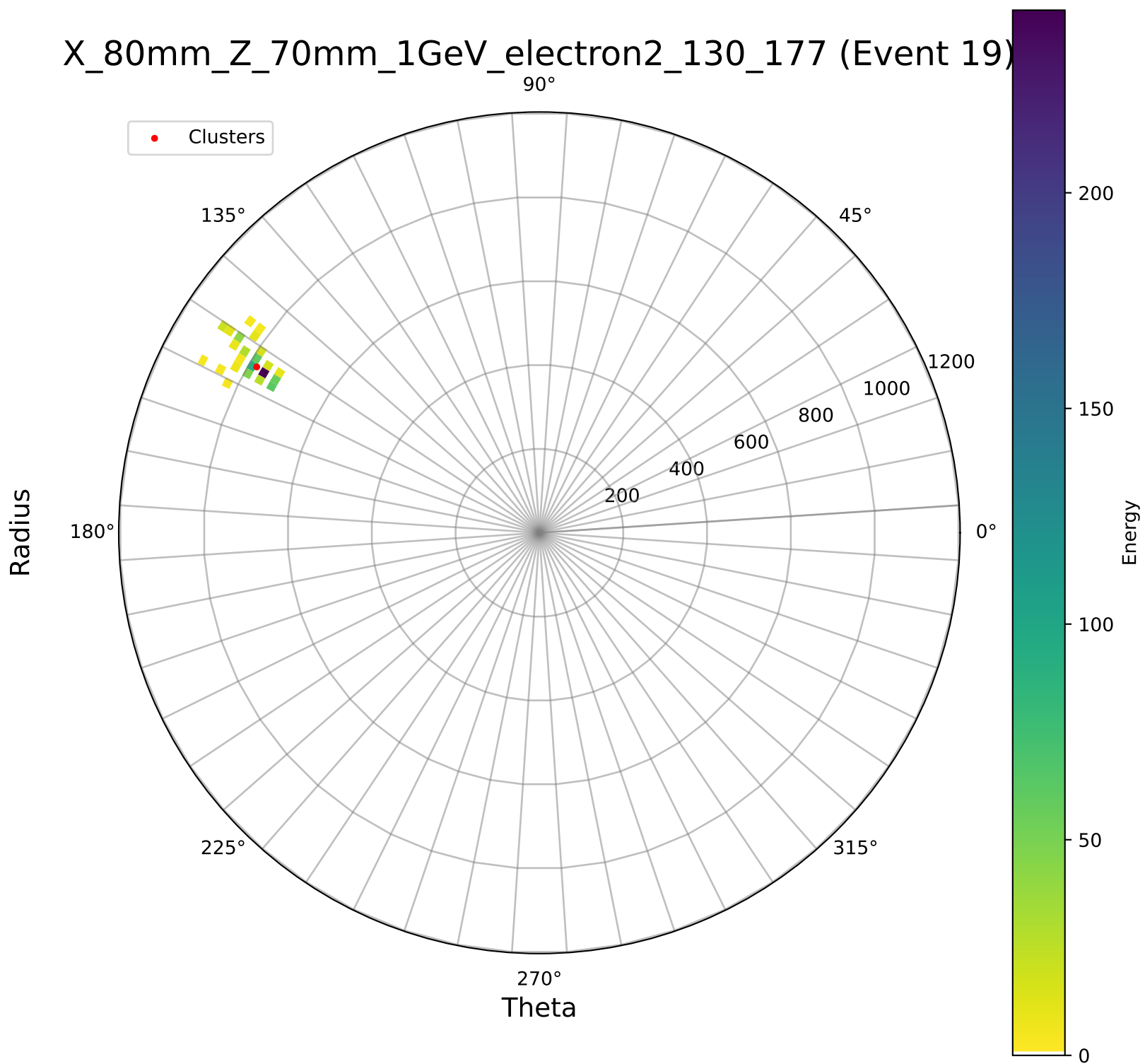
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 17)



# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 18)

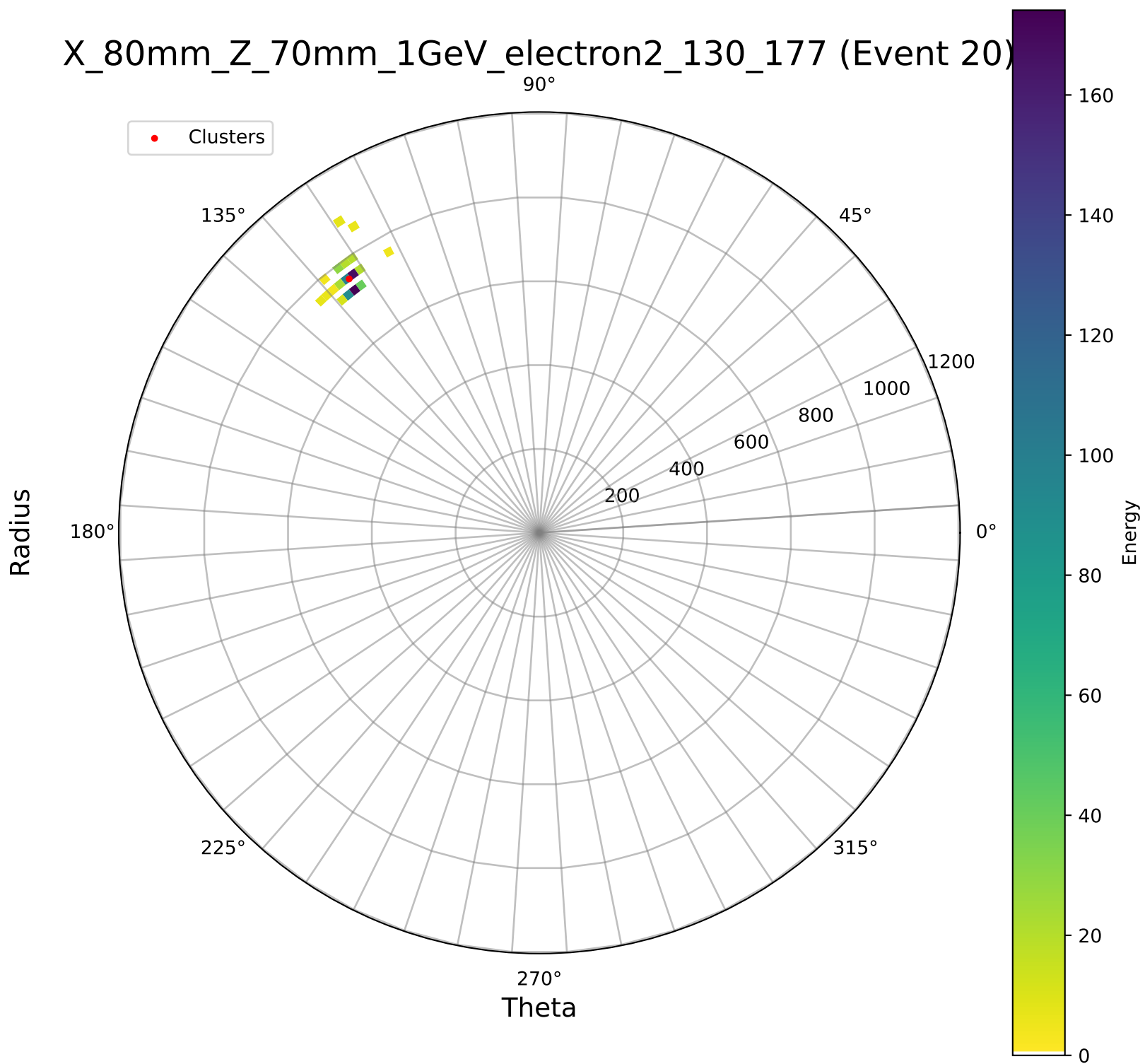


X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 19)

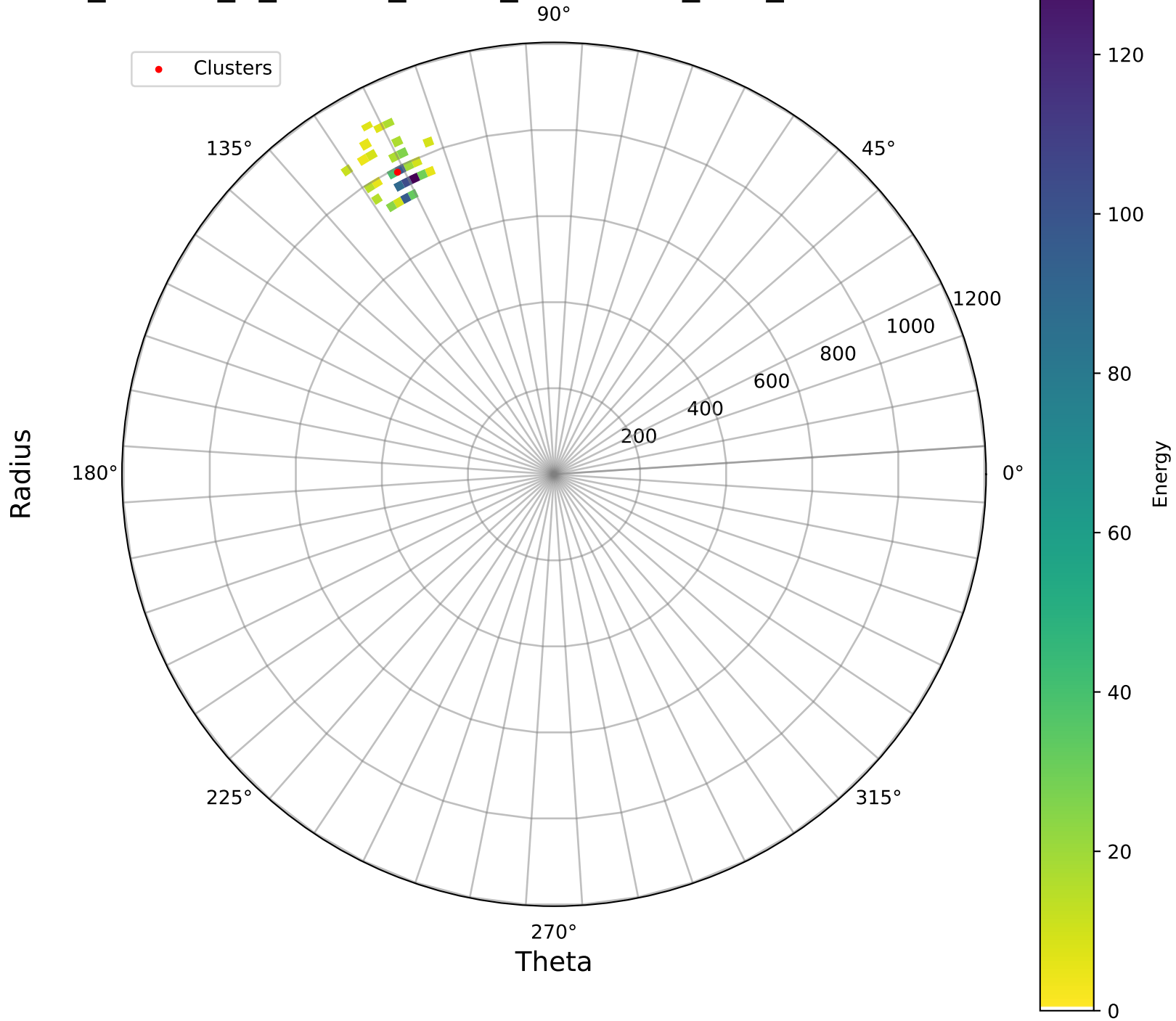




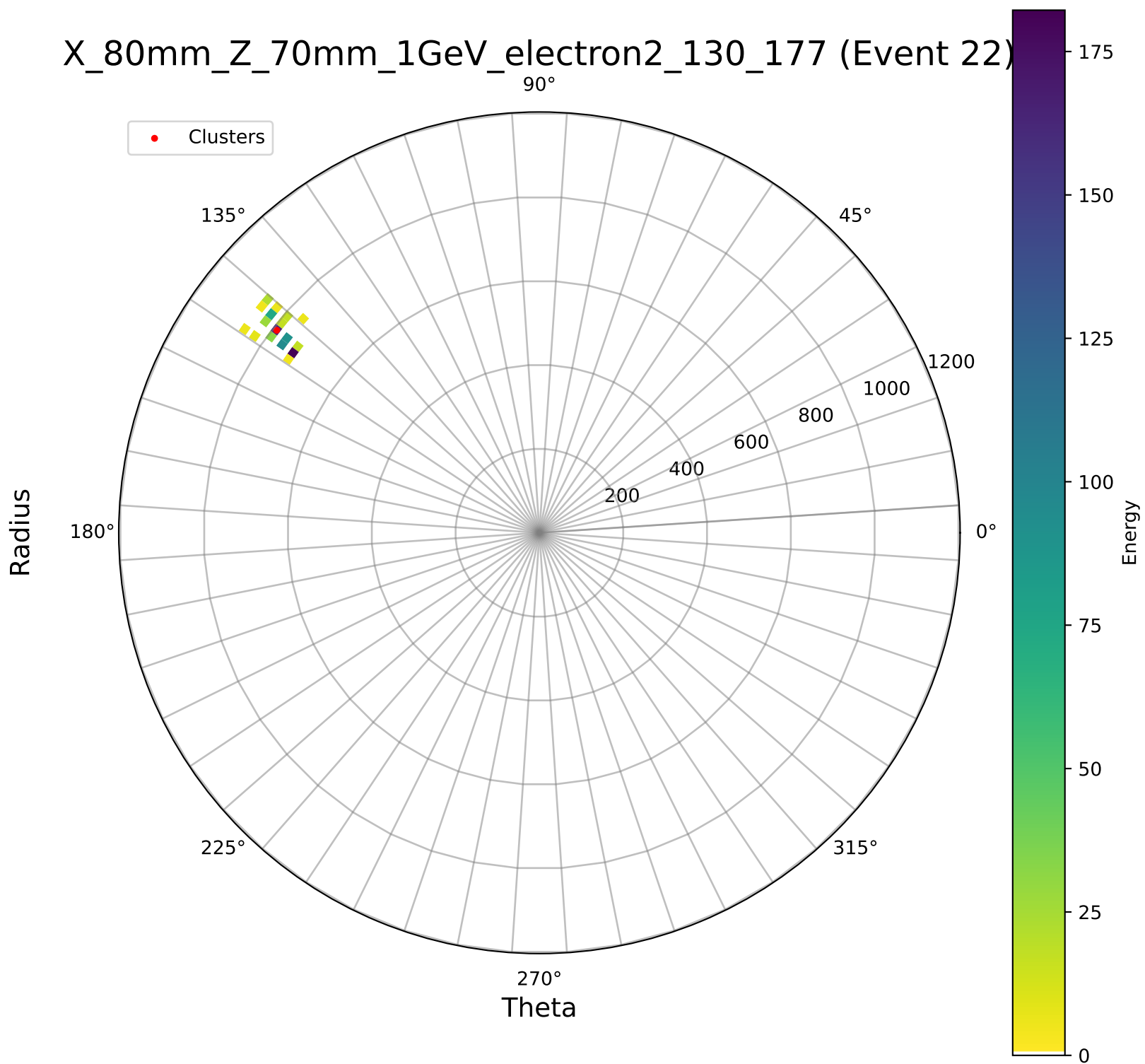
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 20)



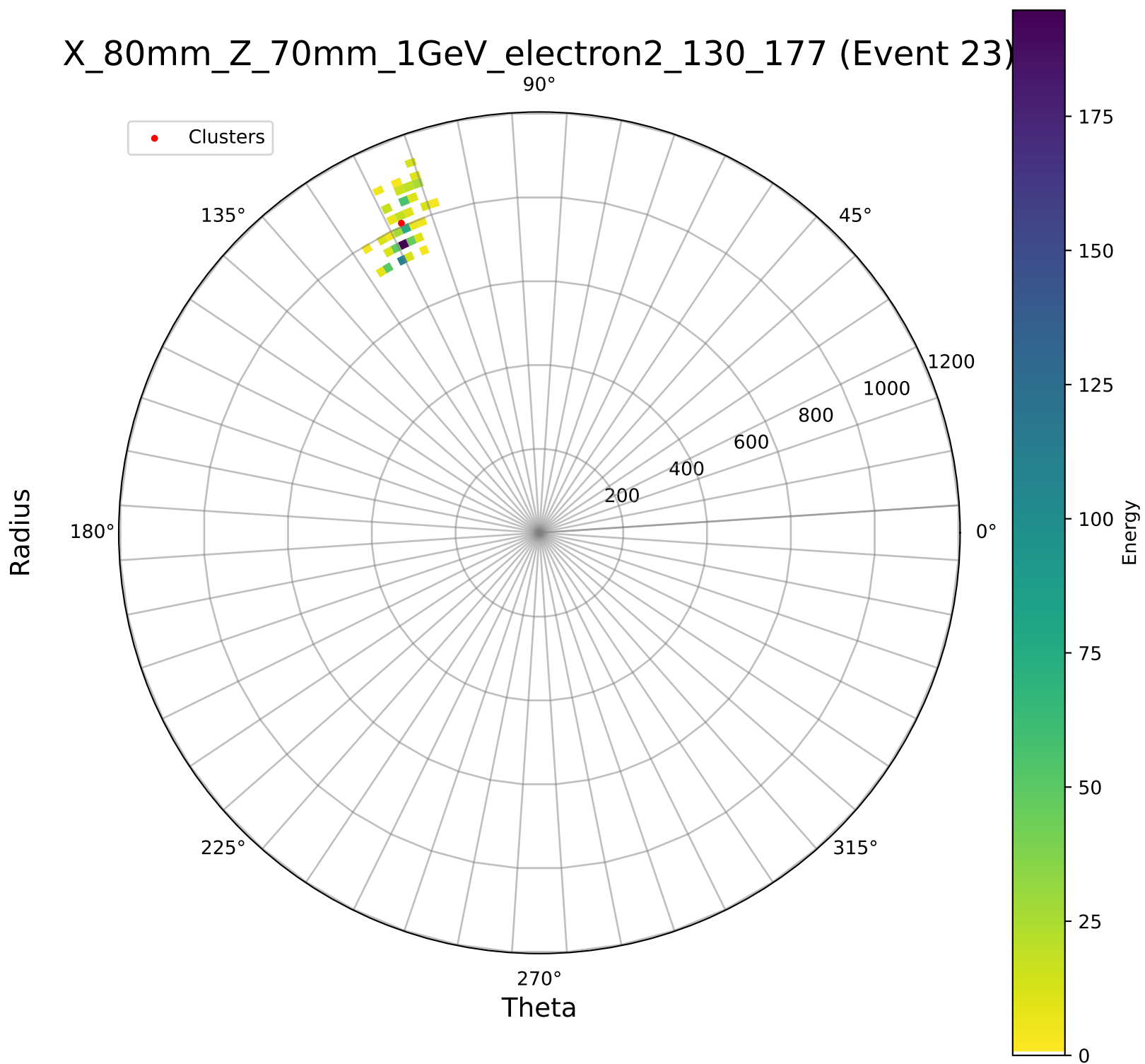
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 21)



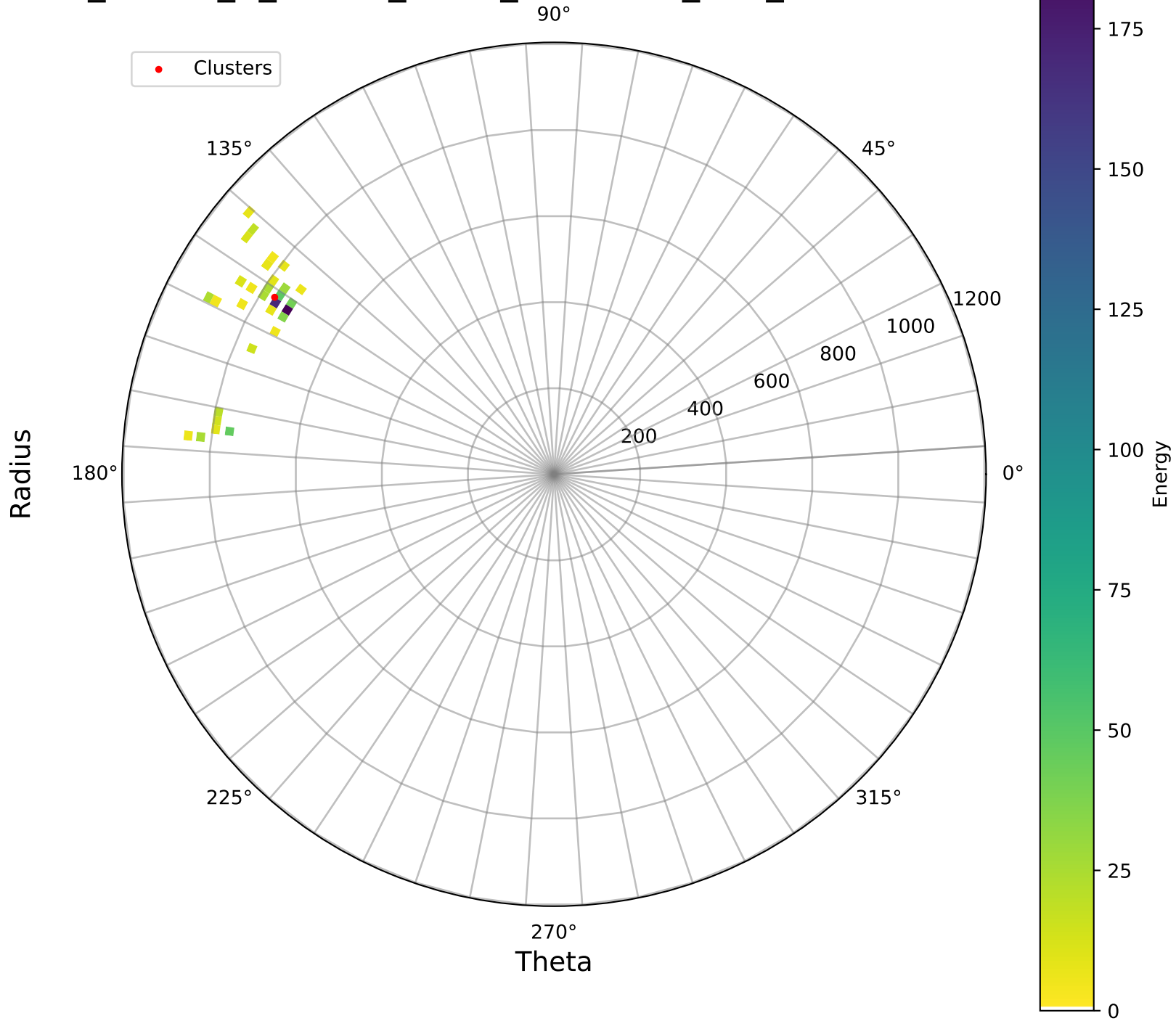
X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 22)



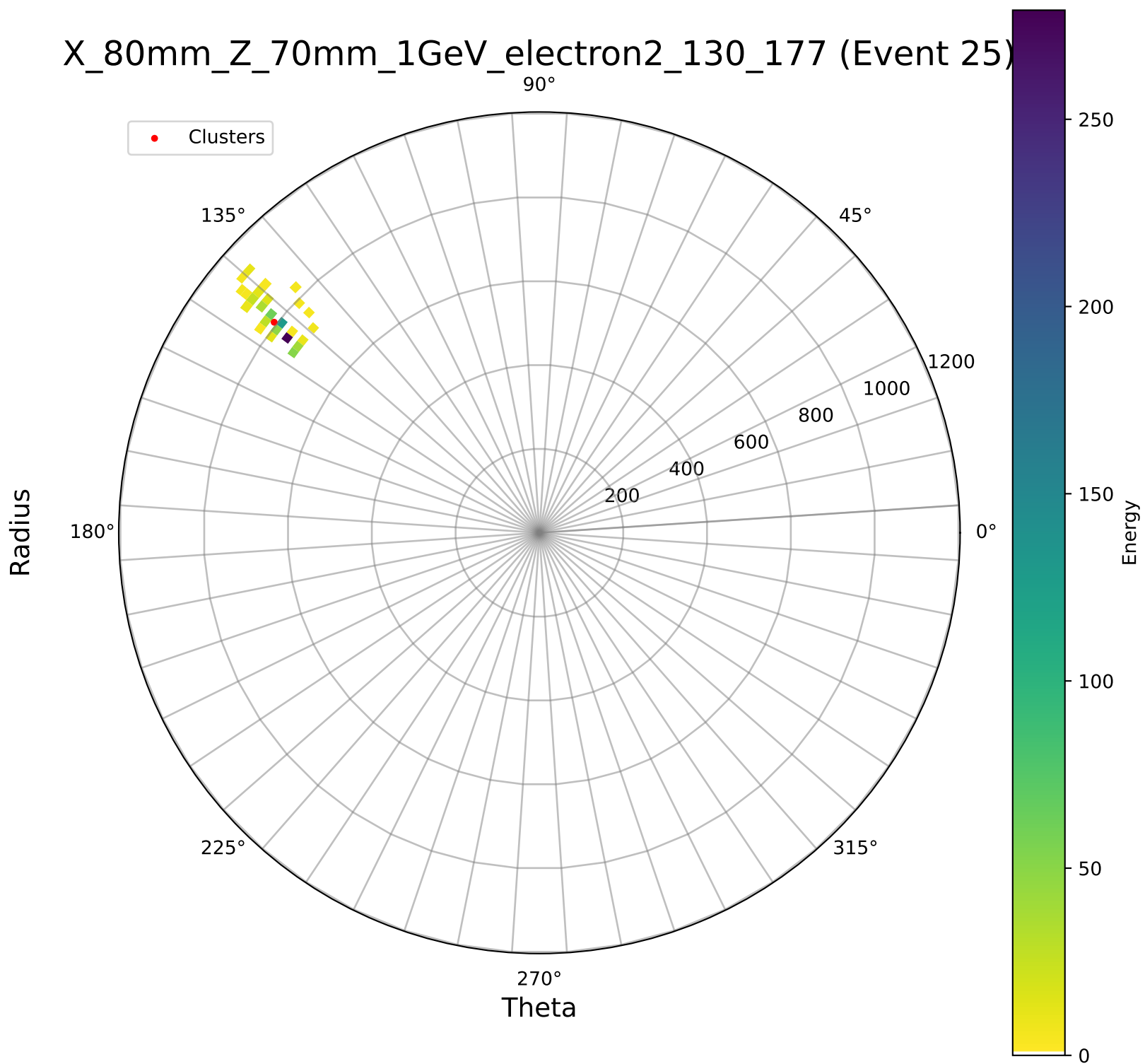
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 23)



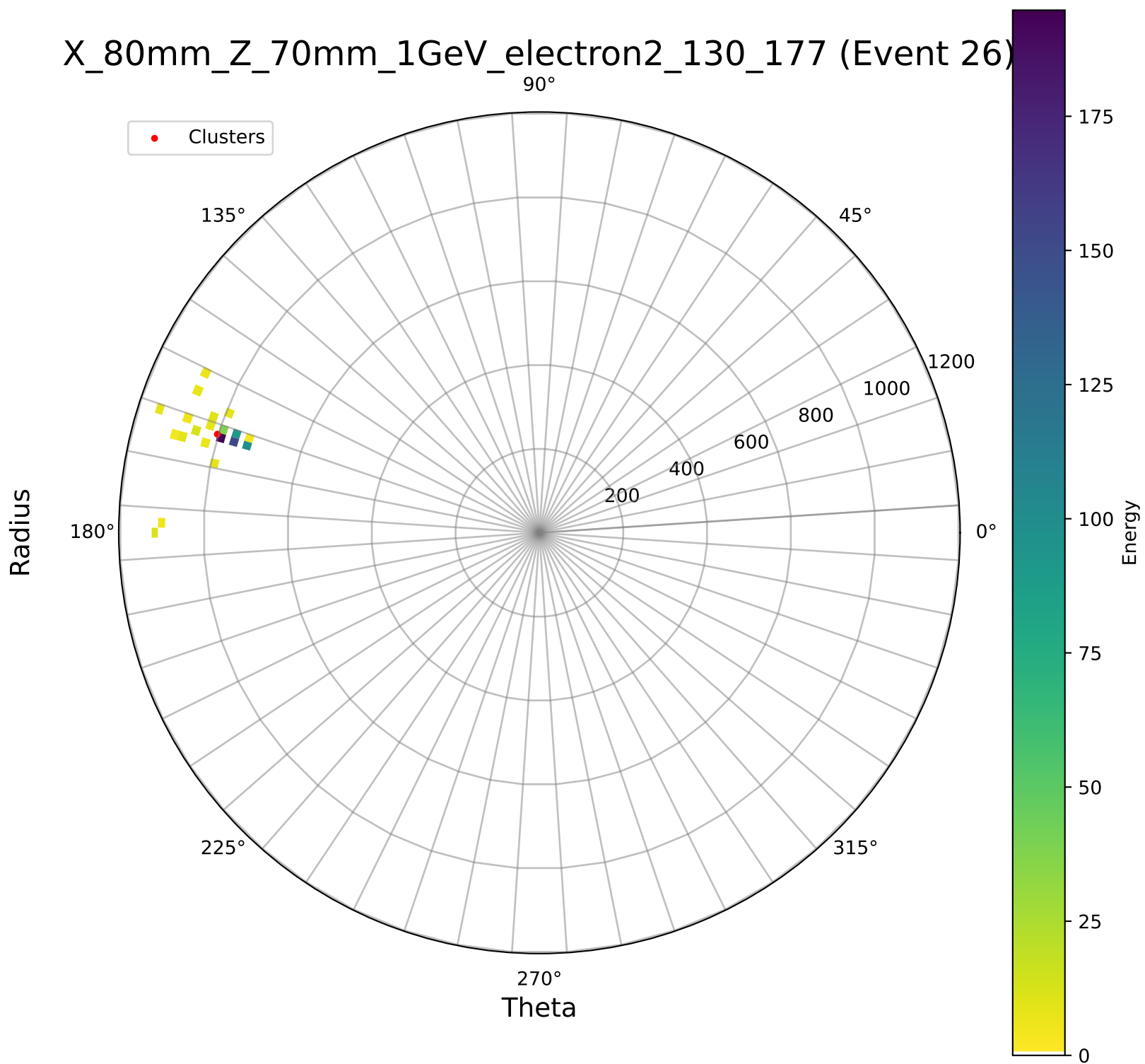
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 24)



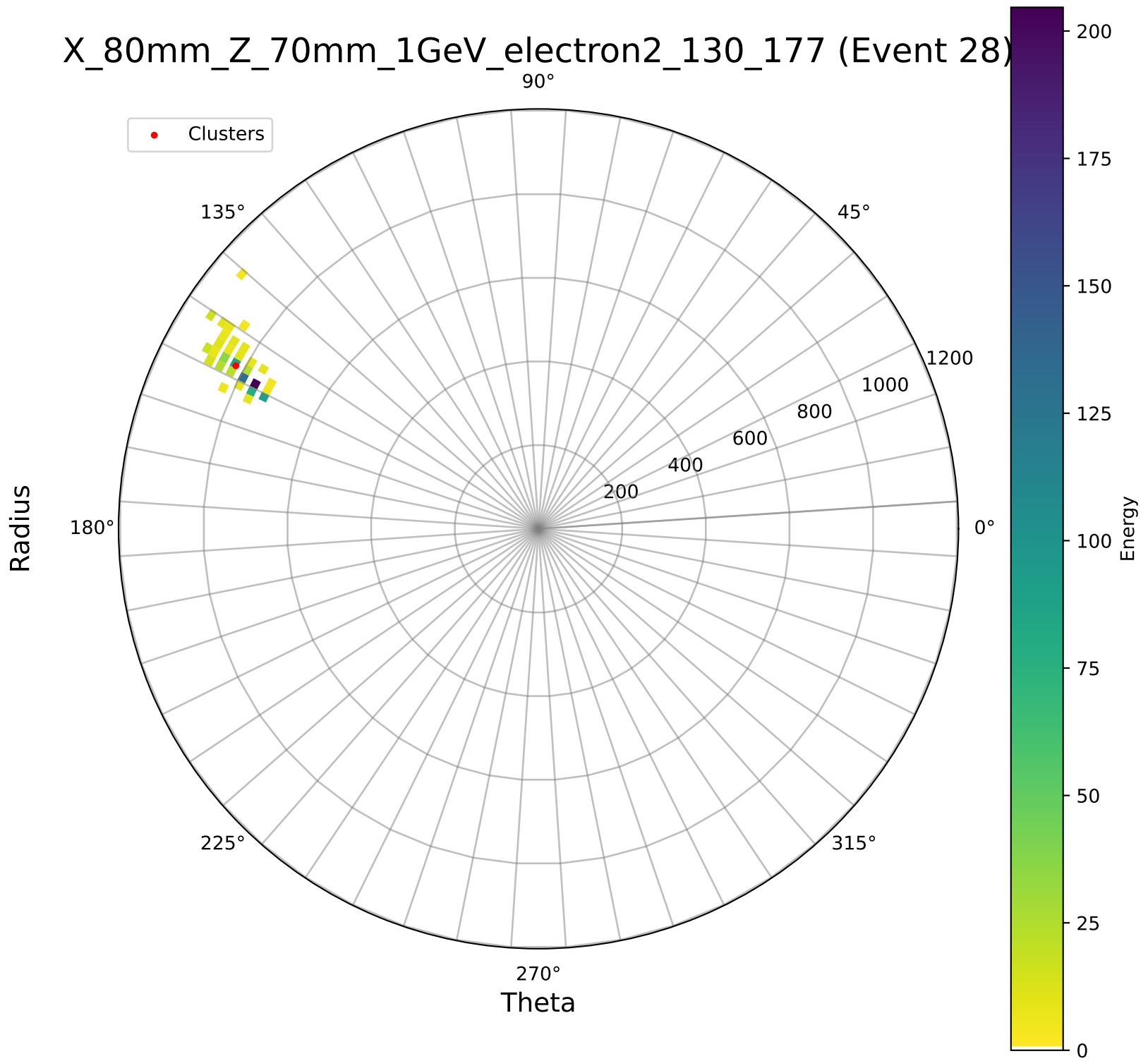
X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 25)



# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 26)

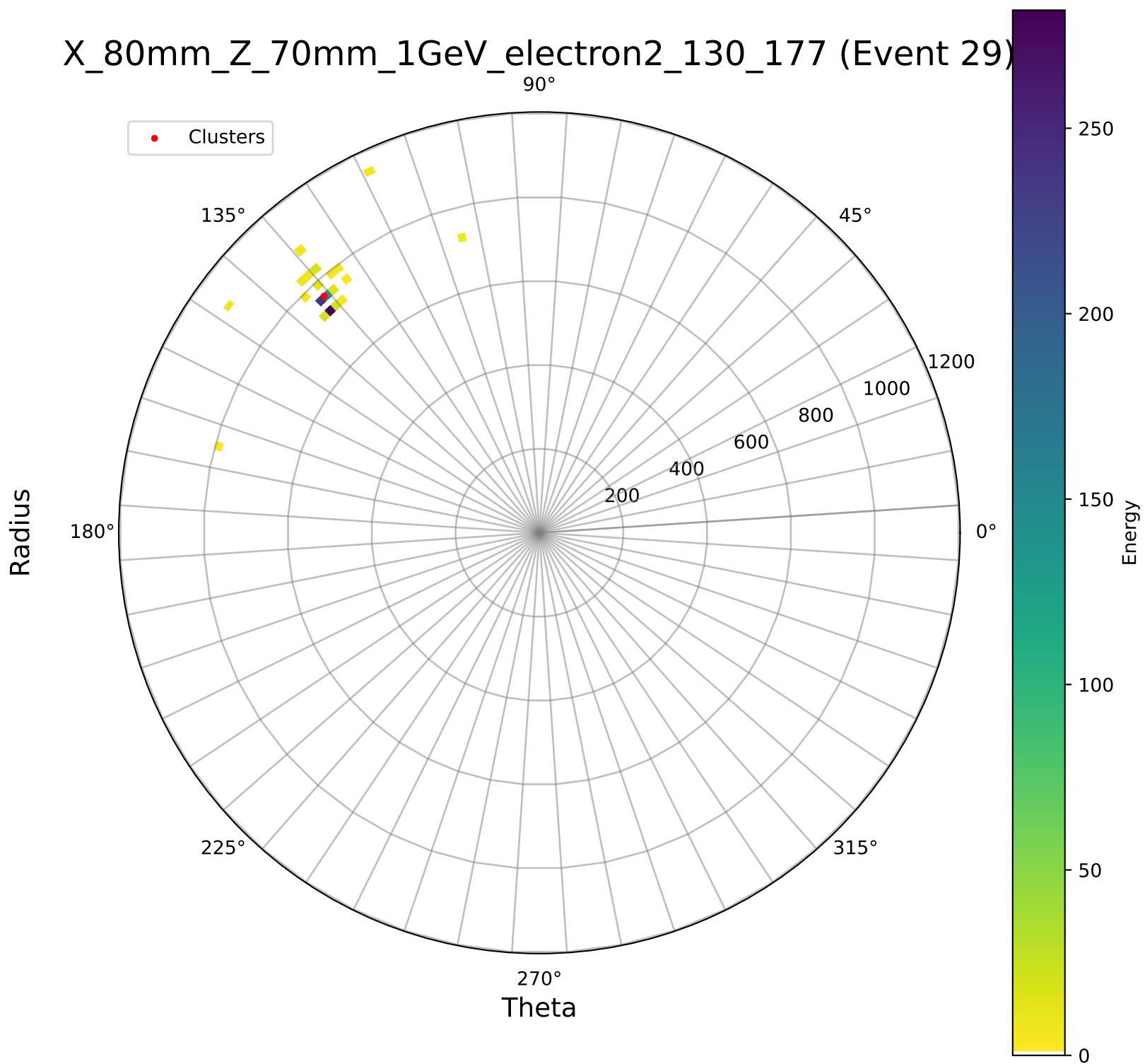


# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 28)

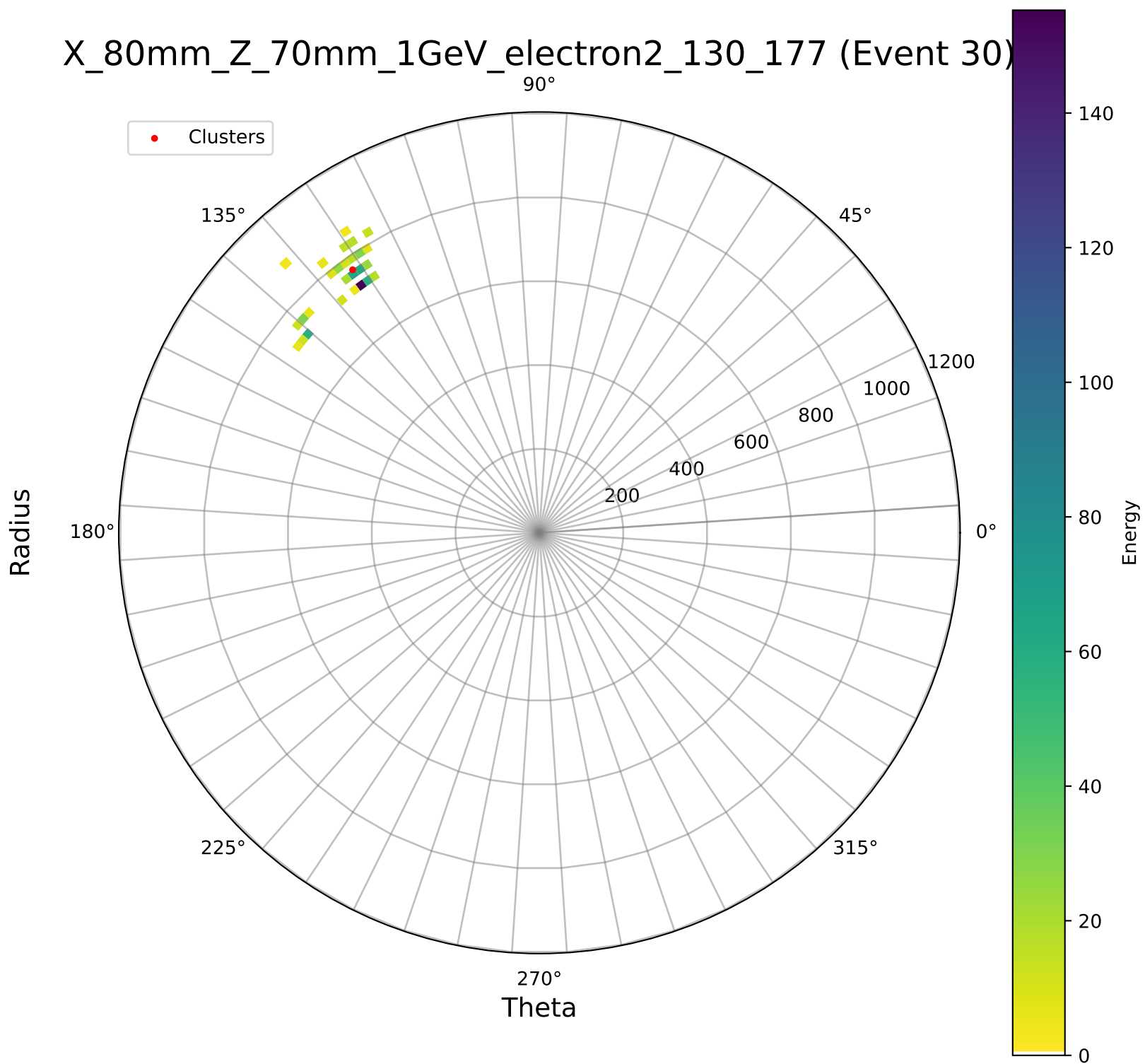




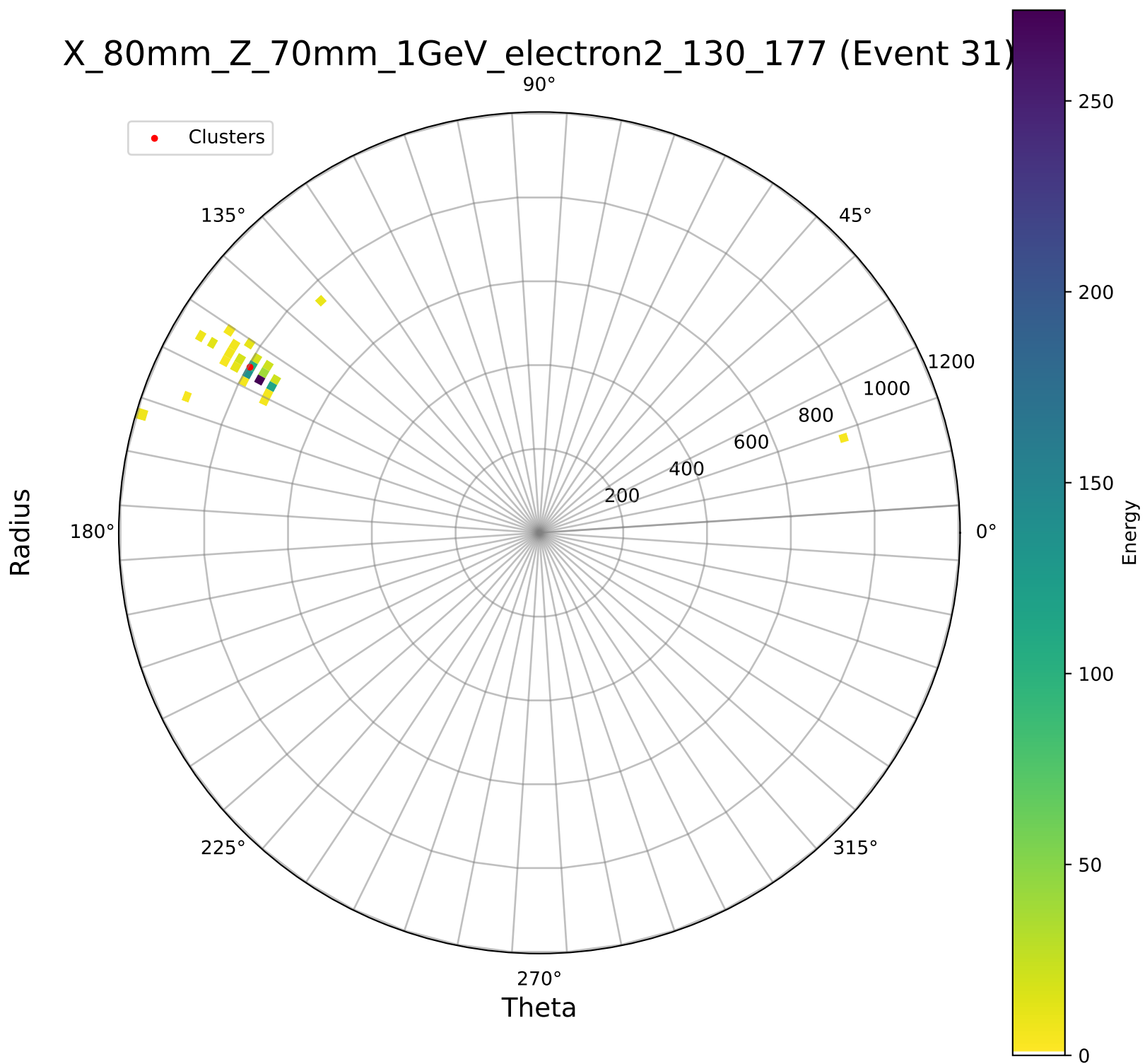
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 29)



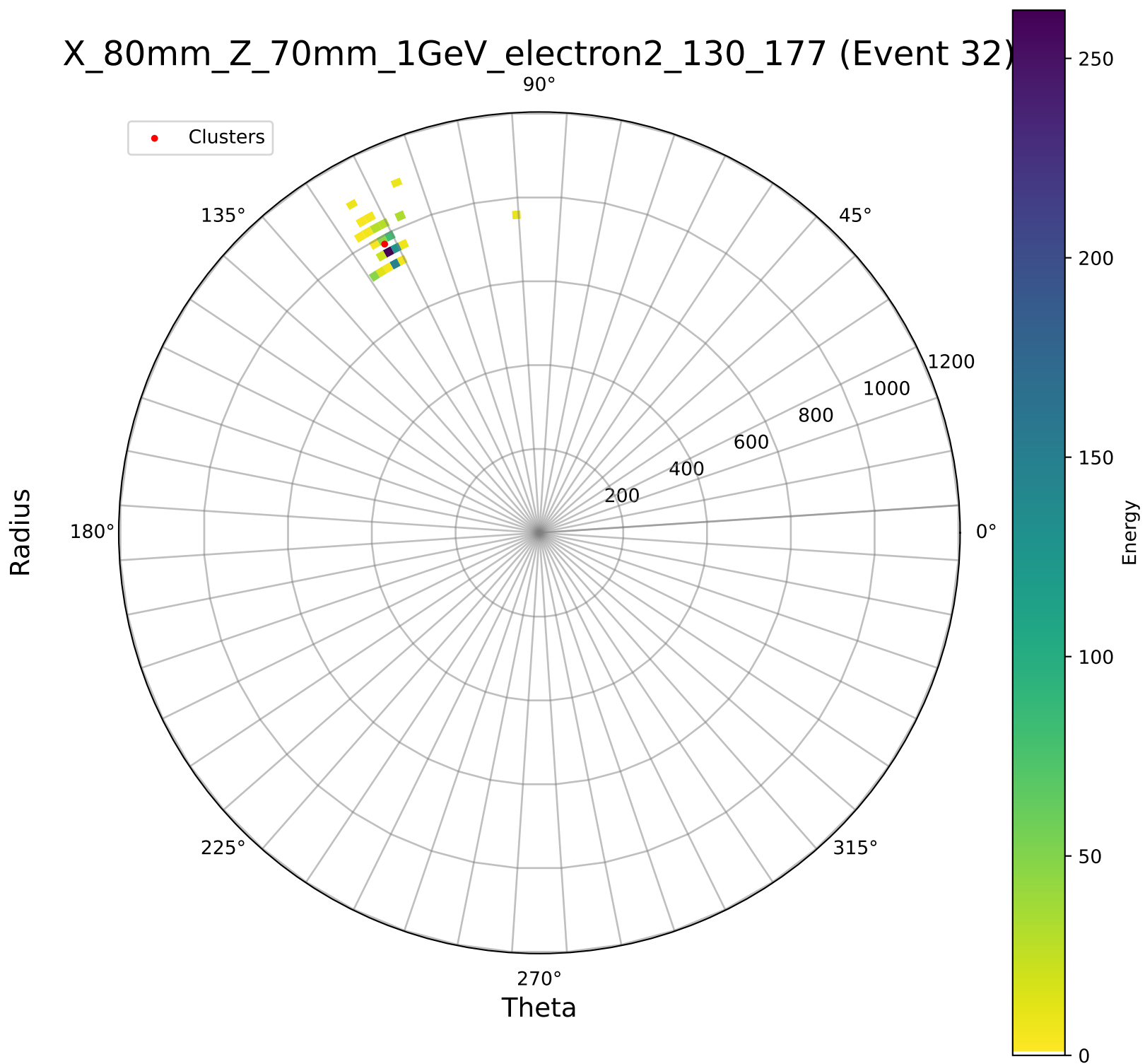
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 30)



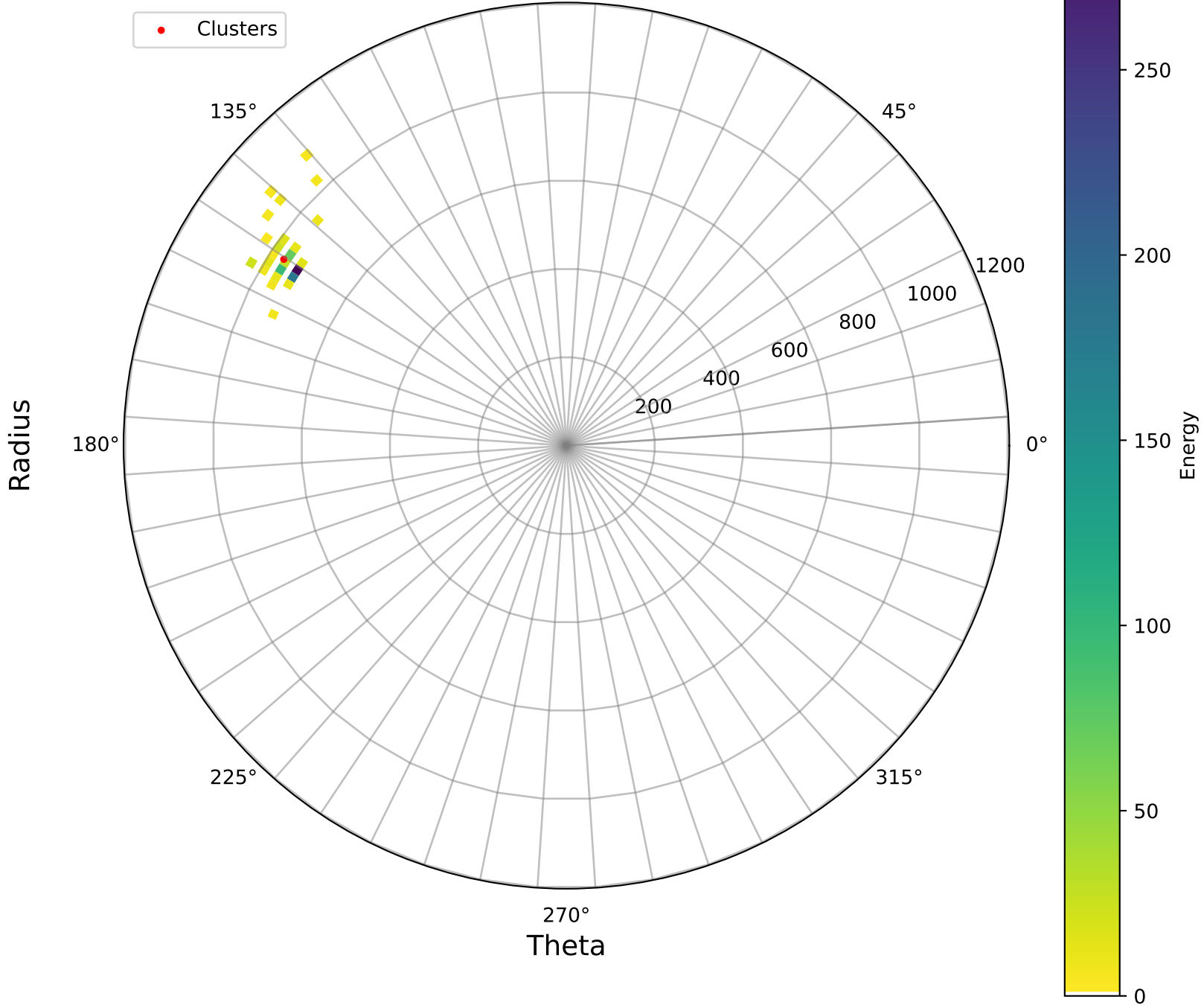
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 31)



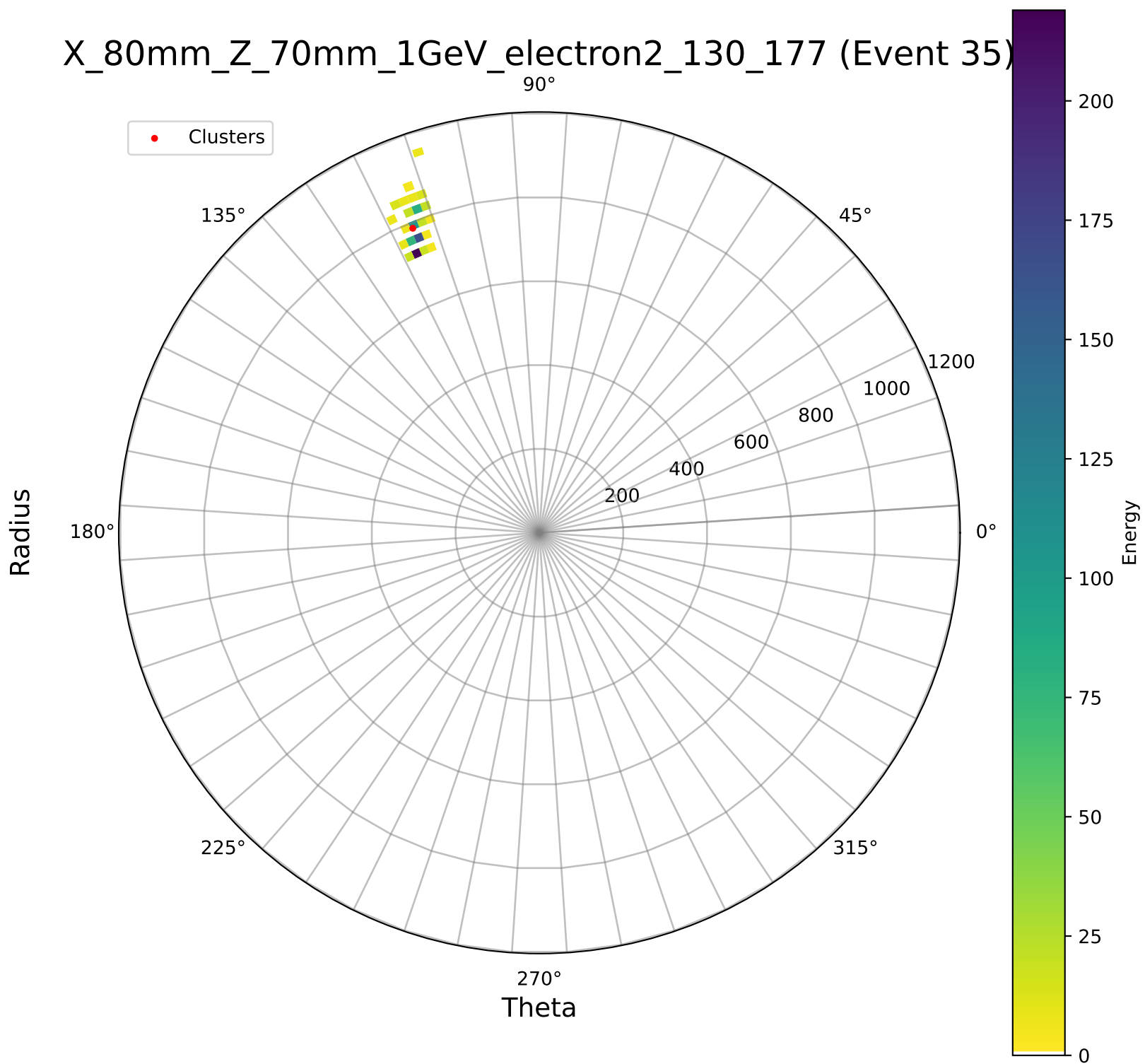
X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 32)



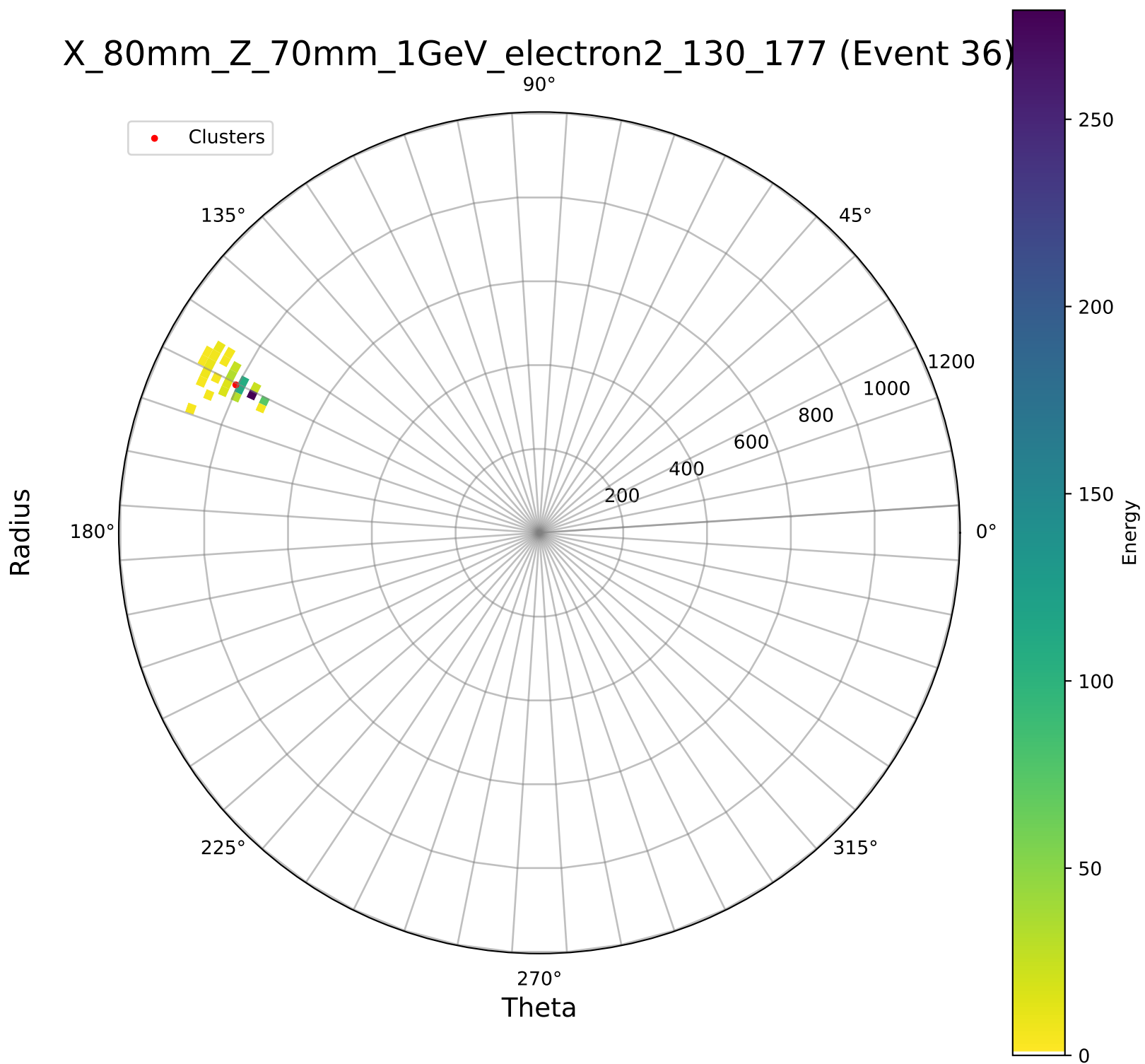
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 34)



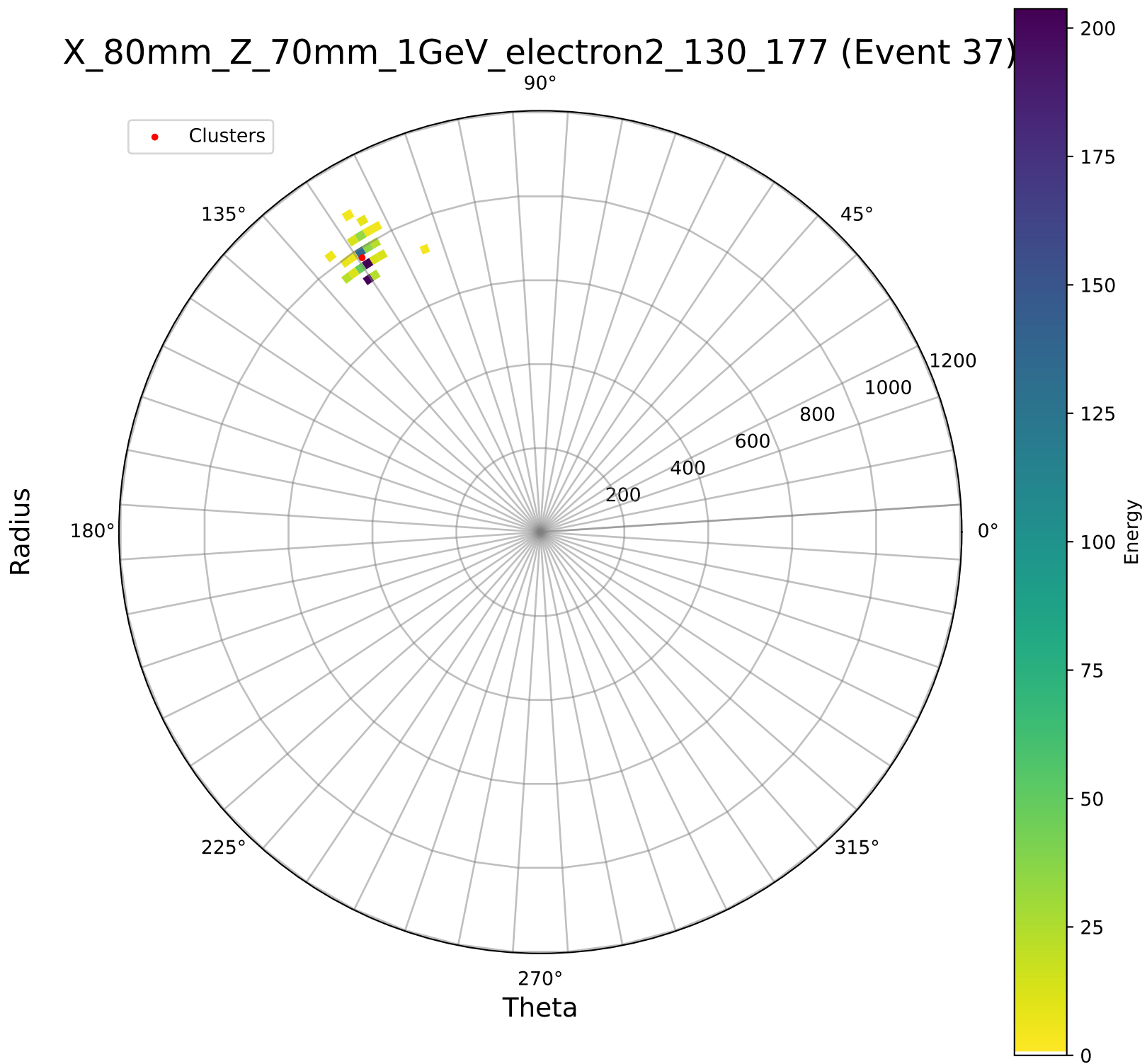
X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 35)



X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 36)

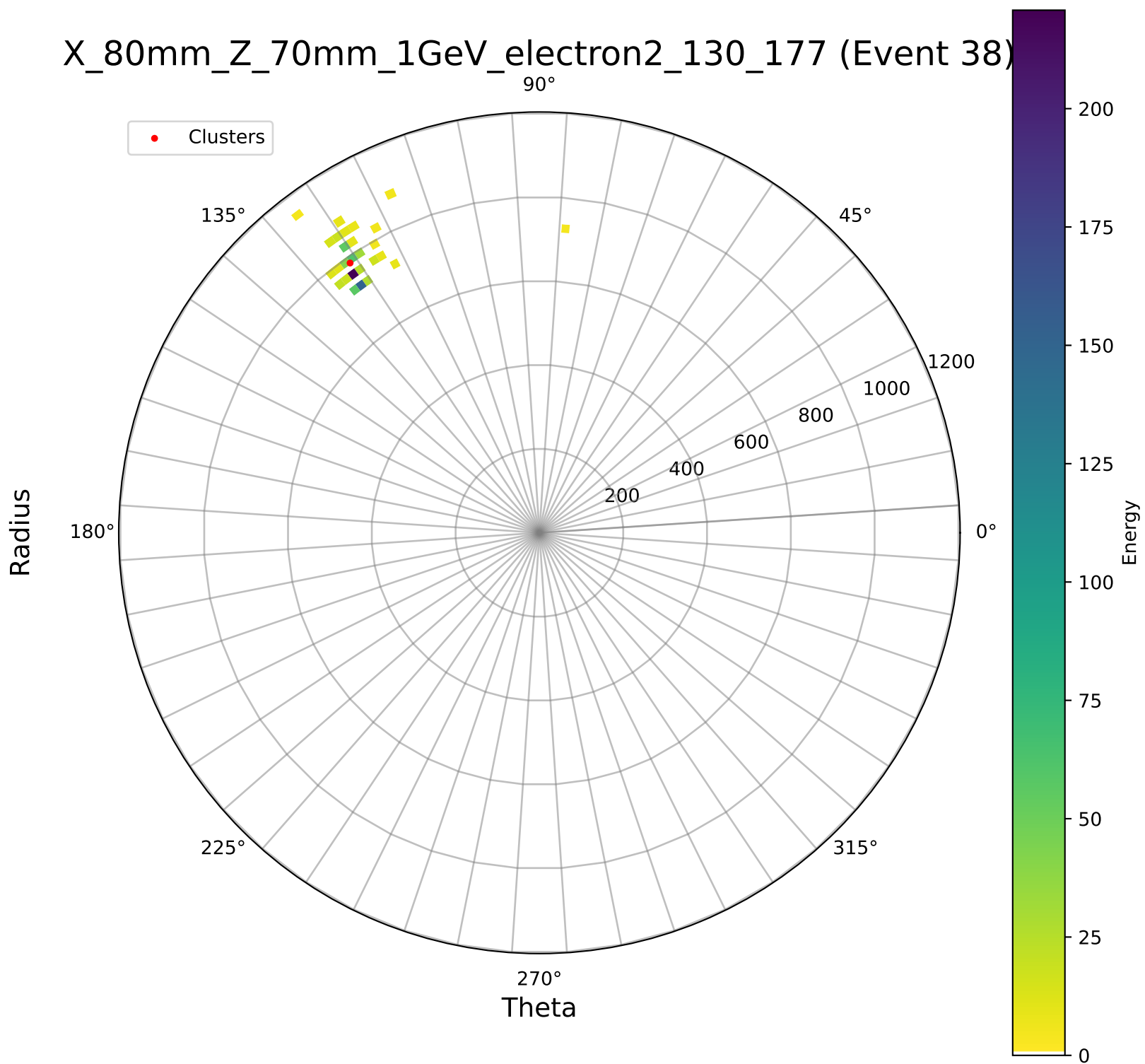


# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 37)

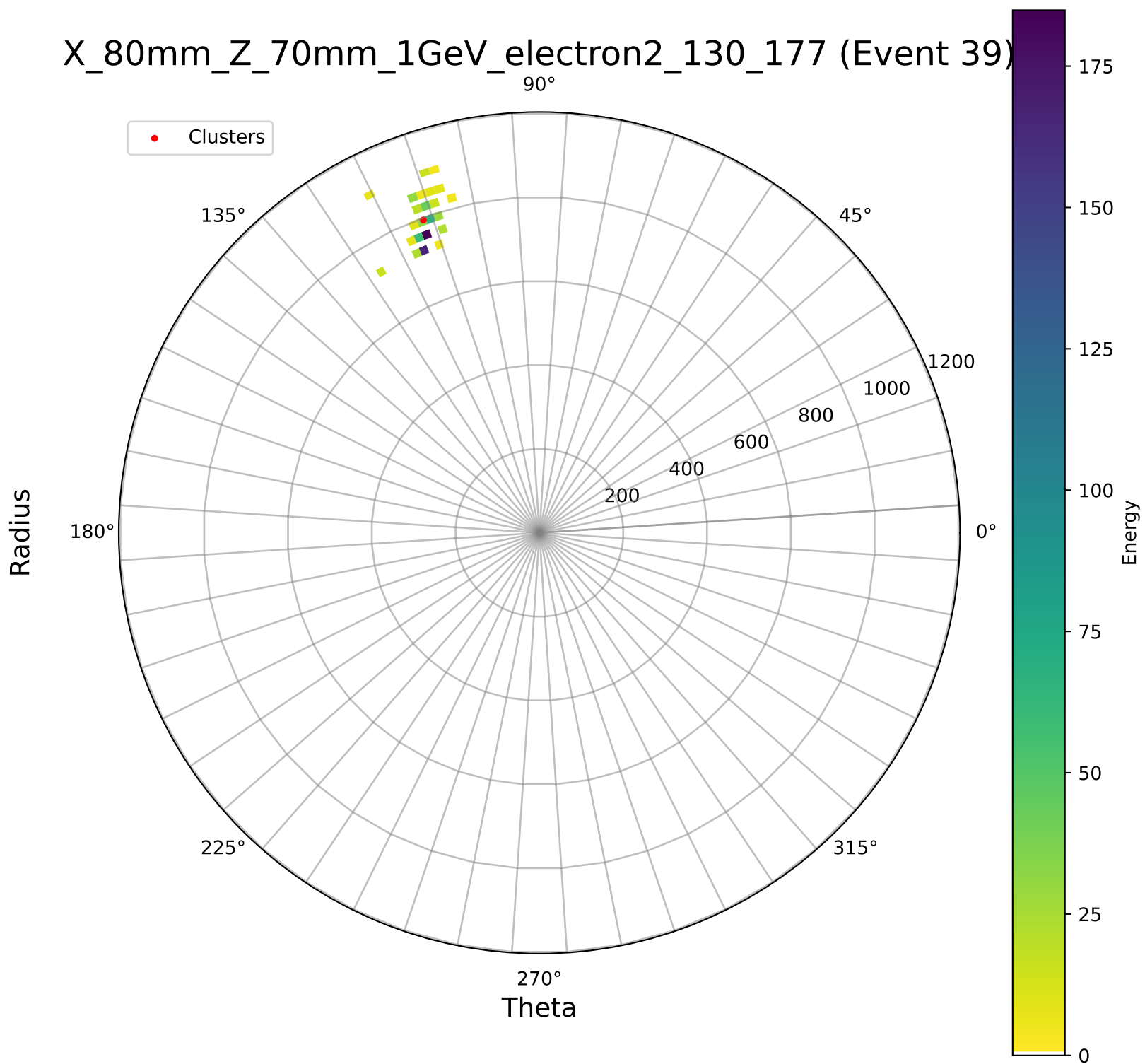




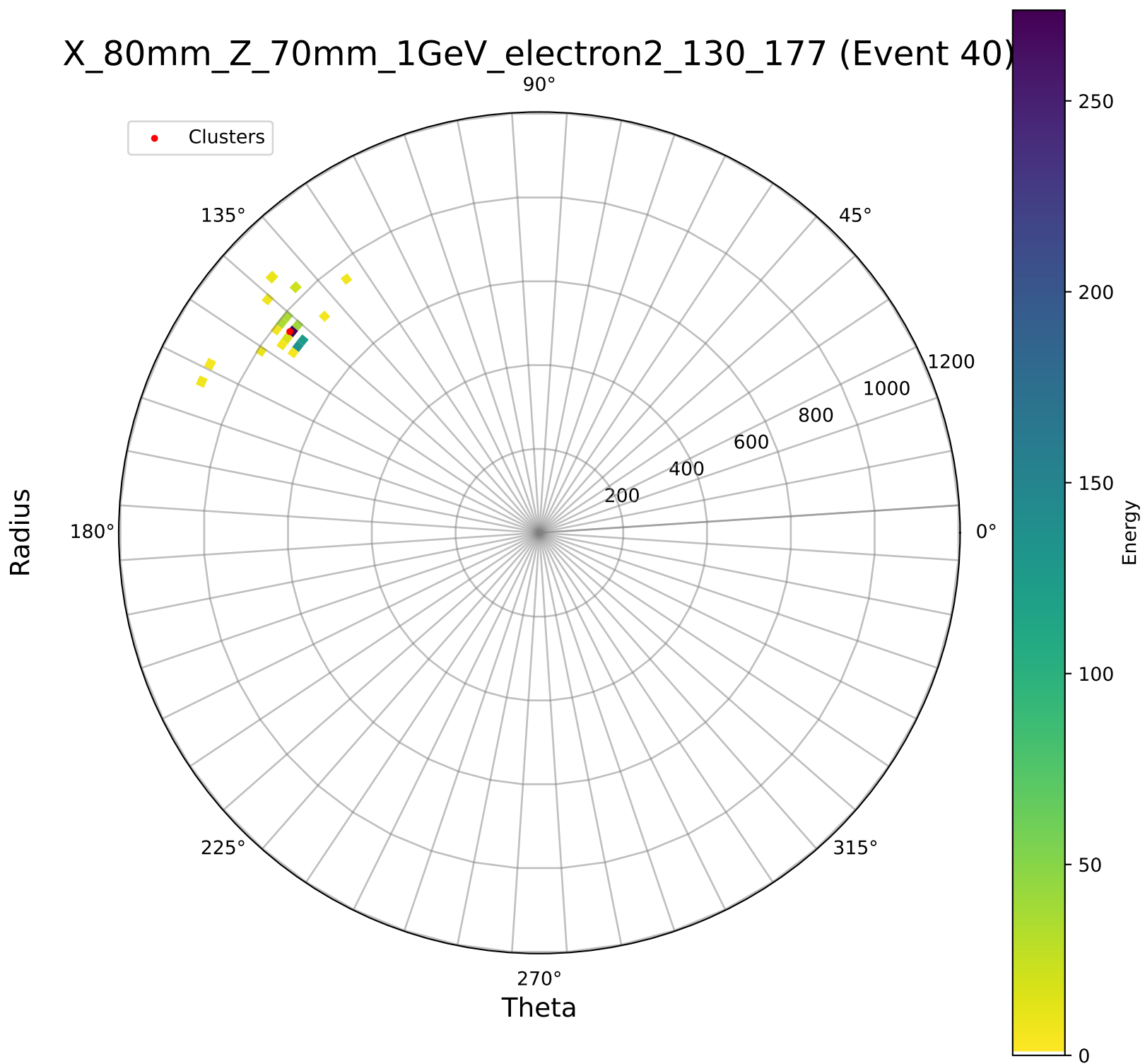
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 38)



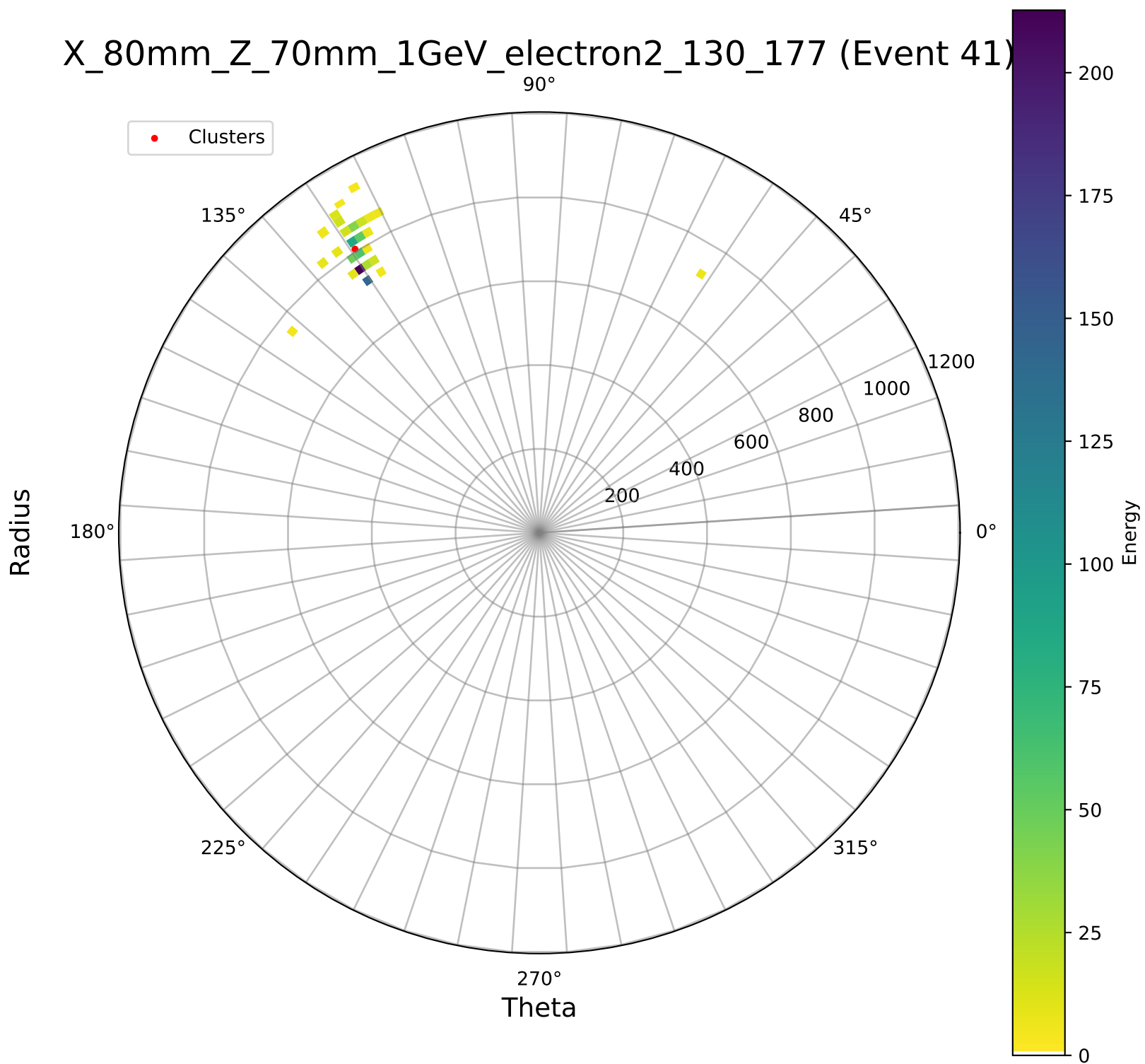
X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 39)



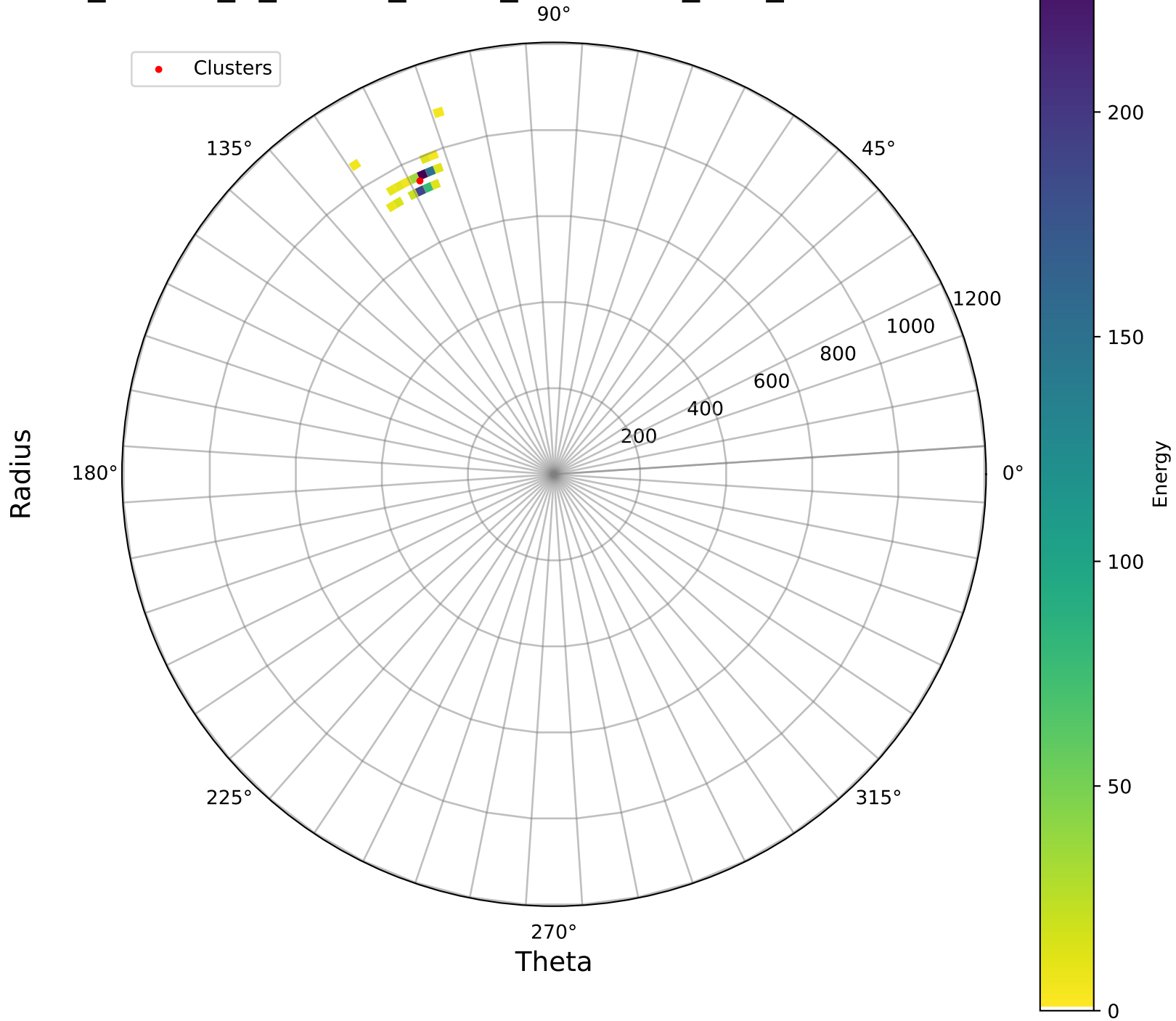
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 40)



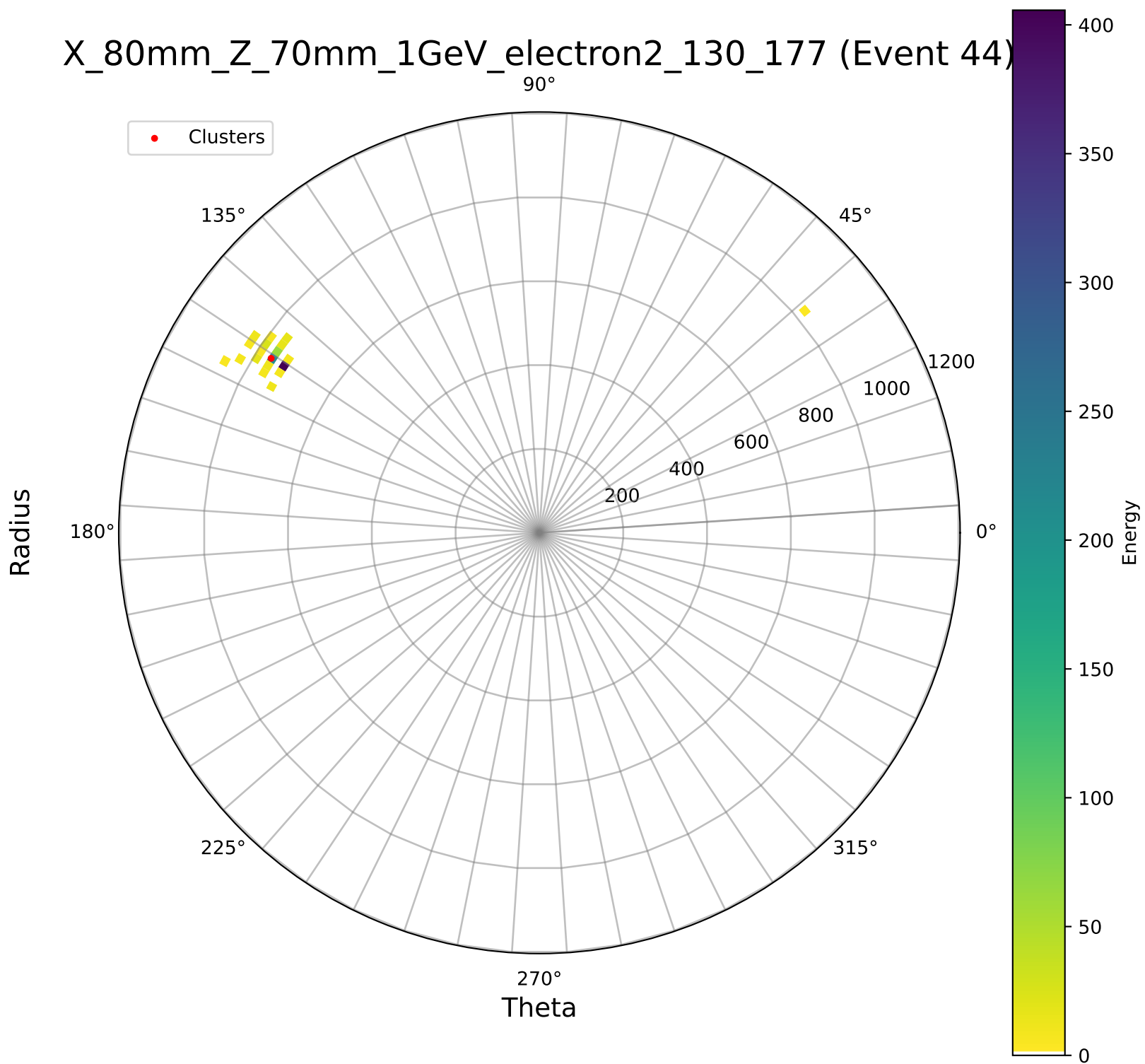
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 41)



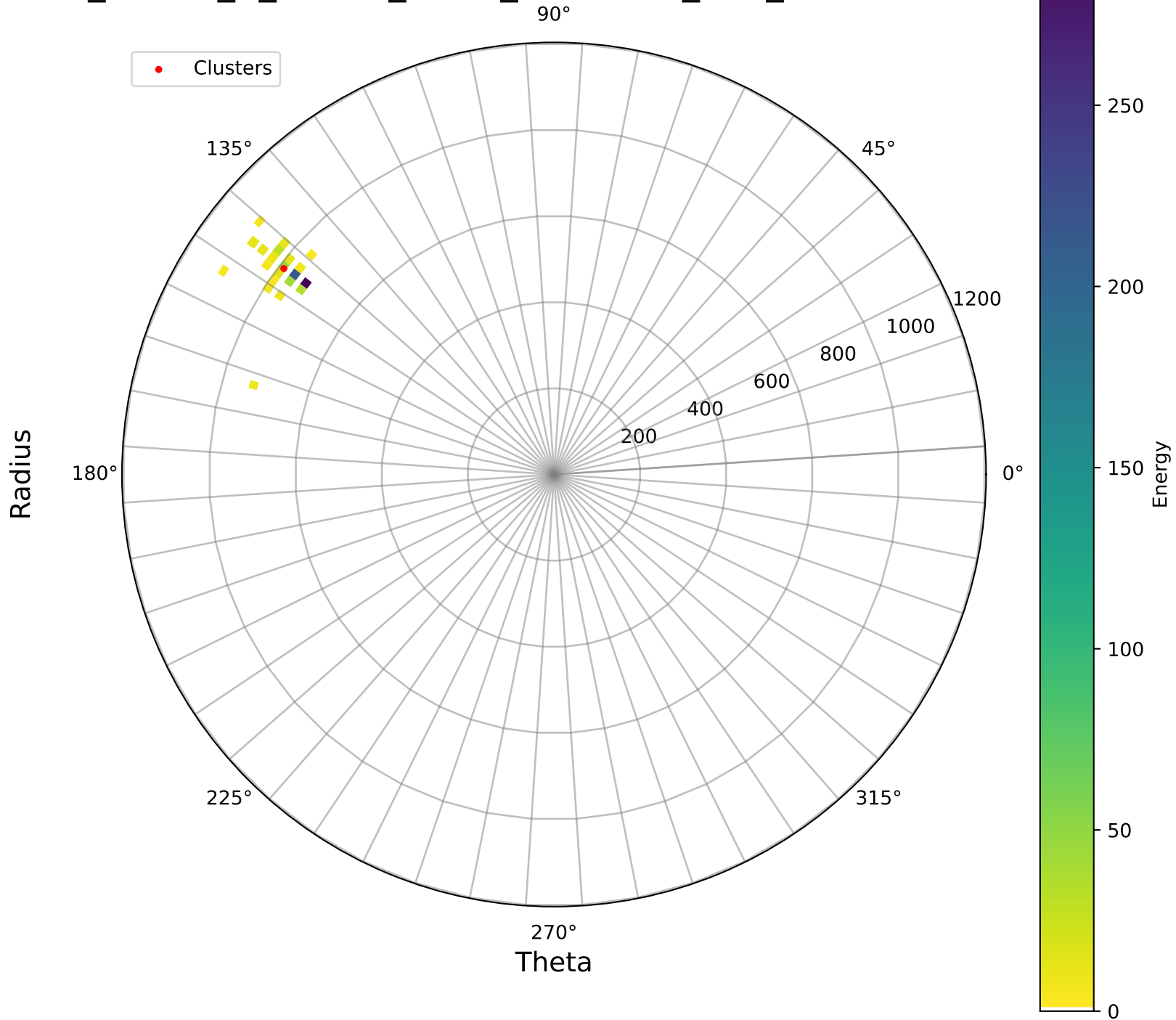
# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 43)



# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 44)



# X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 45)



X\_80mm\_Z\_70mm\_1GeV\_electron2\_130\_177 (Event 49)

