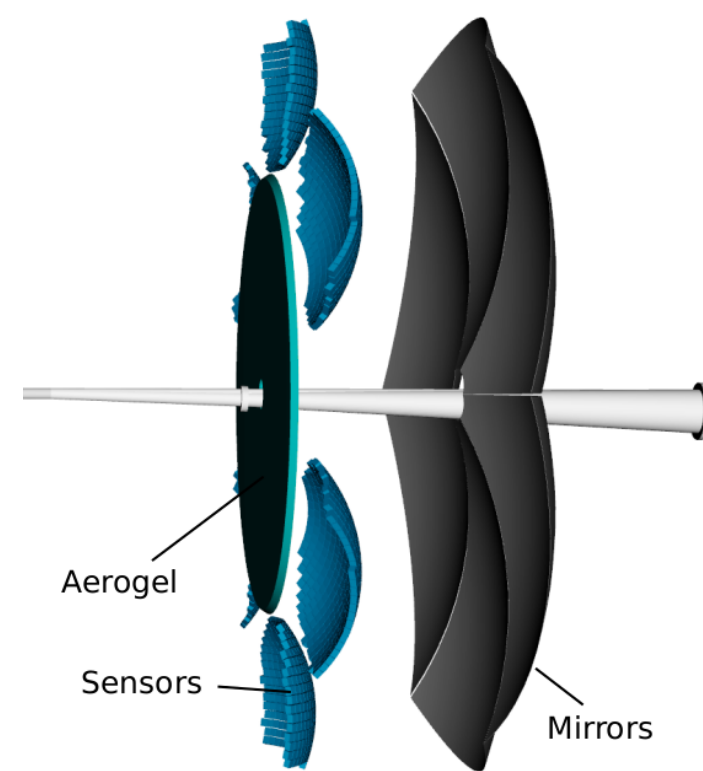
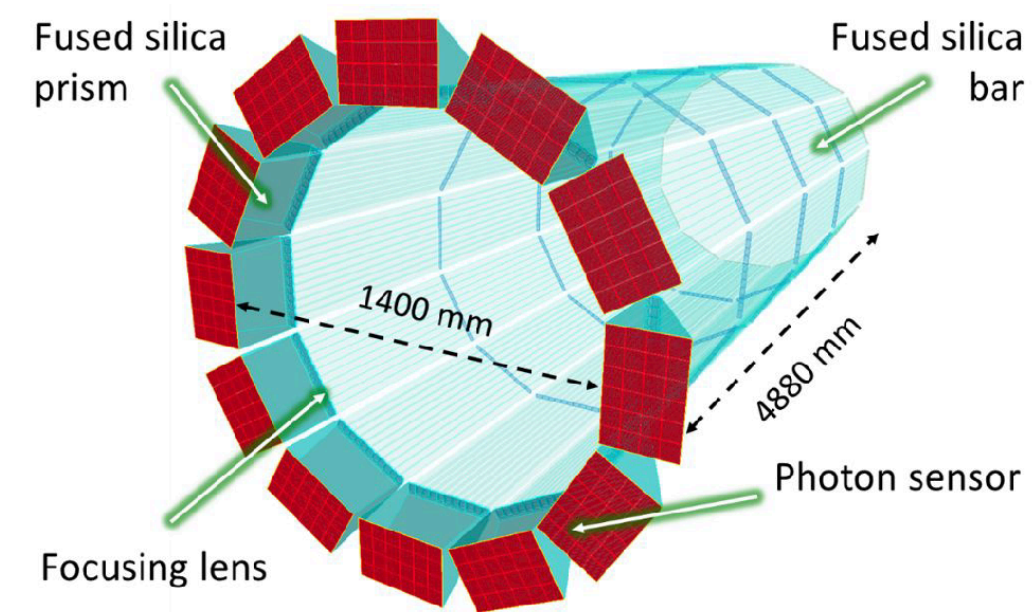


# PID Systems: TDR efforts and Progress

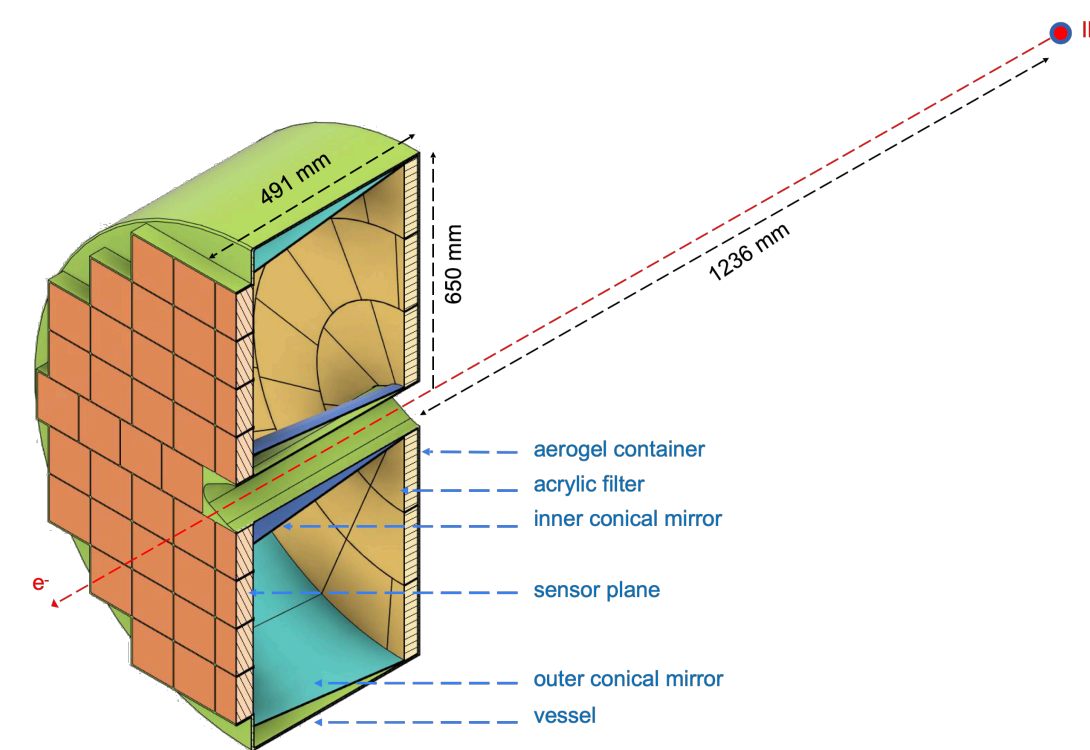
Thomas Ullrich on behalf of the PID DSCs  
TIC Meeting  
June 10, 2024



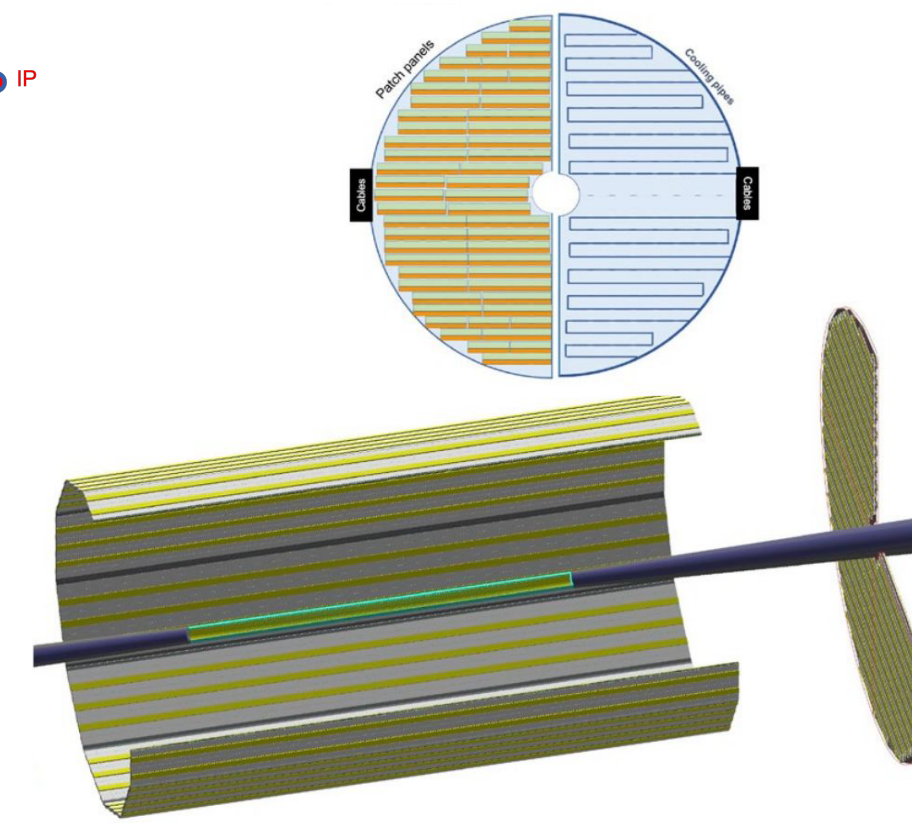
*dRICH*



*hpDIRC*



*pfRICH*



*ToF*

# Upfront - Plans for Lehigh Meeting

- PID DSCs prefer parallel type of meeting
- Focus on Cherenkov based detectors since ToF has separate all day meeting
- Details will vary according to DSCs

PID Working Group at Lehigh Collaboration Meeting (Breaks to be added)

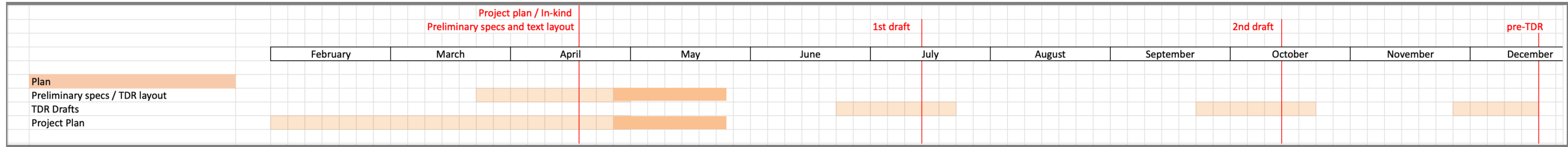
R&D	Personnel	Title	Duration (incl. discussion)	Time Start	Time End
<b>Welcome</b>			5m	9:00	9:05
<b>pfRICH</b>	TBD	Mechanics, frame, mirrors, aerogel	25m	9:05	9:30
<b>pfRICH</b>	TBD	HRPPD, Electronics	25m	9:30	9:55
<b>pfRICH</b>	TBD	Software, Sims, TRD Status	25m	9:55	10:20
<b>dRICH</b>	TBD	Mechanics, frame, mirrors, aerogel, gas	25m	10:20	10:45
<b>dRICH</b>	TBD	SiPM, Electronics	25m	10:45	11:10
<b>dRICH</b>	TBD	Software, Sims, TRD Status	25m	11:10	11:35
<b>hpDIRC</b>	TBD	Mechanics, frame, bars, exp vol	25m	11:35	12:00
<b>hpDIRC</b>	TBD	MCP-PMTs (HRPPD), Electronics	25m	12:00	12:25
<b>hpDIRC</b>	TBD	Software, Sims, TRD Status	25m	12:25	12:50
<b>ToF</b>	TBD	Update	25m	12:50	13:15
<b>Common Software</b>	Umberto	Belle-II and other modes + Discussion	30m	13:15	13:45
<b>Adjourn</b>			0m	13:45	13:45

Breaks to be added

Possibly move to common reco mtg. ←

**d***RICH*

# TDR Effort (2024)



April: Preliminary specs & text layout Project plan / in-kind preview

July: 1<sup>st</sup> draft

October: 2<sup>nd</sup> draft

December: Pre-TDR

Assumptions: Pre-TDR (CD2) required at the end of the year

Scheme driven by manpower/lead time: remains the same for a TDR (CD3)

Extra-time needed for real-scale mechanics & RDO demonstrators

# Si-PM Technical Specs

SiPM LLP Review Sep 2023

**Baseline sensor**

64 (8x8) channel SiPM array  
3x3mm<sup>2</sup>/channel

Parameters	Value	Notes (all parameters at the recommended operating voltage and T = 25 C, unless specified)
Device type	SiPM array	
Number of channels	64	8 x 8 matrix
Active Area	3 x 3 mm <sup>2</sup>	active area of one channel, total active area is 64 x 3 x 3 mm <sup>2</sup>
Device Area	< 28 x 28 mm <sup>2</sup>	device area should be small such as to have > 75% fraction of active area over device total area
Pixel Size	40 - 80 um	pitch of the microcell SPAD
Package Type	surface mount	
Operating voltage	< 64 V	
Peak Sensitivity	400 - 450 nm	
PDE	> 35%	at peak sensitivity wavelength
Gain	> 1.5 10 <sup>6</sup>	
DCR	< 1.5 MHz	
Temperature coefficient of Vop	< 60 mV / C	
Direct crosstalk probability	< 10%	
Terminal capacity	< 600 pF	
Packing granularity		
Vop variation within a tray	< 300 mV	Vop variation between channels in one device
Recharge Time	< 100 ns	ctau recharge time constant
Fill Factor	> 70%	
Protective Layer	silicone resin (n = 1.5 - 1.6)	radiation resistant, heat resistant (up to T = 180 C)
DCR at low temperature	< 10 kHz	at T = -30 C
DCR increase with radiation damage	< 1 MHz / 10 <sup>9</sup> neq	at T = -30 C, after a radiation damage corresponding to 10 <sup>9</sup> 1-MeV neutron equivalent / cm <sup>2</sup> (neq)
Residual DCR after annealing	< 25 kHz / 10 <sup>9</sup> neq	at T = -30 C, after a radiation damage of 10 <sup>9</sup> neq and a 150 hours annealing cycle at T = 150 C
Single photon time resolution	< 200 ps FWHM	corresponding to < 85 ps RMS

very important parameters to ensure detector performance over the years

we will evaluate as part of QA, testing sensor samples in received batches

# Preliminary Specs: ALCOR FEB & RDO & Mirror

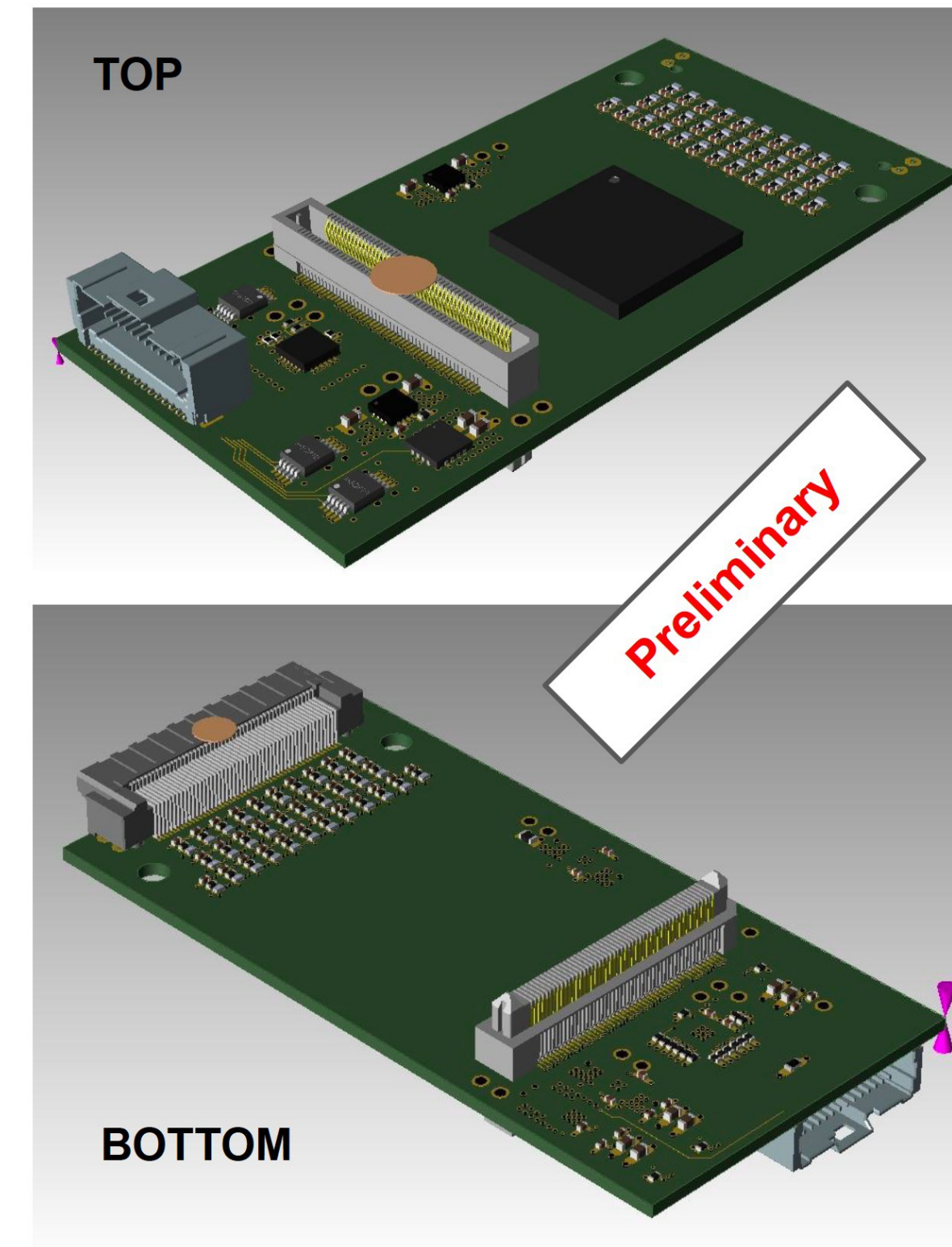
## RDO:

Component function	QTY	Baseline option	V	Comments
Main FPGA	1	Xilinx AU15P-SBVB484	0.85, 0.9, 1.2, 1.8, 2.5	<a href="#">Artix Ultrascale+ Overview</a>
Scrubber FPGA	1	Microchip MPF050T-FCSG325	1.0 1.2 1.8	<a href="#">Polarfire overview</a>
QSPI Flash	1	MT25QU01	1.7 - 2.0 V	package W9 6x8 mm <a href="#">Datasheet</a>
VTRX+	1	CERN	1.2V, 2.5V	<a href="https://edms.cern.ch/ui/file/2149674/1/VTRxPlusApplicationNote.pdf">https://edms.cern.ch/ui/file/2149674/1/VTRxPlusApplicationNote.pdf</a>
SIPMbus connector	2	<a href="#">Samtec</a> ERF5-020-05.0-L-DV-TR	N/A	
ALCORbus connector	2	Samtec ERF5-050-05.0-L-DV-K-TR	N/A	
ADC for NTC (4 = 1 per FEB)	2	Texas Instruments ADS1219-4	2,3-5.5 V	3x3 mm (WQFN package) <a href="#">Datasheet</a>
IO expander (I2C)	2	Microchip MCP23017	1.8-5.5 V	likely needed: we save 32 I/O on FPGA 6x6 mm 16 I/O <a href="https://ww1.microchip.com/downloads/en/DeviceDoc/90000000009837783.pdf">https://ww1.microchip.com/downloads/en/DeviceDoc/90000000009837783.pdf</a> <a href="https://www.ti.com/lit/ds/symlink/tmp119.pdf?ts=1711373203560&amp;ref_url=https%253A%252Fwww.ti.com%252Fproduct%252FTMP119">https://www.ti.com/lit/ds/symlink/tmp119.pdf?ts=1711373203560&amp;ref_url=https%253A%252Fwww.ti.com%252Fproduct%252FTMP119</a>
LDO	2	LTM4709	VDH VDL	6x12 mm <a href="#">Datasheet link</a> , <a href="#">Demo board link</a>
Temperature sensors	2	TMP116NAIDRV1 or TMP119	2.5	Close to LDO and VTRX <a href="https://docs.rs-online.com/2b49/A7000000009837783.pdf">https://docs.rs-online.com/2b49/A7000000009837783.pdf</a> <a href="https://www.ti.com/lit/ds/symlink/tmp119.pdf?ts=1711373203560&amp;ref_url=https%253A%252Fwww.ti.com%252Fproduct%252FTMP119">https://www.ti.com/lit/ds/symlink/tmp119.pdf?ts=1711373203560&amp;ref_url=https%253A%252Fwww.ti.com%252Fproduct%252FTMP119</a>
Step-Up Charge Pump	1	LTC3203	VDH	ededDH VBIAS a LDO <a href="#">Datasheet</a>
uC to read current monitor	1	ATtiny416	VDH	<a href="#">Datasheet</a>
Clock multiplier/ jitter cleaner	1	SkyWorks SI5326	1.8 or 2.5 V	6x6 mm, 2 input - 2 output <a href="#">Family Datasheet</a> and <a href="#">SI5326 Datasheet</a>
30T Crystal for SI5236	1		N/A	3.2 x 2.5 mm <a href="#">SkyWorks guidance</a>
Crystal oscillator	1			A 98.5 MHz crystal or "similar"

## ALCOR FEB:

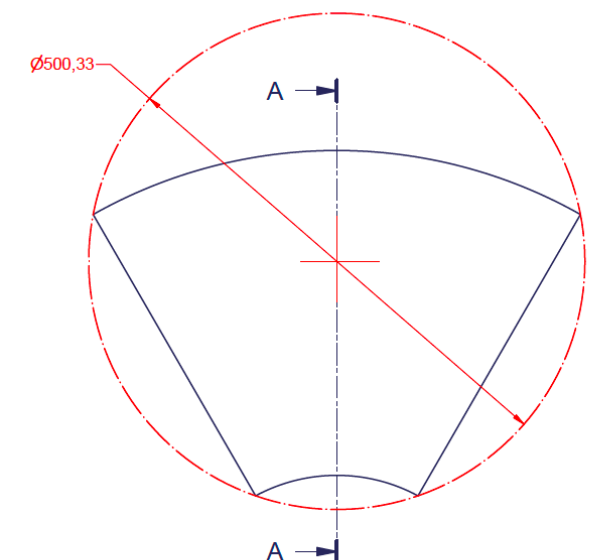
Preliminary selection of connectors and components:

- **Linear Regulators** (2.5 V DVDD\_IO, 1.2 V DVDD, 1.2 V AVDD): *Analog Devices ADP1752ACPZ-2.5-R7, ADP1761ACPZ-R7*
- **Current monitors** (before regulators): *Microchip Technology MIC2040-1YMM-TR*
- **I2C to Parallel-Port Expander** (read/control regulators and current monitors): *Texas Instruments PCF8575RGER*
- **RC High Pass Filter** (AC-coupling between SIPMs and ALCOR)
- **Annealing circuit:** to be included



## Mirror:

- Radius within 1% of nominal RoC value (the nominal RoC values is defined by the customer before production in the range 2000 mm +/- 10%),
- Roughness < 2 nm,
- Pointlike image spot size  $D_0 < 2.5$  mm,
- Compatibility with fluorocarbon gases ( $C_2F_6$ ),
- Compatibility with  $SiO_2$  reflecting coating.

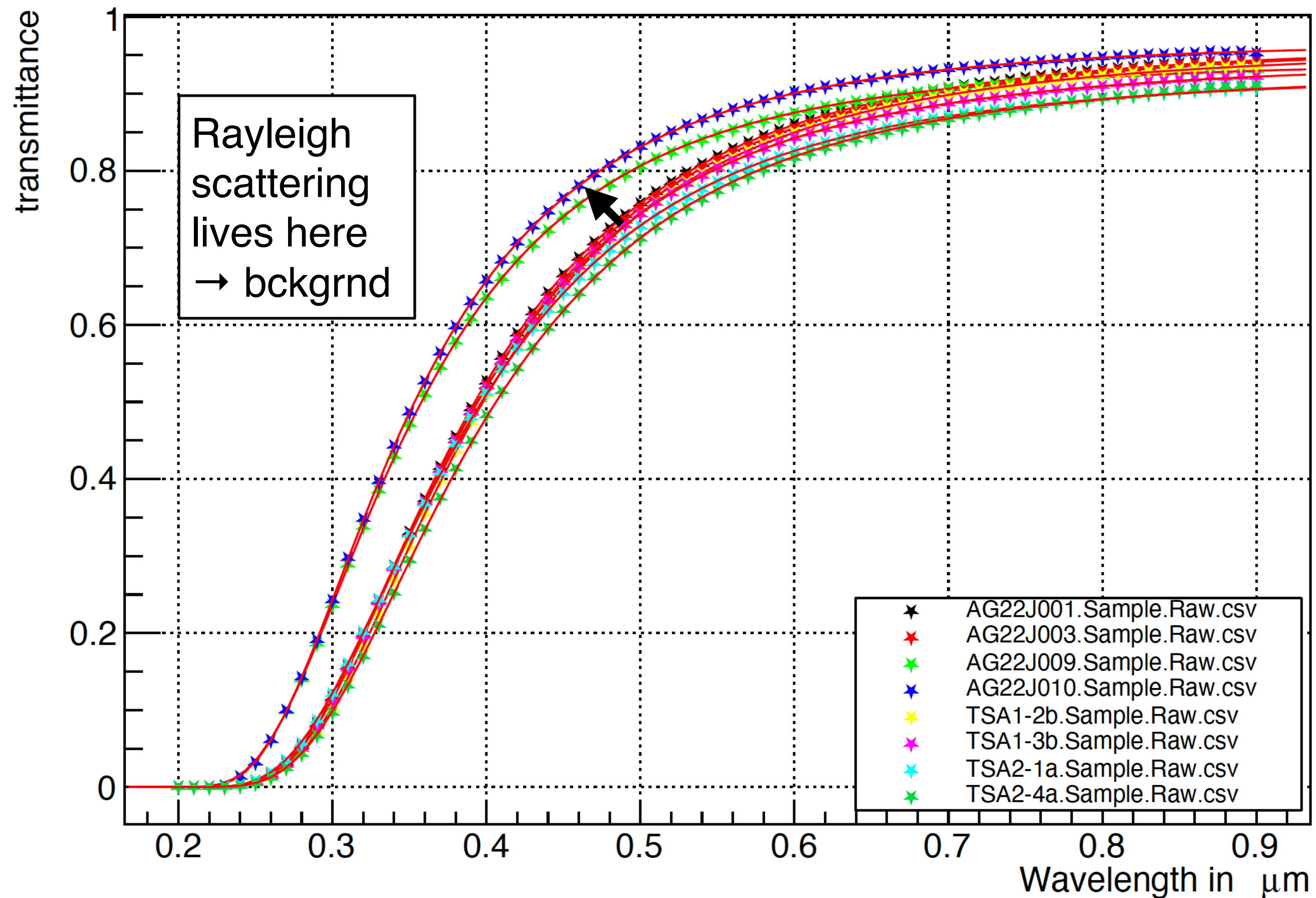


# Preliminary Specs: Aerogel

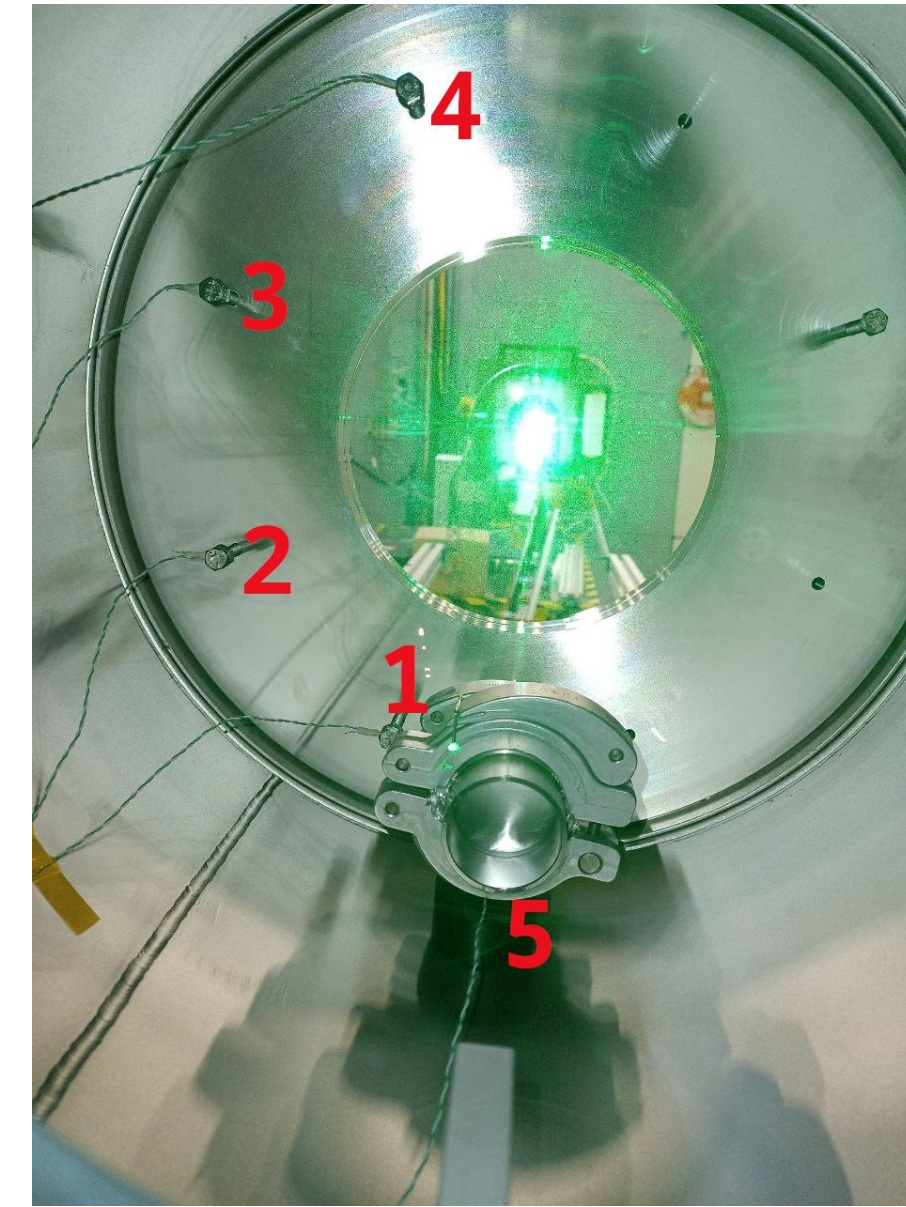
Optimization ongoing in the refractive index range 1.02-1.03

New samples received from Aerogel Factory

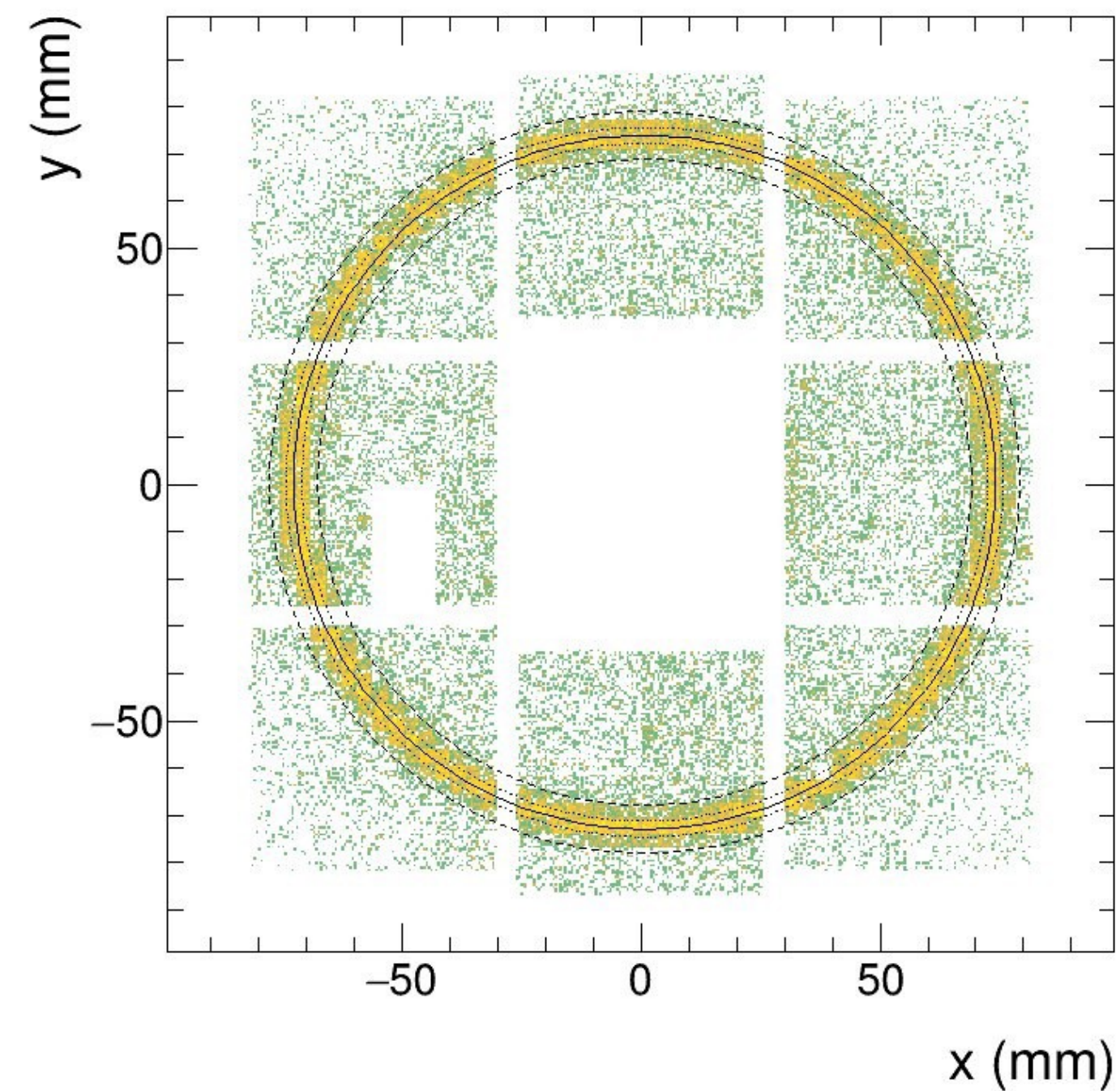
gTrans + Fit



# Prototype Test Beam at CERN



Temperature:



Aerogel Imaging:

$$X_0 = 0.72 \pm 0.01 \text{ mm}$$

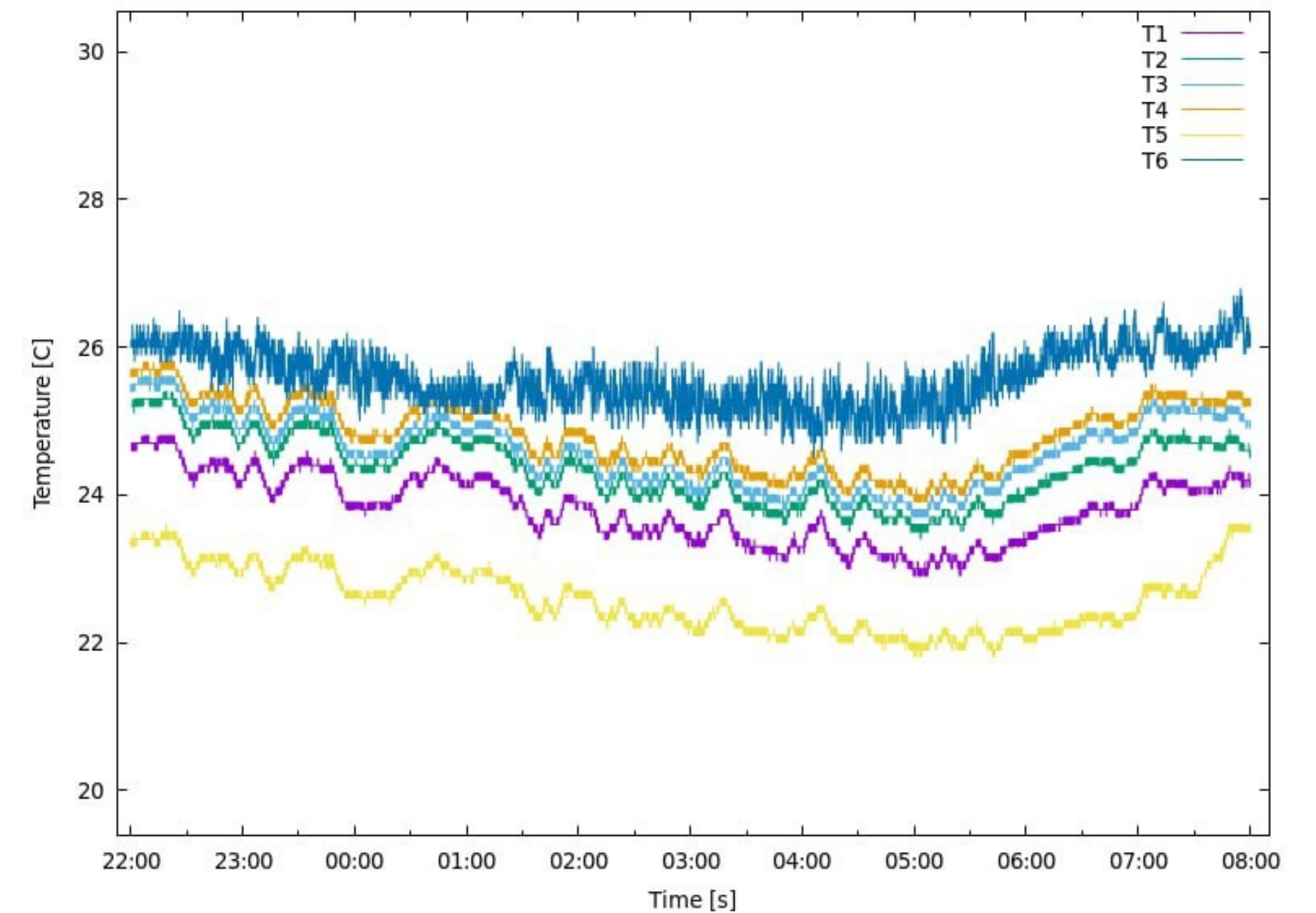
$$Y_0 = 0.50 \pm 0.01 \text{ mm}$$

$$R = 73.42 \pm 0.01 \text{ mm}$$

$$\sigma_R = 1.68 \pm 0.01 \text{ mm}$$

$$N_{\text{sig}} = 20.12 \pm 0.09$$

$$N_{\text{bkg}} = 12.55 \pm 0.10$$

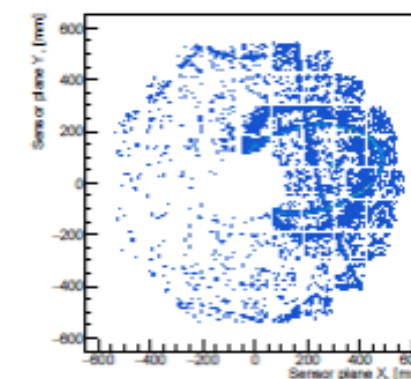
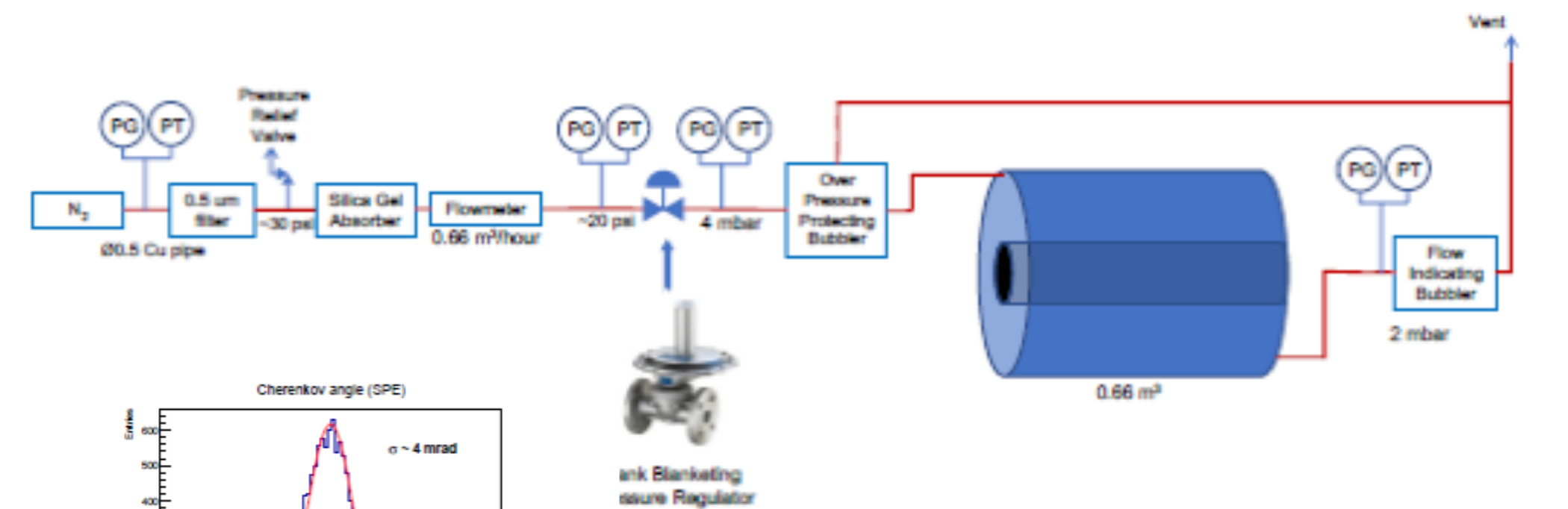
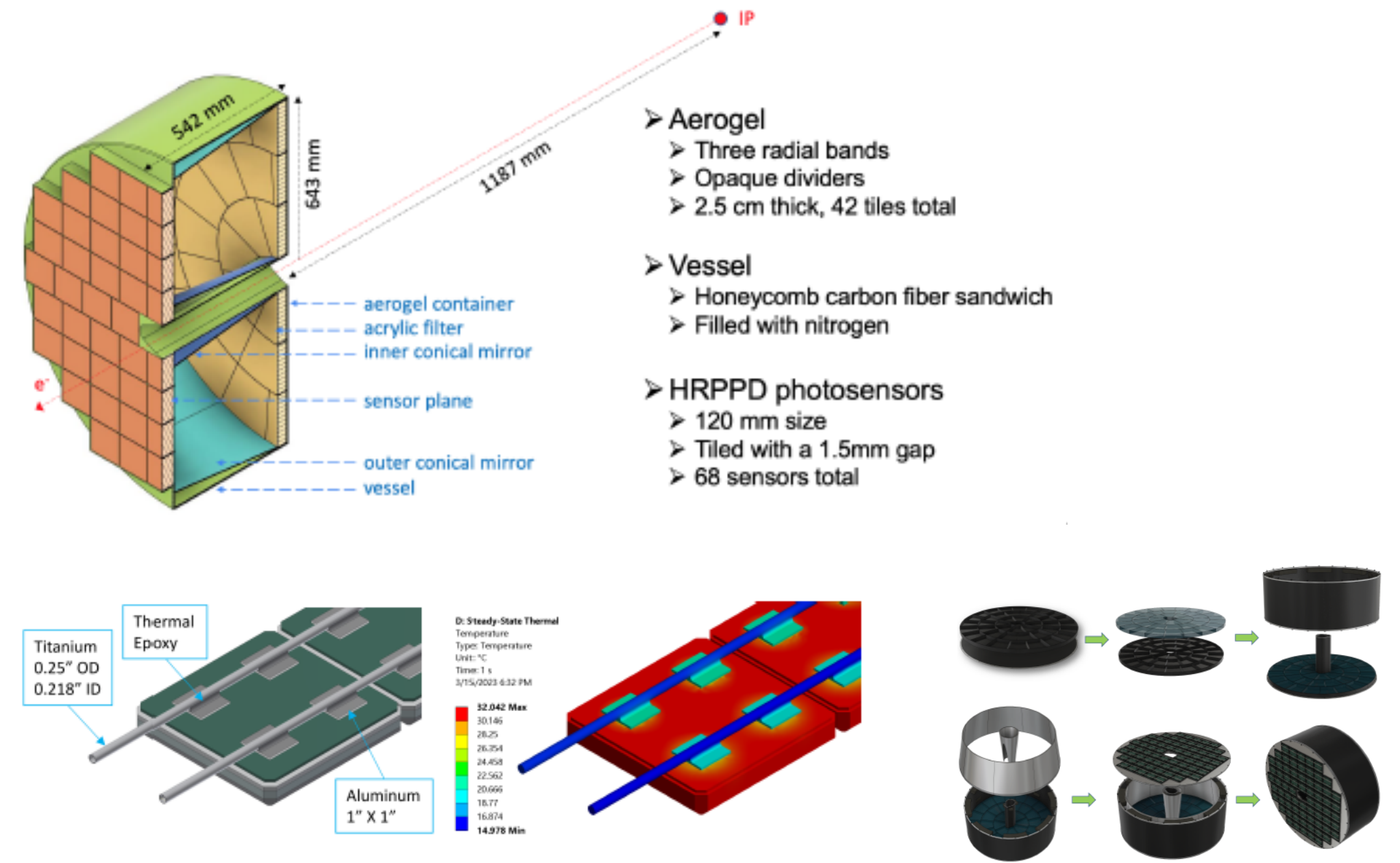




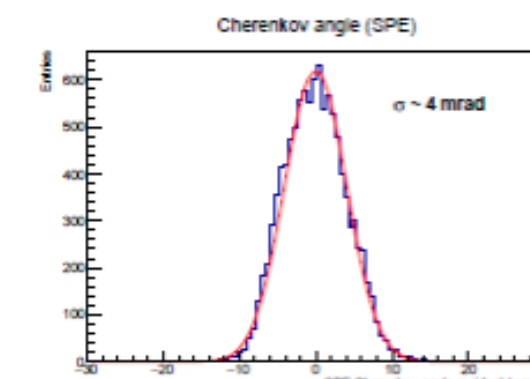
pf*RICH*

# TDR Document

- Solid base in the existing CDR
  - ▶ Much needs to be updated to reflect progress in last year
  - ▶ Several new sections will need to be planned / written
  - ▶ Need to condense existing sections to fit within allotted space
- Planning meetings ongoing
- Responsibilities and Plots established
  - ▶ Requirements
  - ▶ Justification
  - ▶ Implementation



(a) Photons reflected from the mirror detected in the sensor plane. As viewed in the event display



(b) Reconstructed SPE Cherenkov angle

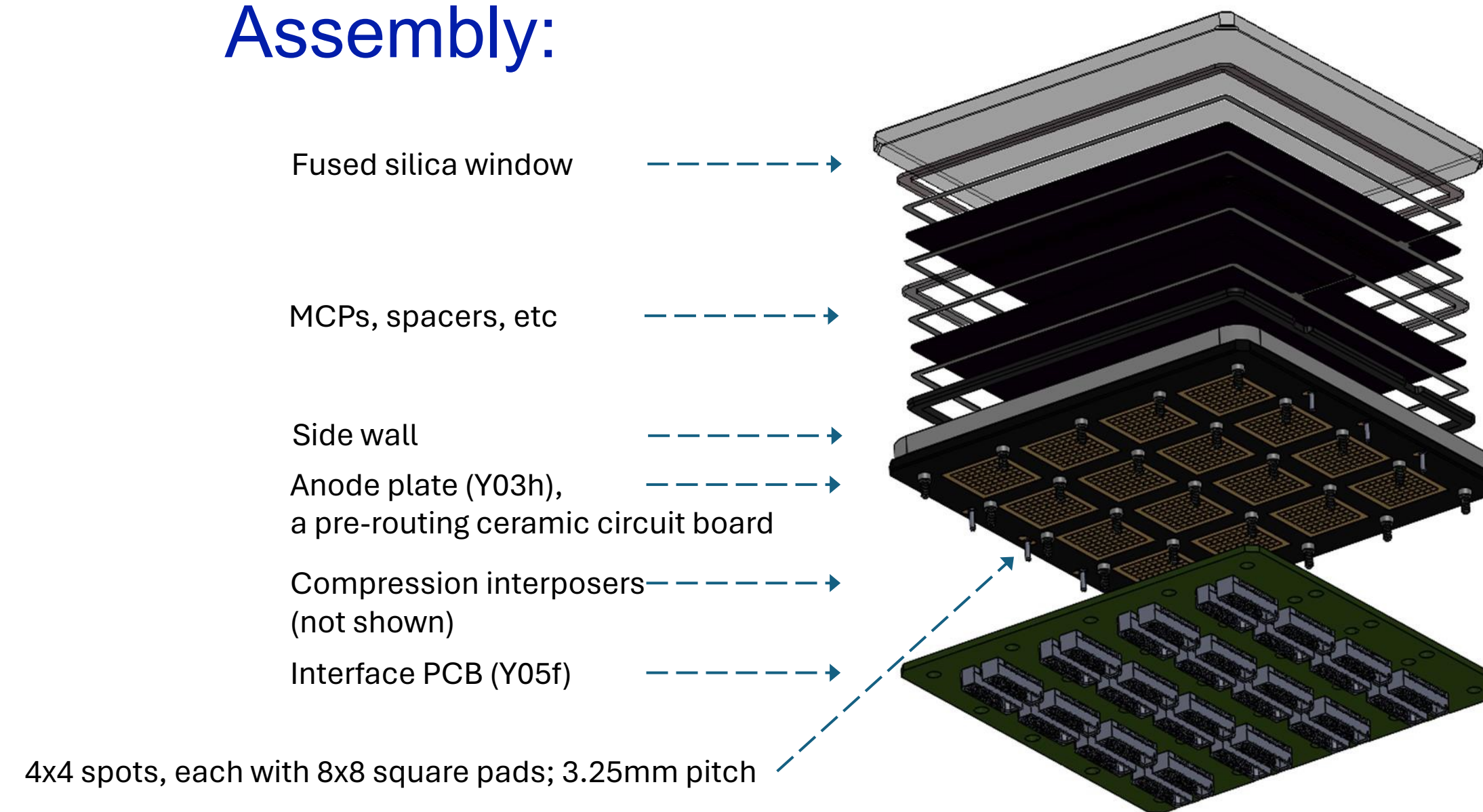
# HRPPDs

Remaining 2 EIC specific HRPPDs send to Lab

First HRPPDs now at BNL being tested

Started regular meetings with INCOM (Wed 10am)

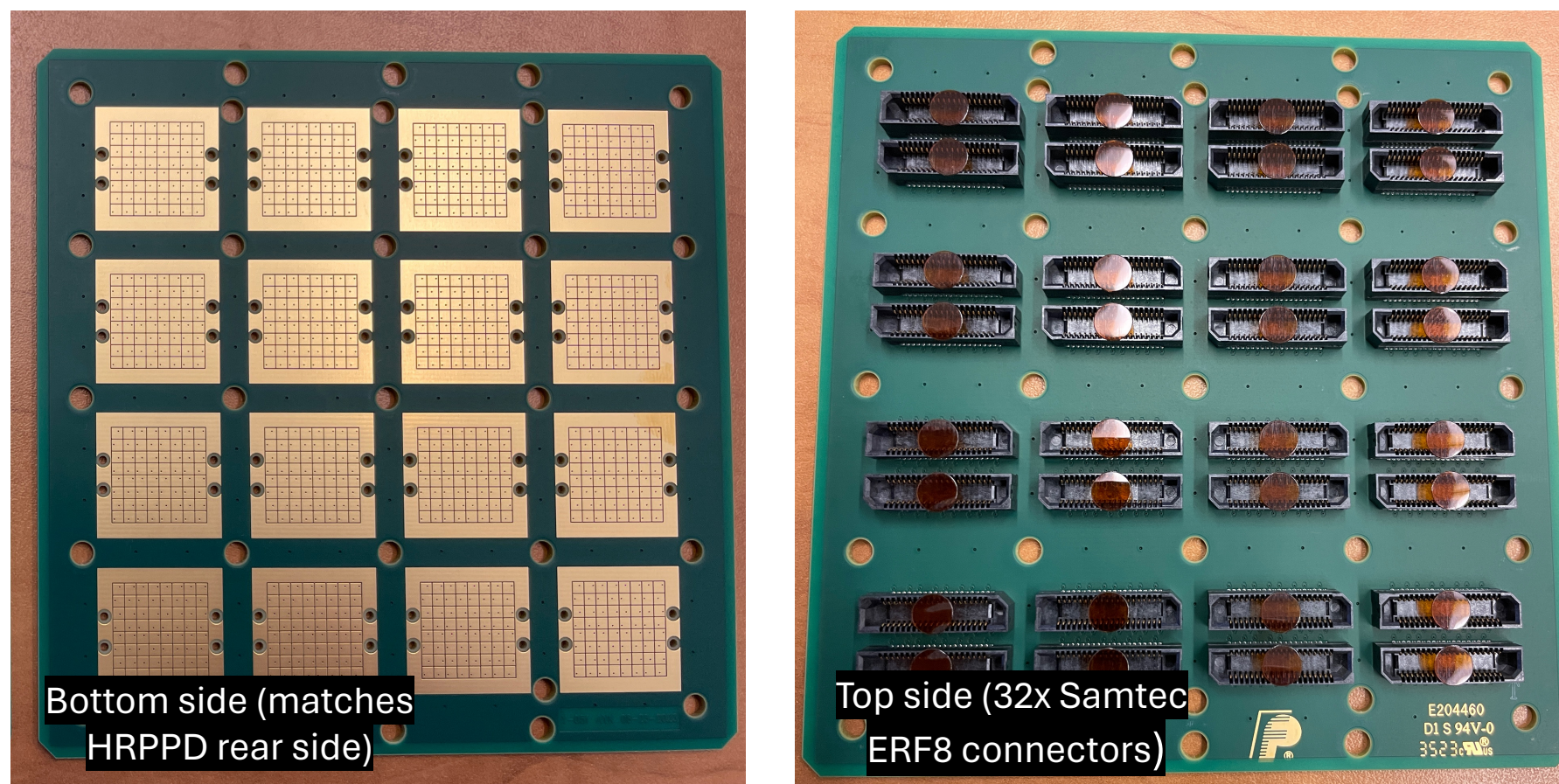
## Assembly:



## Charge path:

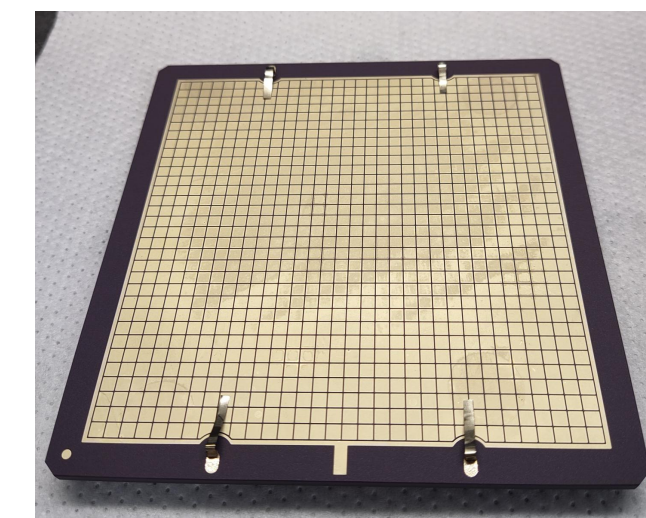
- inner side anode pads
- anode plane stackup
- outer pads
- compression interposers
- interface PCB
- MMCX adapter PCB
- pigtail RG-316 (?) cables
- 6" RG-174 cables
- V1742 digitizer

## Passive interface PCB:

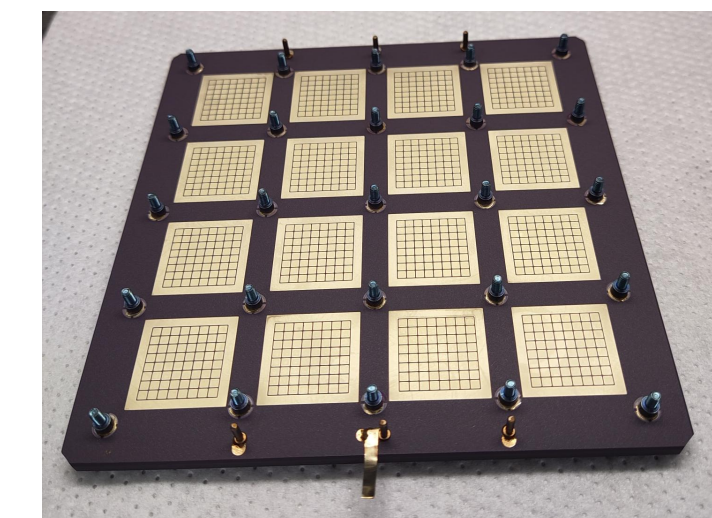


➤ A simple 119mm x 119mm two-layer board with Samtec ERF8 connectors

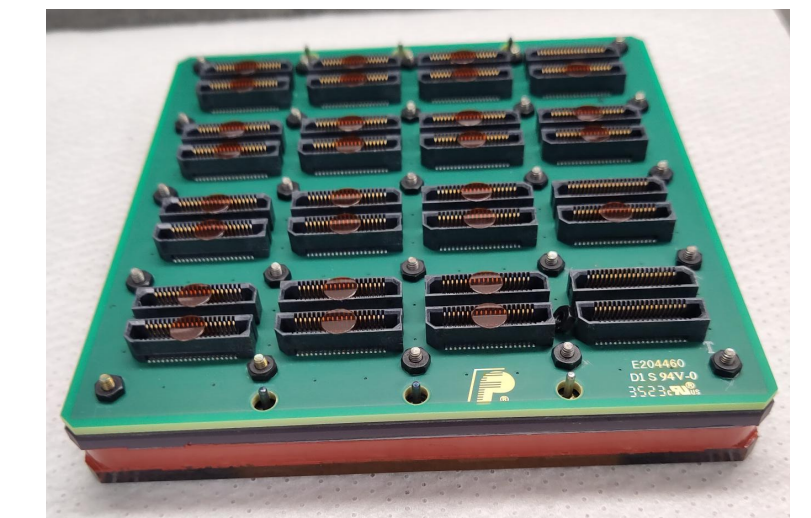
## Gallery:



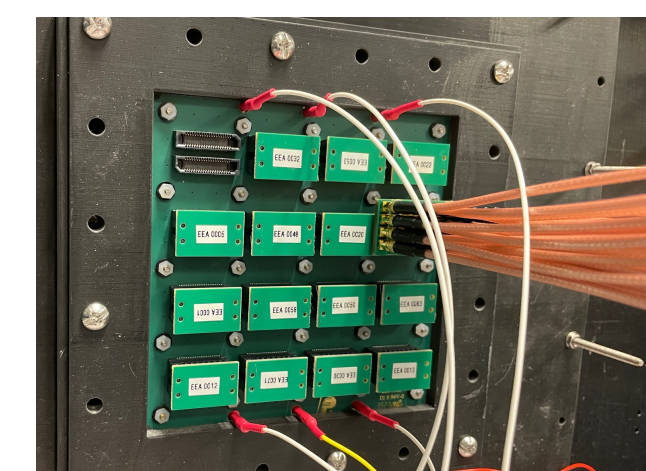
Anode plate vacuum side



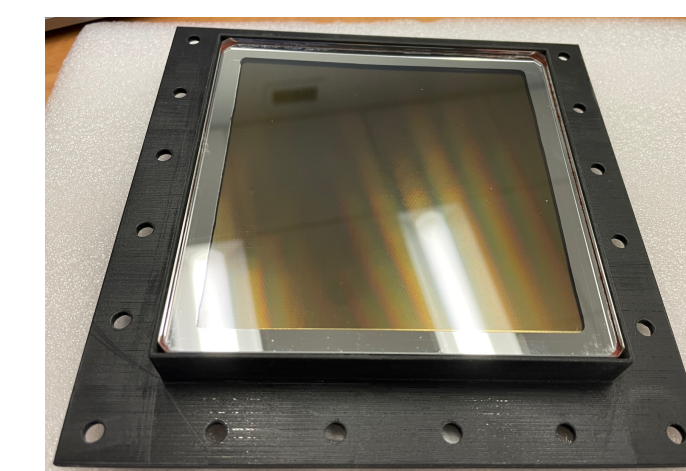
Anode plate air side



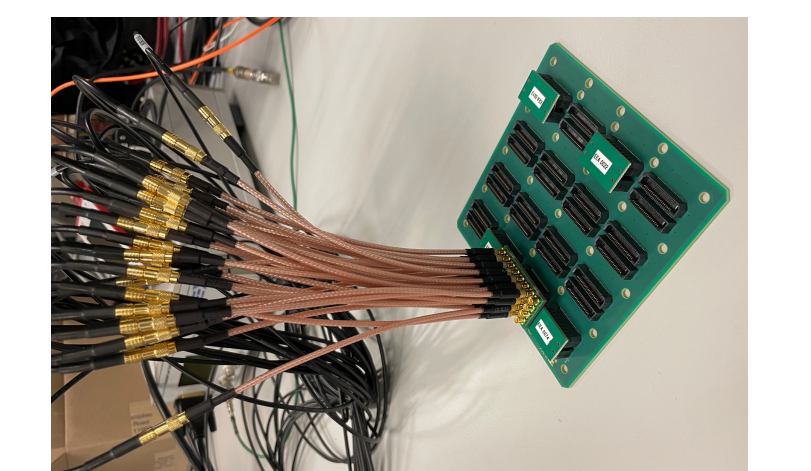
With Y05f board mounted



With MMCX interface



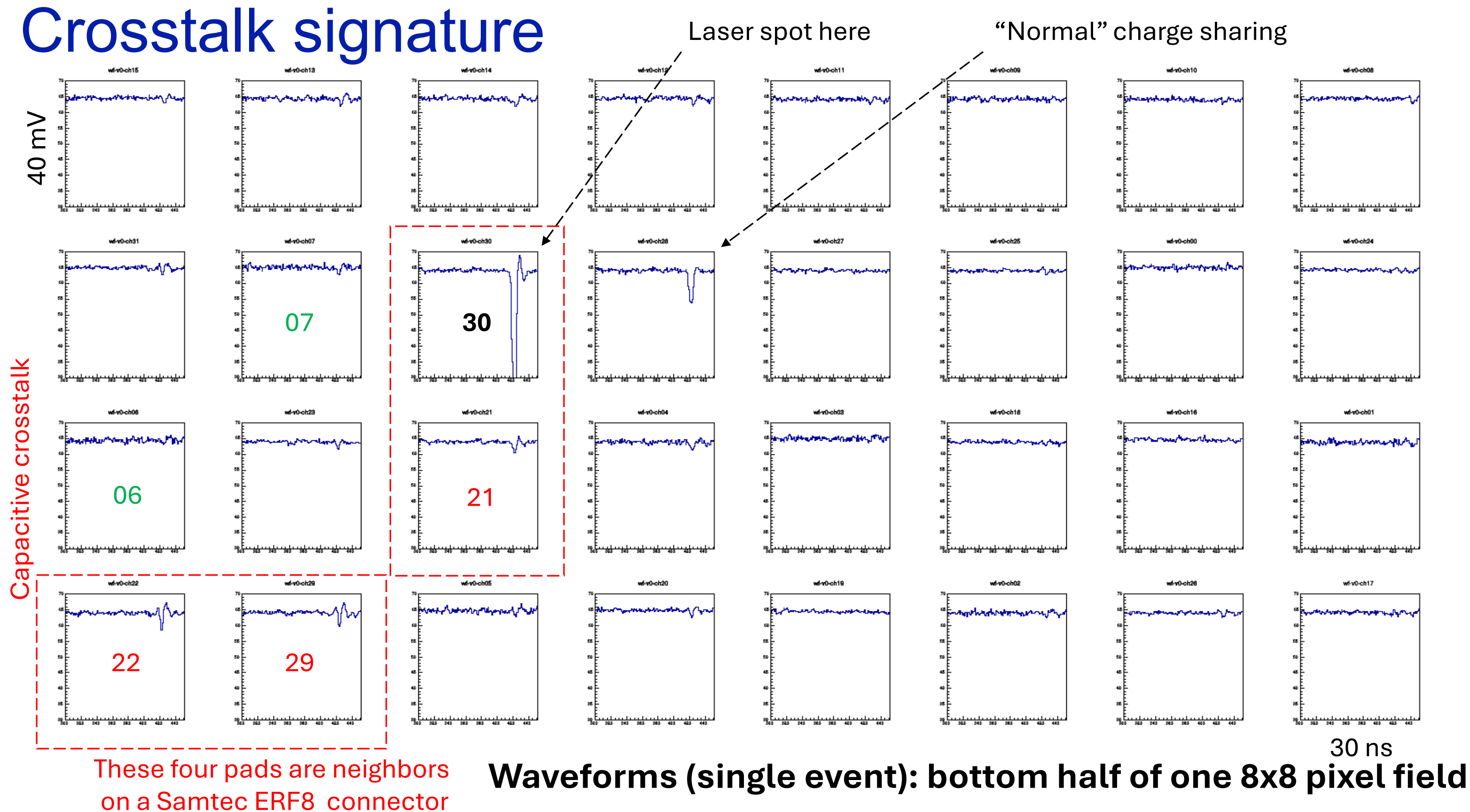
Fused silica window



MMCX -> MCX pigtail cables

# Crosstalk

- Observe crosstalk in HRPPDs
- Not related to HRPPD itself but in current backplane
- Good enough for single photon but not for high occupancies studies



hp *DIRC*

# hpDIRC Annual Workshop in Jefferson Lab

- May 16th – 22nd ( <https://indico.bnl.gov/event/23332/> )
- 11 participants in person, 7 participants online (some only for specific sessions)
- All sessions had focus on TDR readiness and overall hpDIRC/ePIC schedule
- Designated TDR sessions used to identify remaining studies, required figures, and write detailed plan
- Several days before and after the meeting were used to work on hpDIRC project planning, schedule, and updating P6 plan

Day	Date	Morning	Afternoon
Thursday	May 16	MCP-PMTs	Sensors SiPMs / TDR
Friday	May 17	Test Besam NIM paper	BaBar bar boxes / eRD103
Saturday	May 18	Simulation Studies	CRT / PicoSec / eRD103
Sunday	May 19	ePIC Simulation	TDR
Monday	May 20	Simulation Studies	BaBar bar boxes / eRD103
Tuesday	May 21	Mechanical Design	TDR
Wednesday	May 22	TDR	BaBar bar boxes / Project Planning

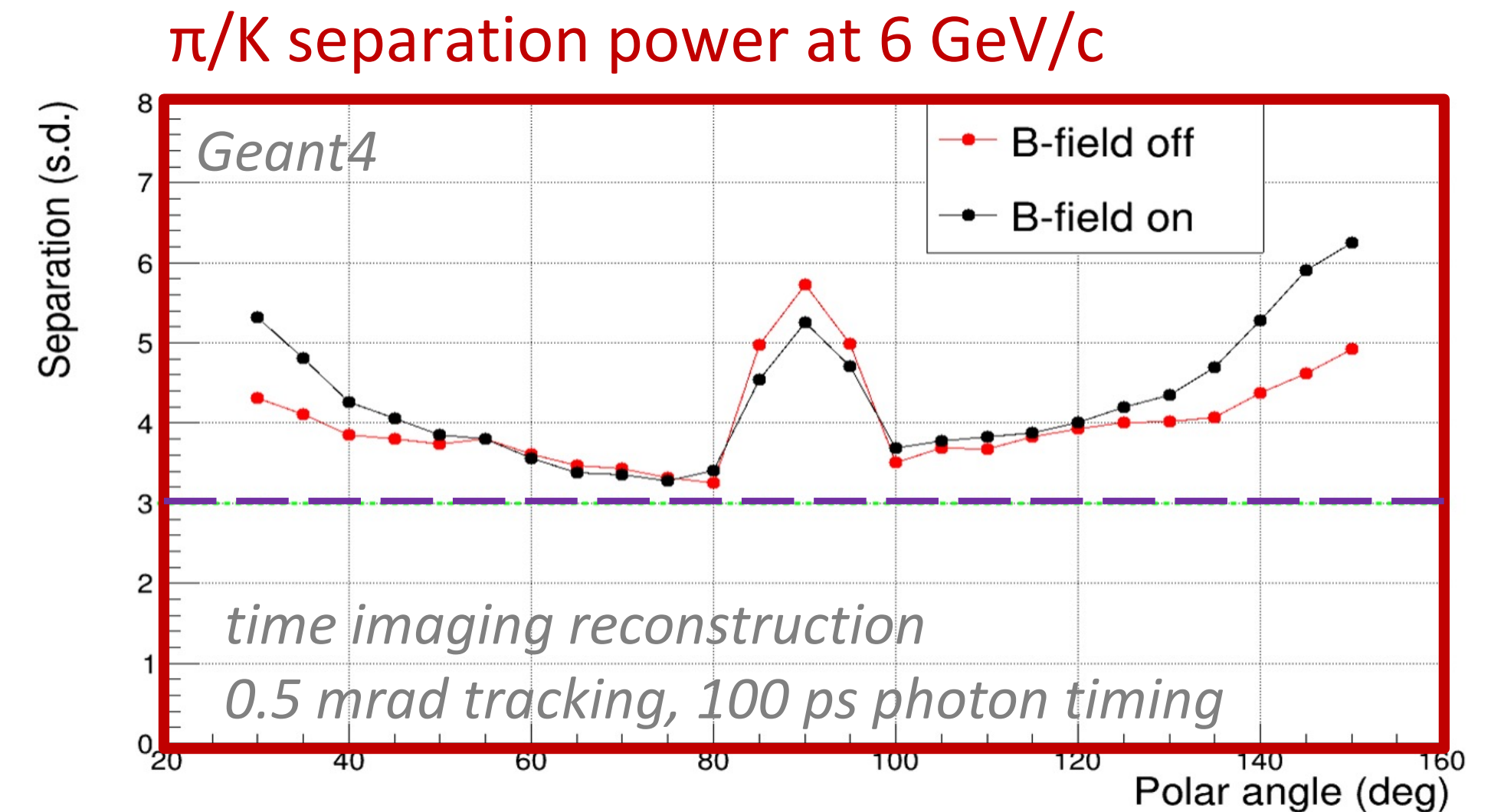
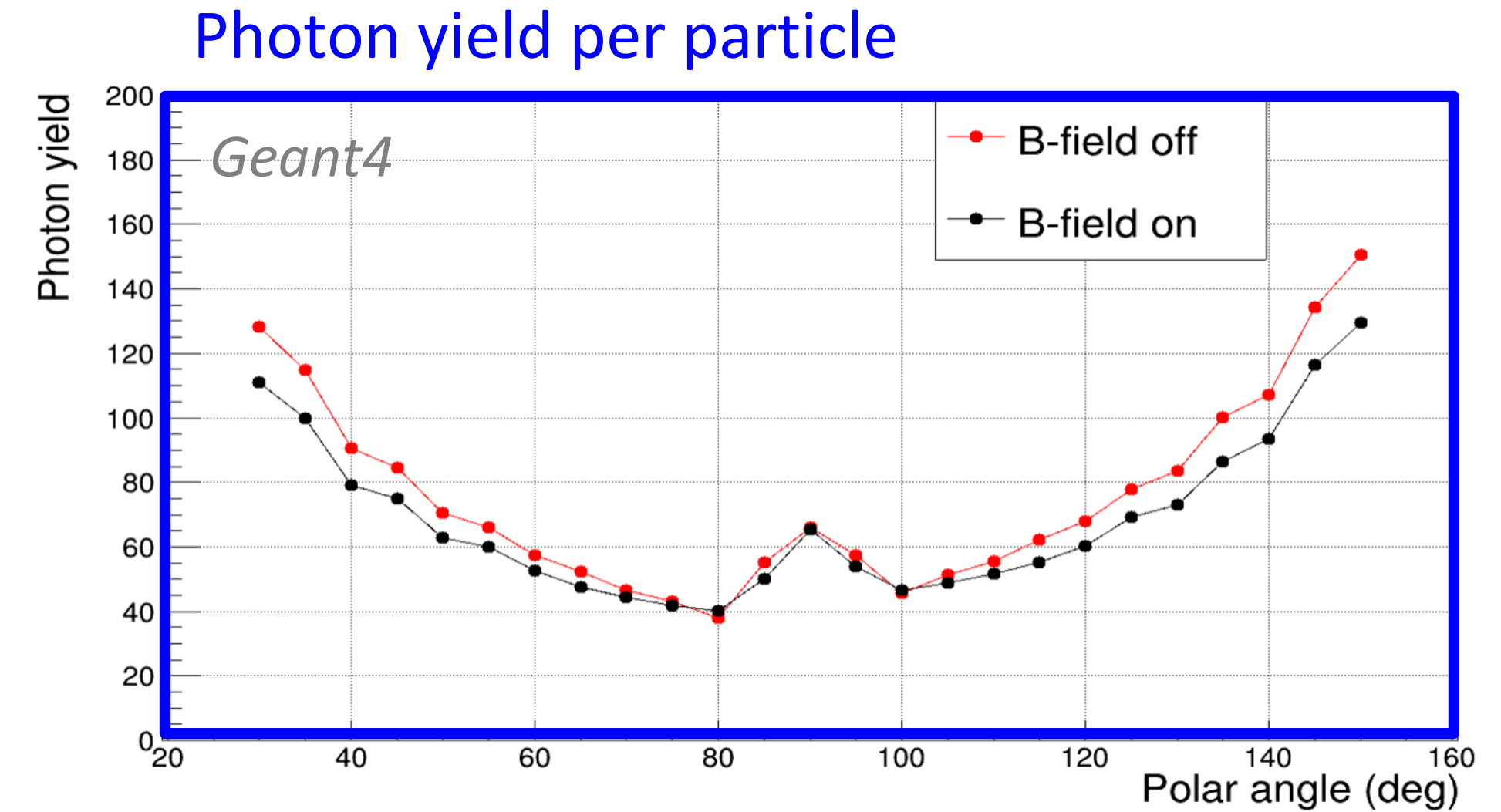
# TDR Section Outline Prepared

- Relevant needed figures identified
- Some needed figures will be referenced (B field, radiation map, etc.), might include them in paper with ZOOM to hpDIRC region
- Detailed breakdown of needed content – ready to write!
- Remaining questions/studies identified and assigned
- Performance plots will be updated for final geometry and are easy to adjust to uniformly agreed representation and style with other systems

Section	Subsection	Content
Requirements/Motivation		
	Performance	
	Integration	
System Description		
	Concept	hpDIRC unique aspects
	Design	description of components, how the required performance (KPP) will be achieved
	Performance	description of simulation and reconstruction method, CERN validation
	Calibration	alignment - survey marks, experimental data for calibration
Implementation		
	Mechanical	Design and integration, Assembly of modules, Installation
	Services	nitrogen, cooling, voltage, controls and monitoring, laser calibration
	Other activities needed	
	QA	CRT (Full module), Readout (Sensors + Front-end Electronics), Bars/Mirrors (Laser Lab in JLab), Prisms (?), Lenses (ODU setup)
	Timeline, workforce, work packages	
	ES&H	
	Risk mitigation	Readout electronics, Sensor (Whatever is not tested)

# Performance Studies

- Updates to hpDIRC reconstruction previously done have no impact on performance, small impact on acceptance
- Studies of hpDIRC performance were done with test beam validated simulation
  - ▶ Realistic ePIC magnetic field map was used
  - ▶ Studies with Pythia physics events were done
  - ▶ Multiple tracks per event in single bar showed very small impact on performance
  - ▶ Most studies assumed **0.5 mrad angular tracking resolution** but software ready to import and include detailed parametrization of tracking

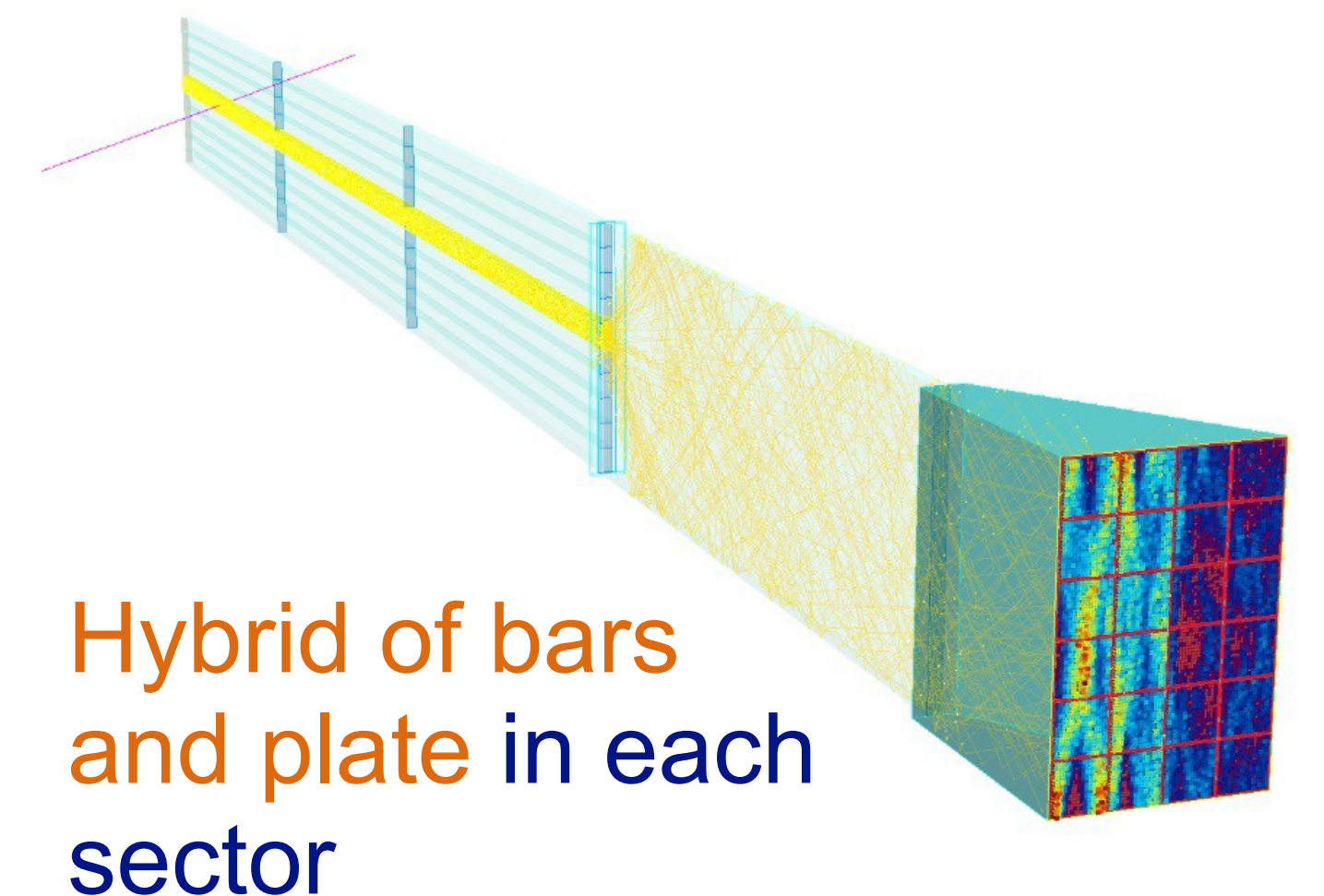
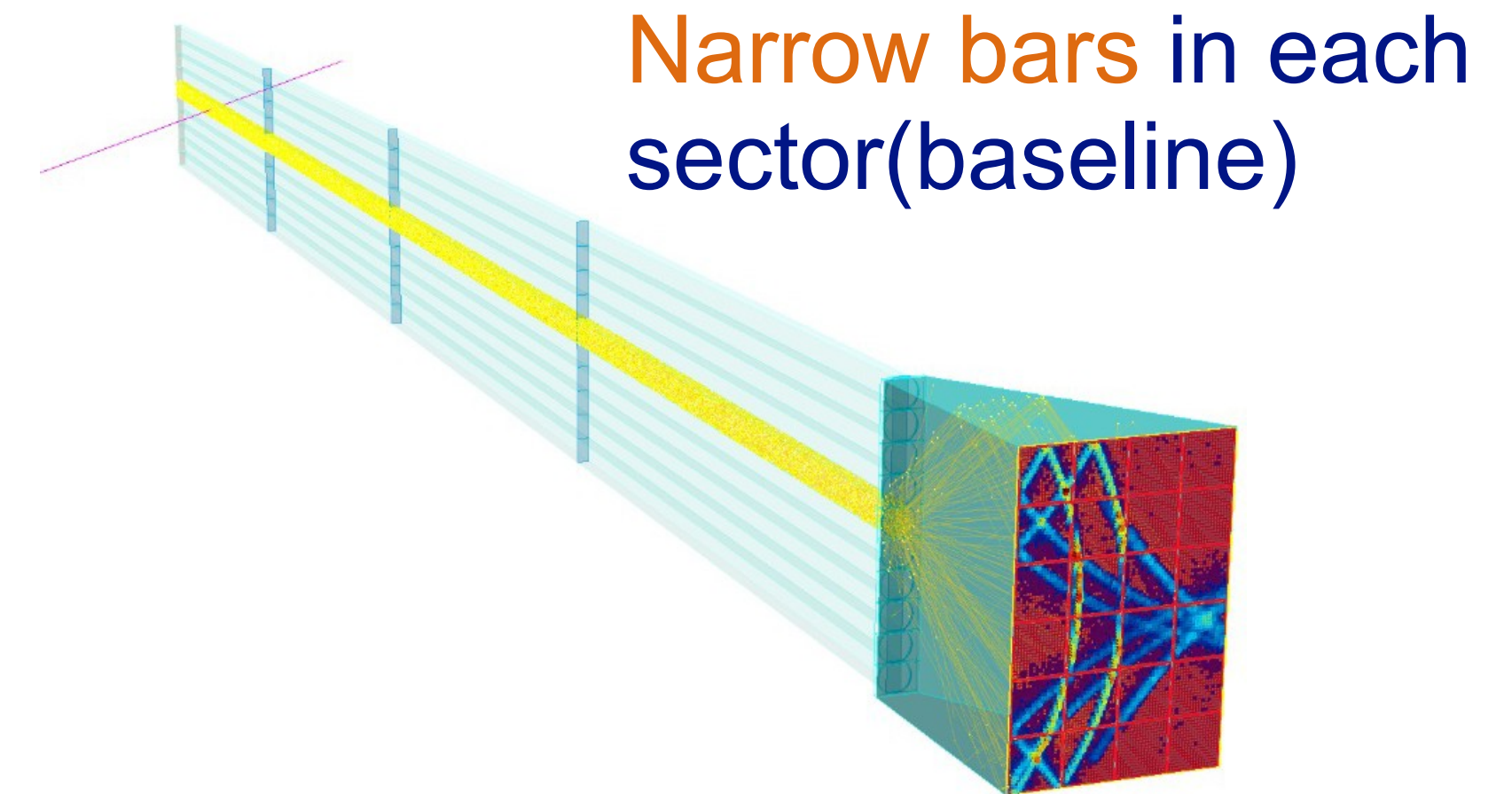




# TDR Preparations: Remaining Questions/Studies

- Possibility of reusing BaBar DIRC bars
  - ▶ late fall (currently still in boxes)
- Decision on plate vs narrow bars for lightguide section
  - ▶ late summer/early fall
- Optimal bar width in case new are needed
  - ▶ late summer/early fall
- "Split-Prism" expansion volume option as part of cost/risk mitigation
  - ▶ late summer/early fall
- Potential software-based multiple scattering mitigation
  - ▶ late summer/early fall

Geant4 visualization of the two light-guide options

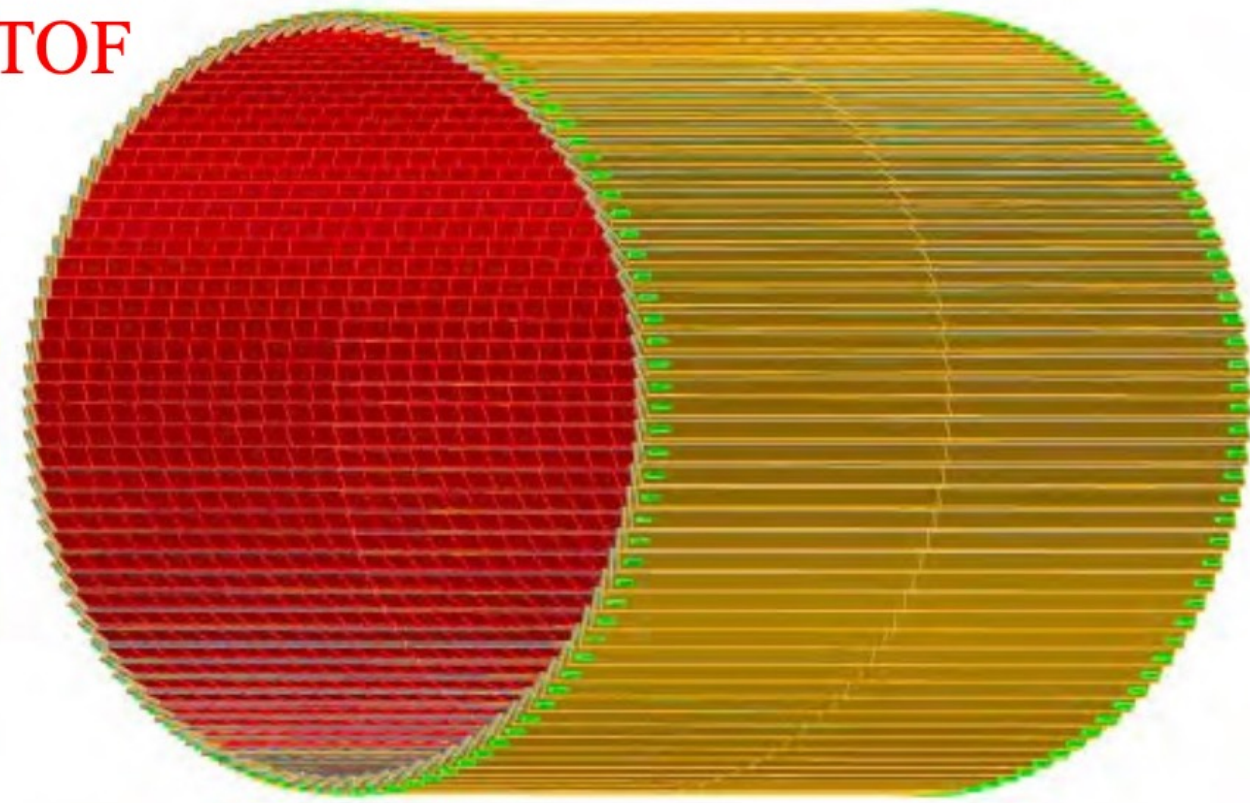


TOF

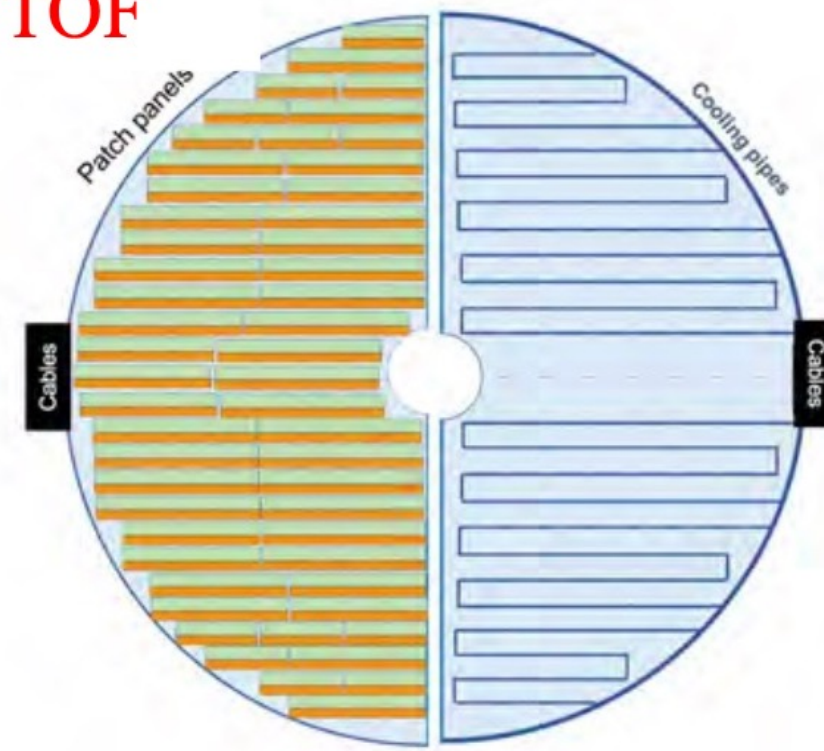
# ToF - Key Elements for TDR

## Detector configurations and Key requirements

BTOF



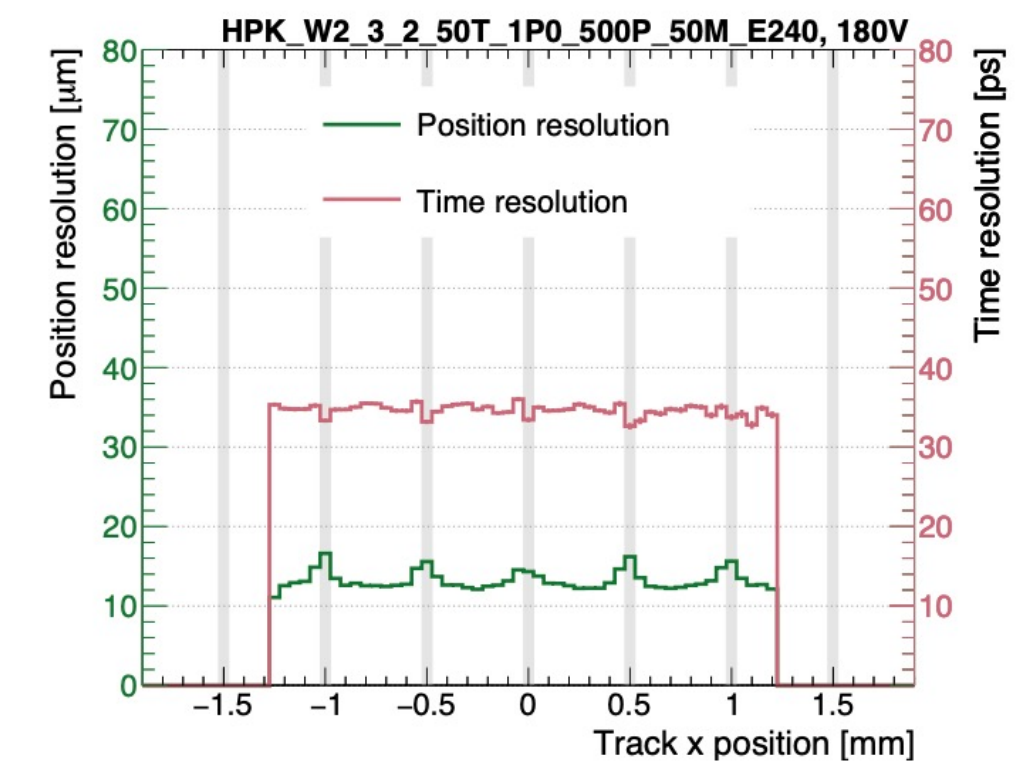
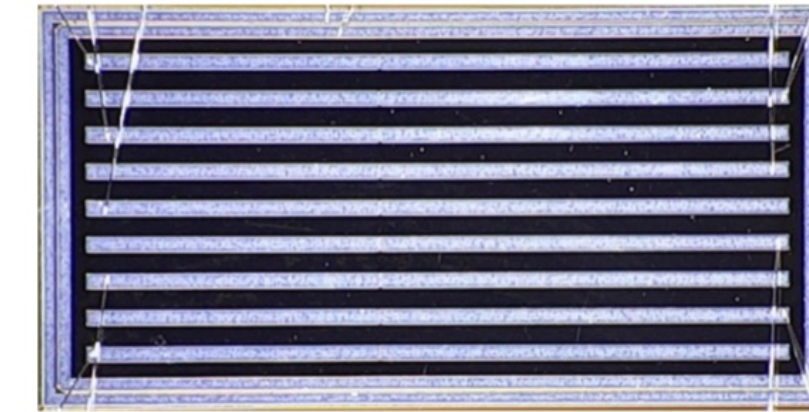
FTOF



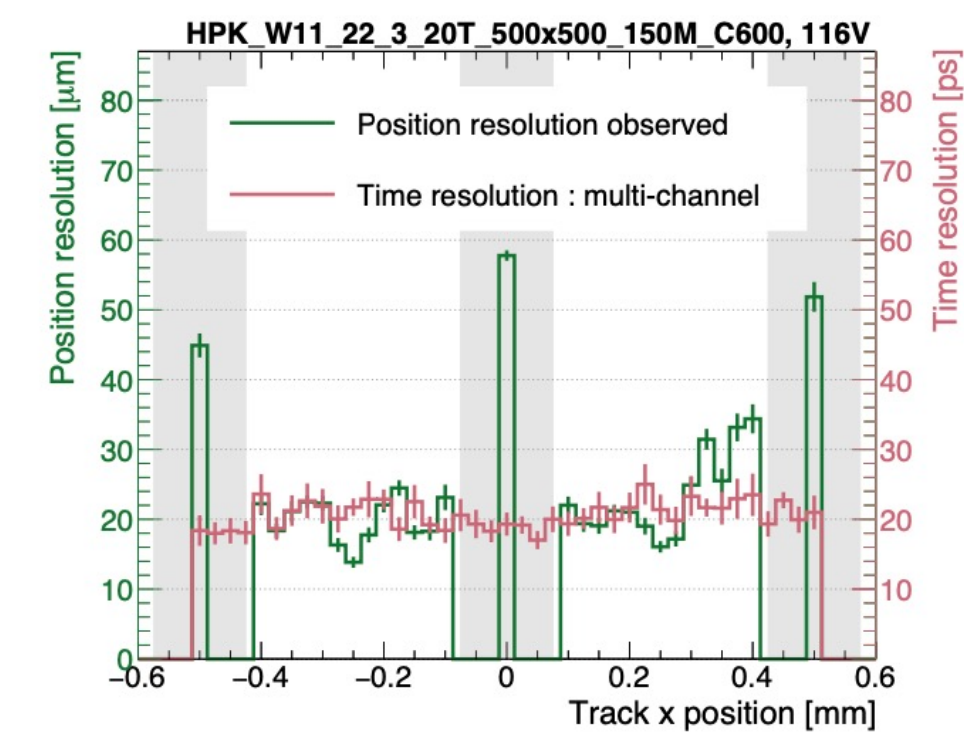
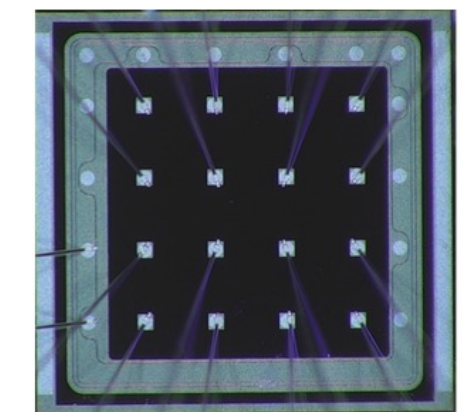
	Area (m <sup>2</sup> )	Channel size (mm <sup>2</sup> )	# of Channels	Timing Resolution	Spatial resolution	Material budget
Barrel TOF	10	0.5*10	2.4M	35 ps	30 $\mu\text{m}$ in $r \cdot \varphi$	0.01 $X_0$
Forward TOF	1.4	0.5*0.5	5.6M	25 ps	30 $\mu\text{m}$ in x and y	0.05 $X_0$
B0 tracker	0.07	0.5*0.5	0.28M	30 ps	20 $\mu\text{m}$ in x and y	0.05 $X_0$
RPs/OMD	0.14/0.08	0.5*0.5	0.56M/0.32M	30 ps	140 $\mu\text{m}$ in x and y	no strict req.
Lumi Tracker						

## Position and timing resolutions

HPK Strip Sensor (4.5x10 mm<sup>2</sup>)



HPK Pixel Sensor (2x2 mm<sup>2</sup>)



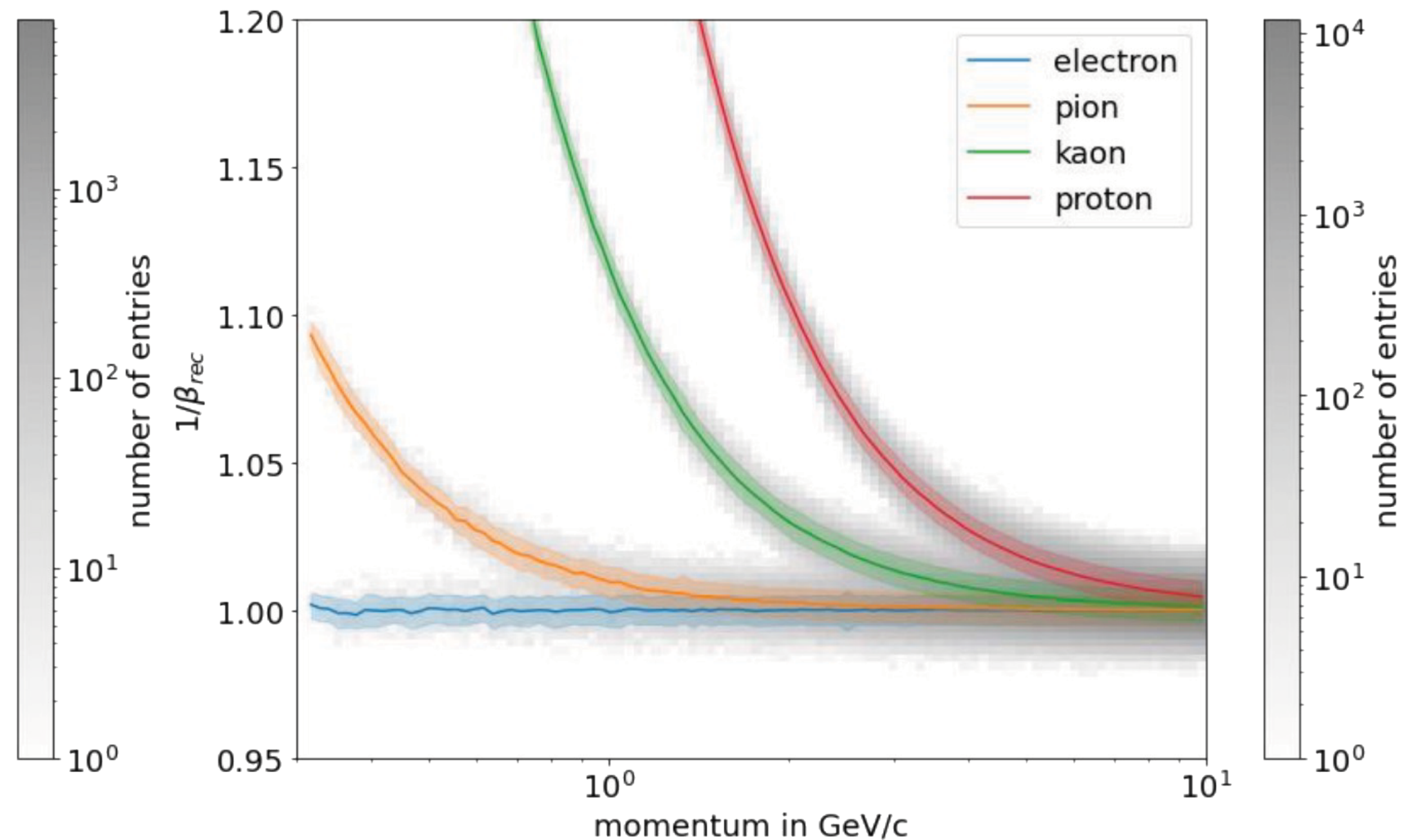
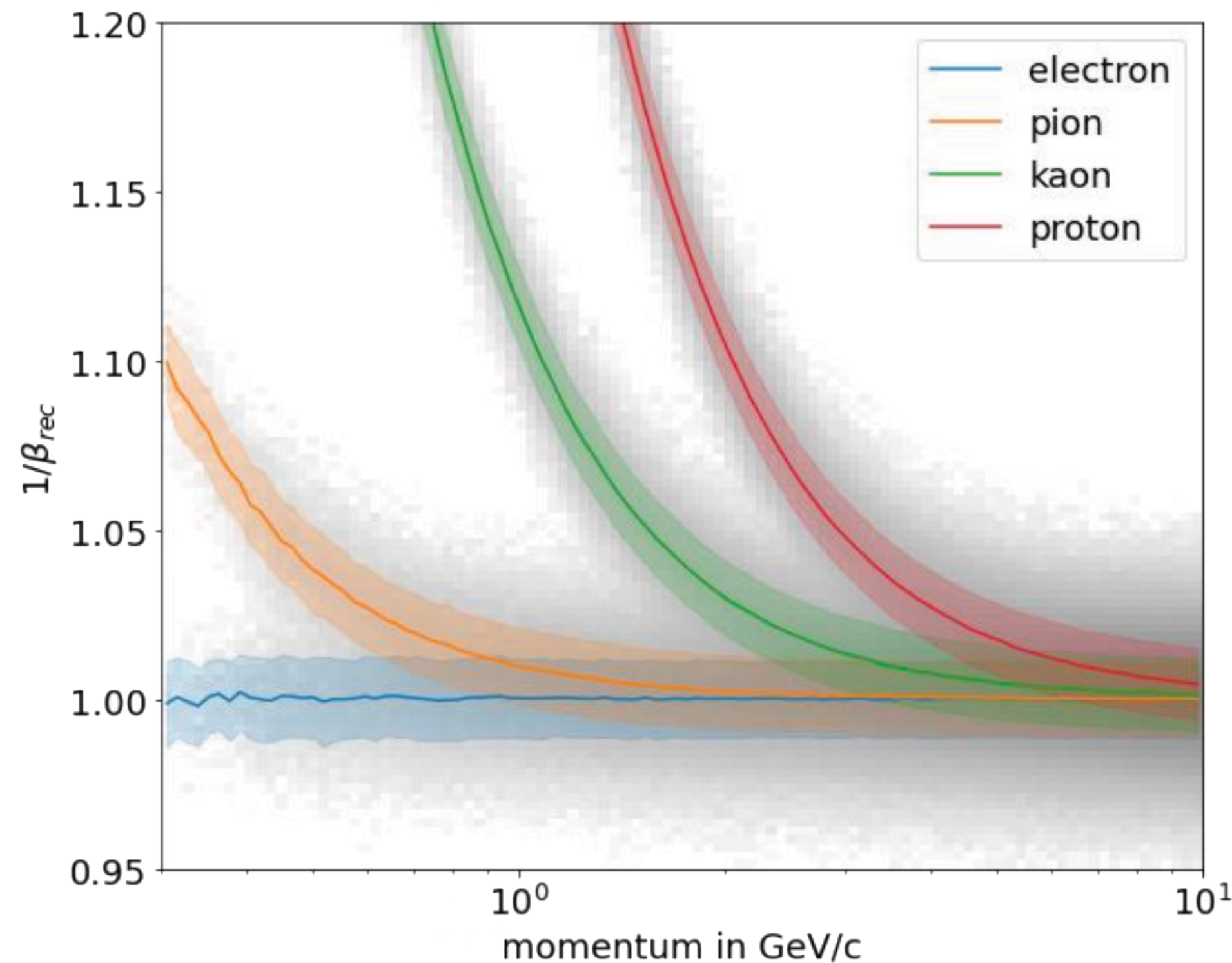
# ToF - Key Plots on ToF Performance

- Barrel Region

- ▶  $e/\pi$  up to 0.5 GeV/c
- ▶  $\pi/K$  up to 1.9 GeV/c
- ▶  $K/p$  up to 3.1 GeV/c

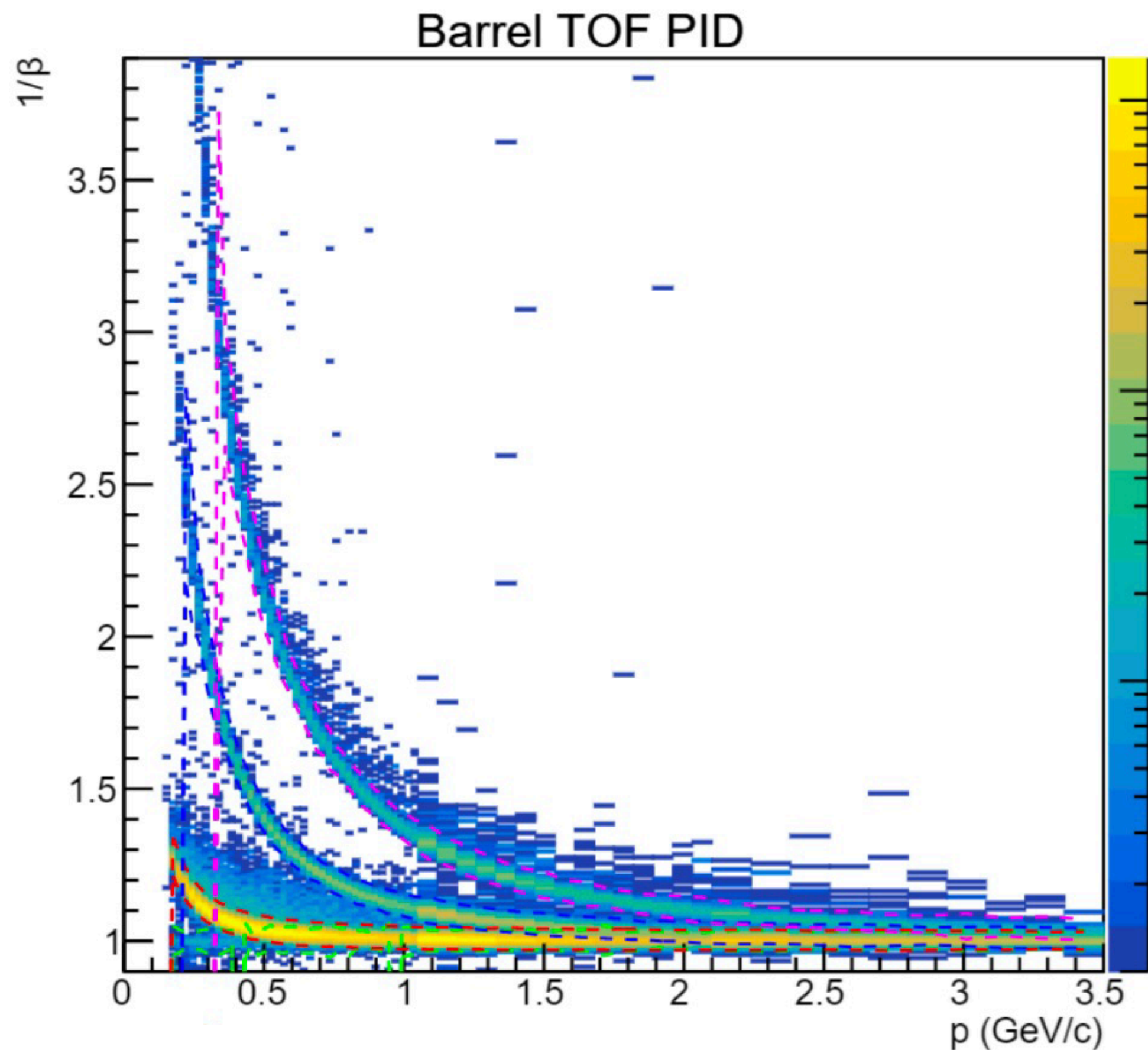
- Endcap Region

- ▶  $e/\pi$  up to 0.8 GeV/c
- ▶  $\pi/K$  up to 2.7 GeV/c
- ▶  $K/p$  up to 4.6 GeV/c

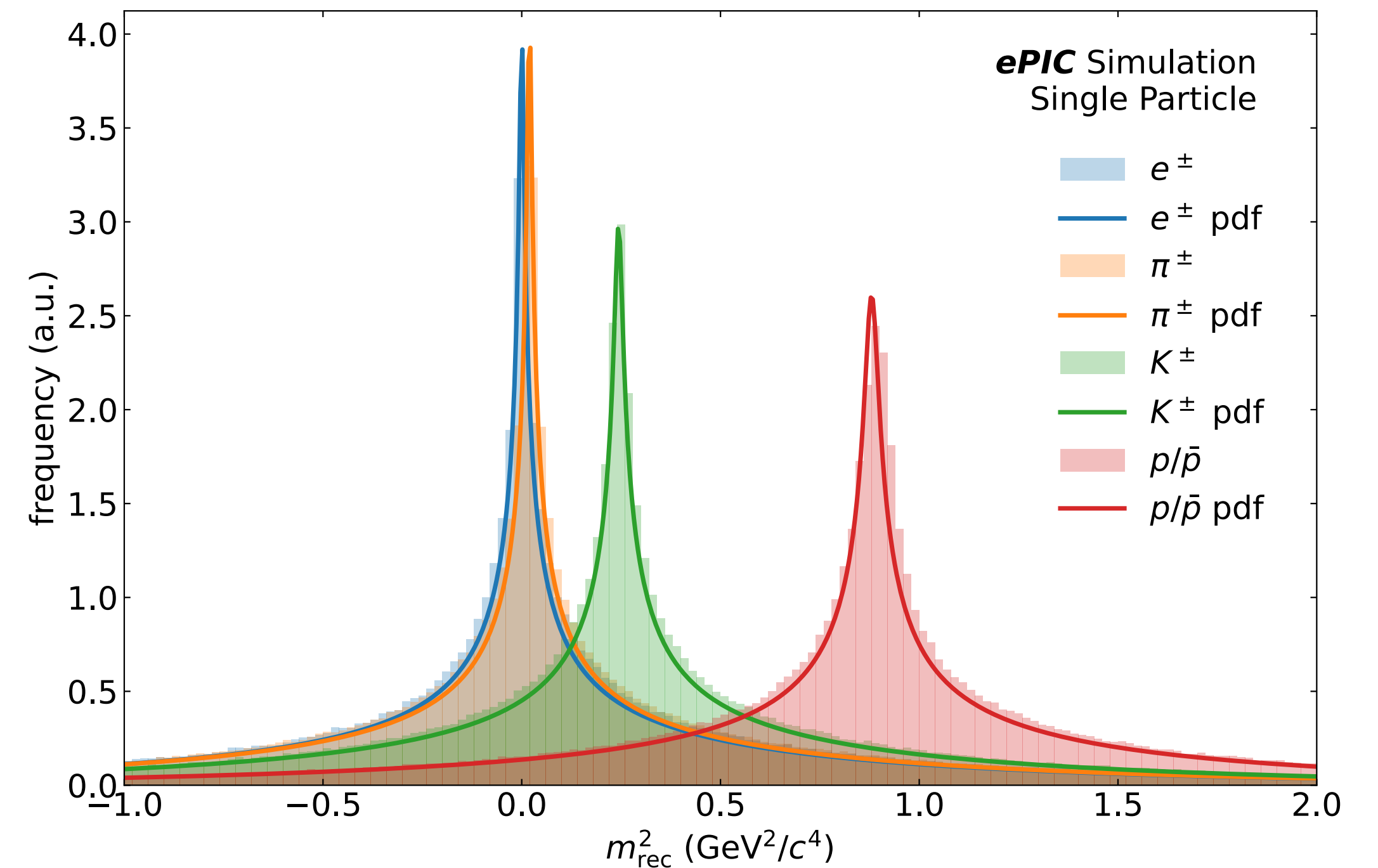


# ToF - PYTHIA DIS Simulations

- PYTHIA DIS event without beam background

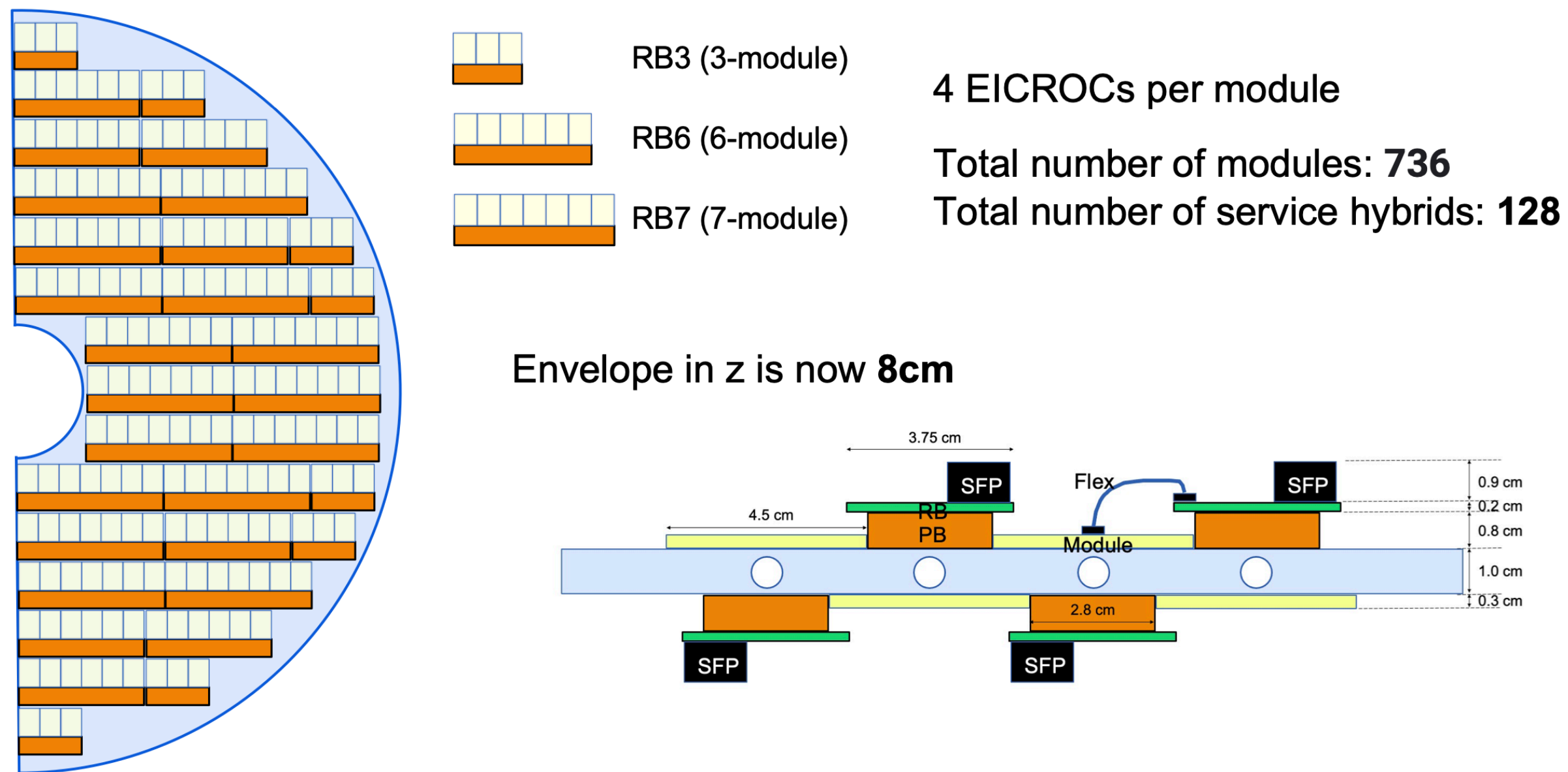


- PYTHIA DIS event with beam background and full reconstruction
- to be done



# New Since Last Report

Updates with detector geometry material



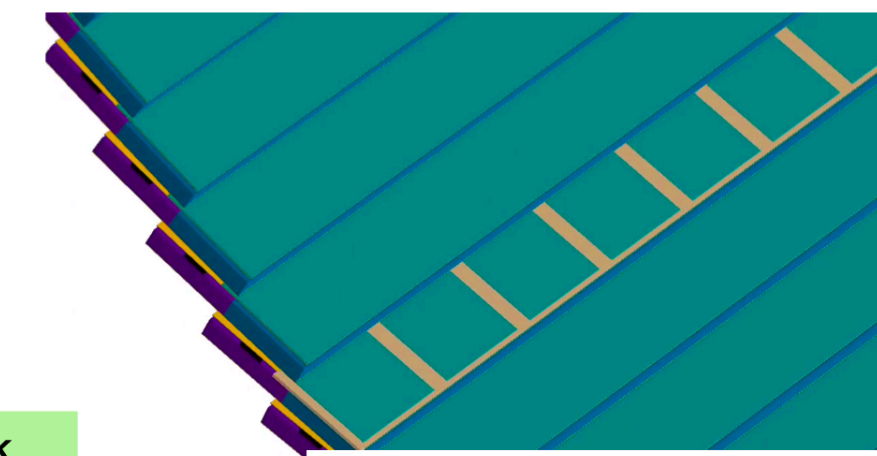
Some confusion about available space settled

Simulation progress: toward more realistic background simulation

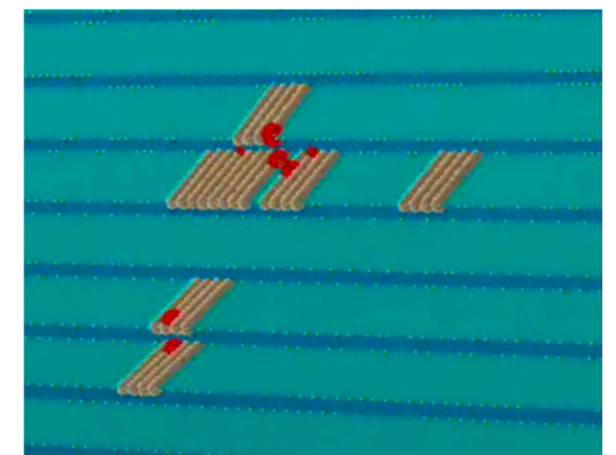
## ToF Digitization in ePIC simulation: Status Update (22nd May 2024)

### RawTrackerHitCollection

- Energy deposited
  - Time of Flight
  - Cell ID
- rawhits



Segmentation: Chun Yuen Tsang



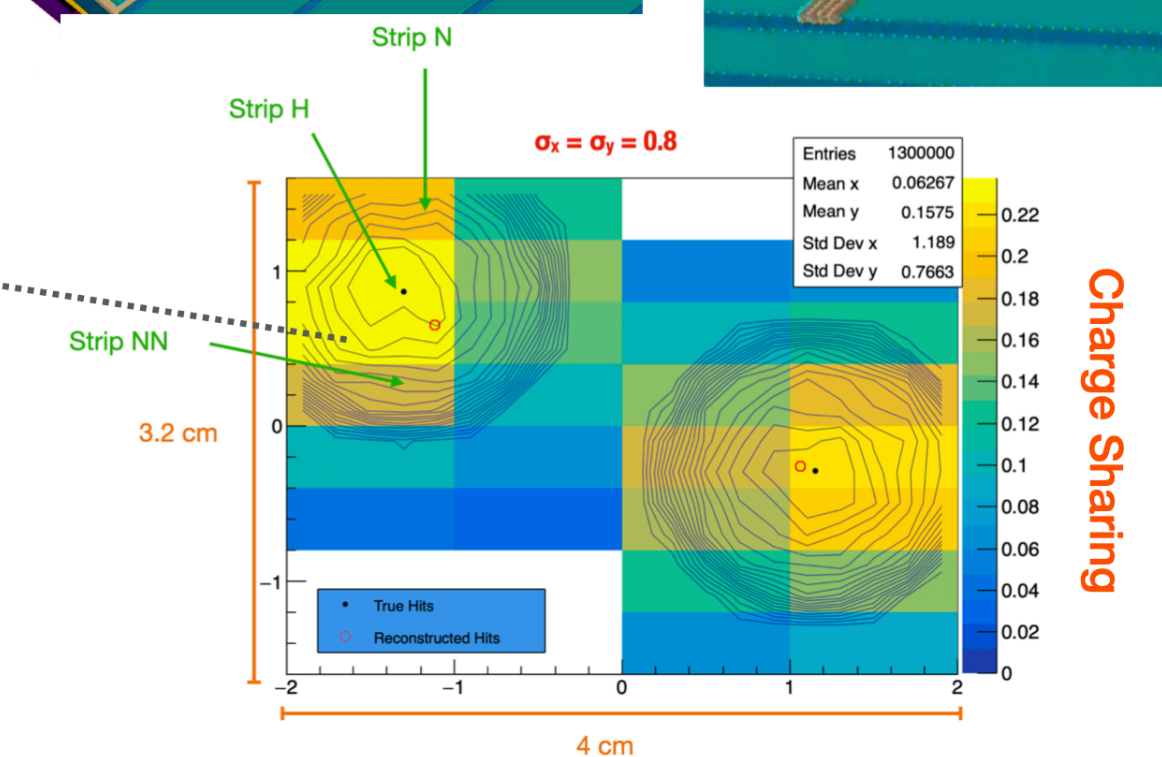
Analog signal generation

Reference clock  
Delay cells  
Threshold voltages

### BTOFHitDigi (Not on git yet)

Resides in [drich-dev/EICrecon/src/algorithms/digi](#)

- ADC (8-bit)
  - TDC (10-bit)
  - Cell ID
  - Sensor ID
- digi



At the End ...

# Comments and Observations

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- All LUT for PID in EICRecon
- Reasonable progress in implementing things in EICrecon
- Need for improved information exchange between engineering and DSCs
  - ▶ Request/Question on shortening hpDIRC bars by 8 cm on dRICH side ?!
  - ▶ Irrelevant for performance but lose vital overlap with dRICH
  - ▶ Requires sims for evaluation of impact
  - ▶ Unclear if just an option or necessity - need to clarify
- PID groups are all progressing towards TDR
  - ▶ Manpower still on the short side
  - ▶ Last round of R&D (proposals due July 1)
    - ◎ dRICH - eRD102
    - ◎ hpDIRC - eRD102
    - ◎ pfRICH/photosensors - eRD110
    - ◎ ToF/AC-LGAD - eRD112
    - ◎ ASICs/FEE - eRD109