

# eRD109 ppRDO Status - LBL

Assuming contract  
in place on Feb 1

Actual contract  
activated on Apr 16

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| <p><b>LBNL</b></p> <p><b>Zhenyu Ye</b><br/><a href="mailto:yezhenyu@lbl.gov">yezhenyu@lbl.gov</a></p><br><br><br><br><br><br><p><b>1-2 ETROC2<br/>planes in the<br/>test beam</b></p> | <p>Understand the interfaces between the Artix FPGA evaluation kit and ETROC2, and finalize the plan to use evaluation kit to communicate with ETROC2</p> | <p>Feb 1, 2024</p>   | <p>✓ April 16</p>   |
|   | <p>Obtain ETROC2 test board, Artix FPGA evaluation kit, extension board and cables needed to connect FPGA Kit and ETROC2 test board</p>                   | <p>March 1, 2024</p> | <p>✓ May 10</p>   |
|   | <p>Complete 50% of the initial version firmware to communicate with ETROC2 using FPGA evaluation kit</p>  | <p>April 1, 2024</p> | <p>✓ May 31</p>   |
|   | <p>Complete the initial version of the firmware to communicate with ETROC2 using FPGA evaluation kit</p>  | <p>May 1, 2024</p>   | <ul style="list-style-type: none"> <li>• June 26</li> </ul> |
|   | <p>Finalize the firmware to communicate with ETROC2 using FPGA evaluation kit</p>   | <p>June 6, 2024</p>  | <ul style="list-style-type: none"> <li>• July 12</li> </ul> |
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