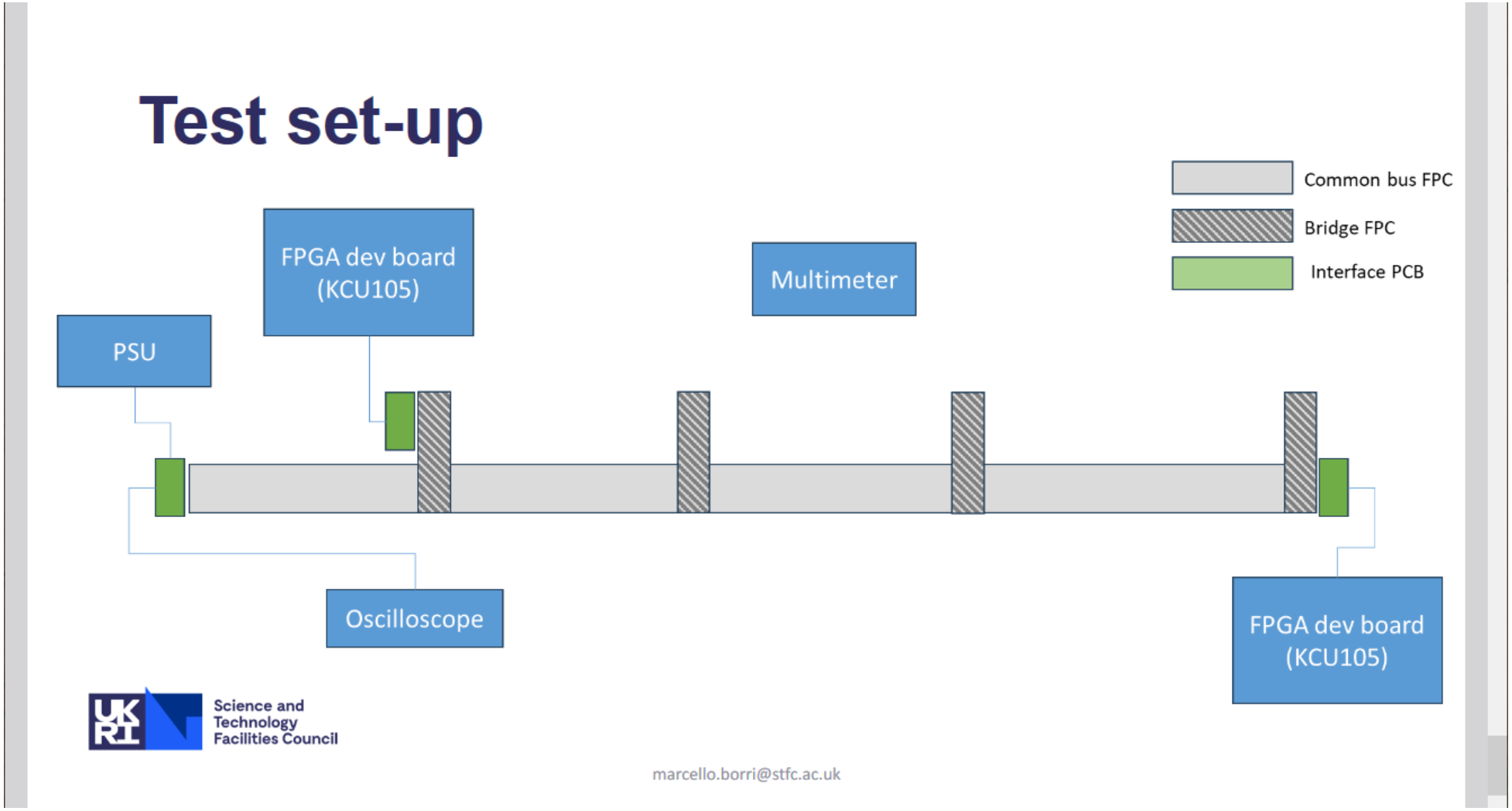
Test plans at RAL

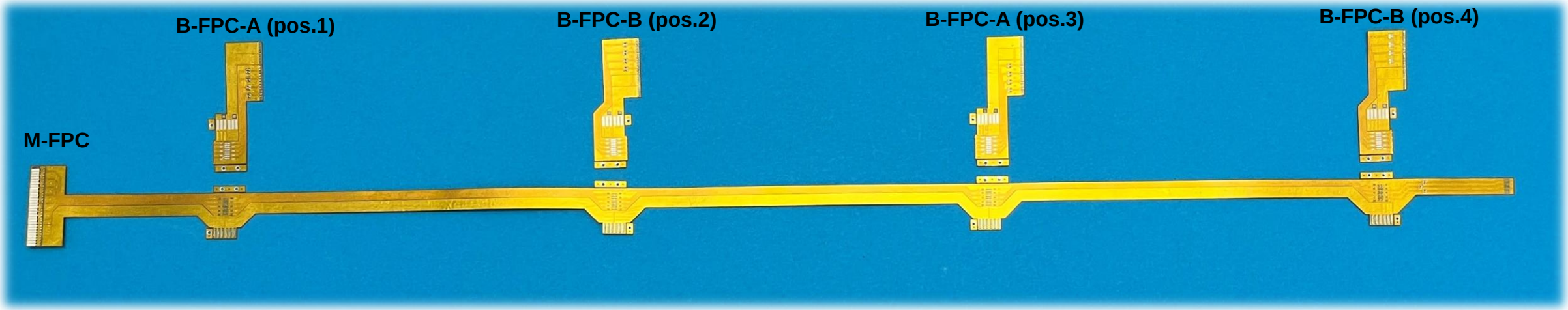
Todd Huffman – Oxford
Rebecca (Becca) Augier - Durham

From Face-2-face meeting at B’ham

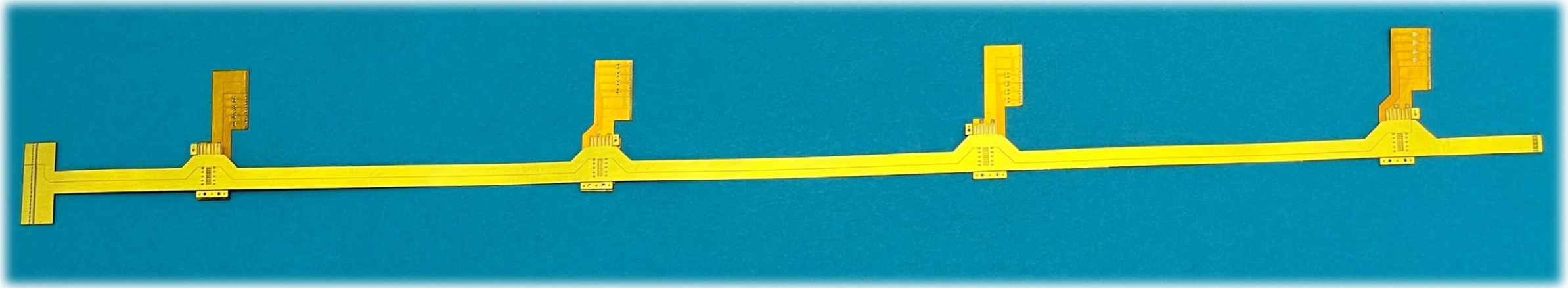


Set of assembled technological FPCs

Set of multilayered FPCs (M-FPC+ B-FPCs) for ePIC SVT multilayered multicomponent FPC



Aligned (<u>but not assembled</u>) components of multilayered multicomponent ePIC SVT FPC



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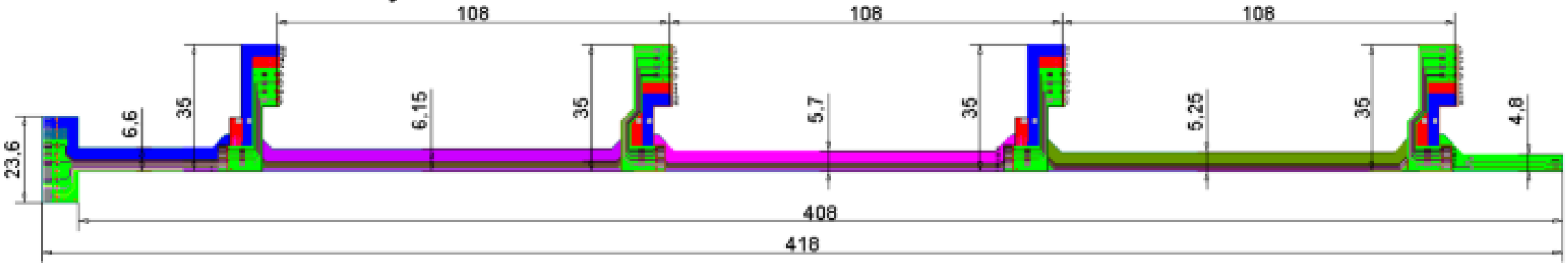
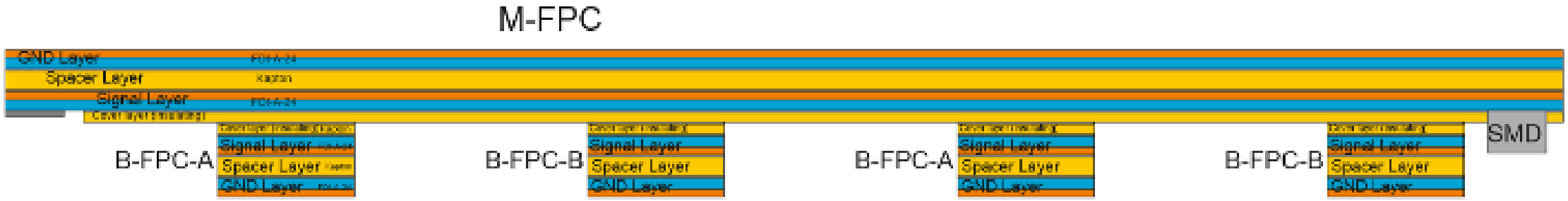
Current cable design. (in mm)

Current status/updates

- Preliminary design (based on latest information from Andy, v03) is complete and sent for review this Monday



Schematic cross-section



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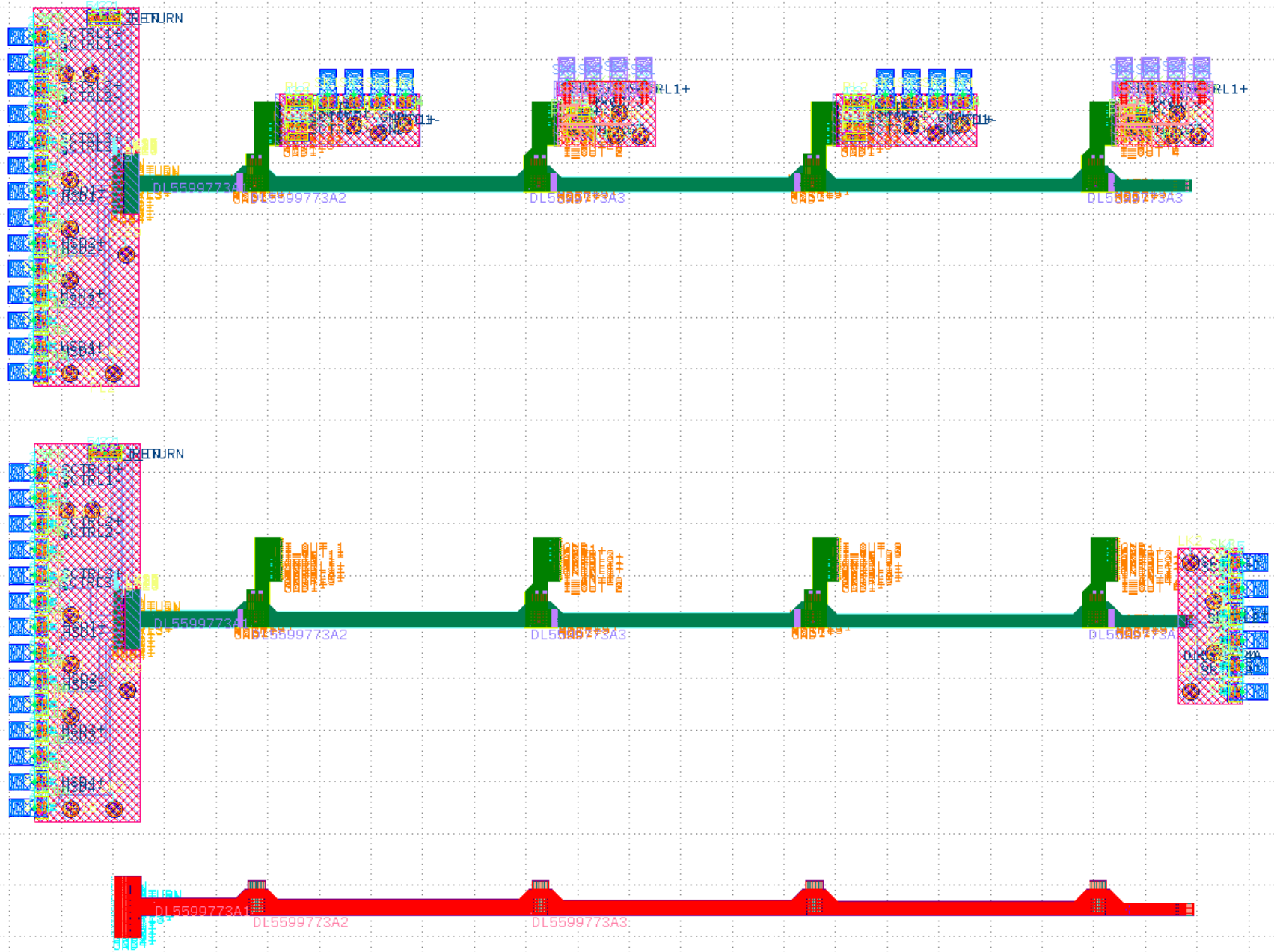
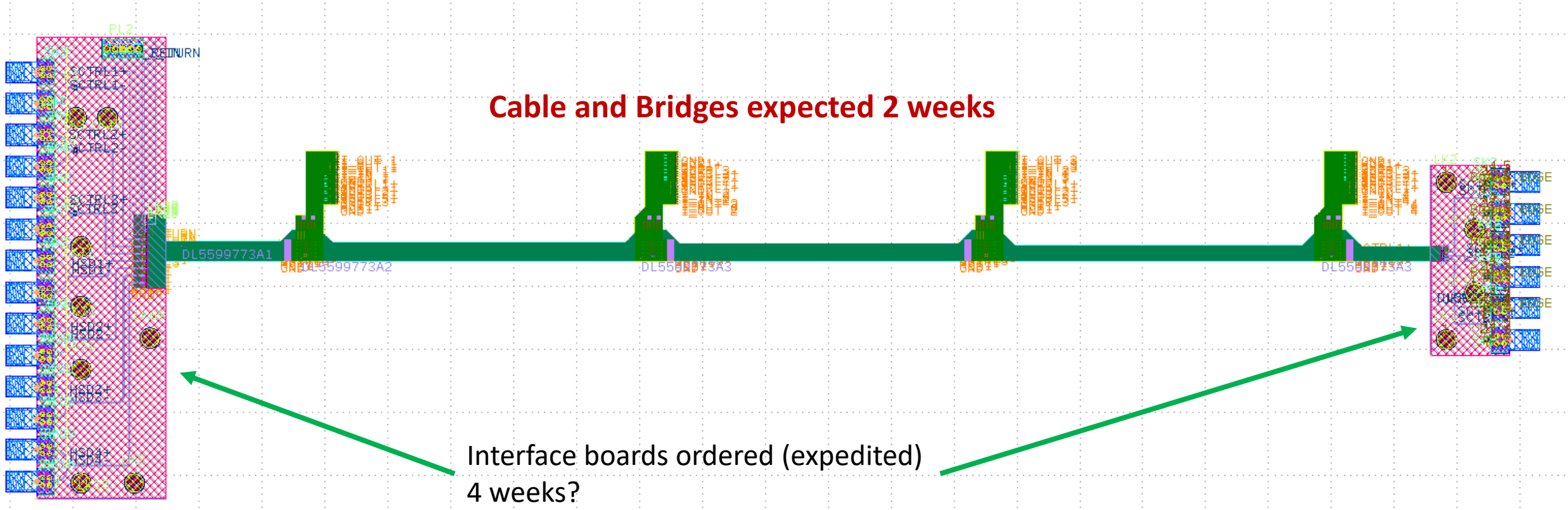
Cable and interface boards



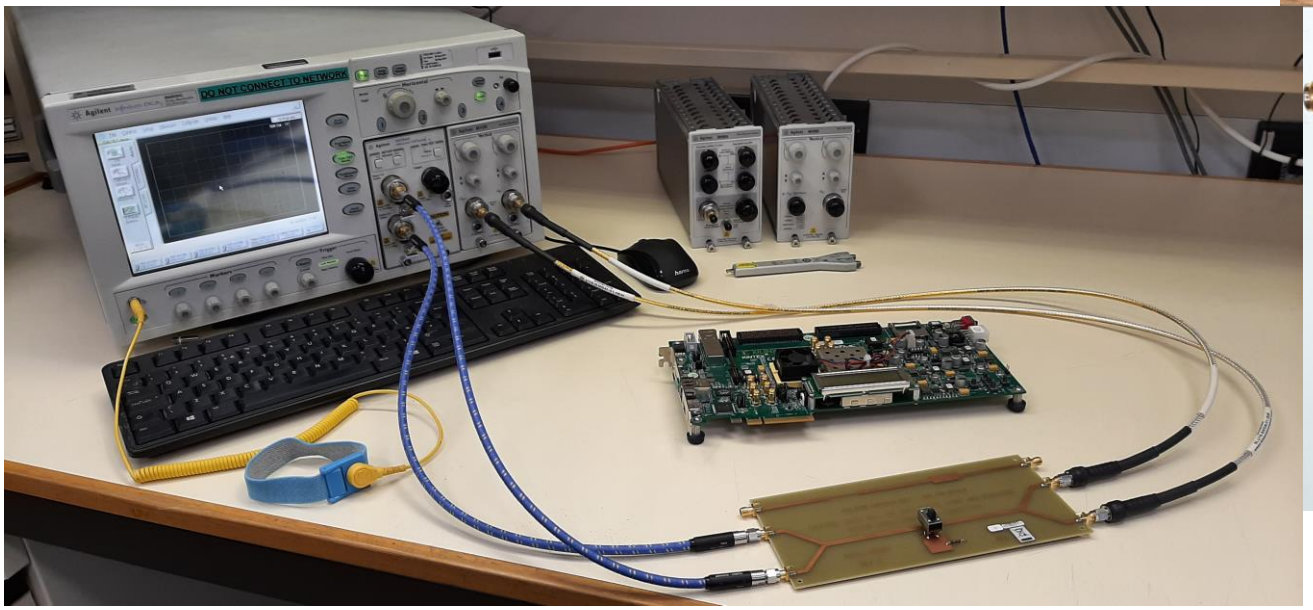
Current status



UNIQ+ summer student @Oxford Equipment @RAL

54754A TDR

86112A 20 GHz (12.4 GHz)

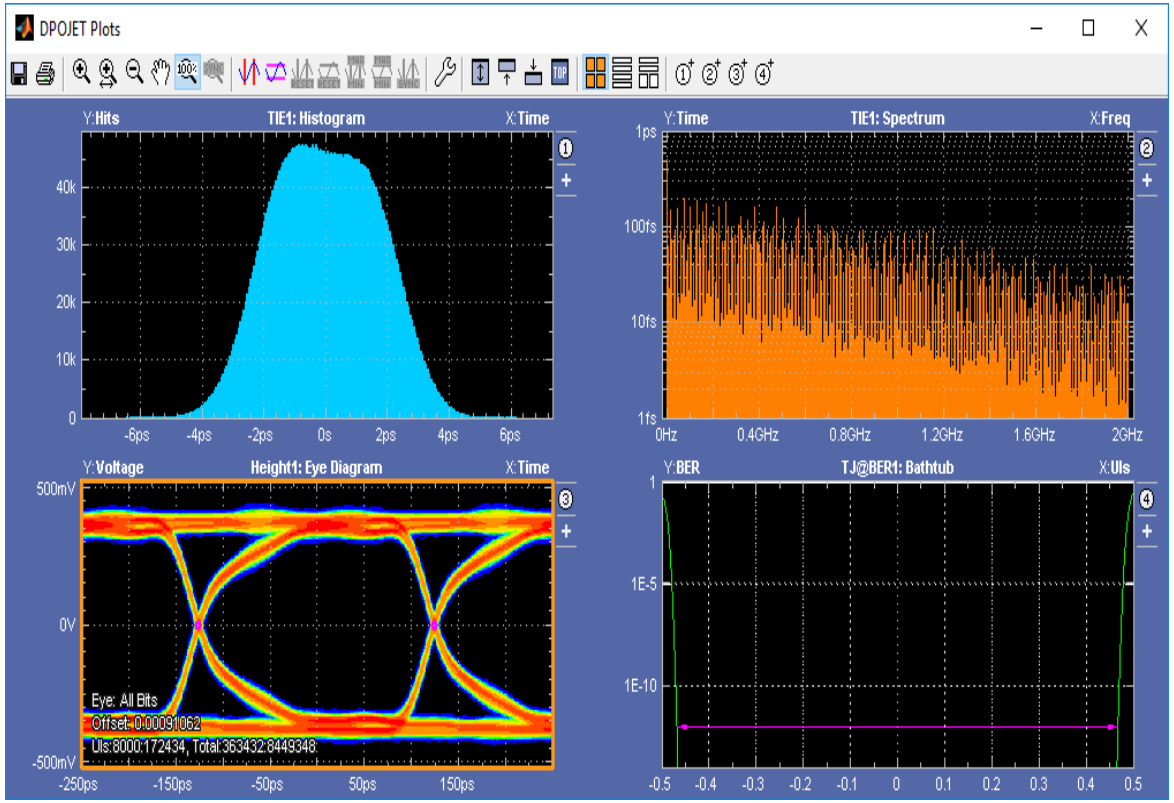


Test board: Use to set up tests

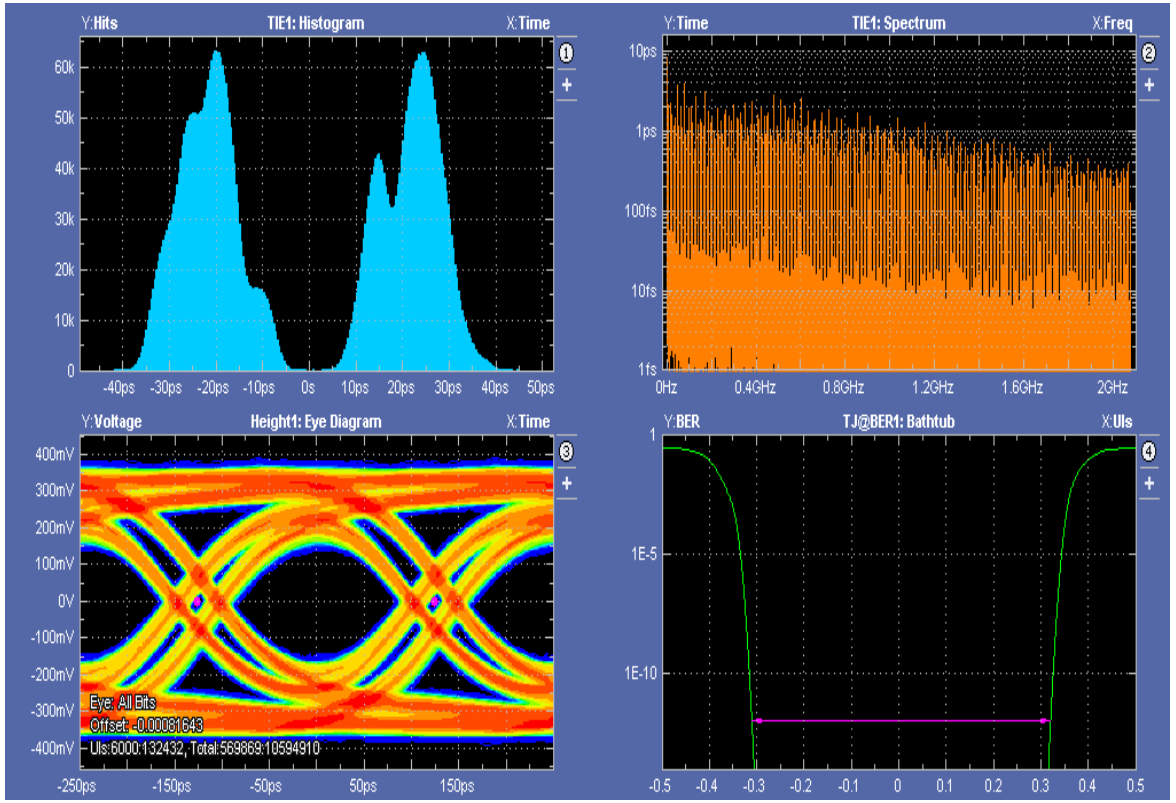
Also have a Kintex 7 board to generate Psuedorandom bit streams

Hoping to do something similar with FPC

KCU105 direct to ’scope (72 inch cables)



KCU105 to DUT to ’scope (72 inch cables)



Pictures From
William Helsby – Daresbury lab.

Eye diagrams, bathtub plots – other suggestions?