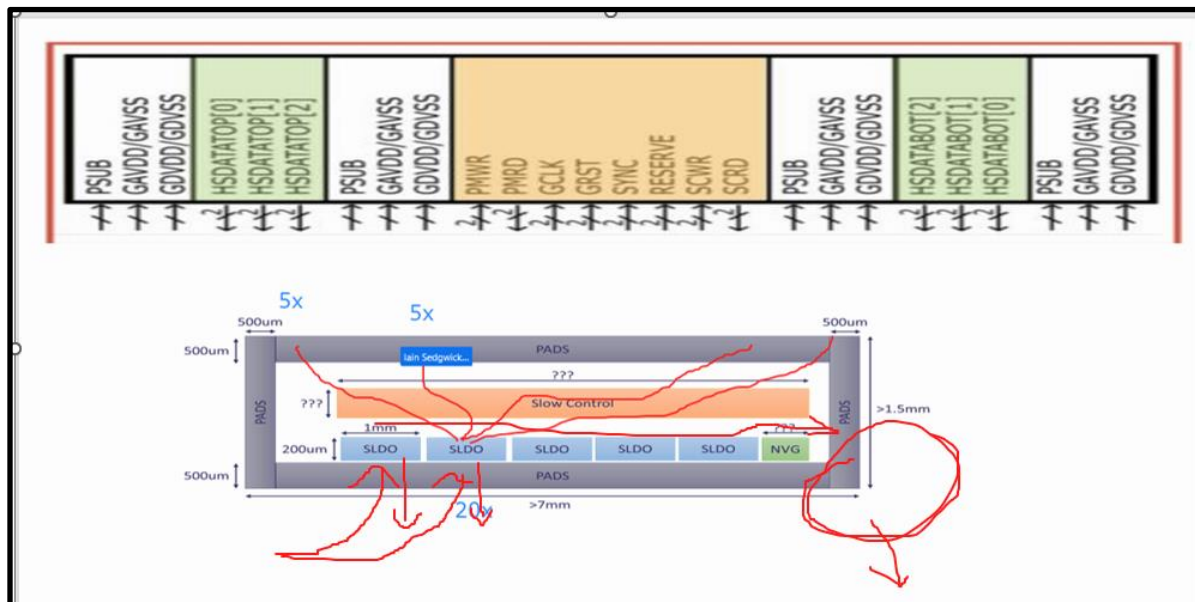# AncAsic and FPC

- Two ad-hoc informal meetings to discuss interfacing of AncAsic to LAS via FPC.

- Requirements from FPC: (top 3)
  - Pinout of AncASIC should match that of the LAS
  - Tab bonding is preferred due to damage to the FPC
  - Tab/wire bonding preferred since more institutes have access to this

- Requirements from chip: (top 3)
  - Capacitors on input side of the SLDO for each supply
  - Capacitors in the output side of the SLDO for each line that goes to the chip
  - **Pitch of TABs on FPC. (To ask LTU)**

Back-up