



Science and
Technology
Facilities Council

Introduction & Miscellaneous Updates

WP3 Electrical interfaces



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Update

- Updated WBS:
 - It includes:
 - FPCs, electrical interconnections (IB, OB, Disks);
 - Modules (OB, Disks);
- Need to resource the WBS:
 - IB: STFC + Trieste (TBC)
 - OB: STFC + Oxford
 - Disks: BNL + LANL + LBNL;
- Stages:
 - R&D;
 - Pre-production;
 - Production;

I need your input to resource the schedule

WP3: Electrical Interfaces

Coordinators: Marcello Borri, TBC

WP3: Electrical interfaces	
3.1	Electrical interfaces IB (L0–2)
3.1.1	Definition of specifications for FPCs & electrical interconnection
3.1.2	Design & supplier evaluation
3.1.3	Prototyping & testing of module, FPCs & electrical interconnection
3.1.4	Iterative improvements of FPC design & electrical interconnection
Milestone	FPC design complete & electrical interconnection validated
3.1.5	Pre-production of FPCs for system test, including QC
3.1.6	Production of FPCs for production detector, including QC
3.2	OB HIC (L3–4)
3.2.1	Definition of specifications for FPCs & electrical interconnection
3.2.2	Design & supplier evaluation
3.2.3	Prototyping & testing of module, FPCs & electrical interconnection
3.2.4	Iterative improvements of module design, FPC & electrical interconnection
Milestone	OB module design complete
3.2.5	Pre-production of FPC for system test, including QC
3.2.6	Production of FPCs for detector grade modules, including QC
3.3	Disks HIC (ED0–4, HD0–4)
3.3.1	Definition of specifications for module, FPCs & electrical interconnection & back plate
3.3.2	Design & supplier evaluation
3.3.3	Prototyping & testing of module, FPC, electrical interconnection & back plate
3.3.4	Iterative improvements of module design, FPC, electrical interconnection & back plate
Milestone	Disk module design complete
3.3.5	Pre-production of FPCs for system test
3.3.6	Production of detector grade FPCs, including QC



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Thank you

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