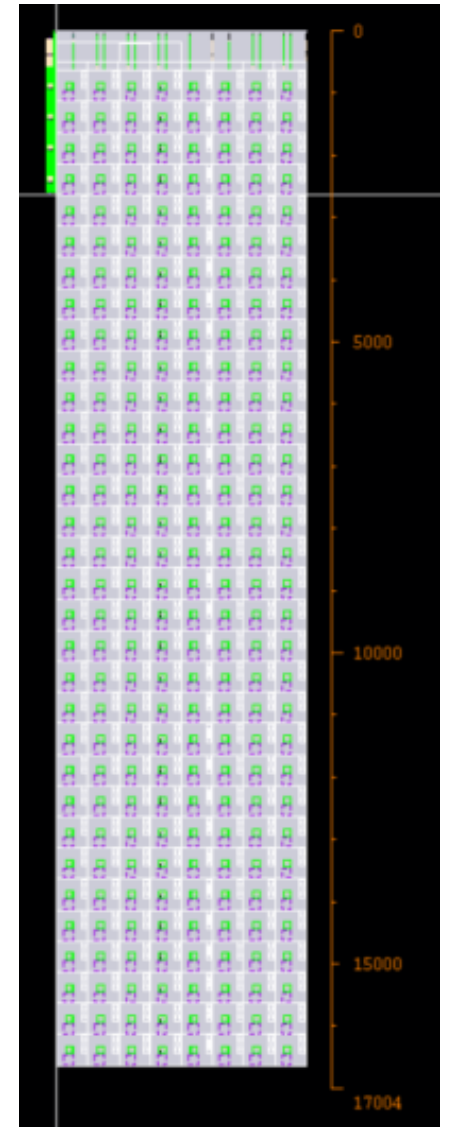


## EICROC status and plans

**eRD109 monthly report 3 oct2024**

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- EICROC1 :  $8 \times 32 = 256$  pixels
  - Layout complete. DRC/LVS OK
  - Still EICROC0 architecture and Readout
  - 2 data outputs : 128 pixels ( $4 \times 32$ )
  - Looking to increase R/O speed. Possibility to skip pixels by SC
  - Looking to improve testability.
  - Verifications in progress (~2 months)
- Considering to make it  $32 \times 32$ 
  - Makes best use of reticle area
  - Allows tests with final sensor
  - Need further discussions with sensor people
  - Still a lot of work needed in 2025 to design EICROC2
- Still targeting to be ready by the end of the year



- TSMC now requires to fully populate the reticle of 24x32
  - Cost ~300 k€
- For EIC we would have
  - EICROC1
  - 2 or 3 EICROC0/A/B
  - 2 CALOROC1/1A
  - ~50% of reticle area
- Possible additionnal partners
  - ~20% of reticle area
  - Still space available (if ready in time !)

