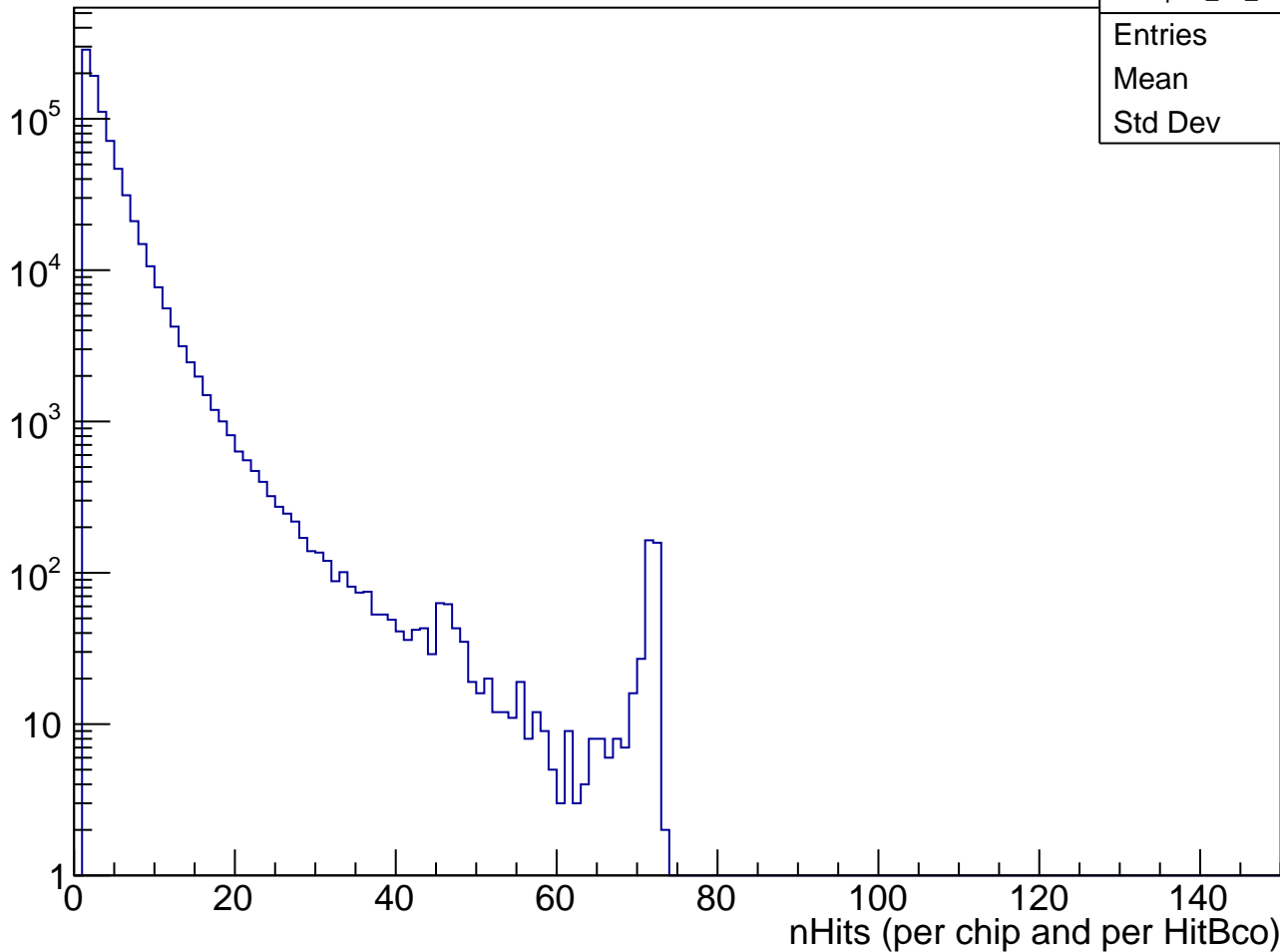


nChipHit_F0_Fch0_Chip0

Entries



nChipHit_F0_Fch0_Chip0

Entries 821136

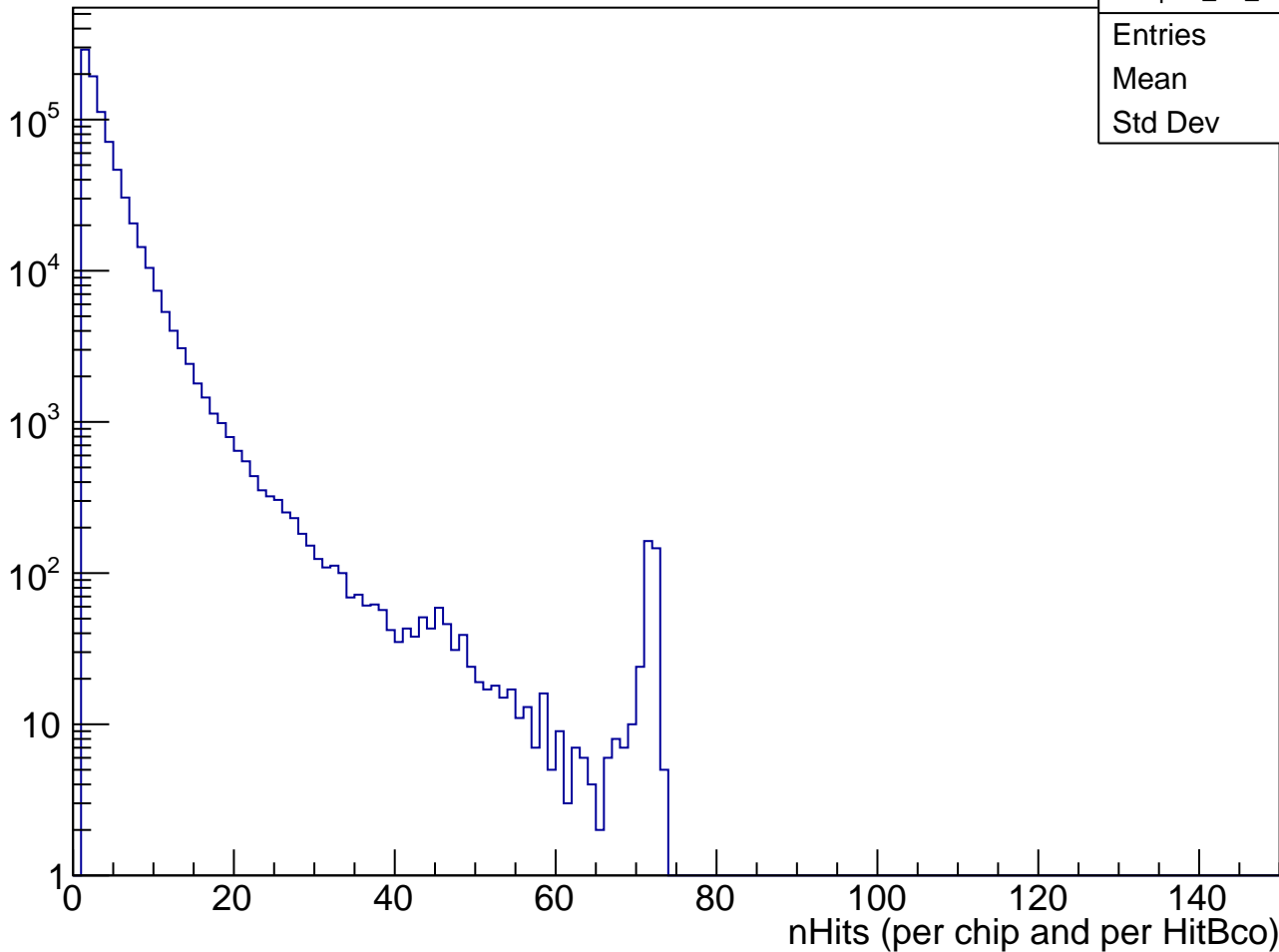
Mean 3.177

Std Dev 3.659

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip1

Entries

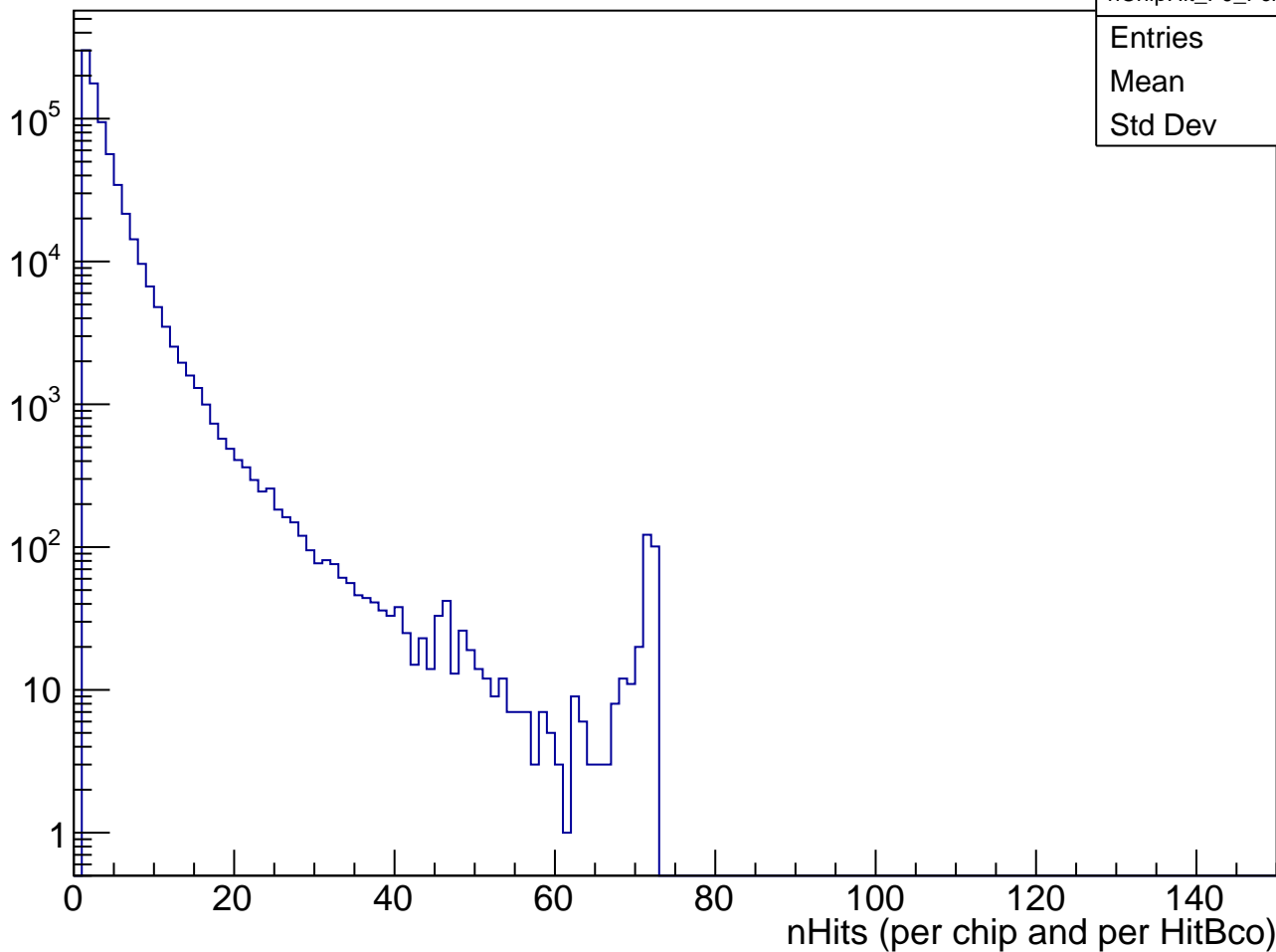


nChipHit_F0_Fch0_Chip1	
Entries	823089
Mean	3.14
Std Dev	3.62

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip10

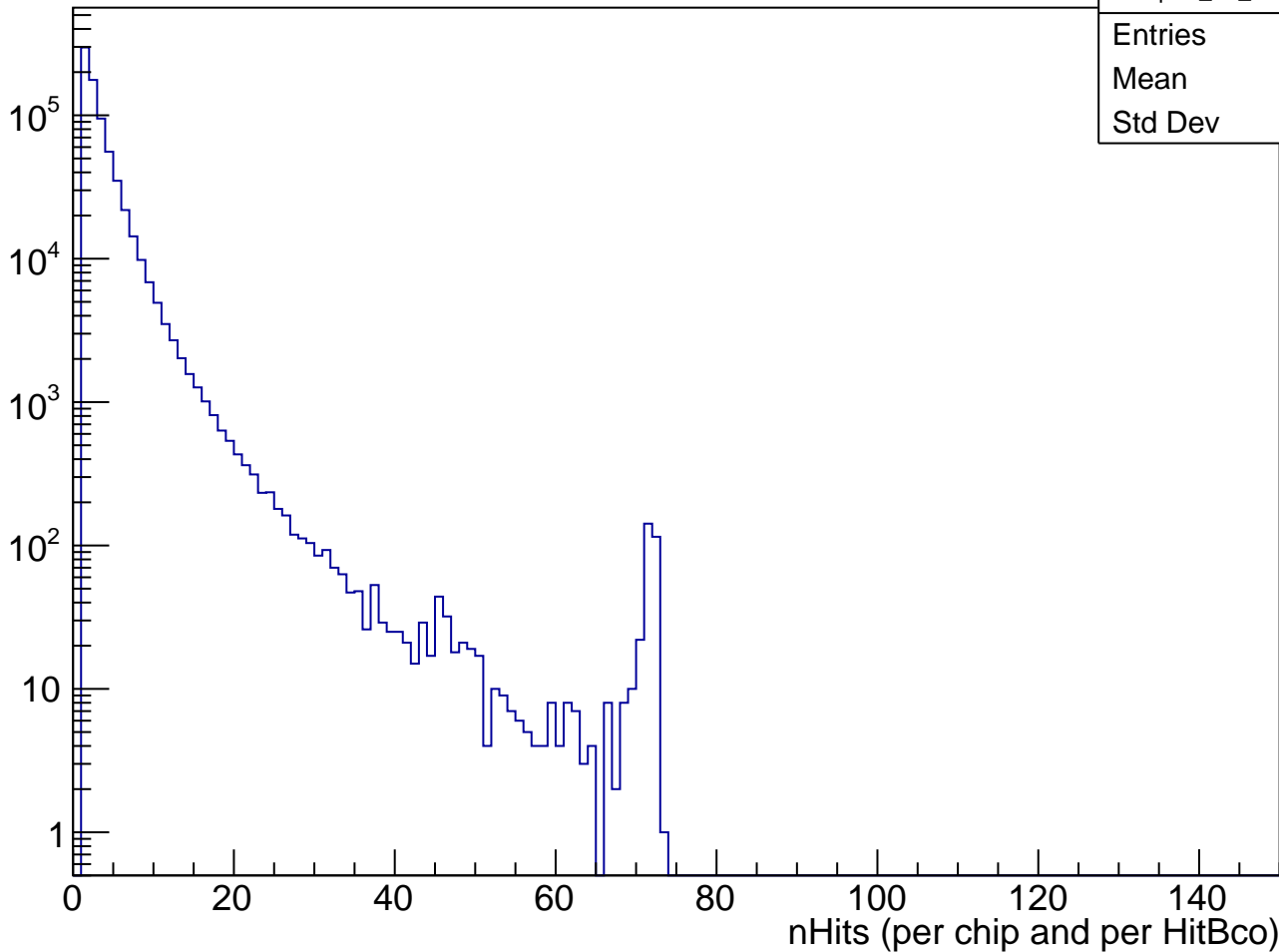
Entries



nChipHit_F0_Fch0_Chip10	
Entries	737449
Mean	2.77
Std Dev	3.236

nChipHit_F0_Fch0_Chip11

Entries

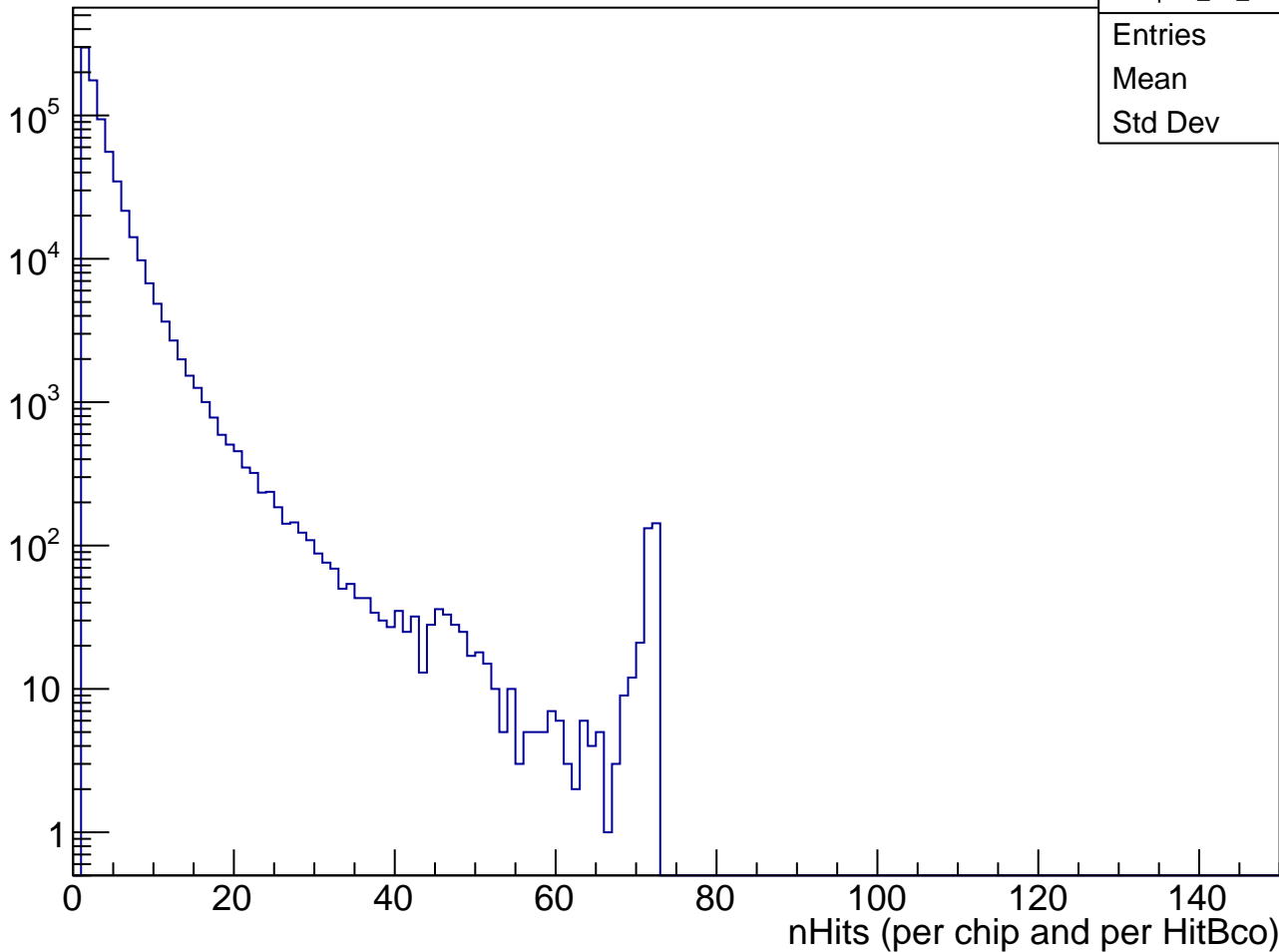


nChipHit_F0_Fch0_Chip11	
Entries	734559
Mean	2.792
Std Dev	3.274

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip12

Entries

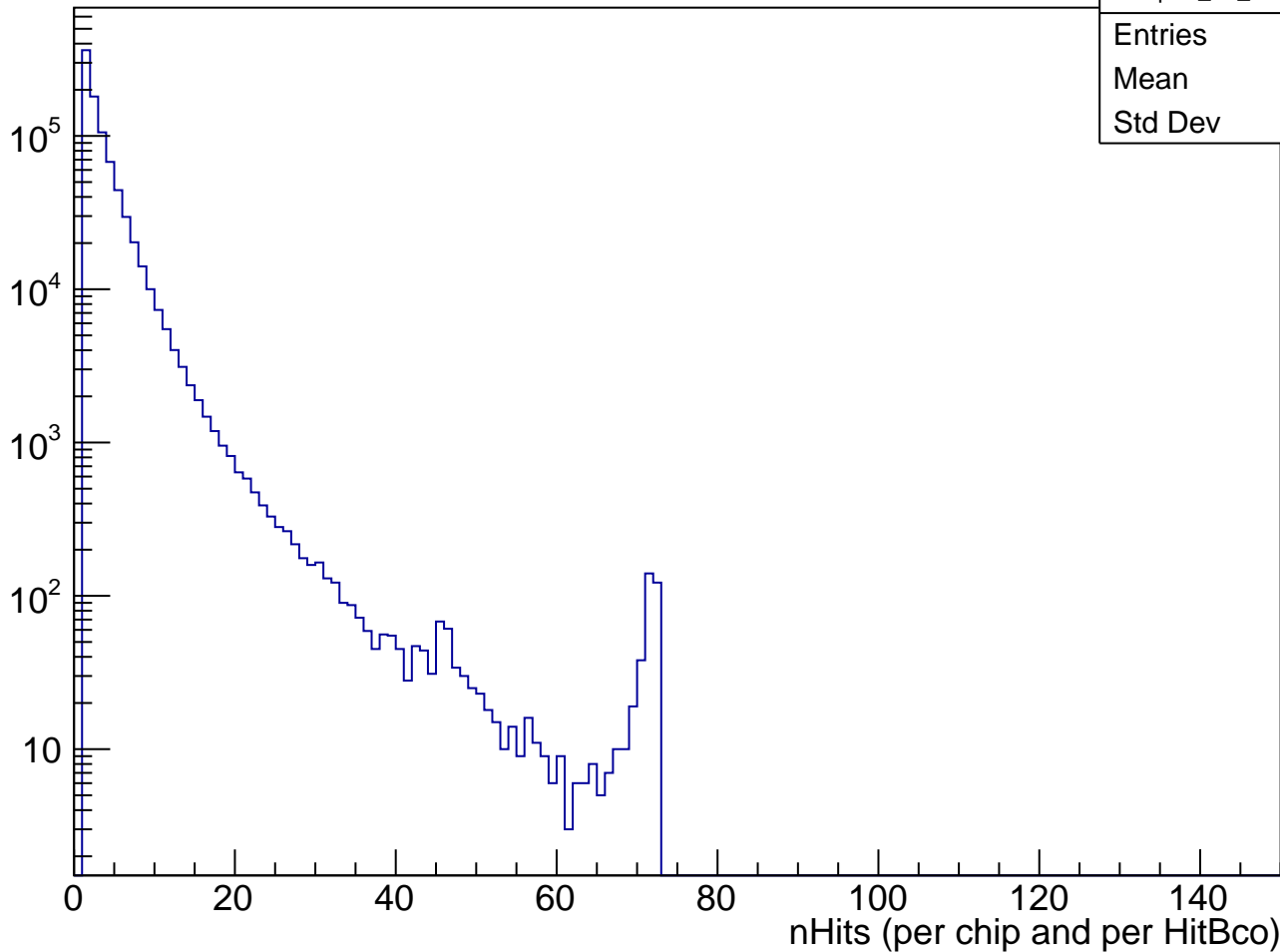


nChipHit_F0_Fch0_Chip12	
Entries	732768
Mean	2.789
Std Dev	3.299

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip13

Entries

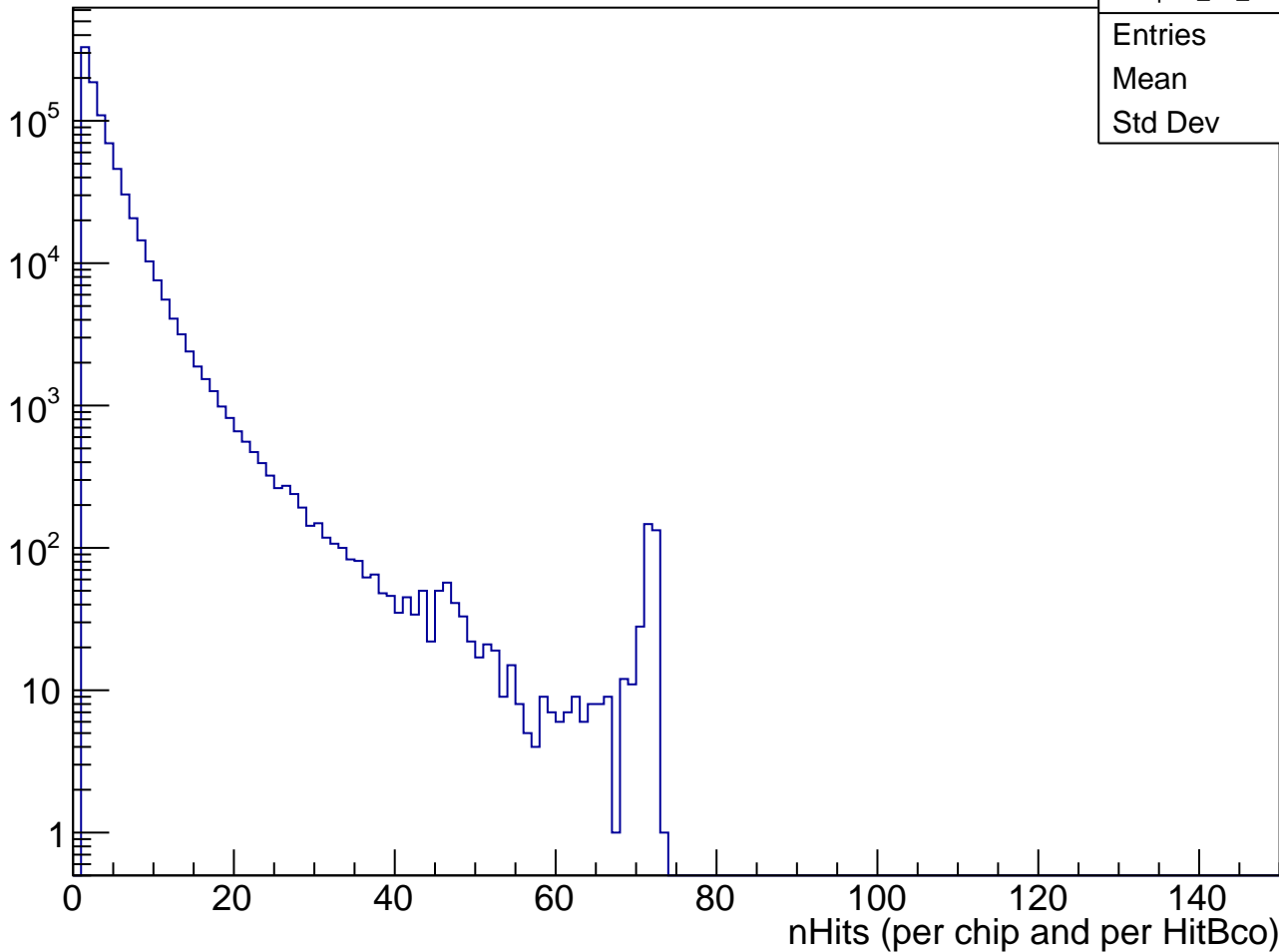


nChipHit_F0_Fch0_Chip13	
Entries	868533
Mean	2.968
Std Dev	3.565

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip14

Entries

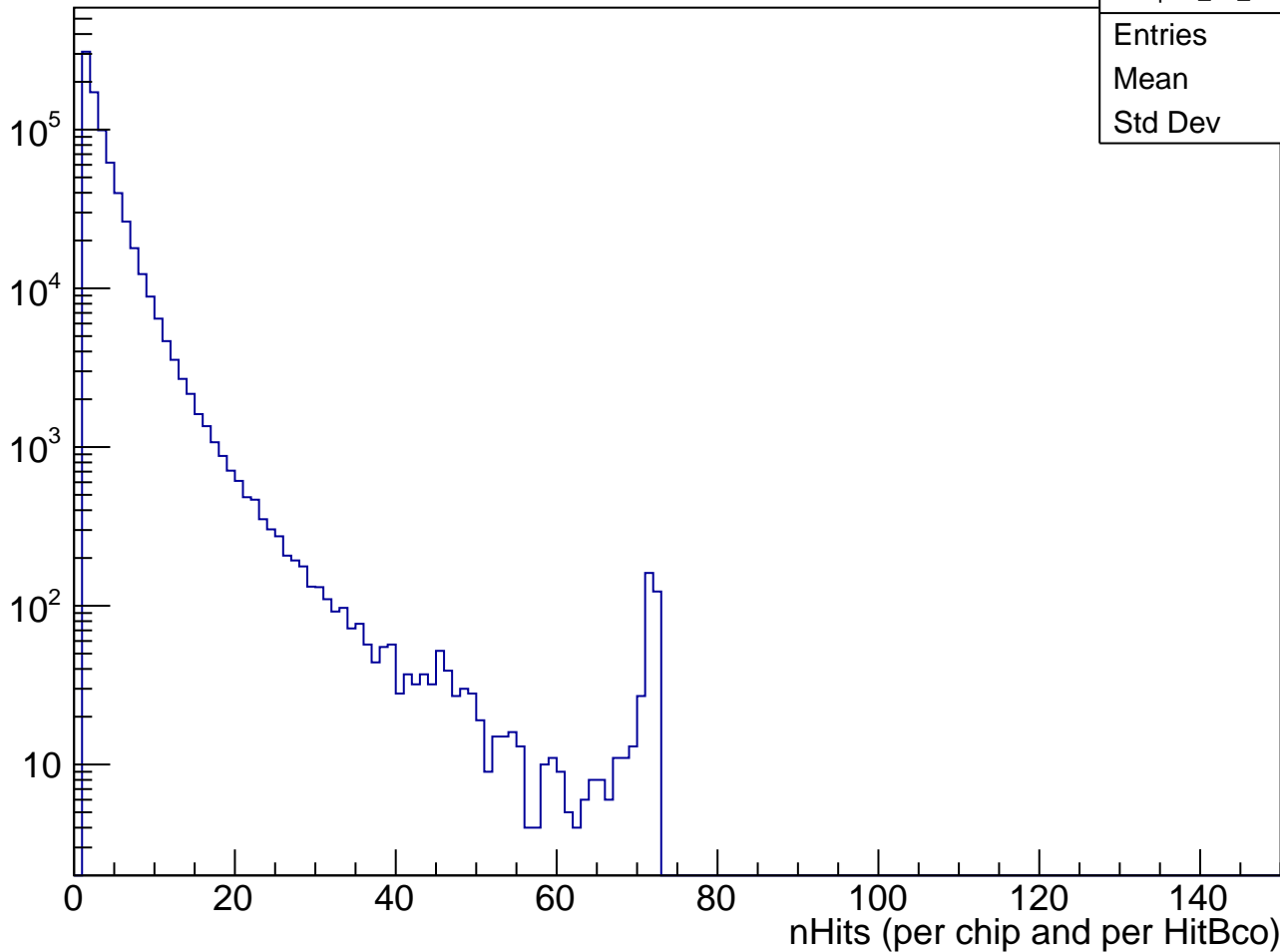


nChipHit_F0_Fch0_Chip14	
Entries	851238
Mean	3.058
Std Dev	3.583

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip15

Entries

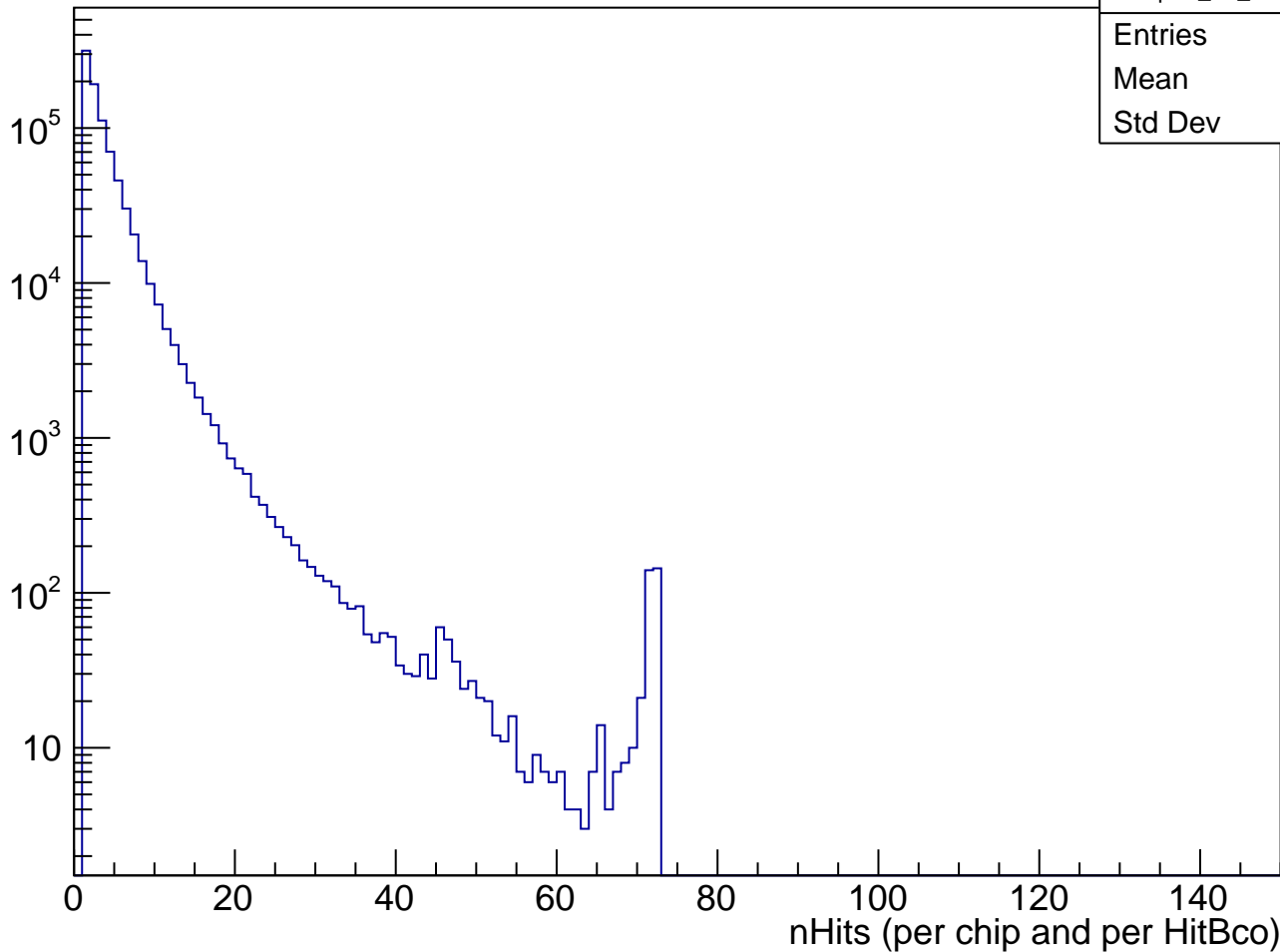


nChipHit_F0_Fch0_Chip15	
Entries	777635
Mean	2.99
Std Dev	3.585

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip16

Entries

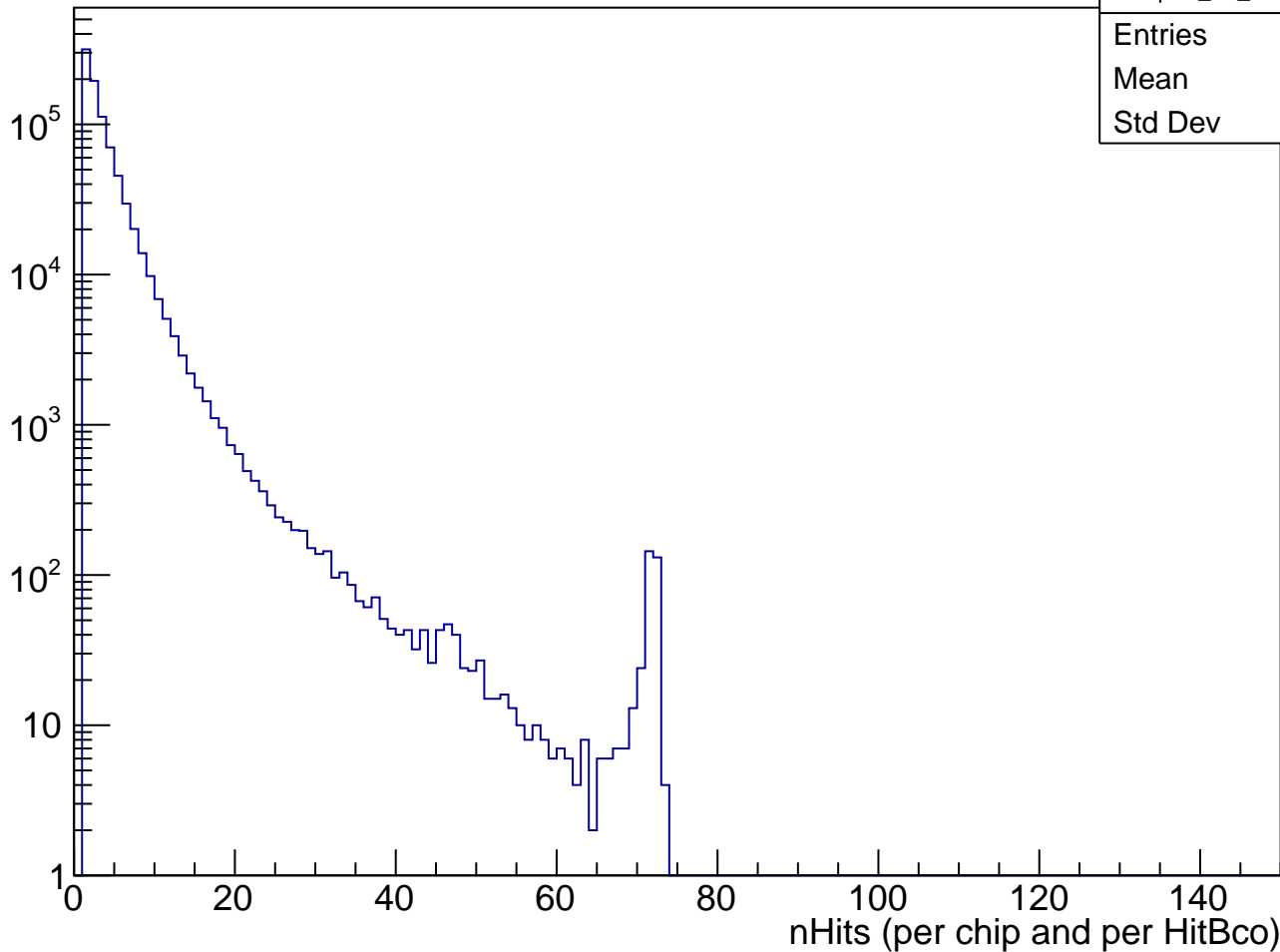


nChipHit_F0_Fch0_Chip16	
Entries	843032
Mean	3.05
Std Dev	3.523

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip17

Entries

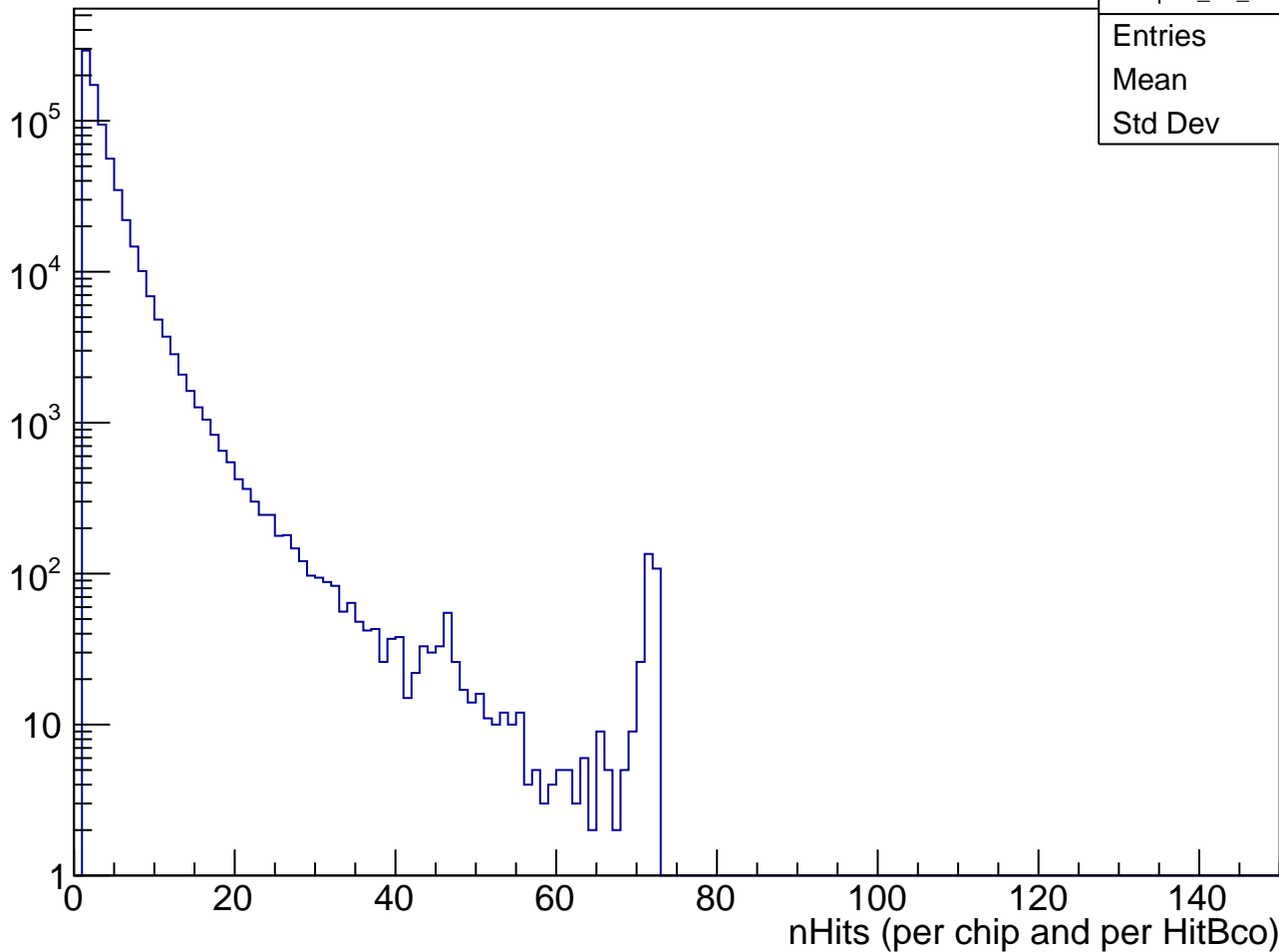


nChipHit_F0_Fch0_Chip17	
Entries	843979
Mean	3.033
Std Dev	3.512

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip18

Entries

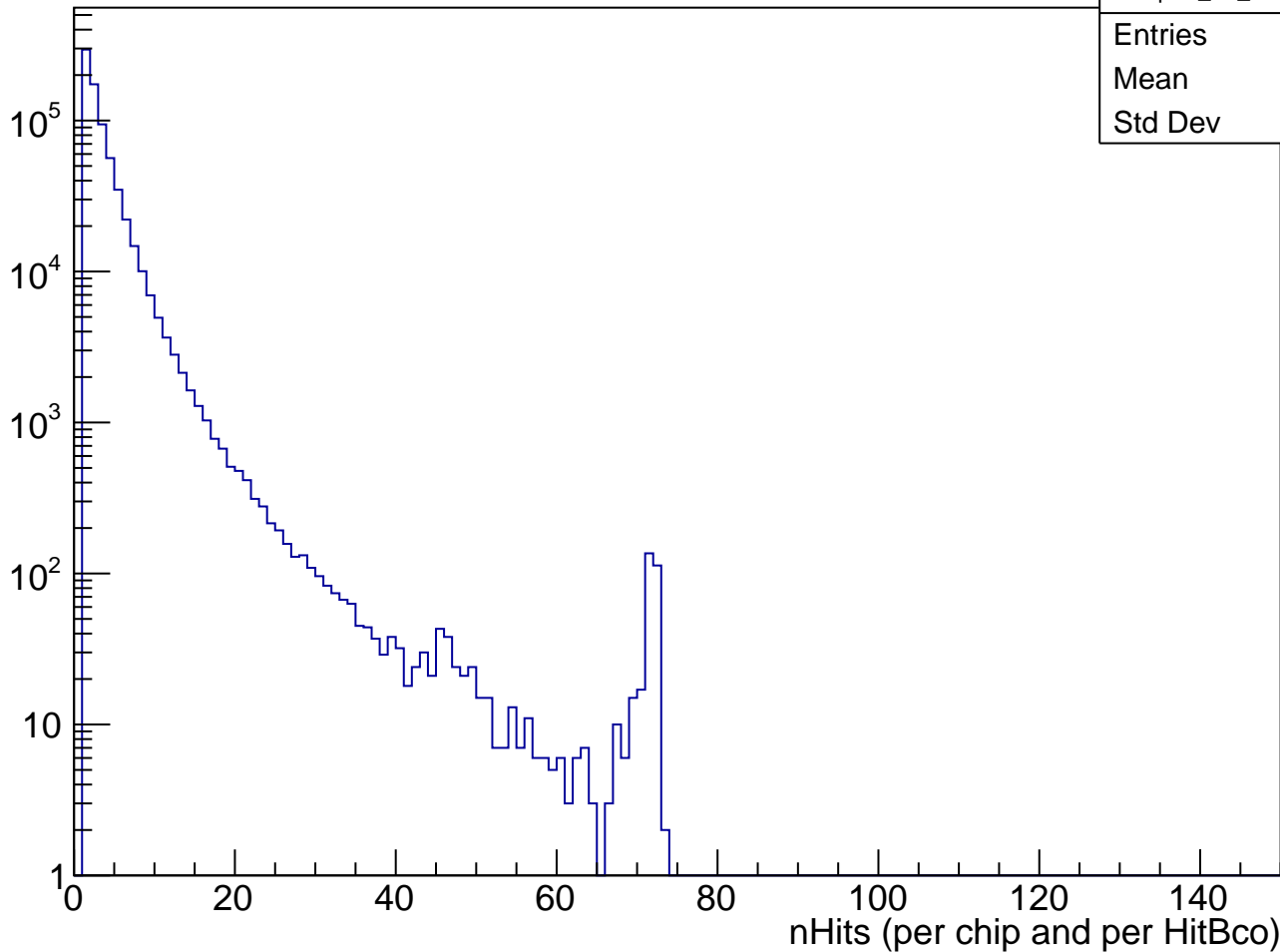


nChipHit_F0_Fch0_Chip18	
Entries	726693
Mean	2.826
Std Dev	3.323

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip19

Entries

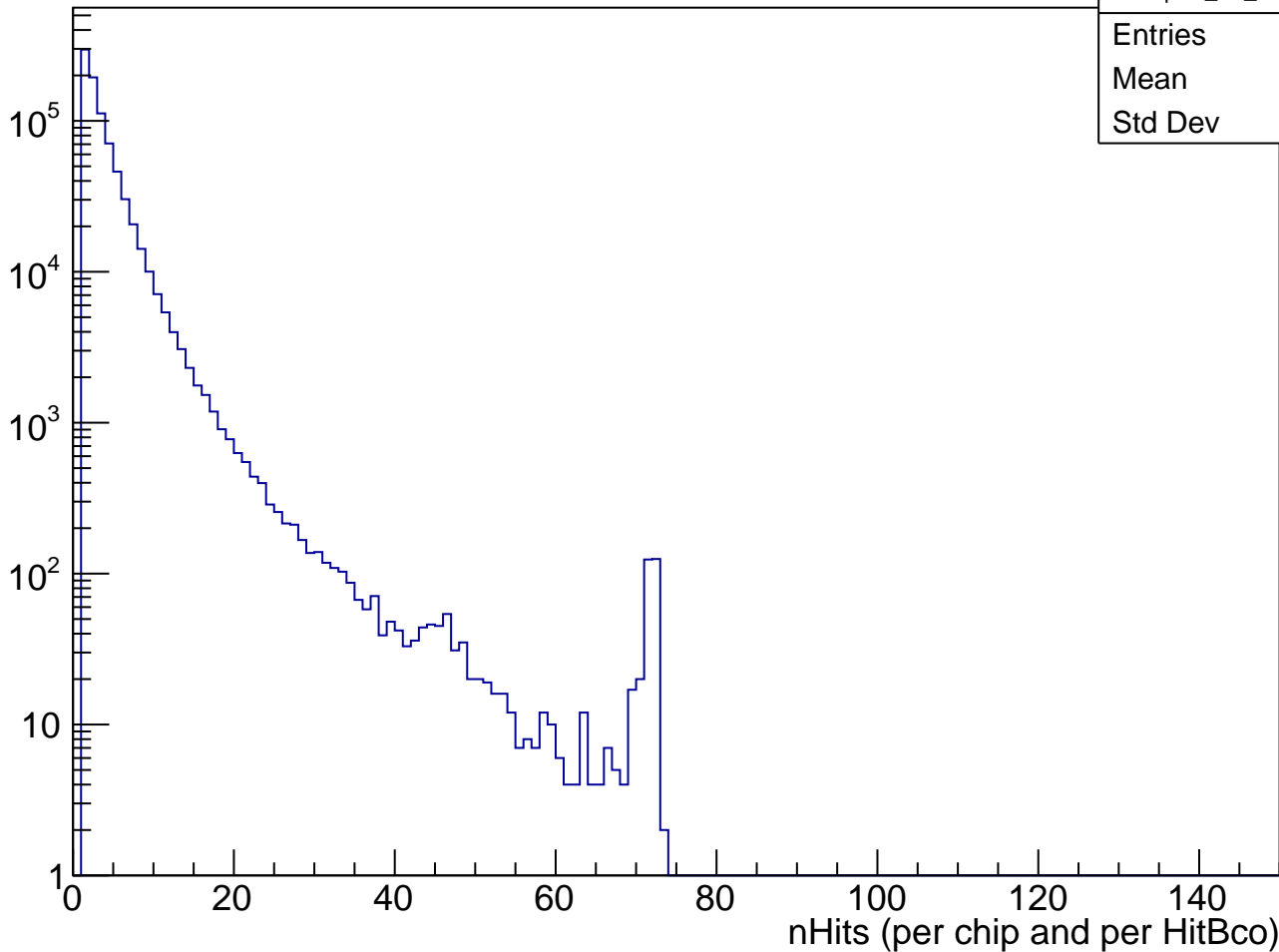


nChipHit_F0_Fch0_Chip19	
Entries	731558
Mean	2.822
Std Dev	3.329

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip2

Entries



nChipHit_F0_Fch0_Chip2

Entries 828214

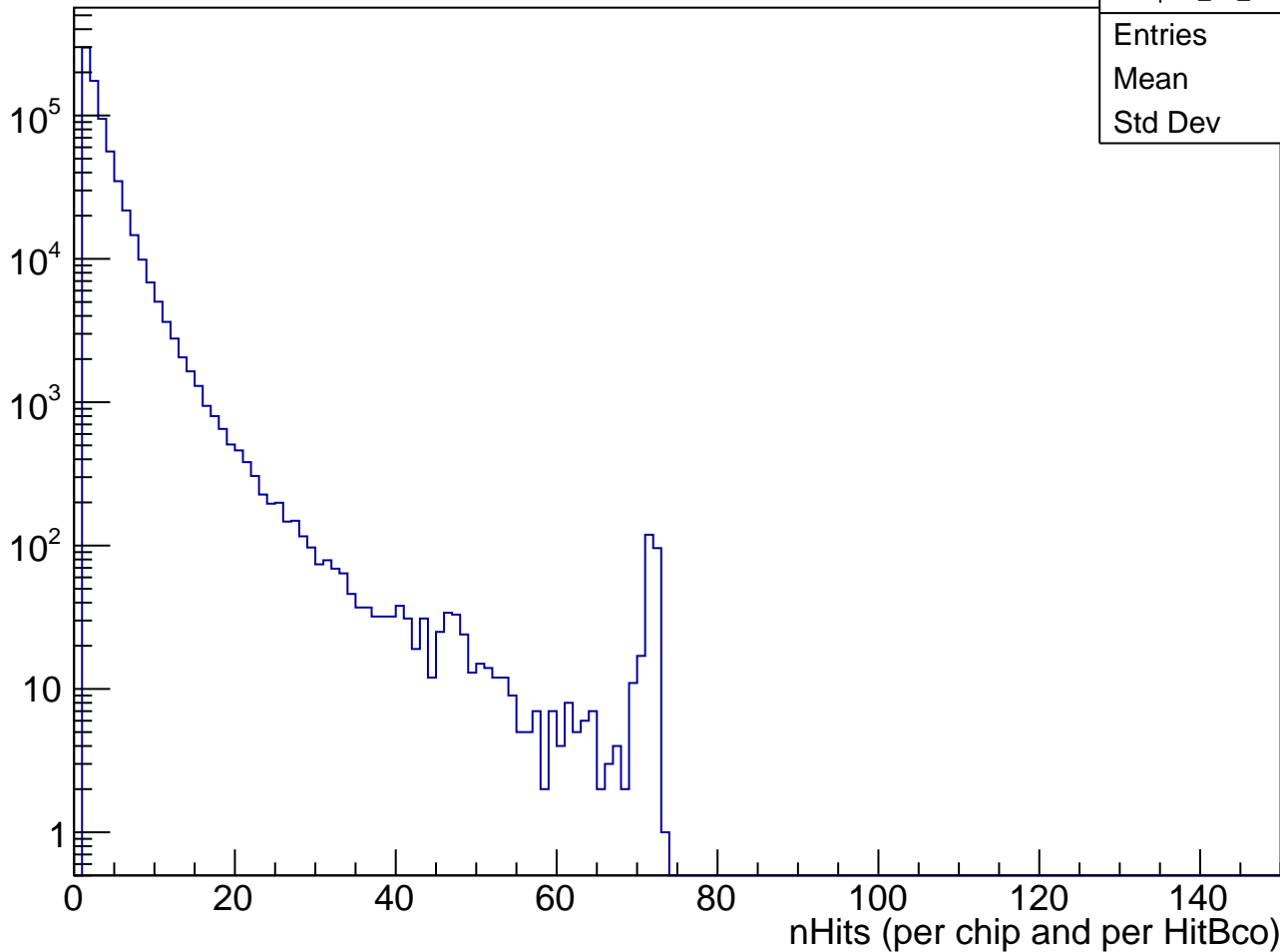
Mean 3.102

Std Dev 3.536

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip20

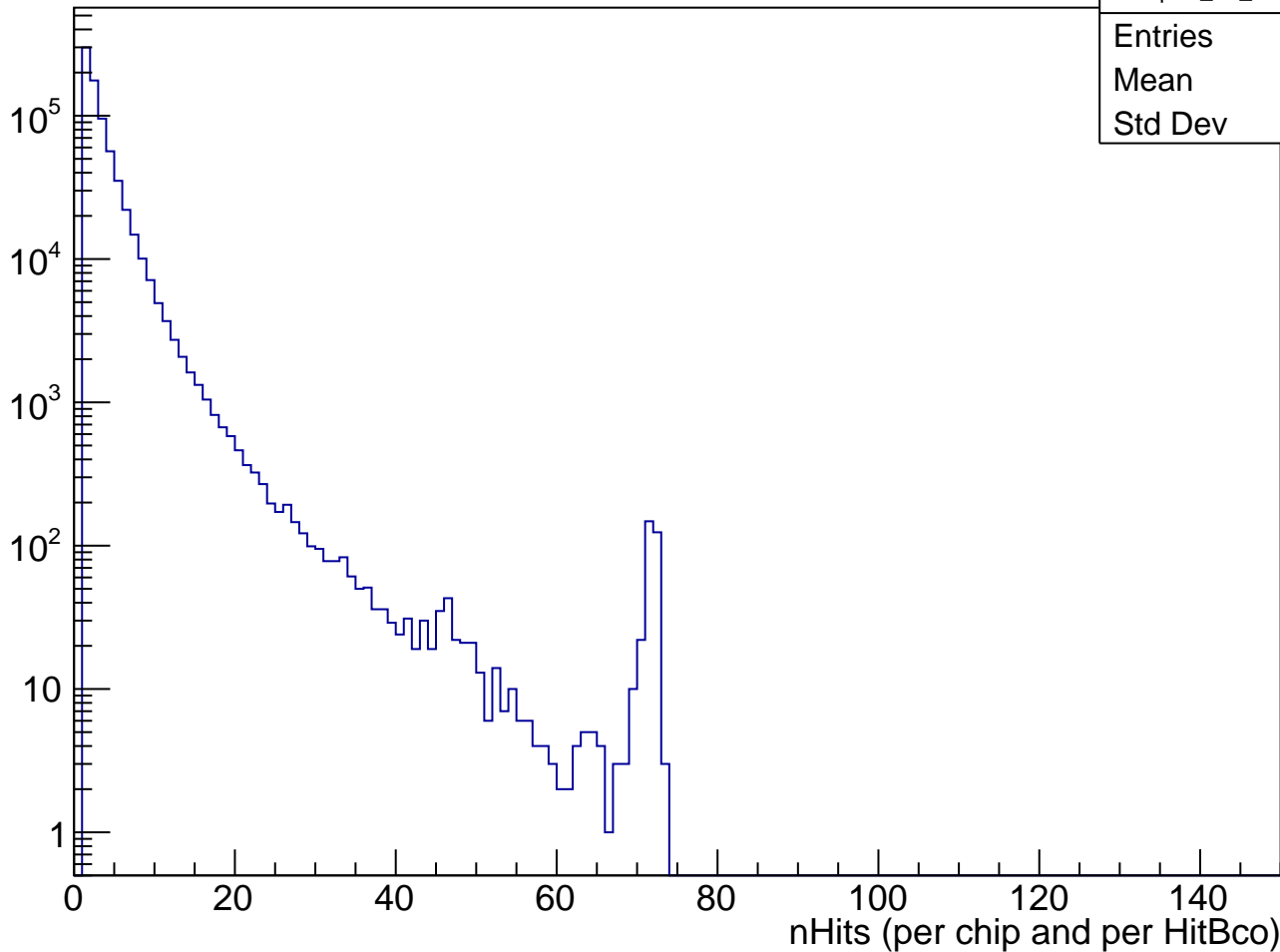
Entries



nChipHit_F0_Fch0_Chip20	
Entries	734667
Mean	2.794
Std Dev	3.239

nChipHit_F0_Fch0_Chip21

Entries

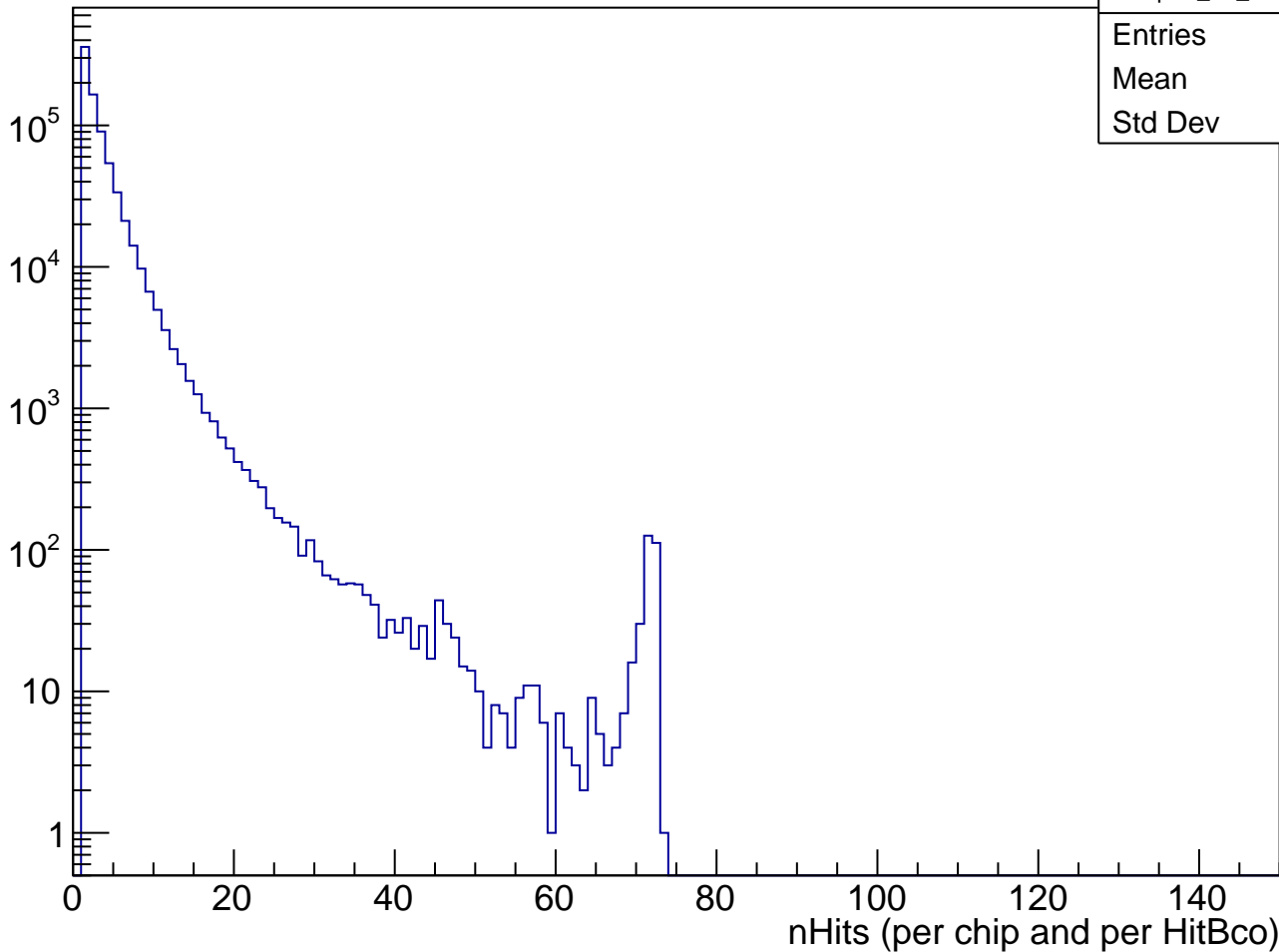


nChipHit_F0_Fch0_Chip21	
Entries	739891
Mean	2.811
Std Dev	3.316

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip22

Entries

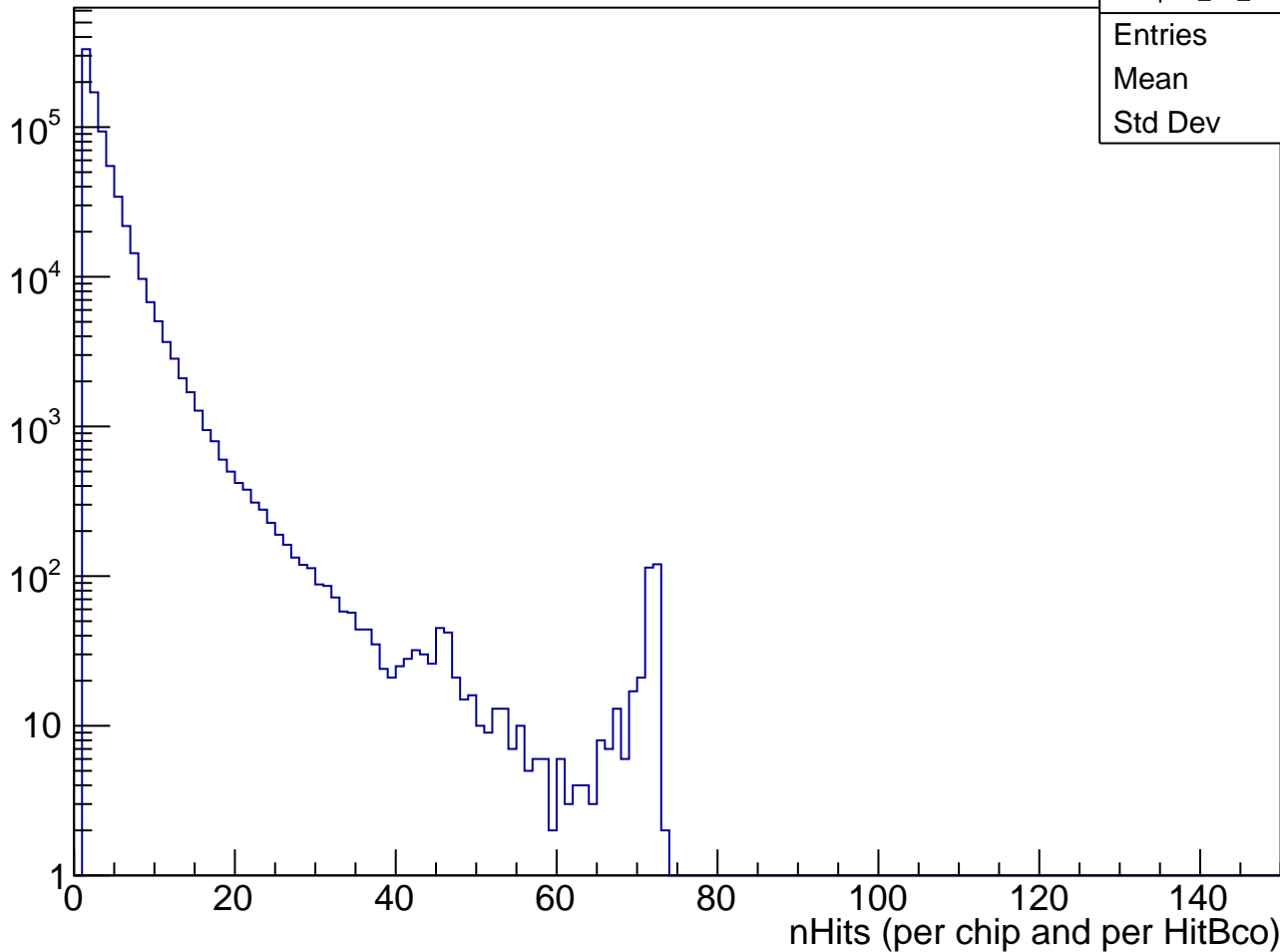


nChipHit_F0_Fch0_Chip22	
Entries	776195
Mean	2.647
Std Dev	3.206

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip23

Entries

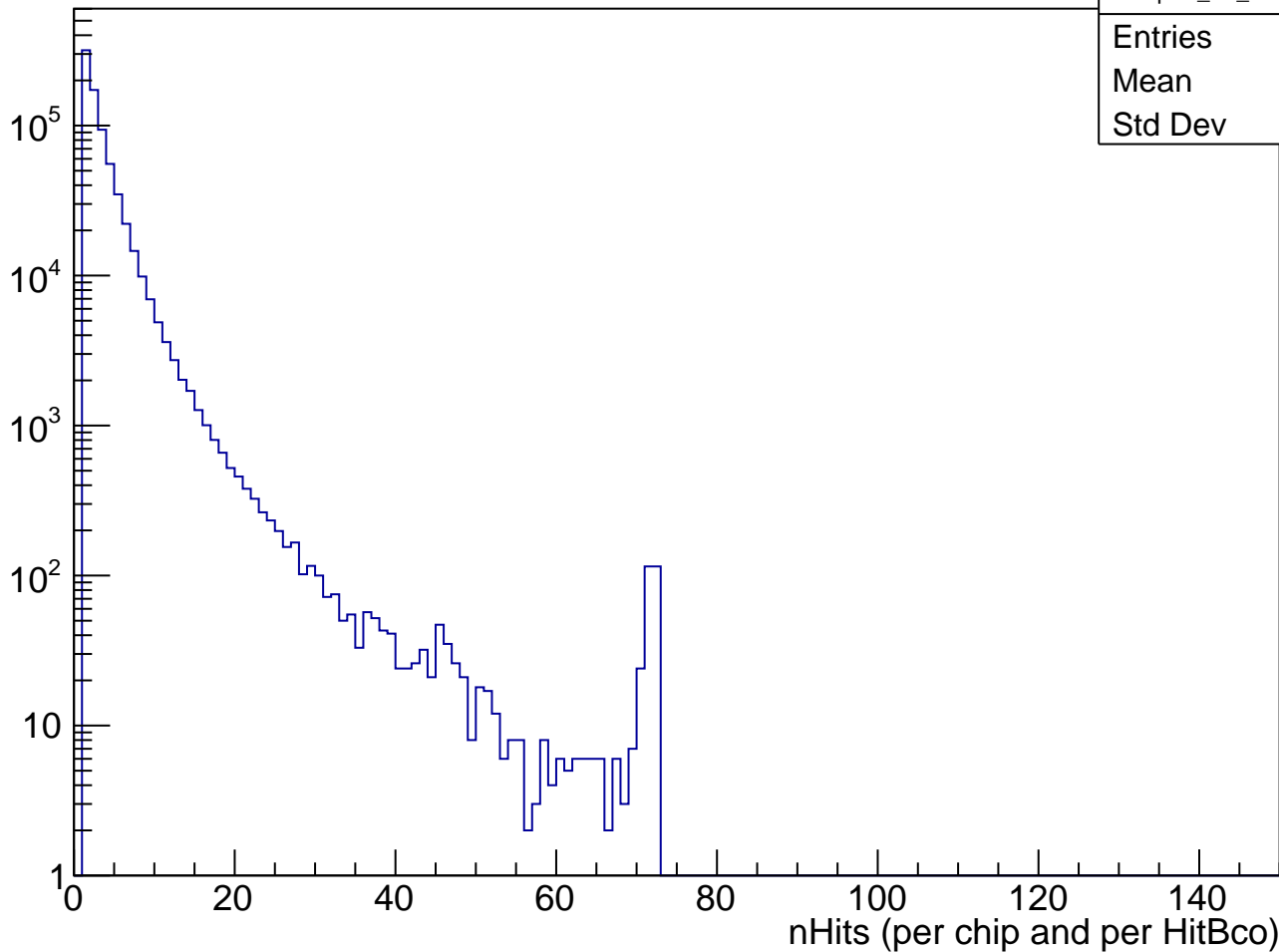


nChipHit_F0_Fch0_Chip23	
Entries	761012
Mean	2.721
Std Dev	3.254

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip24

Entries

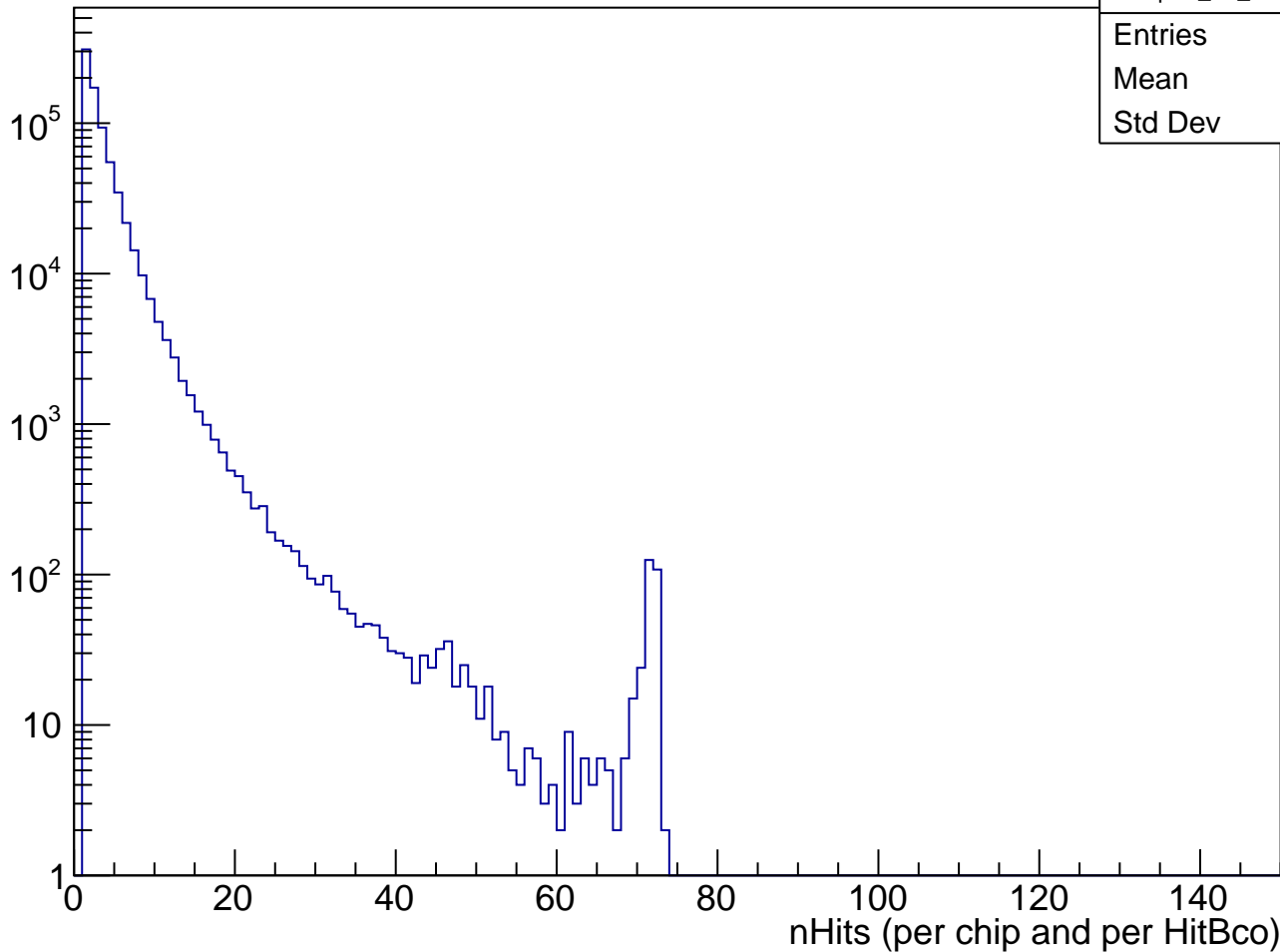


nChipHit_F0_Fch0_Chip24	
Entries	750771
Mean	2.759
Std Dev	3.267

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip25

Entries

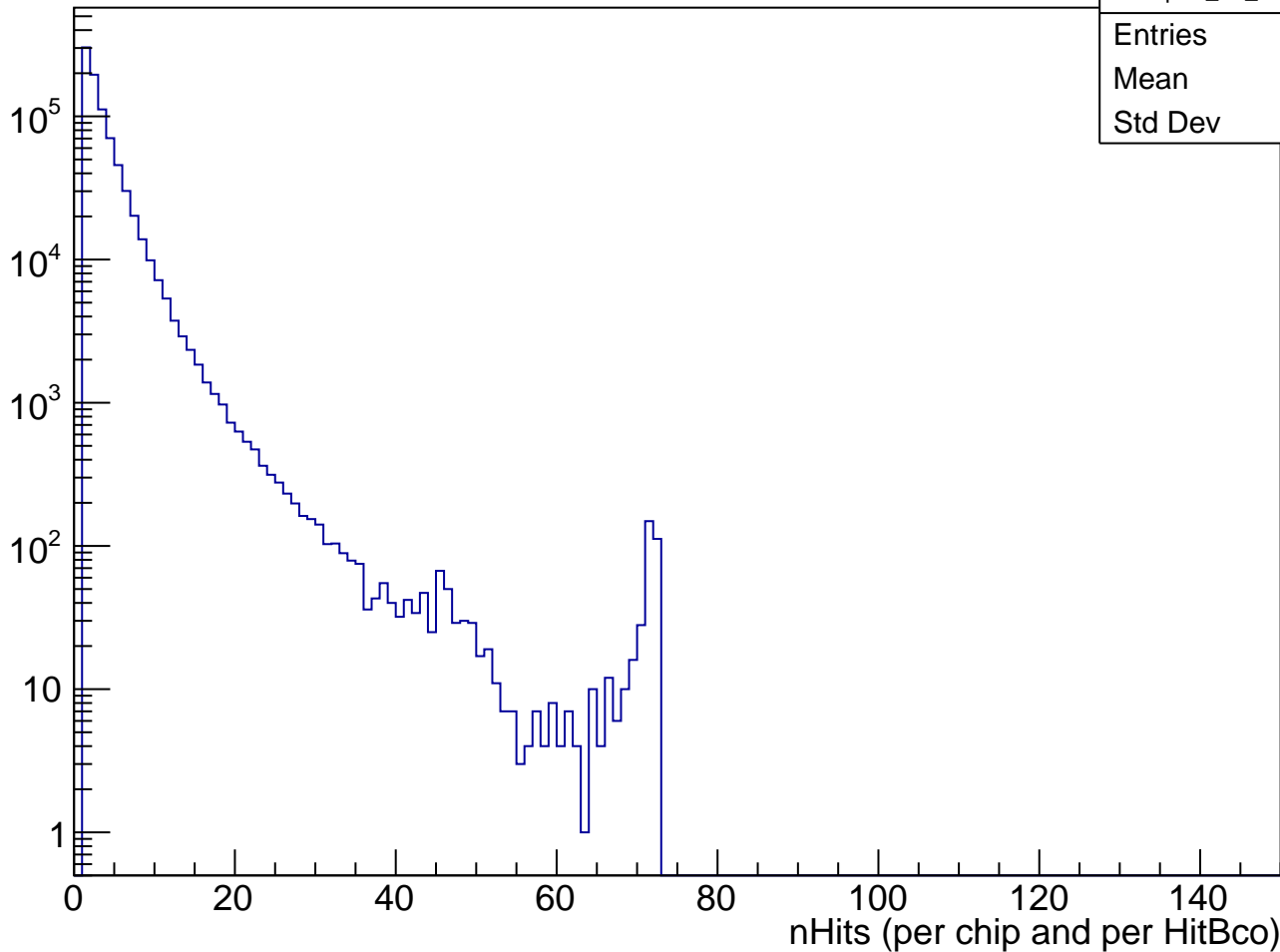


nChipHit_F0_Fch0_Chip25	
Entries	739507
Mean	2.762
Std Dev	3.258

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip3

Entries

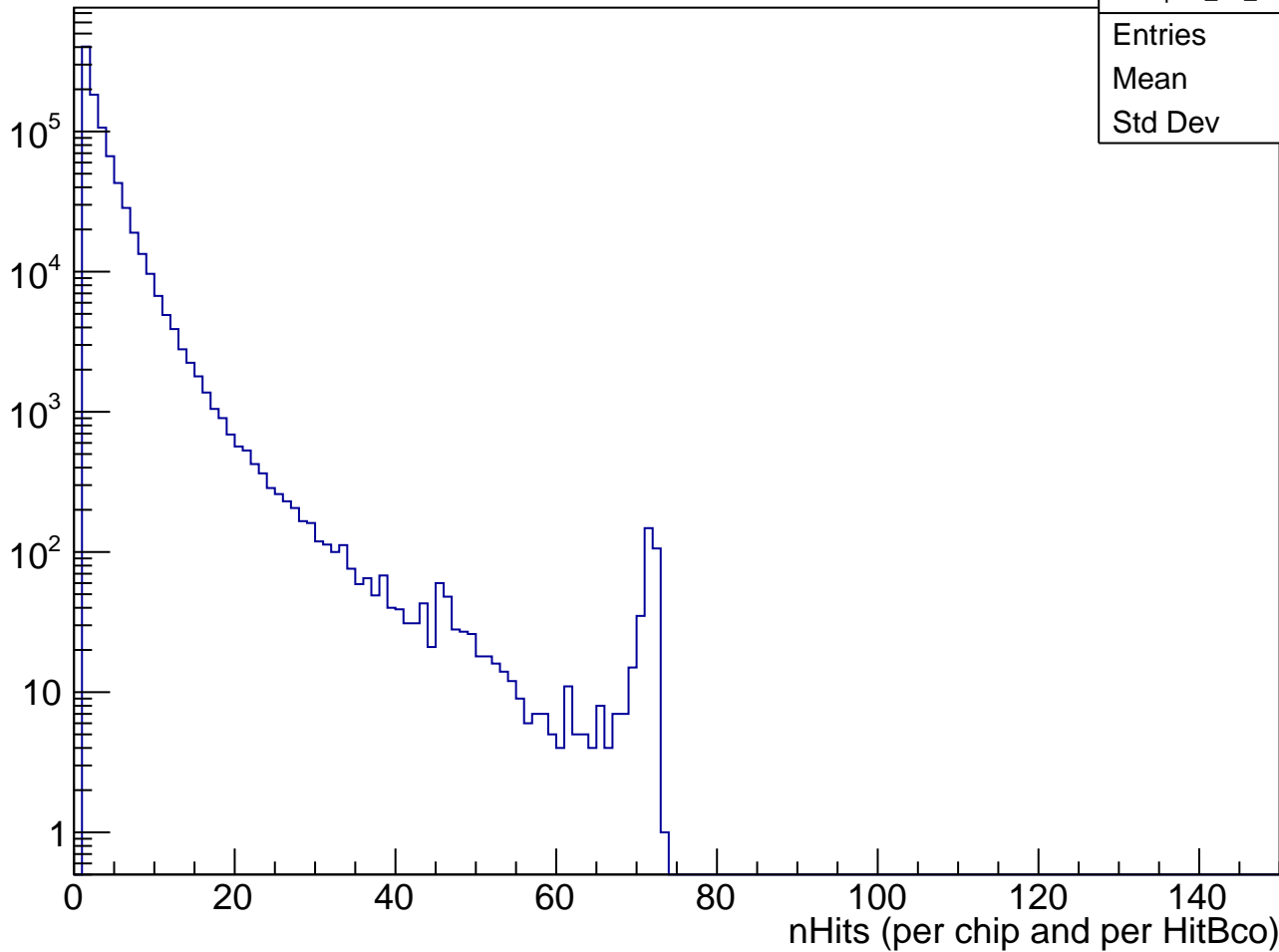


nChipHit_F0_Fch0_Chip3	
Entries	832815
Mean	3.071
Std Dev	3.512

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip4

Entries



nChipHit_F0_Fch0_Chip4

Entries 904623

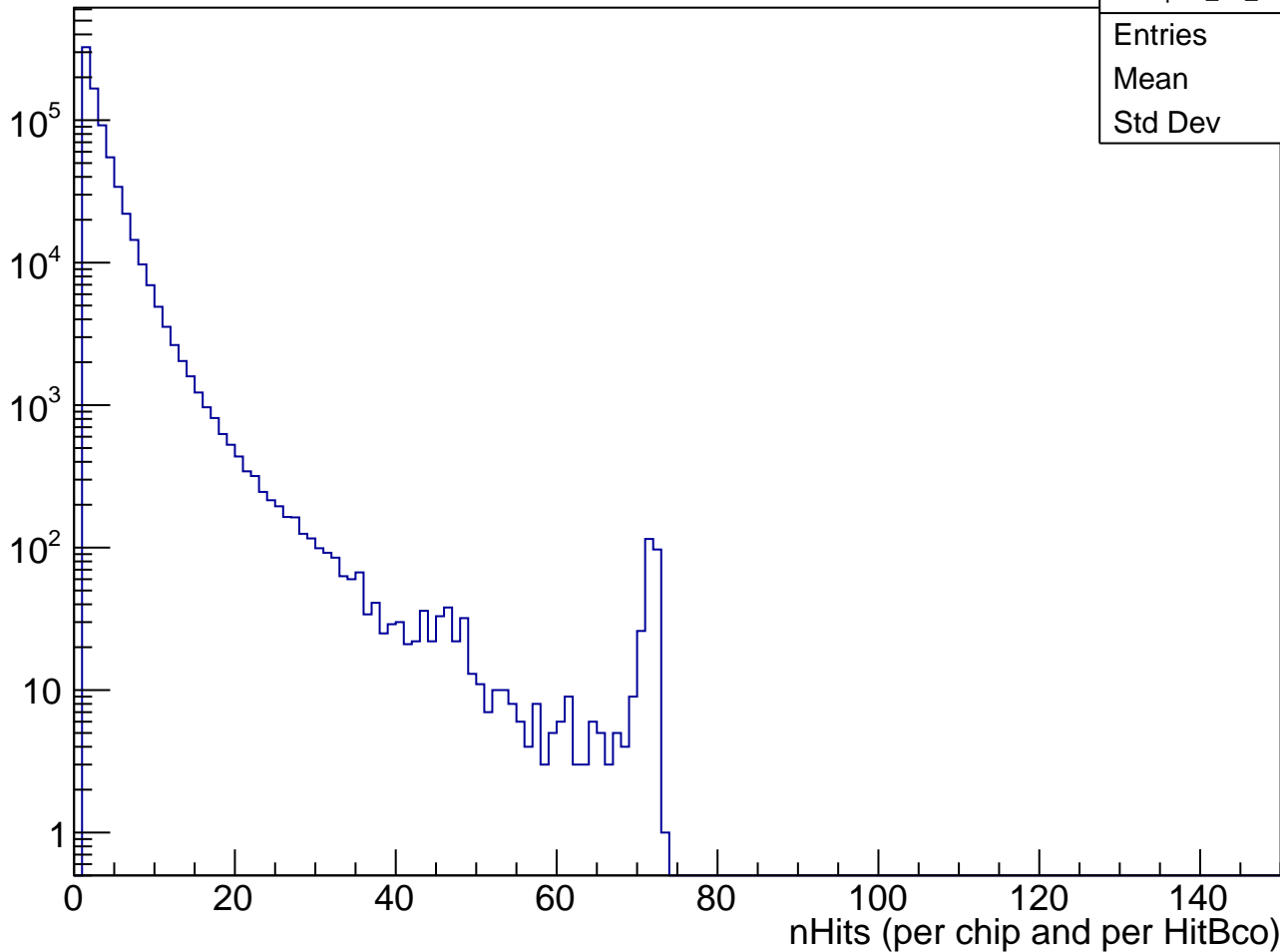
Mean 2.817

Std Dev 3.397

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip5

Entries



nChipHit_F0_Fch0_Chip5

Entries 748707

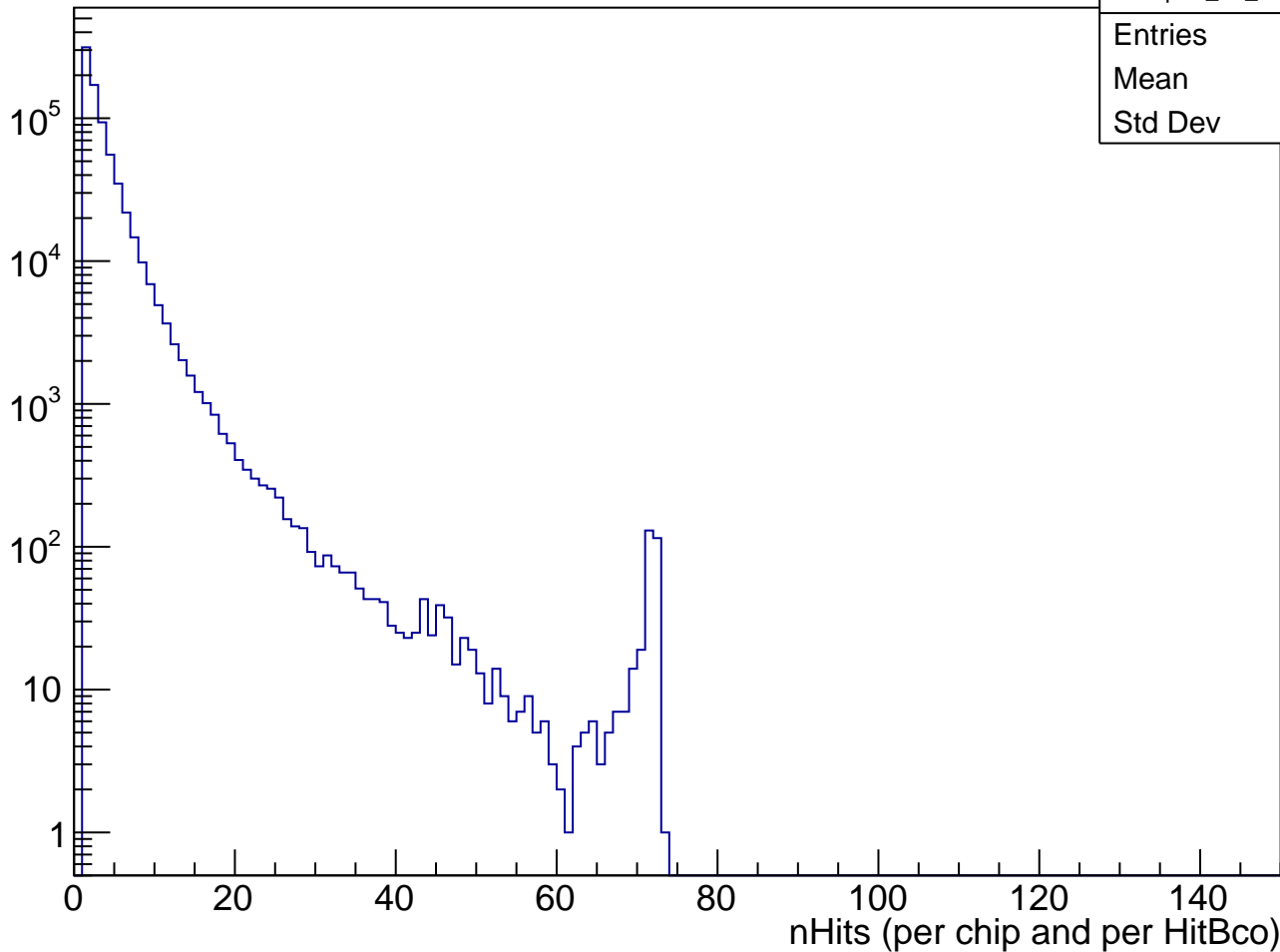
Mean 2.733

Std Dev 3.243

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip6

Entries



nChipHit_F0_Fch0_Chip6

Entries 744675

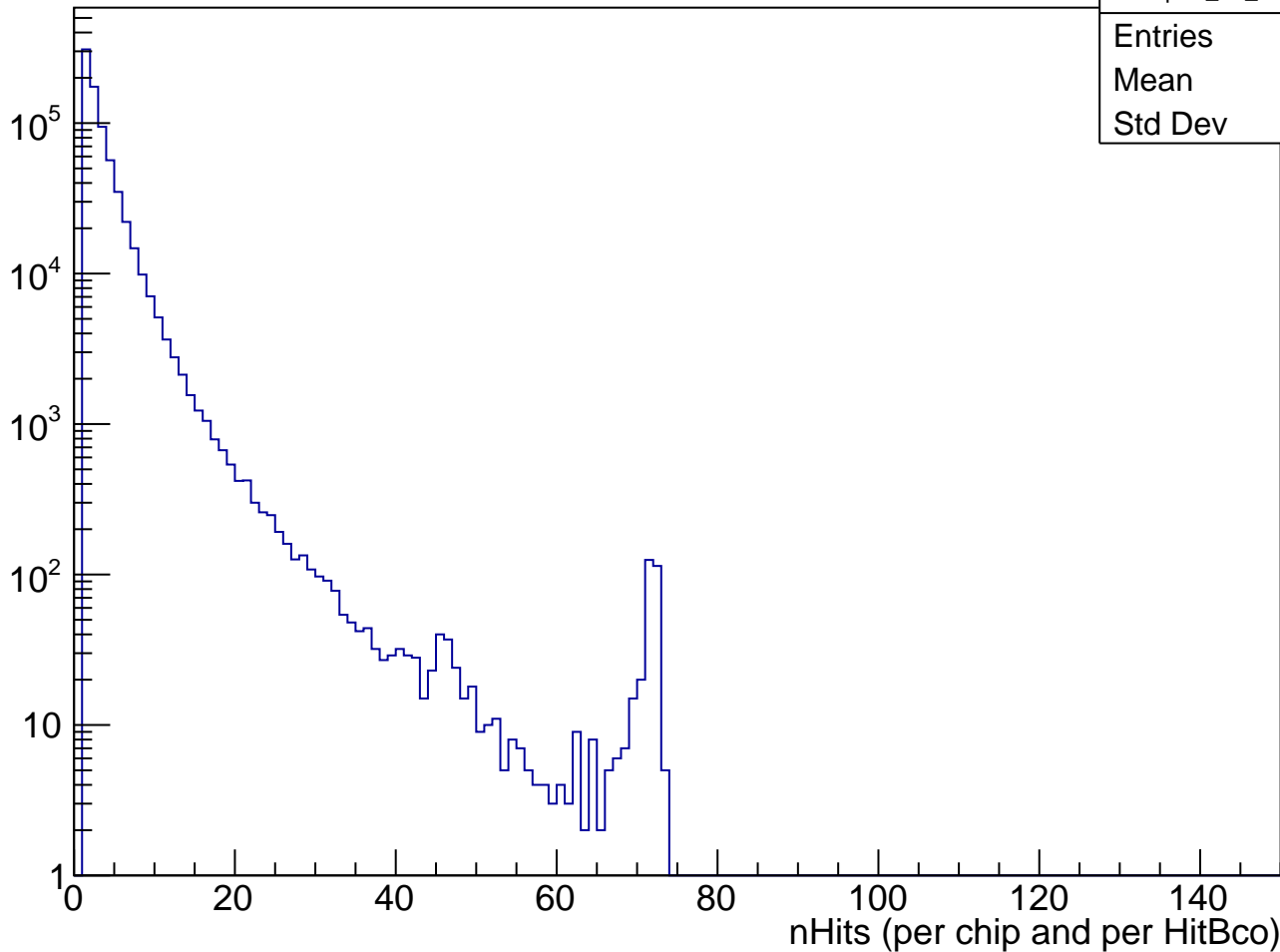
Mean 2.764

Std Dev 3.278

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip7

Entries

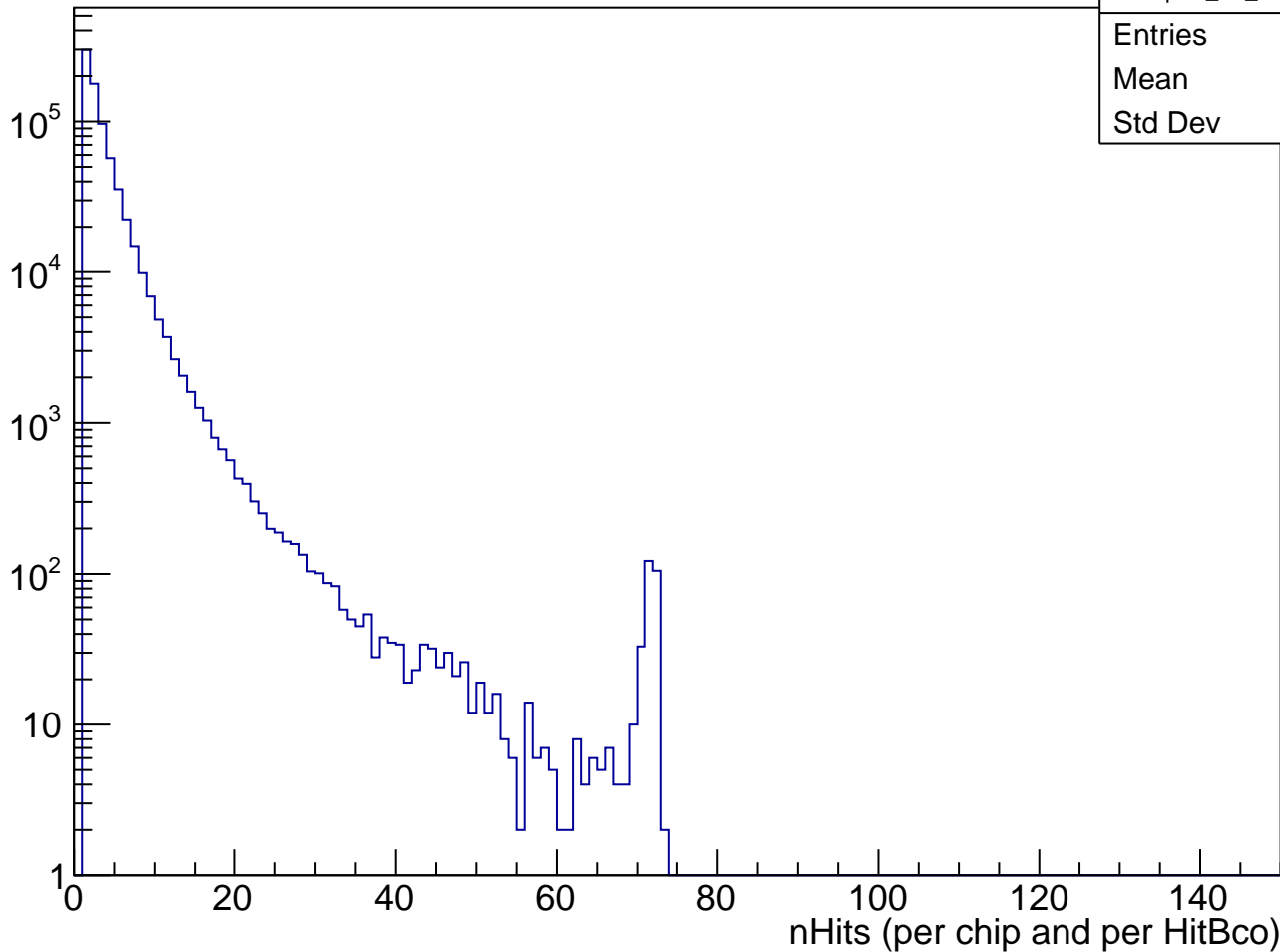


nChipHit_F0_Fch0_Chip7	
Entries	746151
Mean	2.782
Std Dev	3.272

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip8

Entries

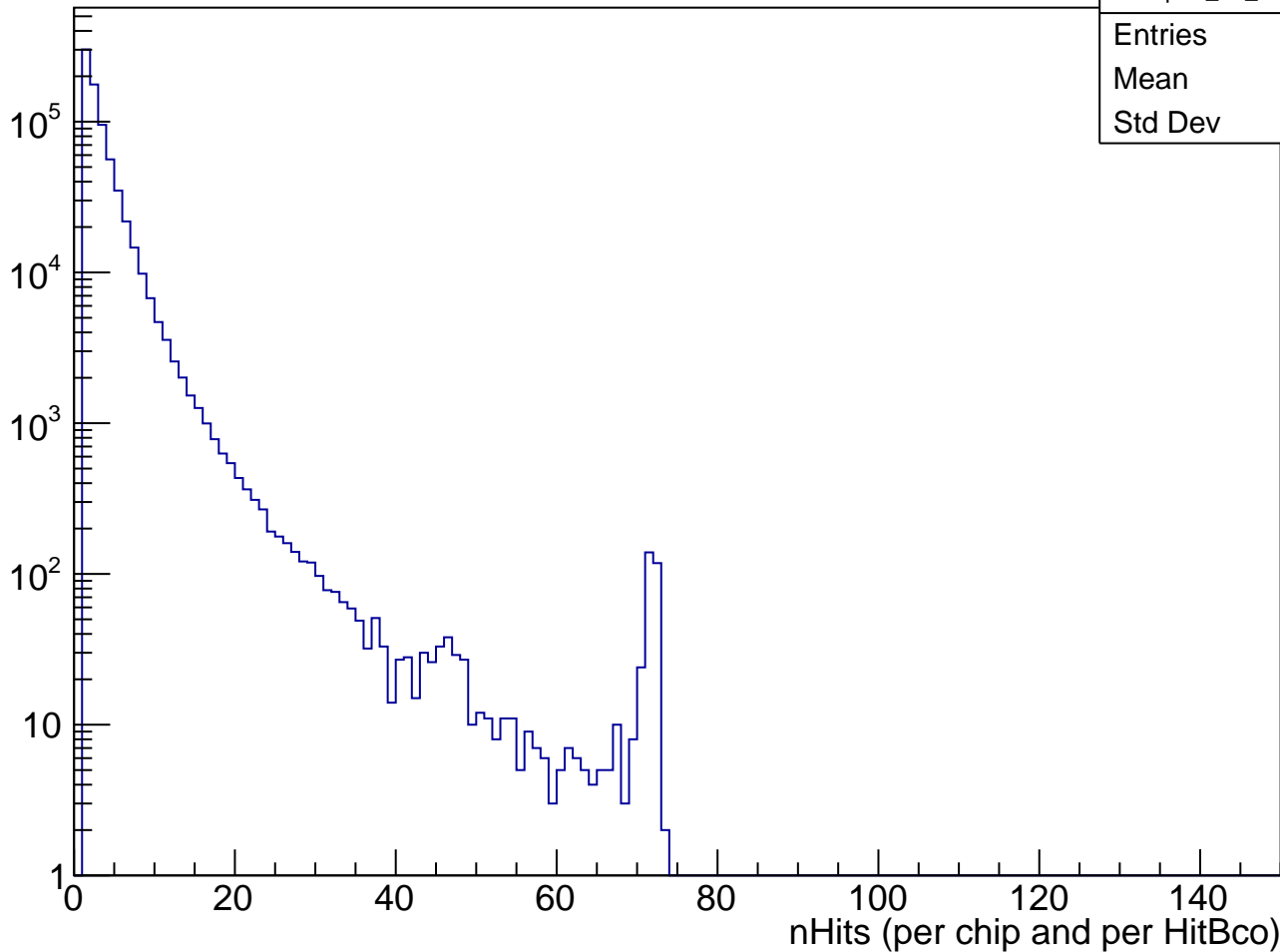


nChipHit_F0_Fch0_Chip8	
Entries	742680
Mean	2.803
Std Dev	3.278

nHits (per chip and per HitBco)

nChipHit_F0_Fch0_Chip9

Entries



nChipHit_F0_Fch0_Chip9

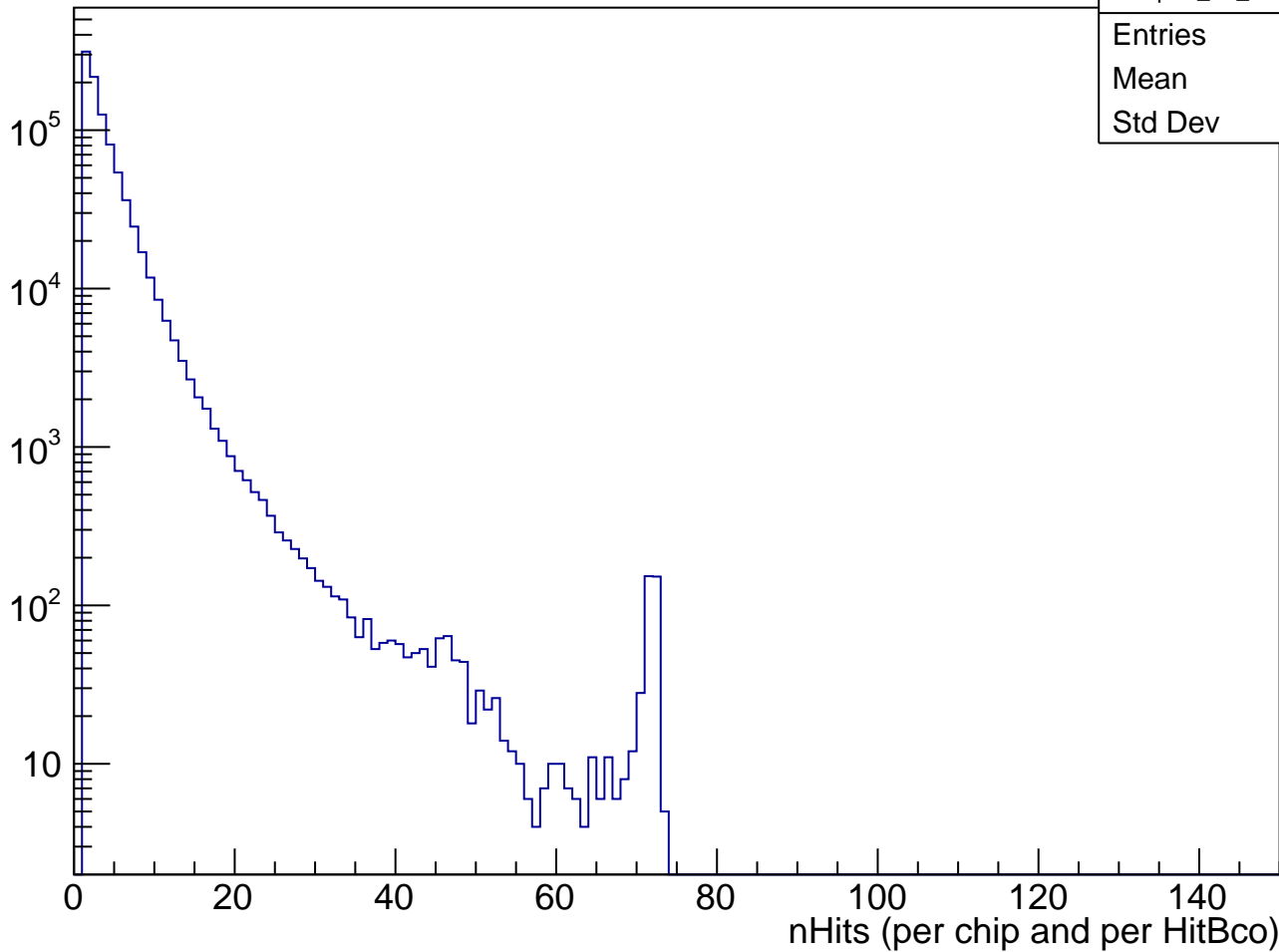
Entries 739015

Mean 2.784

Std Dev 3.284

nChipHit_F0_Fch10_Chip0

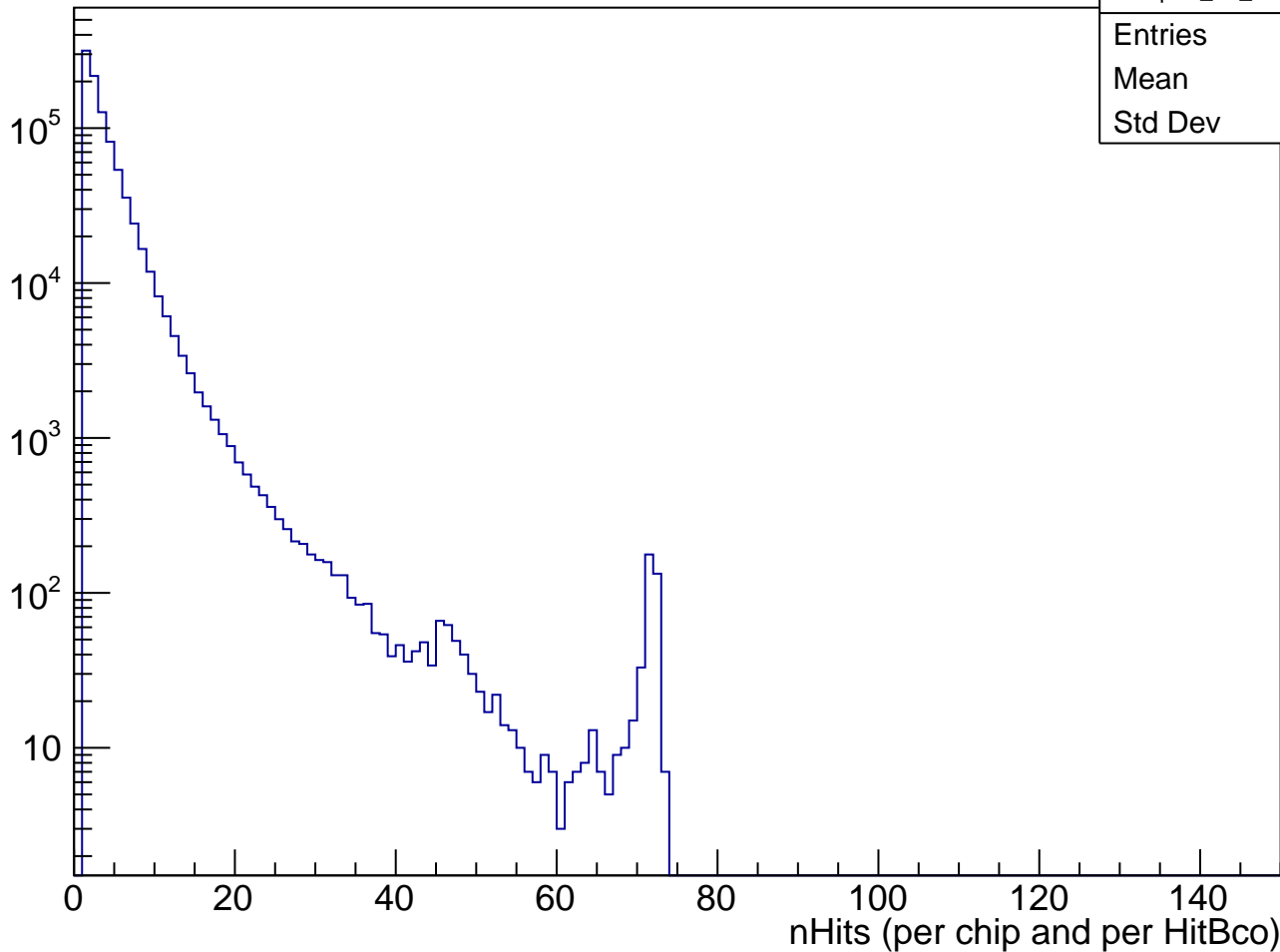
Entries



nChipHit_F0_Fch10_Chip0	
Entries	918854
Mean	3.196
Std Dev	3.611

nChipHit_F0_Fch10_Chip1

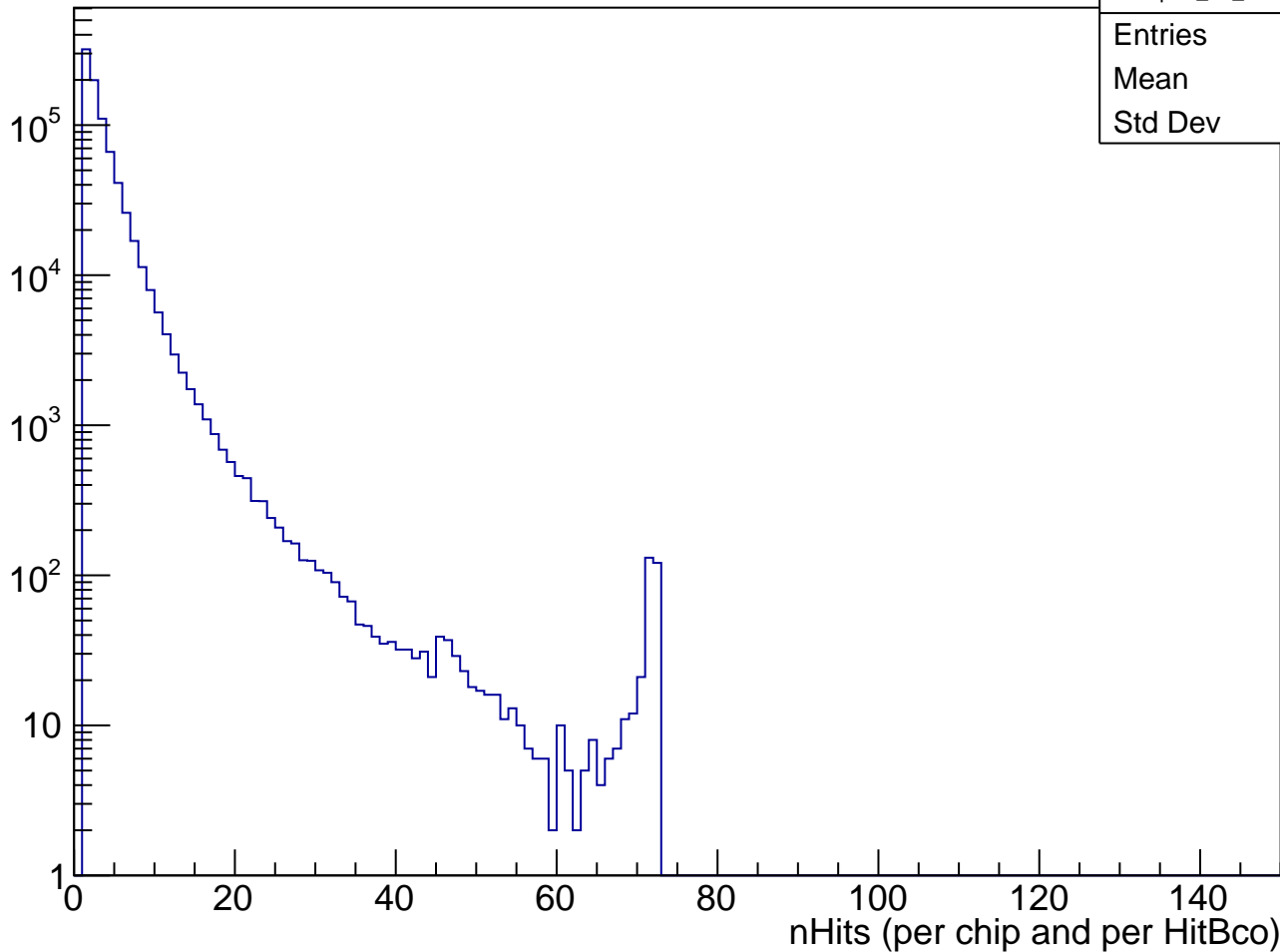
Entries



nChipHit_F0_Fch10_Chip1	
Entries	921208
Mean	3.173
Std Dev	3.597

nChipHit_F0_Fch10_Chip10

Entries



nChipHit_F0_Fch10_Chip10

Entries 824633

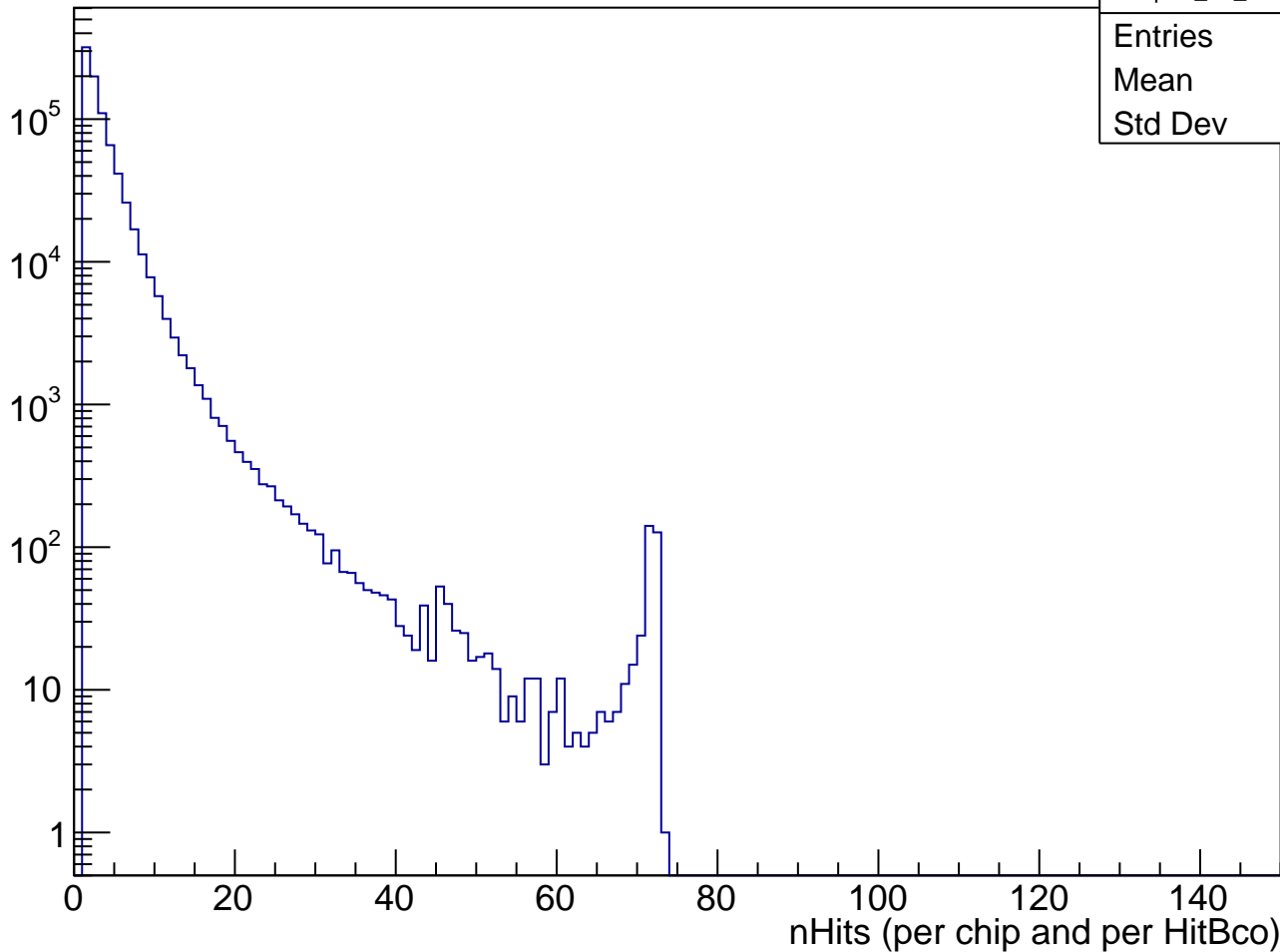
Mean 2.846

Std Dev 3.267

nHits (per chip and per HitBco)

nChipHit_F0_Fch10_Chip11

Entries

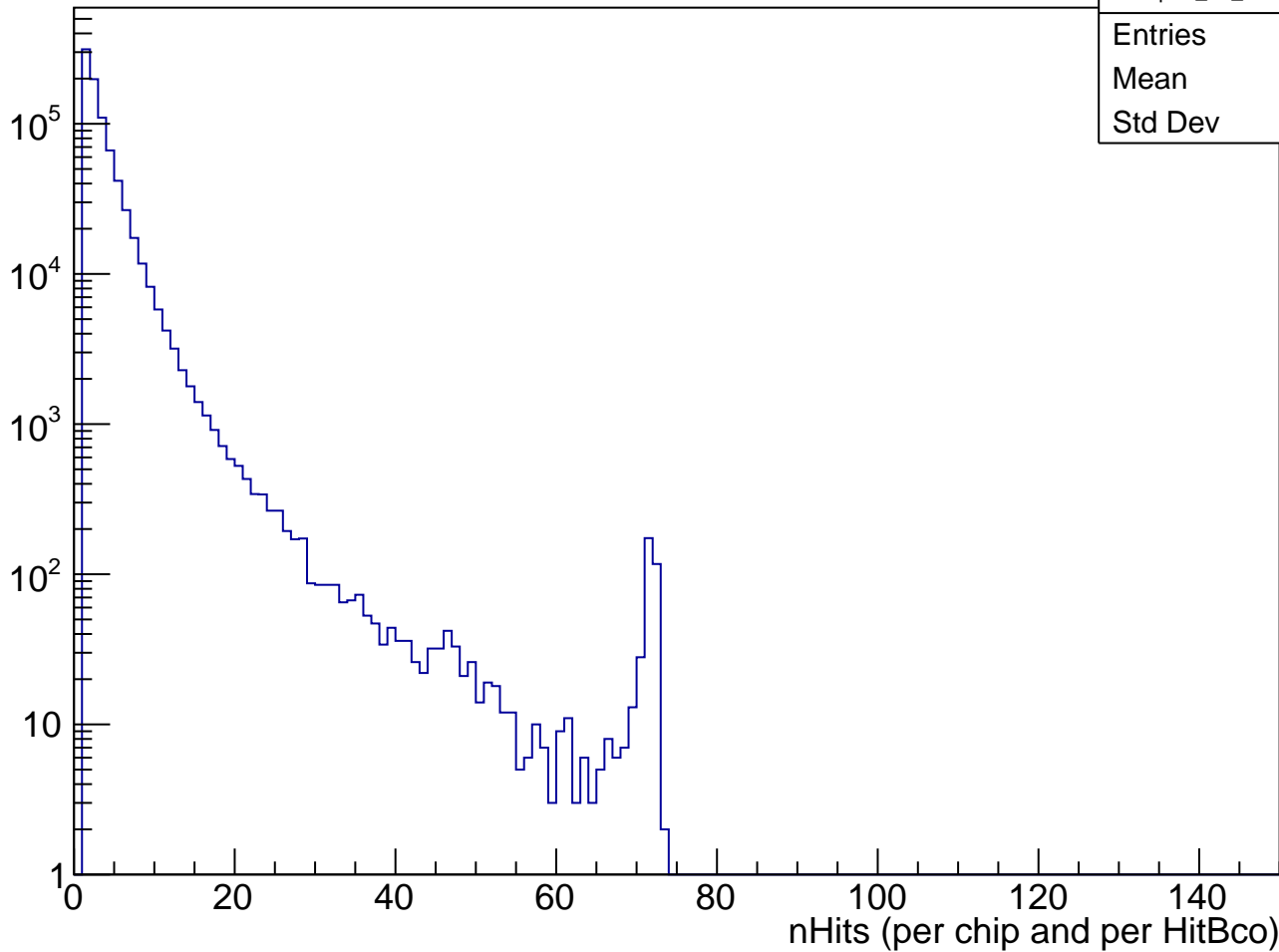


nChipHit_F0_Fch10_Chip11	
Entries	822901
Mean	2.851
Std Dev	3.301

nHits (per chip and per HitBco)

nChipHit_F0_Fch10_Chip12

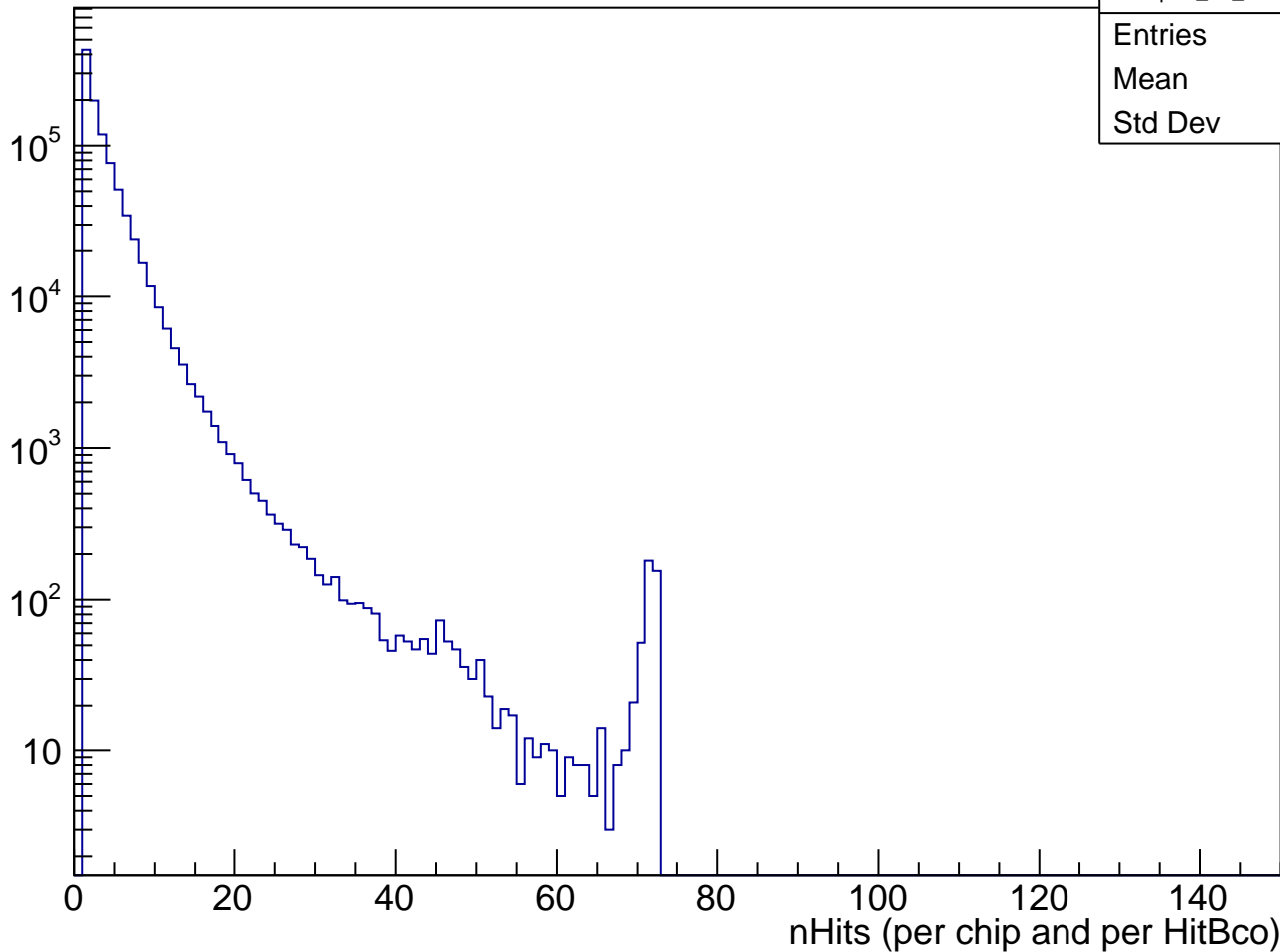
Entries



nChipHit_F0_Fch10_Chip12	
Entries	819876
Mean	2.893
Std Dev	3.36

nChipHit_F0_Fch10_Chip13

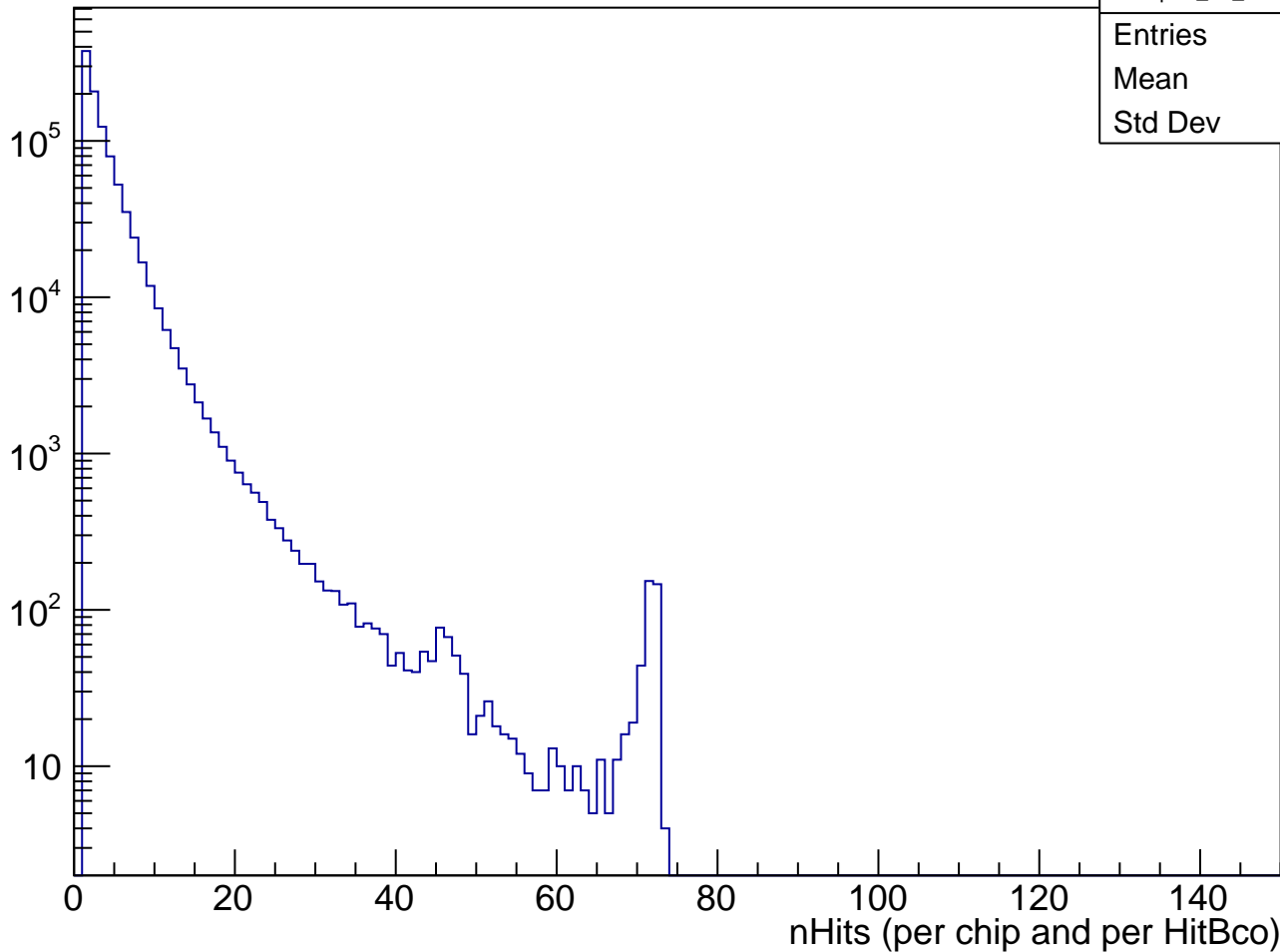
Entries



nChipHit_F0_Fch10_Chip13	
Entries	999685
Mean	2.958
Std Dev	3.595

nChipHit_F0_Fch10_Chip14

Entries



nChipHit_F0_Fch10_Chip14

Entries 965018

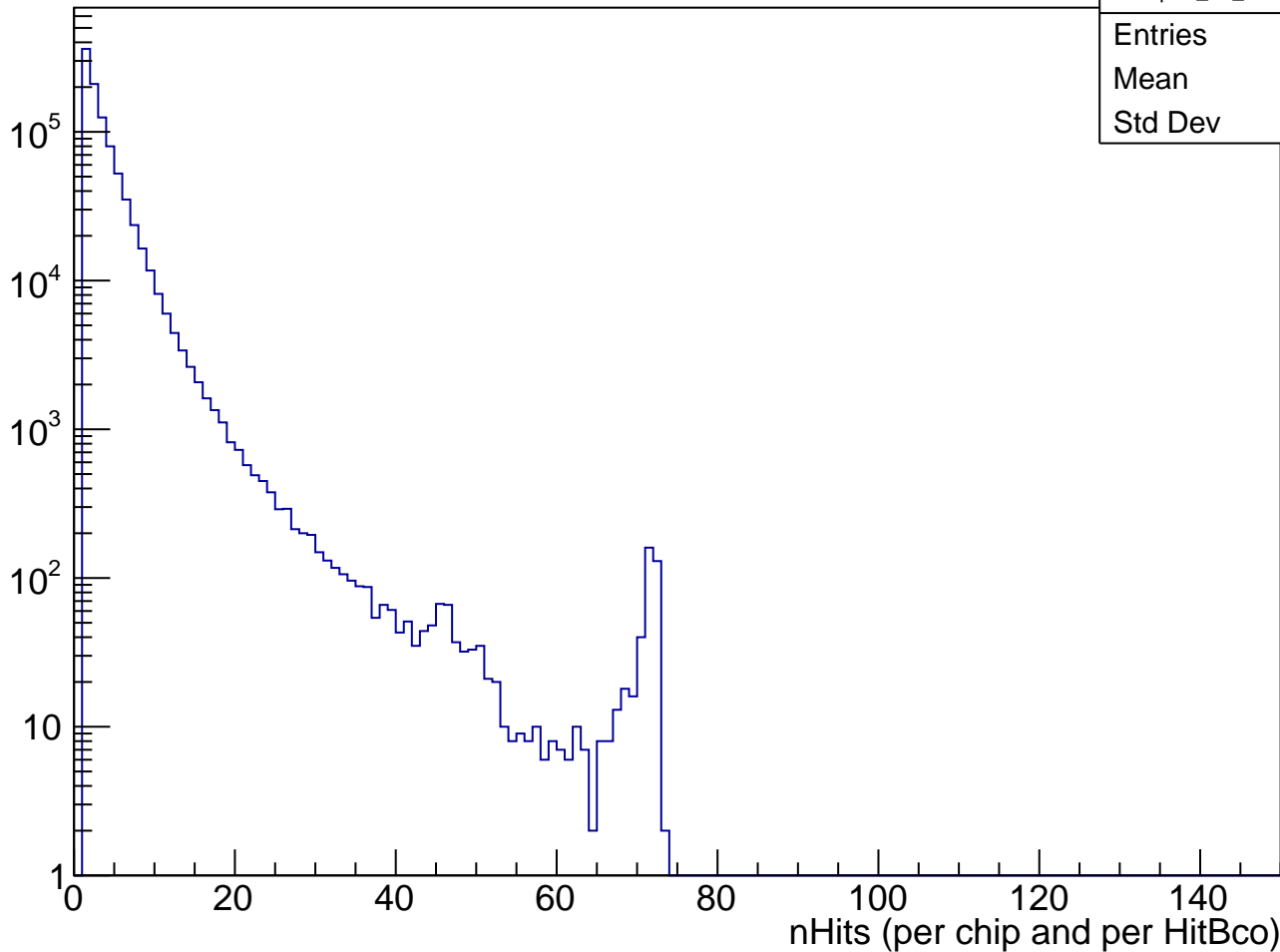
Mean 3.068

Std Dev 3.611

nHits (per chip and per HitBco)

nChipHit_F0_Fch10_Chip15

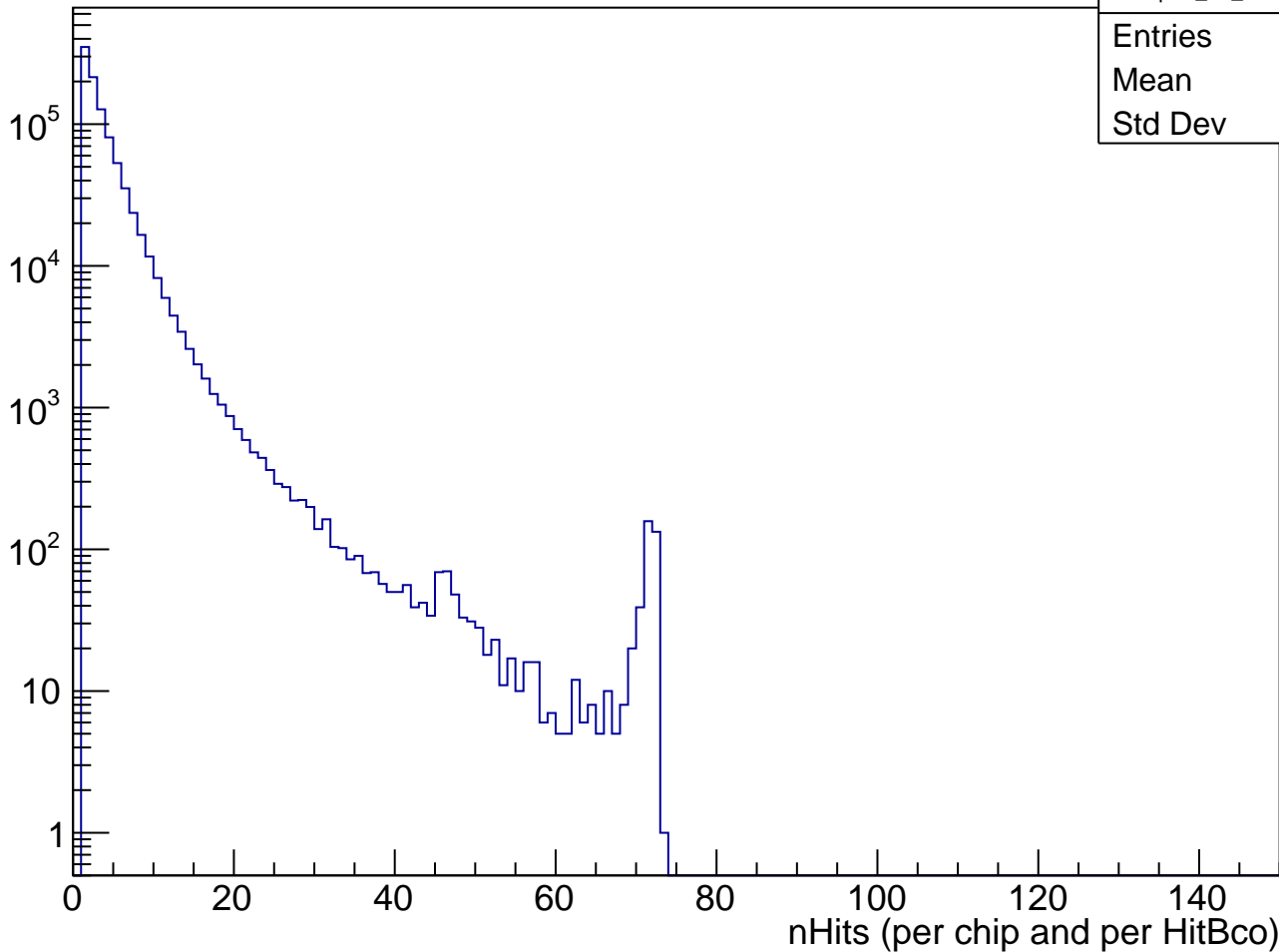
Entries



nChipHit_F0_Fch10_Chip15	
Entries	952587
Mean	3.071
Std Dev	3.564

nChipHit_F0_Fch10_Chip16

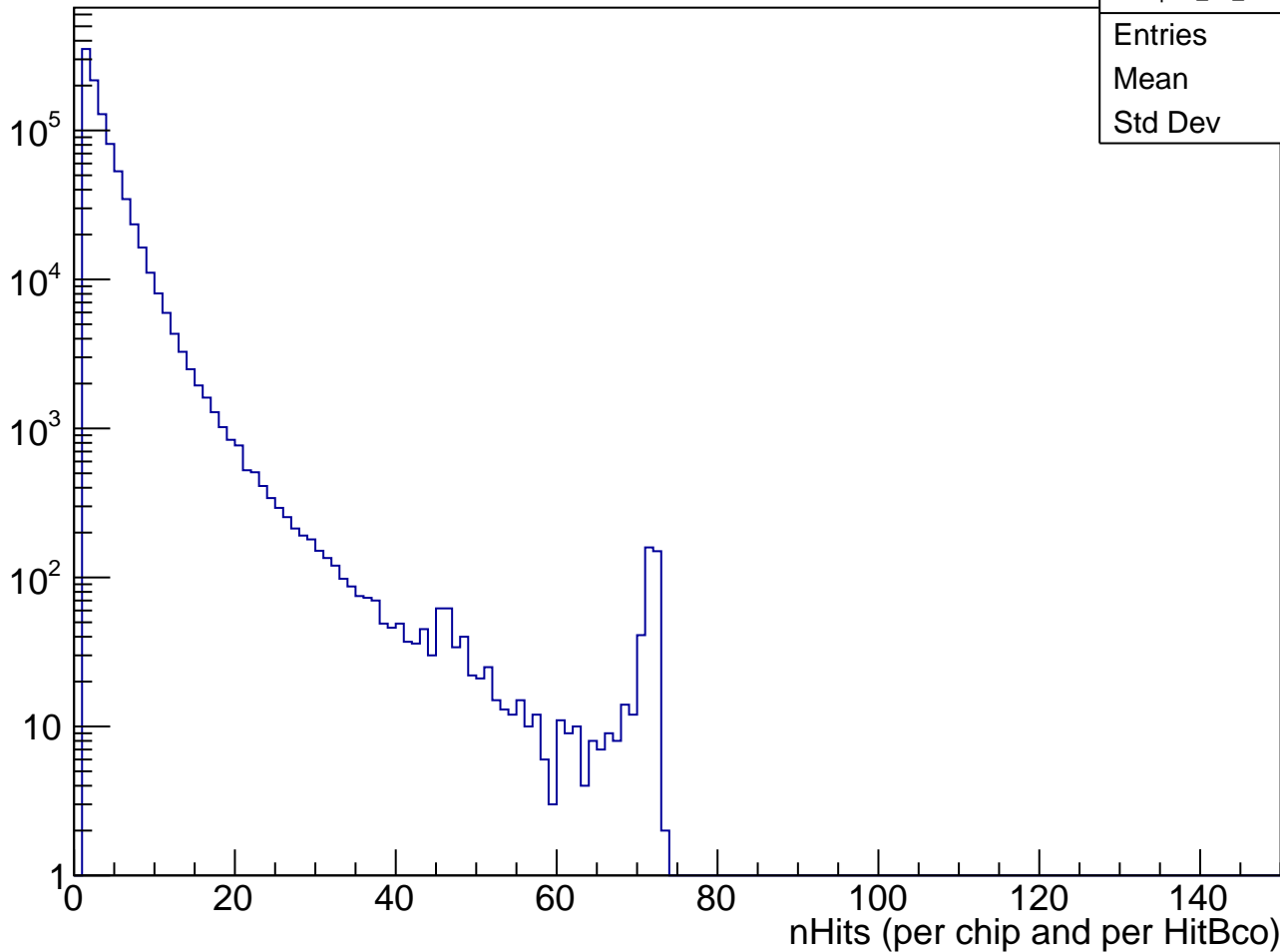
Entries



nChipHit_F0_Fch10_Chip16	
Entries	950381
Mean	3.09
Std Dev	3.558

nChipHit_F0_Fch10_Chip17

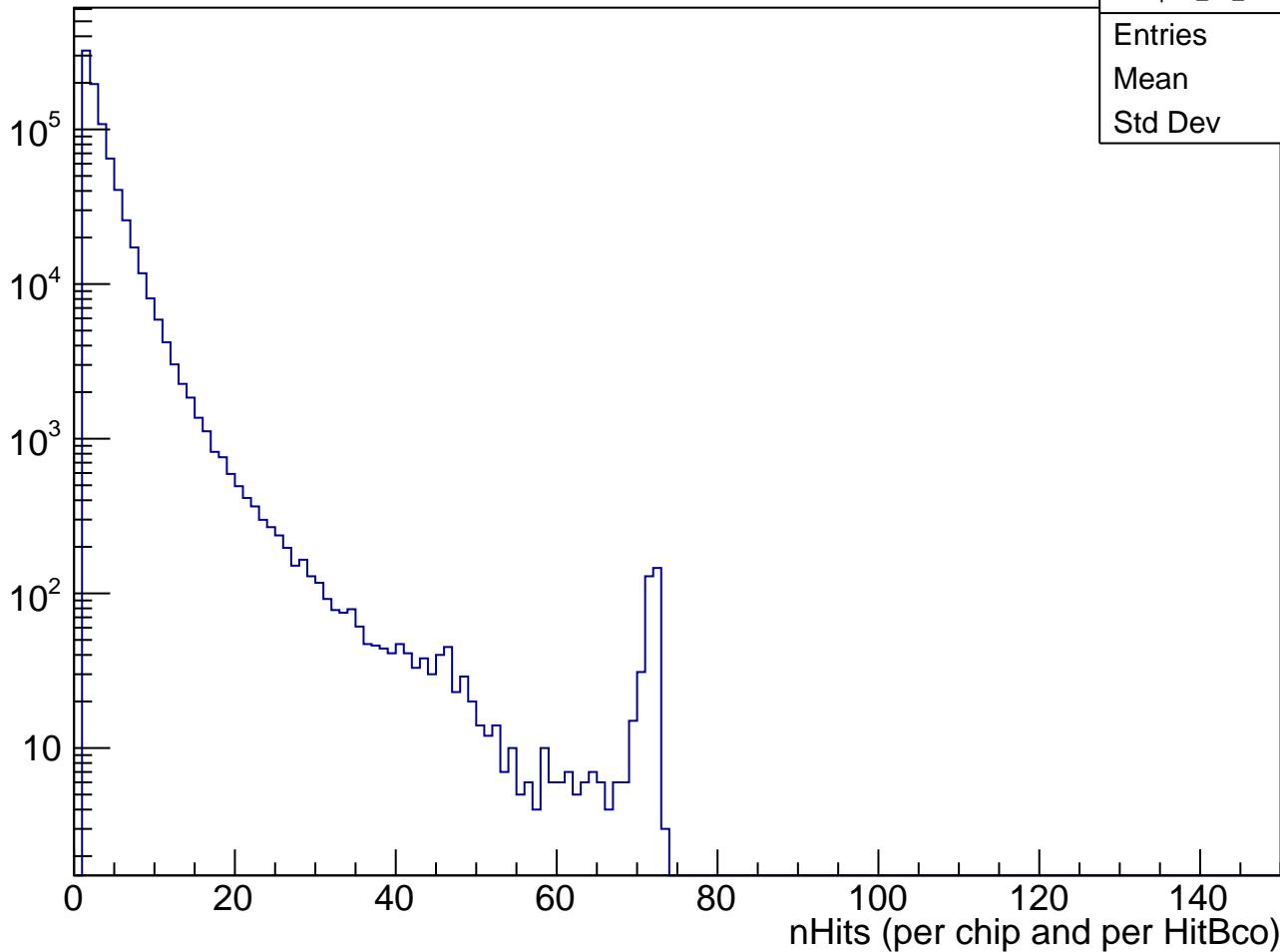
Entries



nChipHit_F0_Fch10_Chip17	
Entries	953658
Mean	3.065
Std Dev	3.522

nChipHit_F0_Fch10_Chip18

Entries



nChipHit_F0_Fch10_Chip18

Entries 822282

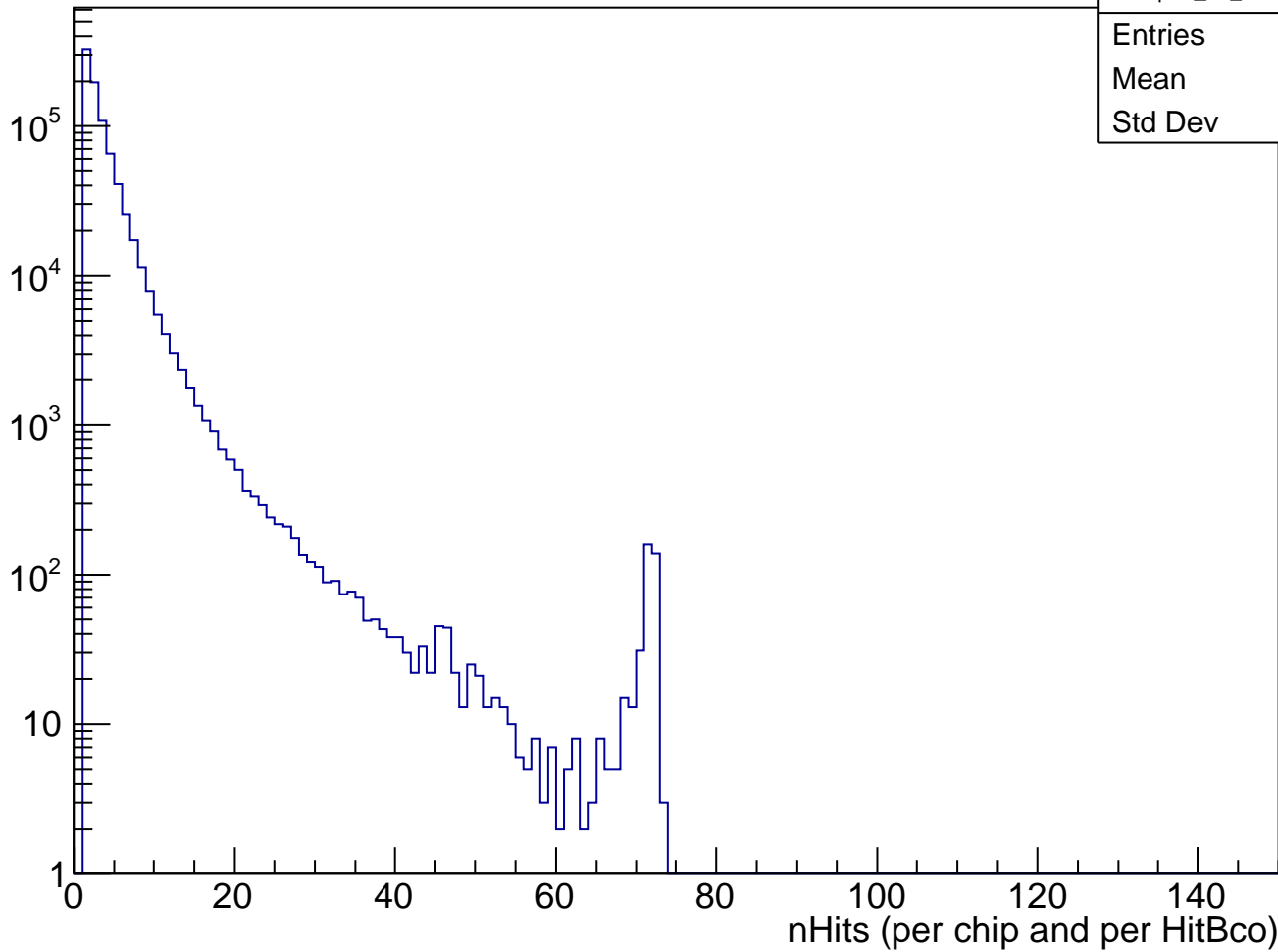
Mean 2.86

Std Dev 3.348

nHits (per chip and per HitBco)

nChipHit_F0_Fch10_Chip19

Entries

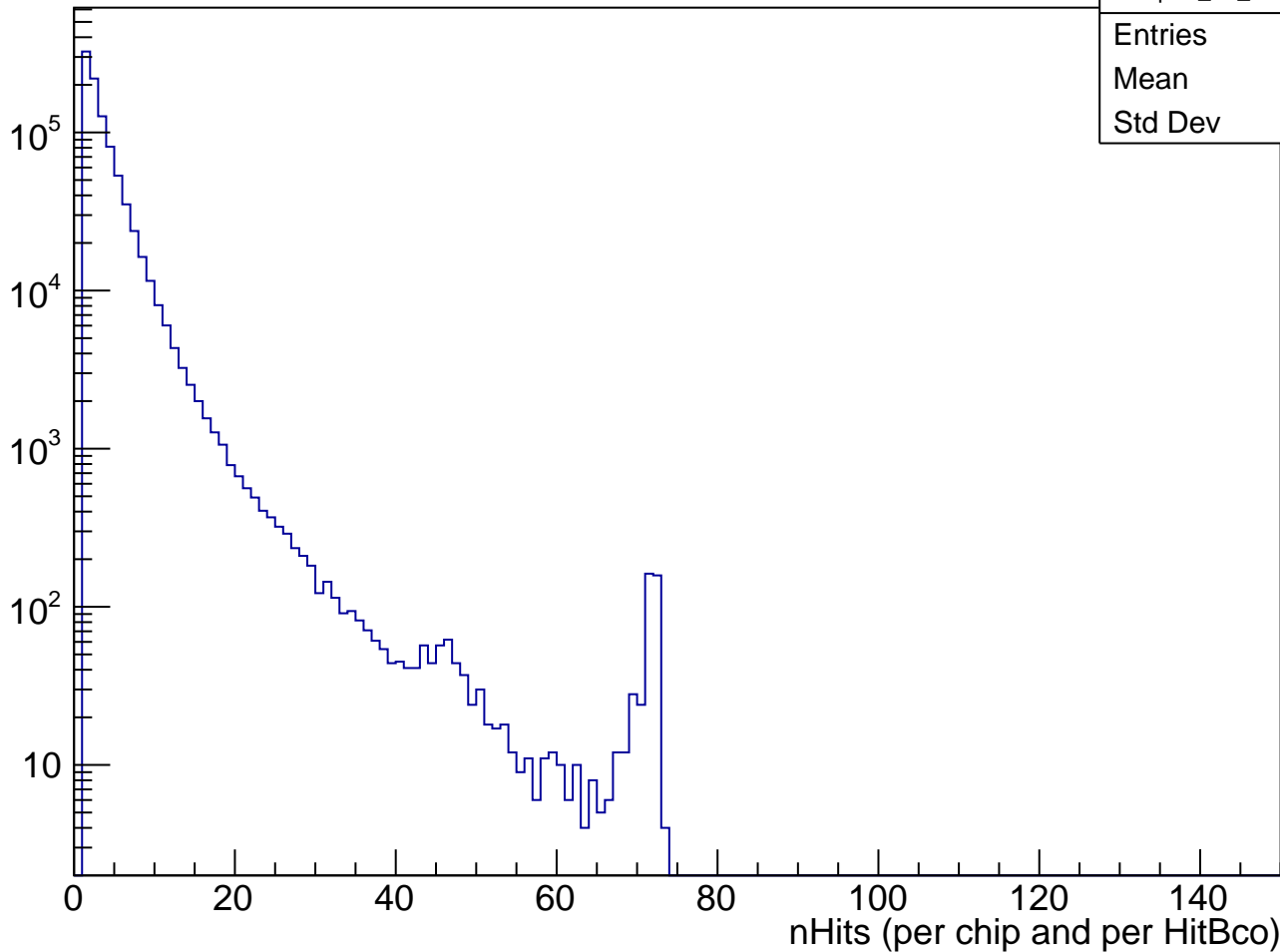


nChipHit_F0_Fch10_Chip19	
Entries	826099
Mean	2.839
Std Dev	3.335

nHits (per chip and per HitBco)

nChipHit_F0_Fch10_Chip2

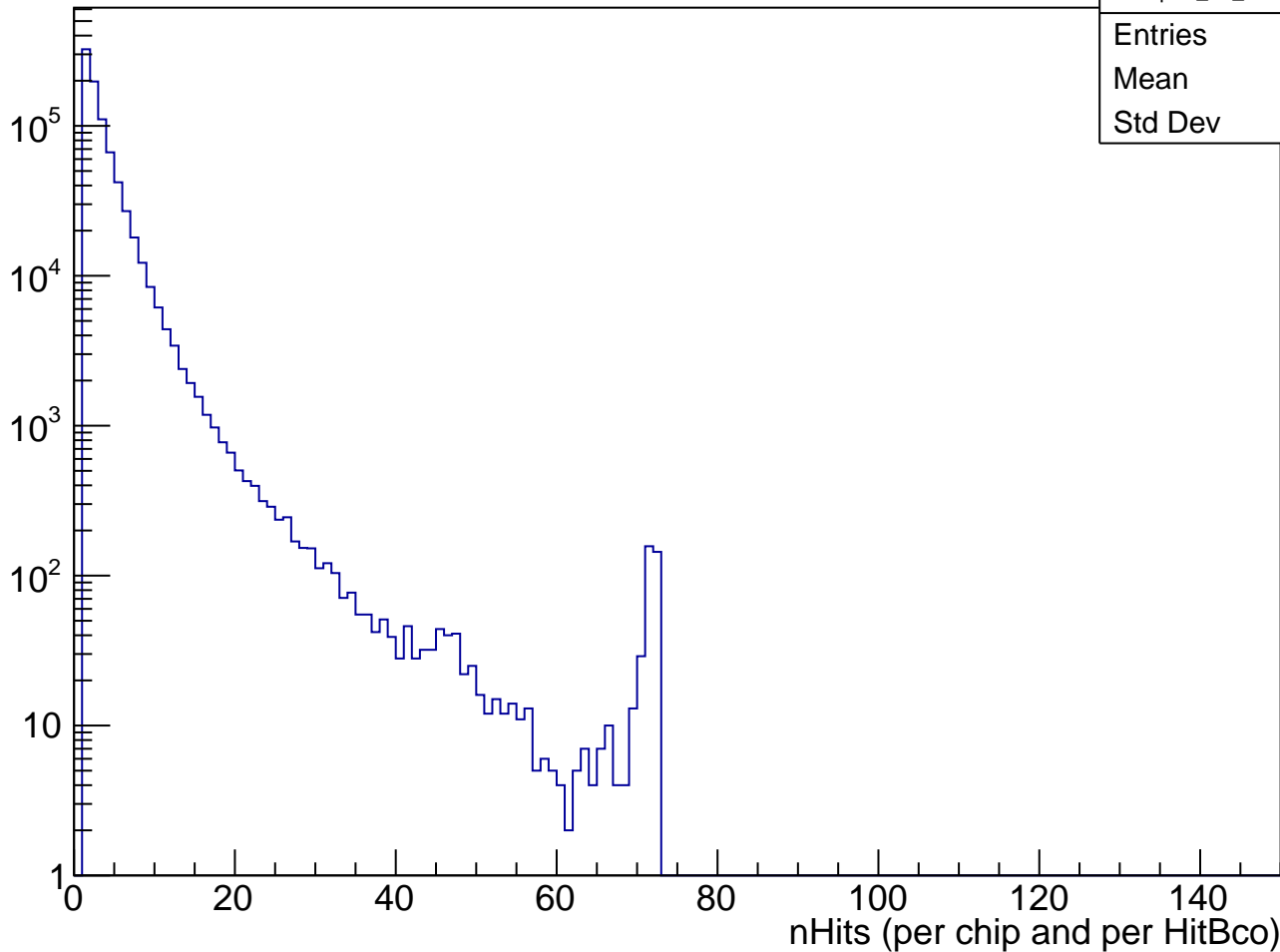
Entries



nChipHit_F0_Fch10_Chip2	
Entries	928327
Mean	3.133
Std Dev	3.578

nChipHit_F0_Fch10_Chip20

Entries



nChipHit_F0_Fch10_Chip20

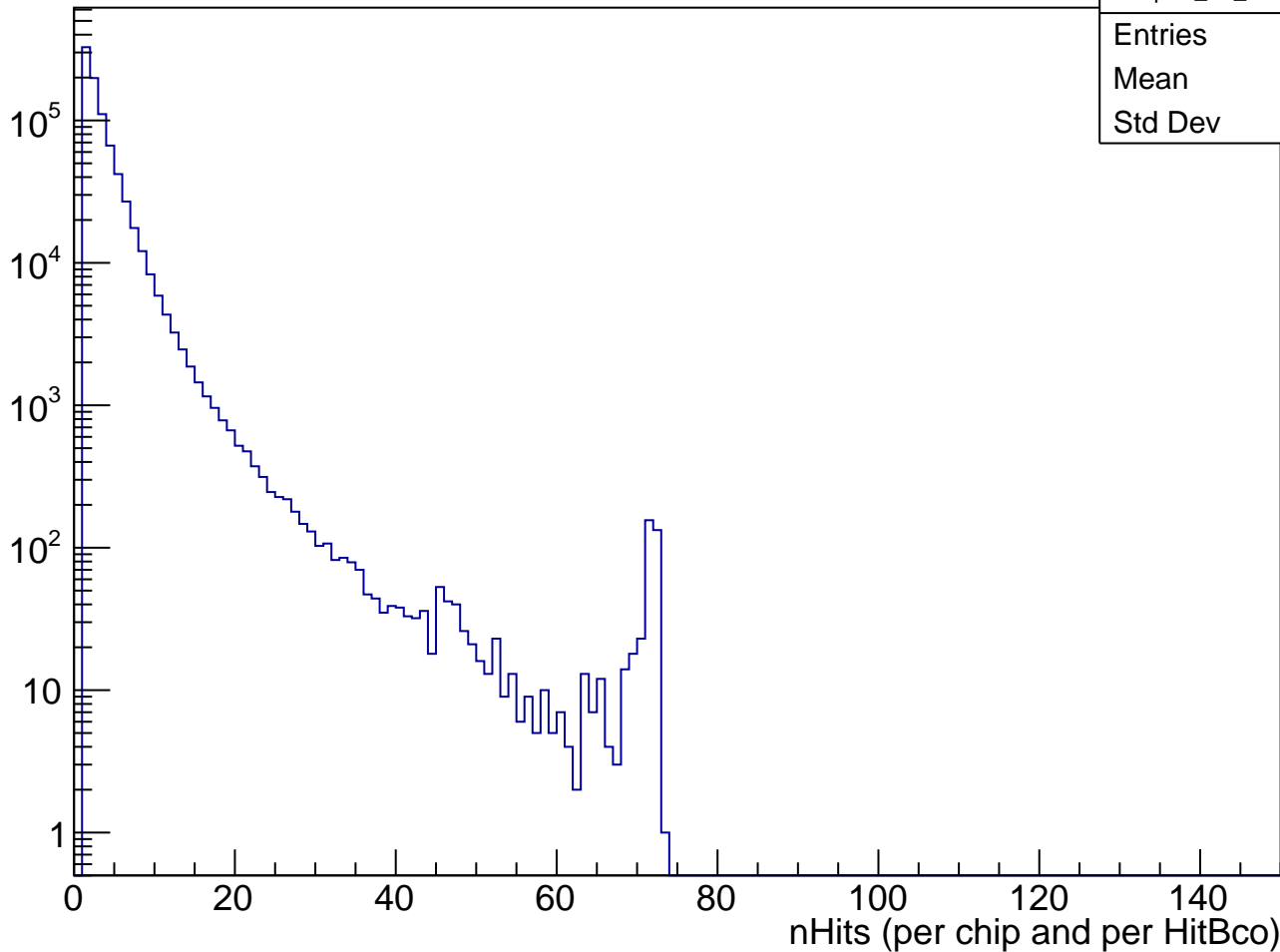
Entries 834627

Mean 2.904

Std Dev 3.407

nChipHit_F0_Fch10_Chip21

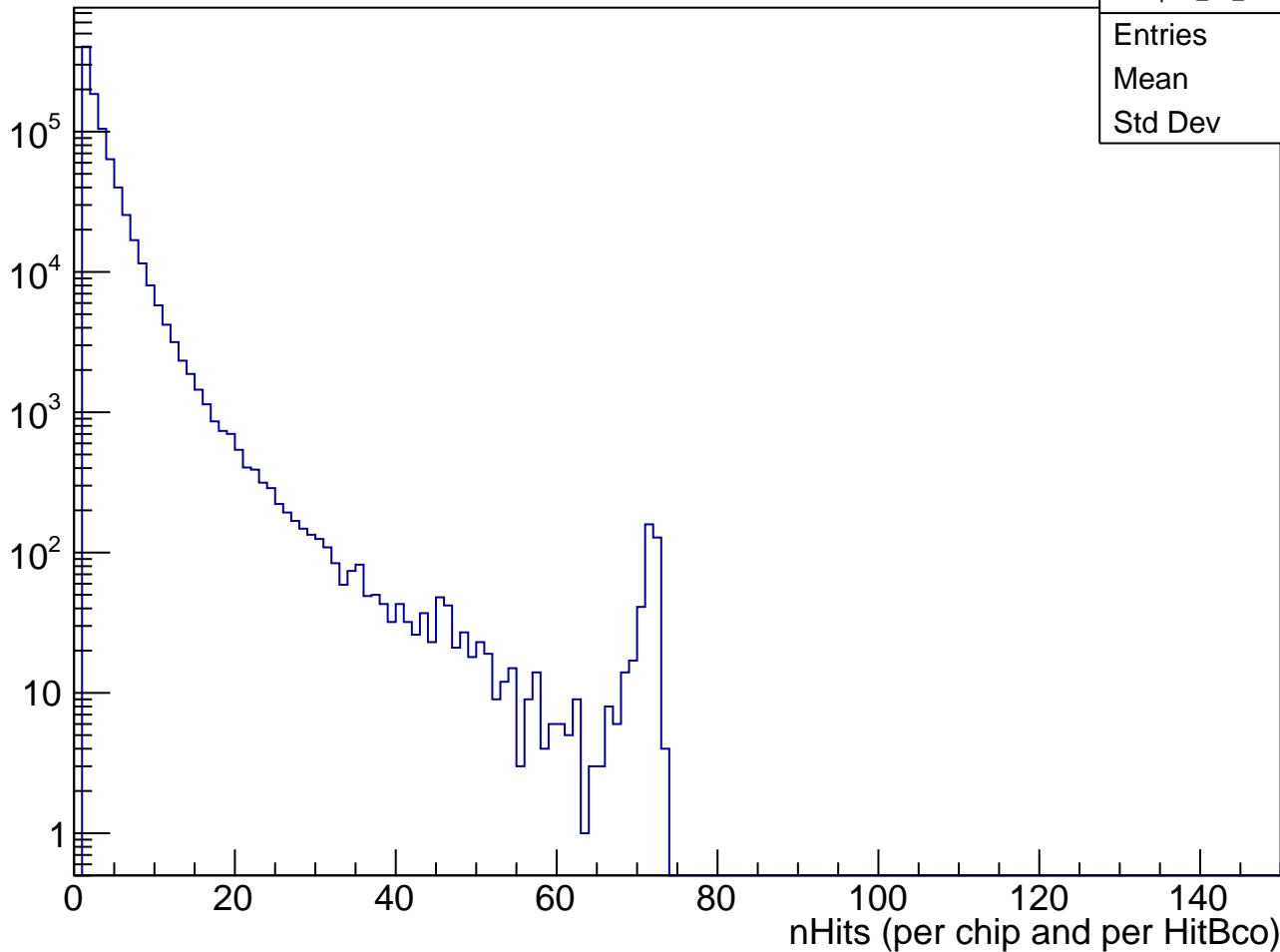
Entries



nChipHit_F0_Fch10_Chip21	
Entries	836624
Mean	2.884
Std Dev	3.383

nChipHit_F0_Fch10_Chip22

Entries

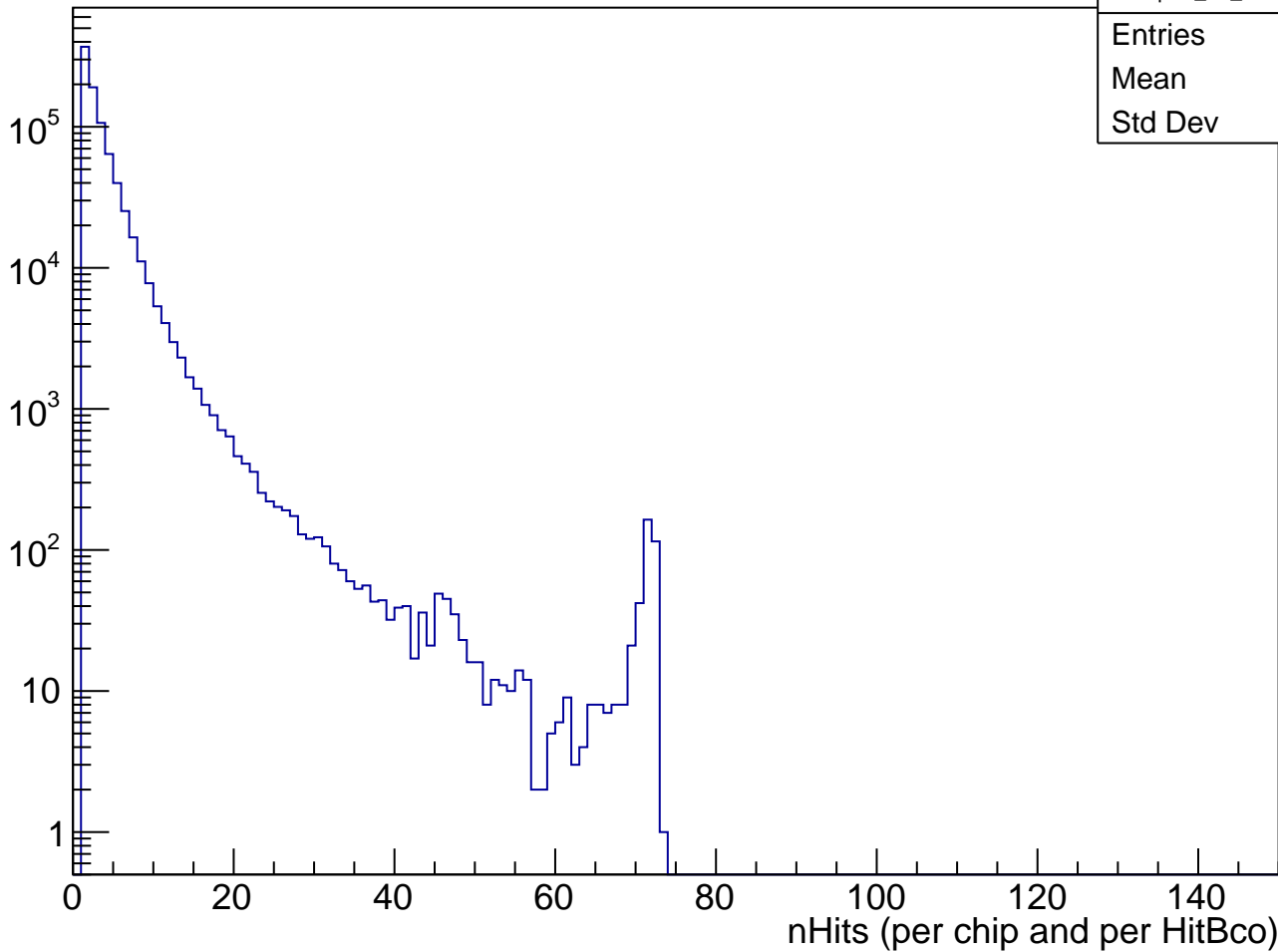


nChipHit_F0_Fch10_Chip22	
Entries	885813
Mean	2.704
Std Dev	3.308

nHits (per chip and per HitBco)

nChipHit_F0_Fch10_Chip23

Entries



nChipHit_F0_Fch10_Chip23

Entries 855997

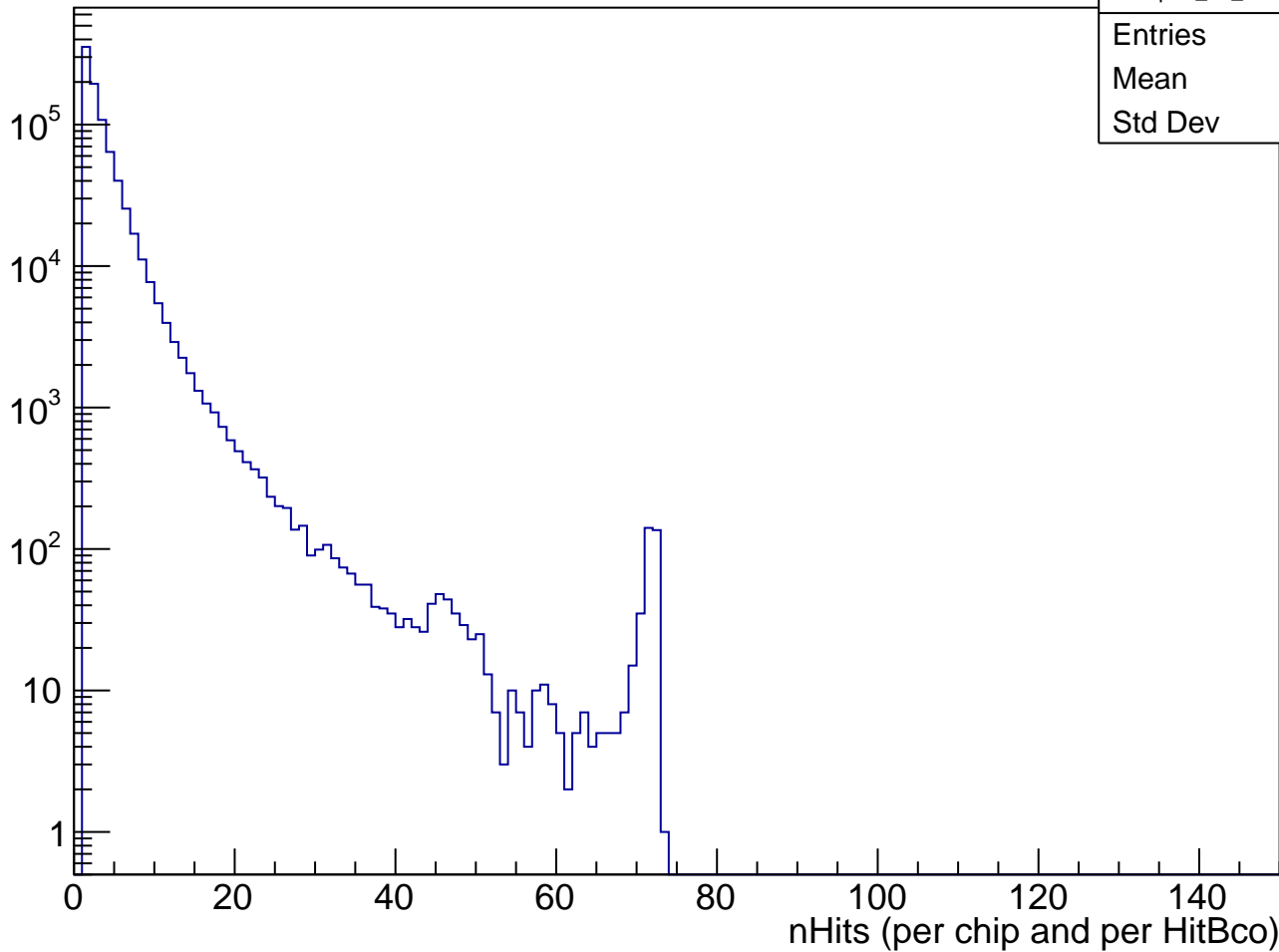
Mean 2.74

Std Dev 3.285

nHits (per chip and per HitBco)

nChipHit_F0_Fch10_Chip24

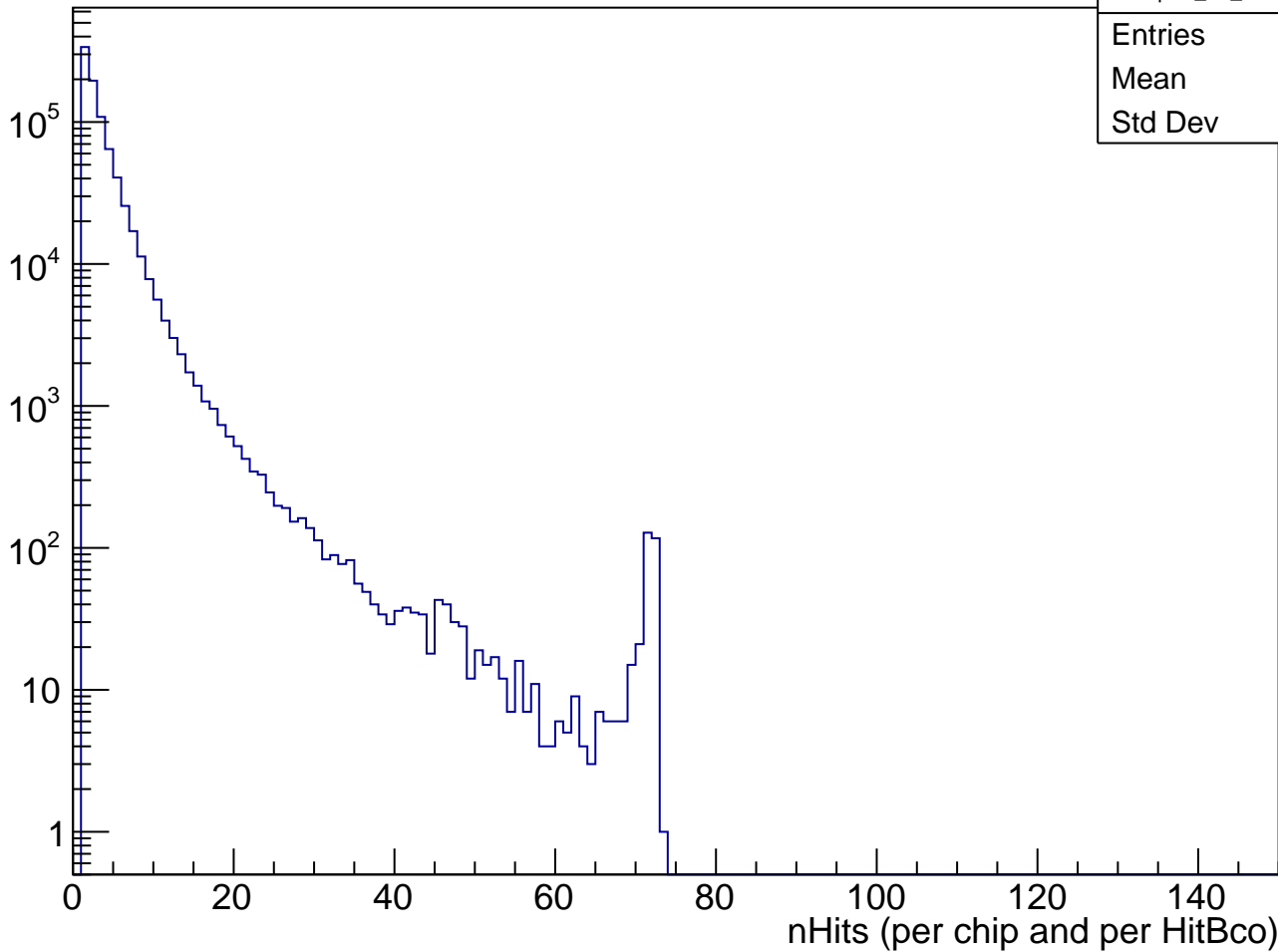
Entries



nChipHit_F0_Fch10_Chip24	
Entries	846526
Mean	2.77
Std Dev	3.282

nChipHit_F0_Fch10_Chip25

Entries

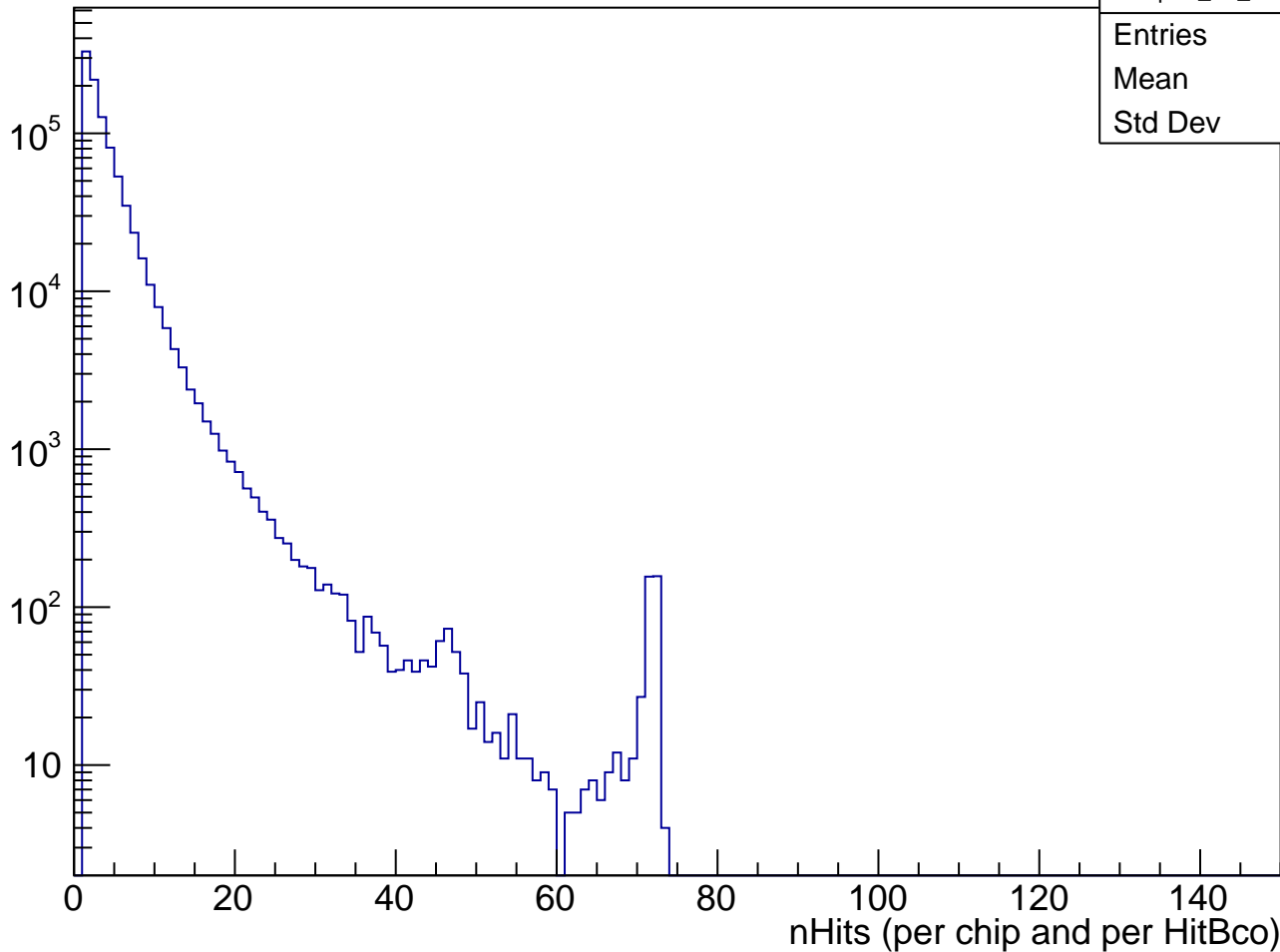


nChipHit_F0_Fch10_Chip25	
Entries	834224
Mean	2.814
Std Dev	3.281

nHits (per chip and per HitBco)

nChipHit_F0_Fch10_Chip3

Entries

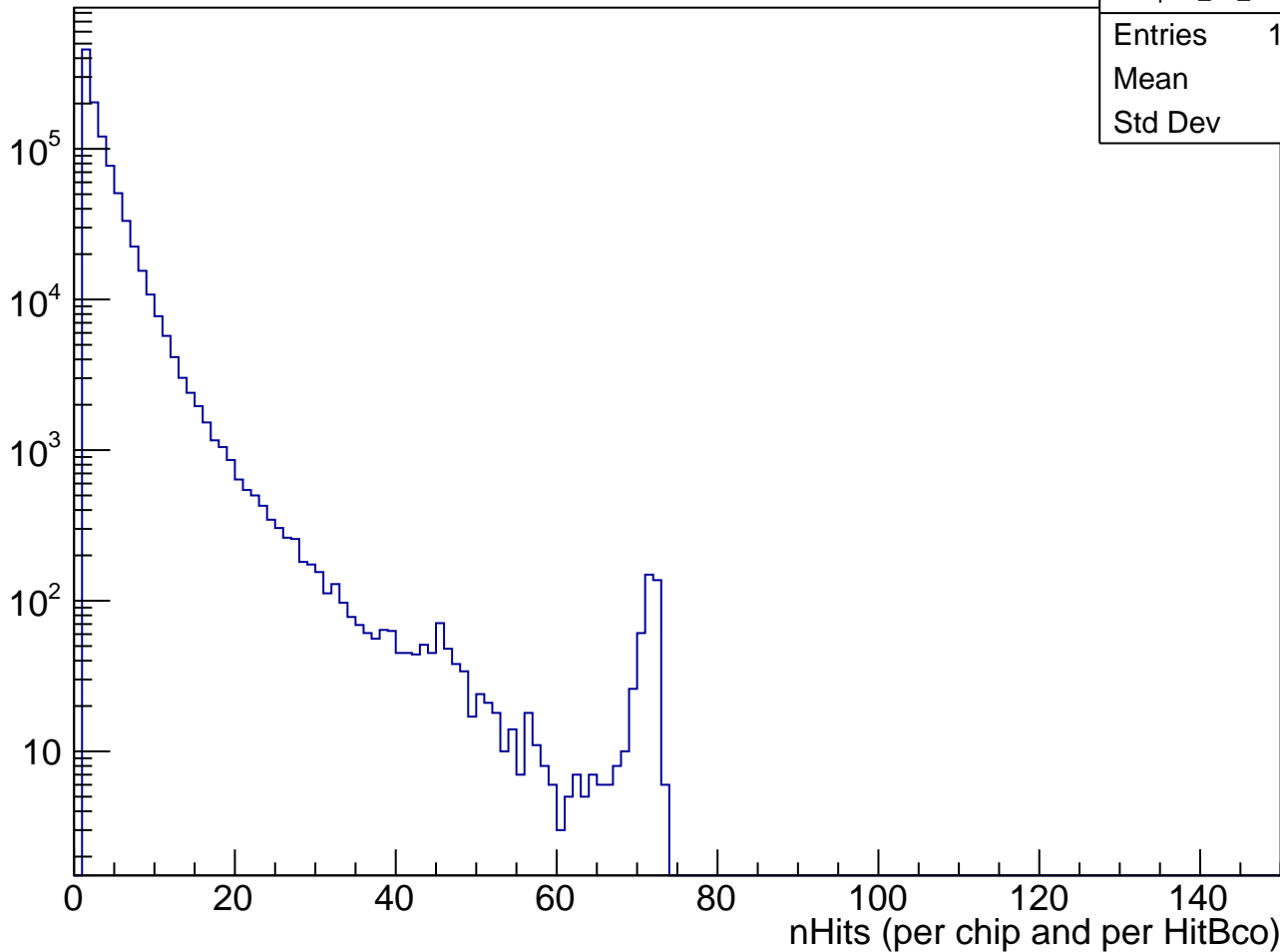


nChipHit_F0_Fch10_Chip3	
Entries	930819
Mean	3.104
Std Dev	3.531

nHits (per chip and per HitBco)

nChipHit_F0_Fch10_Chip4

Entries

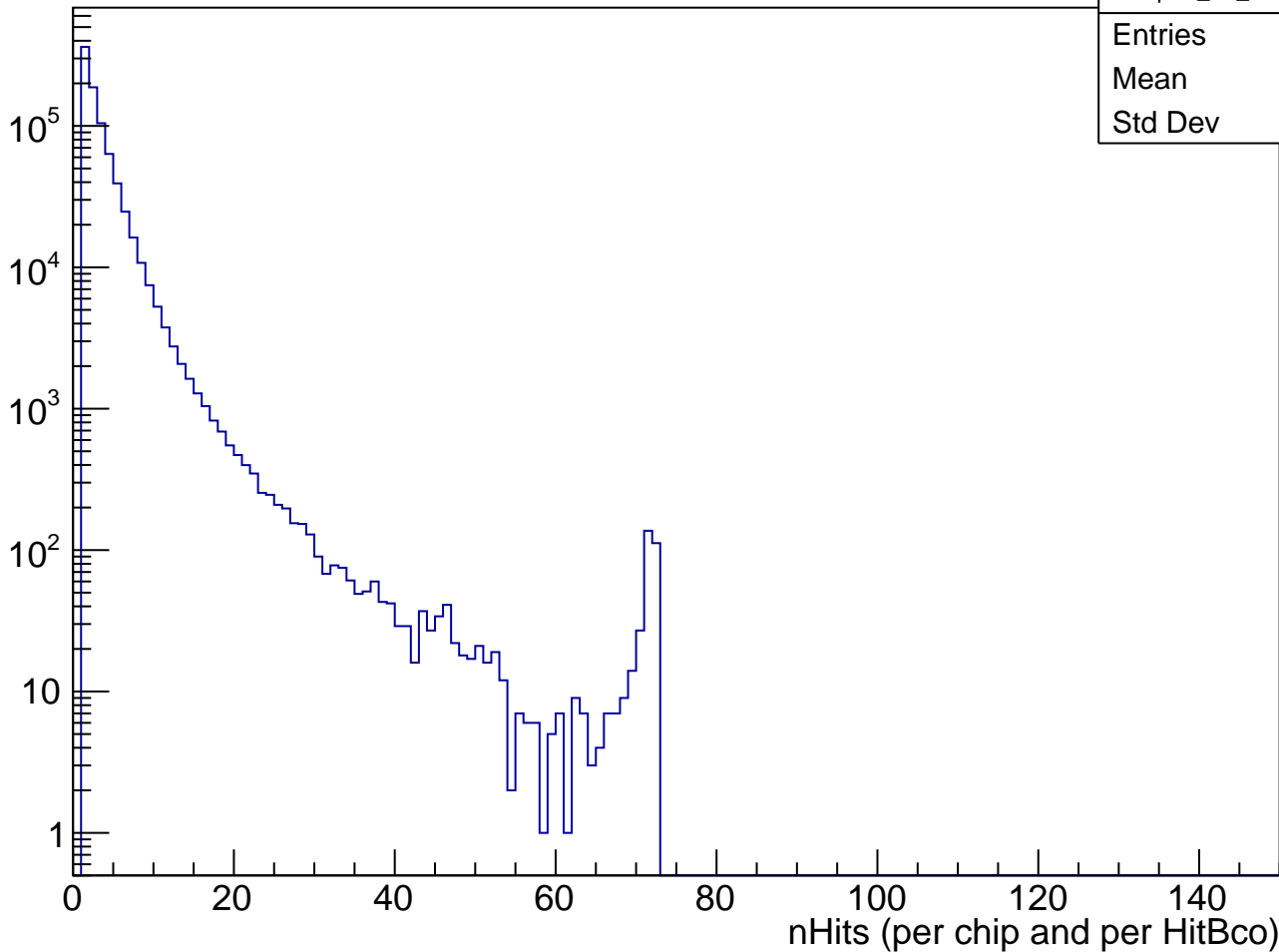


nChipHit_F0_Fch10_Chip4	
Entries	1026585
Mean	2.835
Std Dev	3.426

nHits (per chip and per HitBco)

nChipHit_F0_Fch10_Chip5

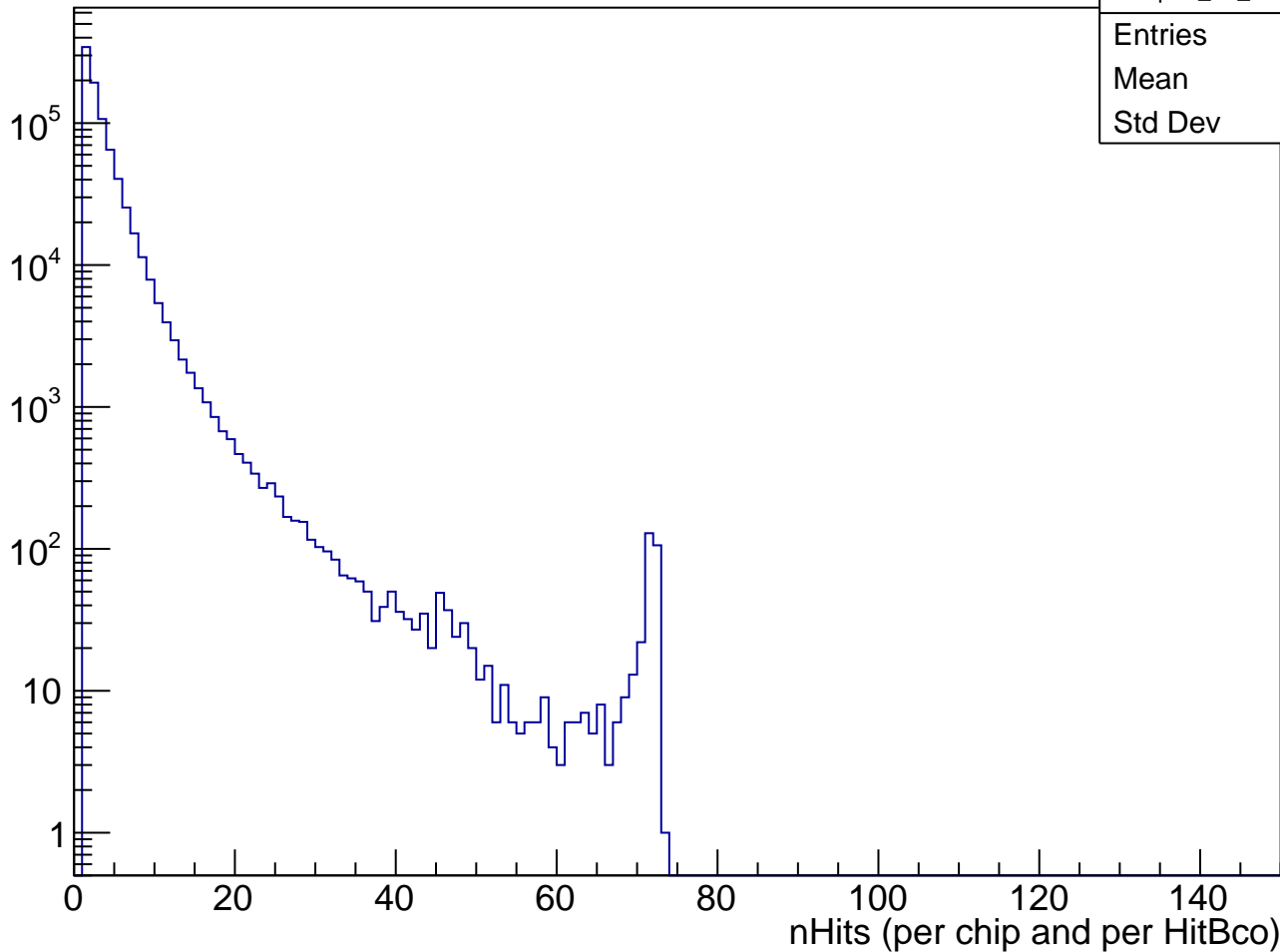
Entries



nChipHit_F0_Fch10_Chip5	
Entries	839742
Mean	2.723
Std Dev	3.215

nChipHit_F0_Fch10_Chip6

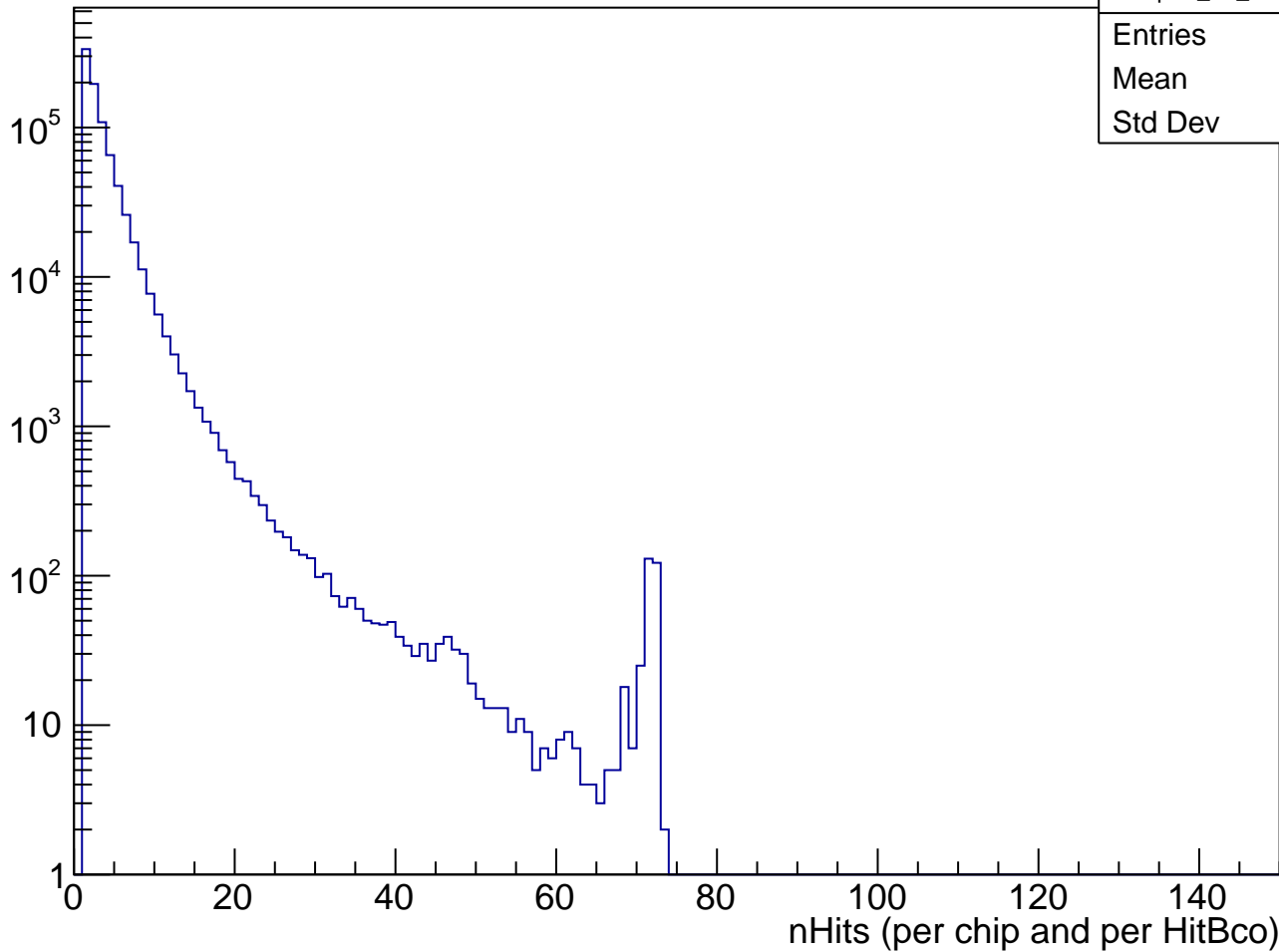
Entries



nChipHit_F0_Fch10_Chip6	
Entries	834976
Mean	2.787
Std Dev	3.236

nChipHit_F0_Fch10_Chip7

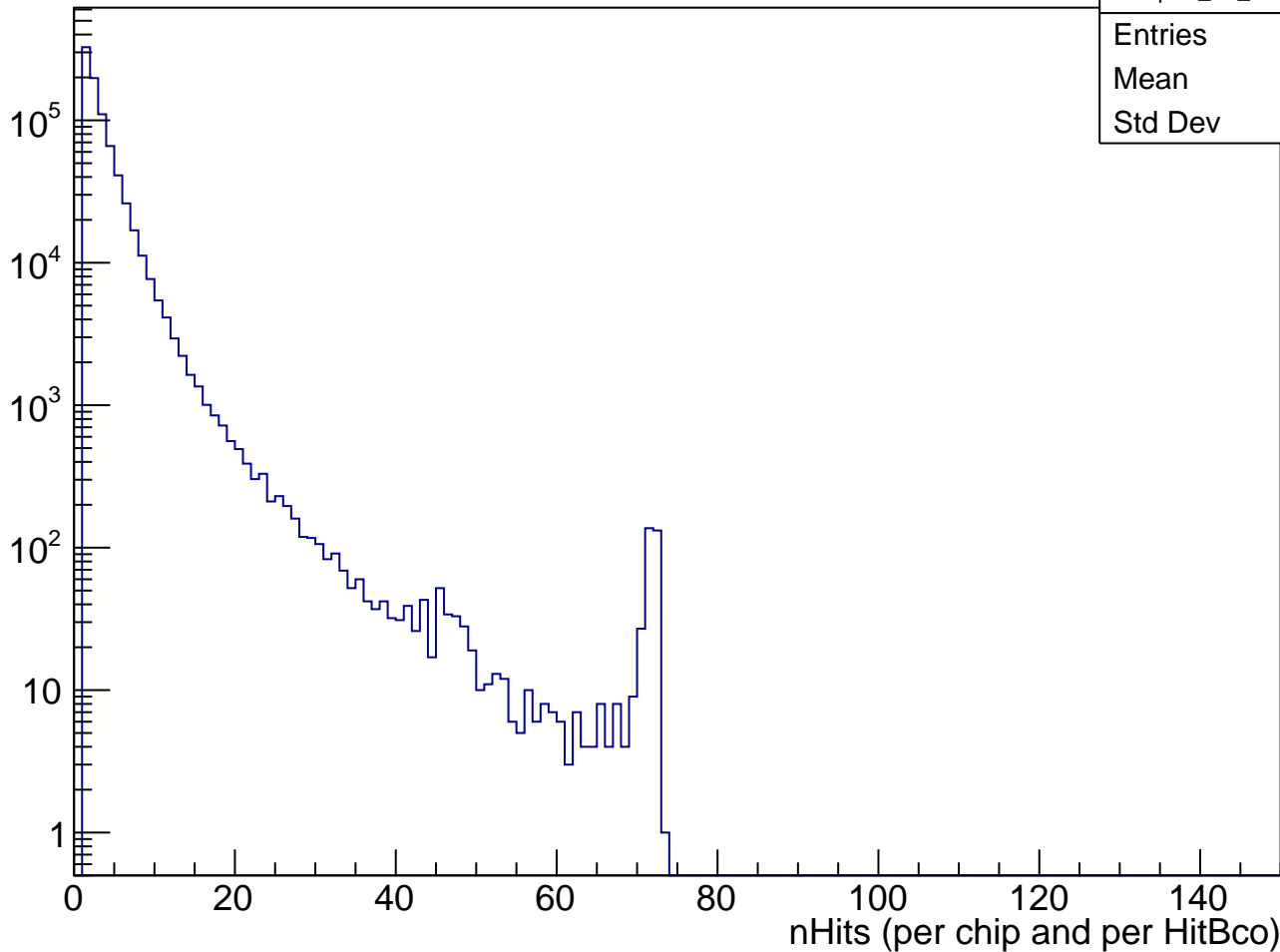
Entries



nChipHit_F0_Fch10_Chip7	
Entries	832152
Mean	2.815
Std Dev	3.277

nChipHit_F0_Fch10_Chip8

Entries

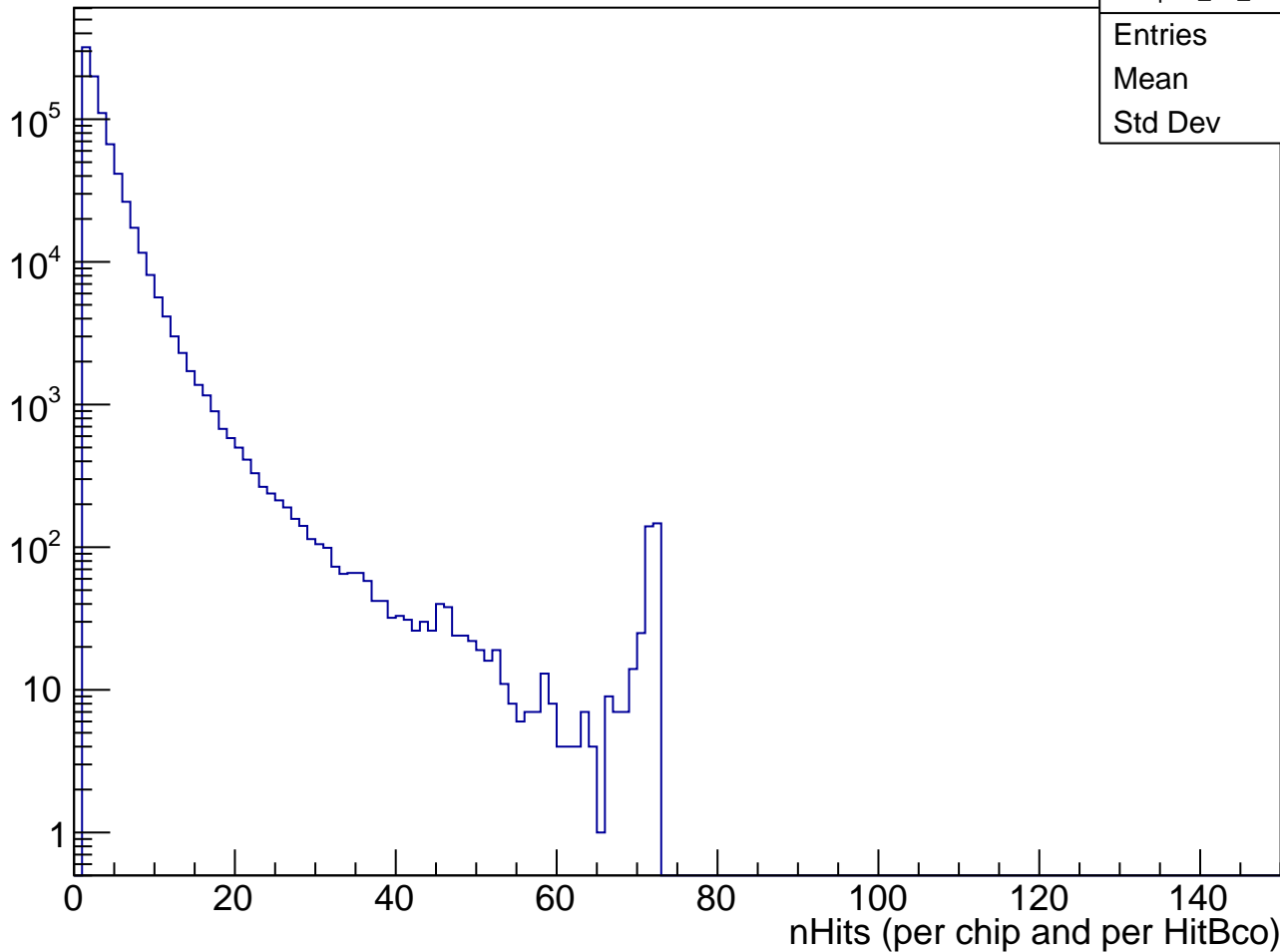


nChipHit_F0_Fch10_Chip8	
Entries	827923
Mean	2.827
Std Dev	3.264

nHits (per chip and per HitBco)

nChipHit_F0_Fch10_Chip9

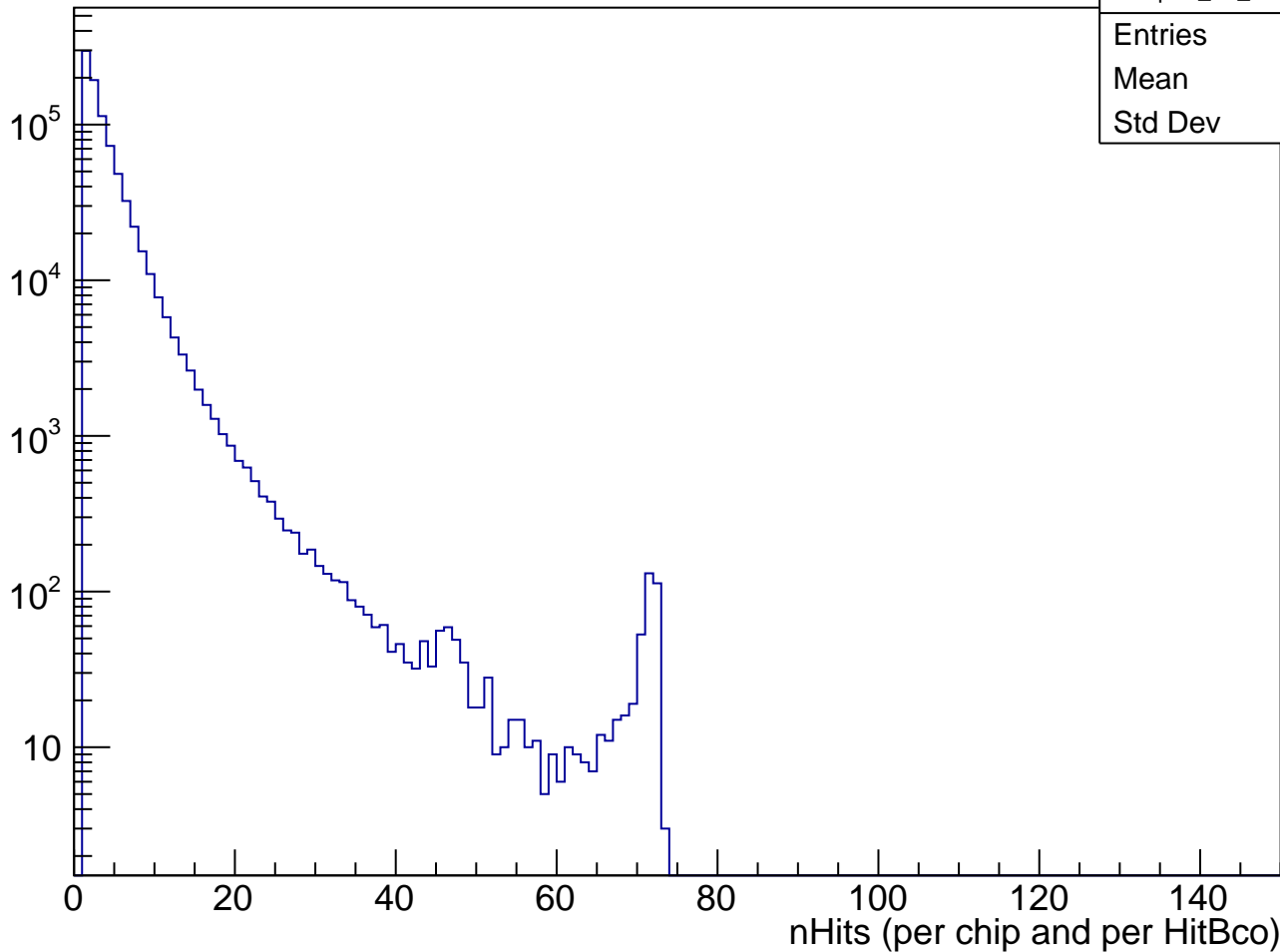
Entries



nChipHit_F0_Fch10_Chip9	
Entries	827033
Mean	2.861
Std Dev	3.309

nChipHit_F0_Fch11_Chip0

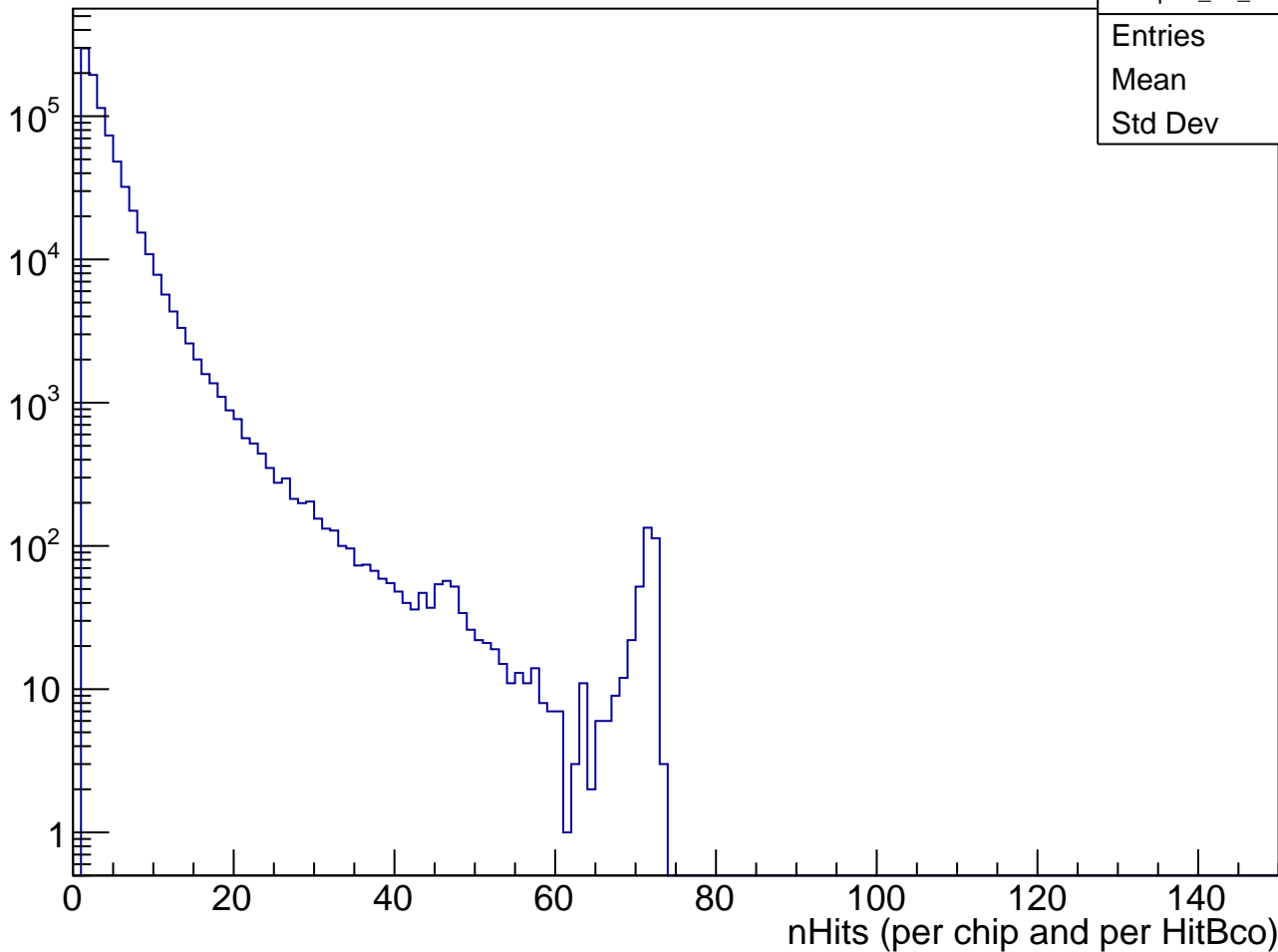
Entries



nChipHit_F0_Fch11_Chip0	
Entries	842342
Mean	3.186
Std Dev	3.669

nChipHit_F0_Fch11_Chip1

Entries

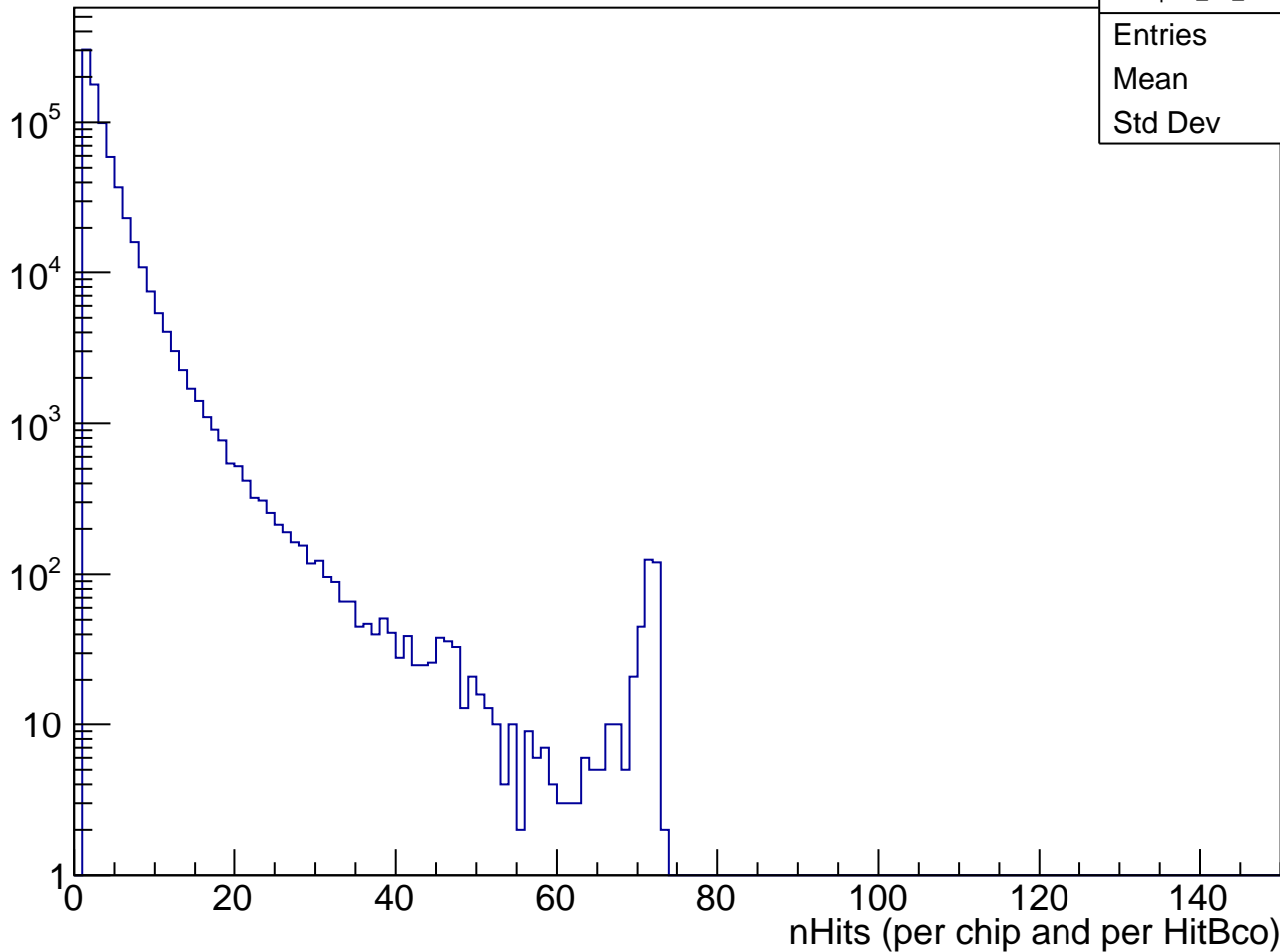


nChipHit_F0_Fch11_Chip1	
Entries	844004
Mean	3.188
Std Dev	3.672

nHits (per chip and per HitBco)

nChipHit_F0_Fch11_Chip10

Entries

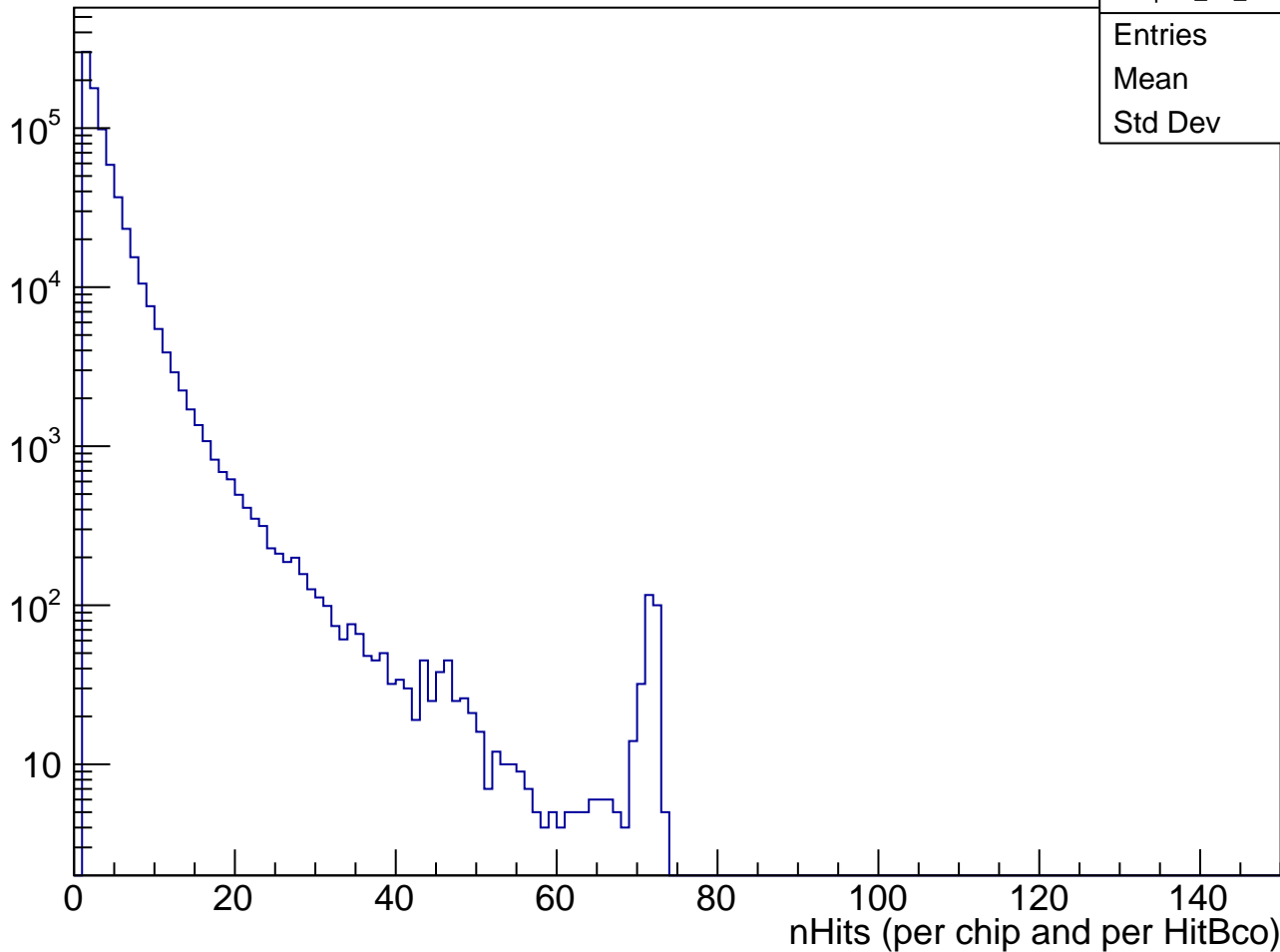


nChipHit_F0_Fch11_Chip10	
Entries	758972
Mean	2.864
Std Dev	3.391

nHits (per chip and per HitBco)

nChipHit_F0_Fch11_Chip11

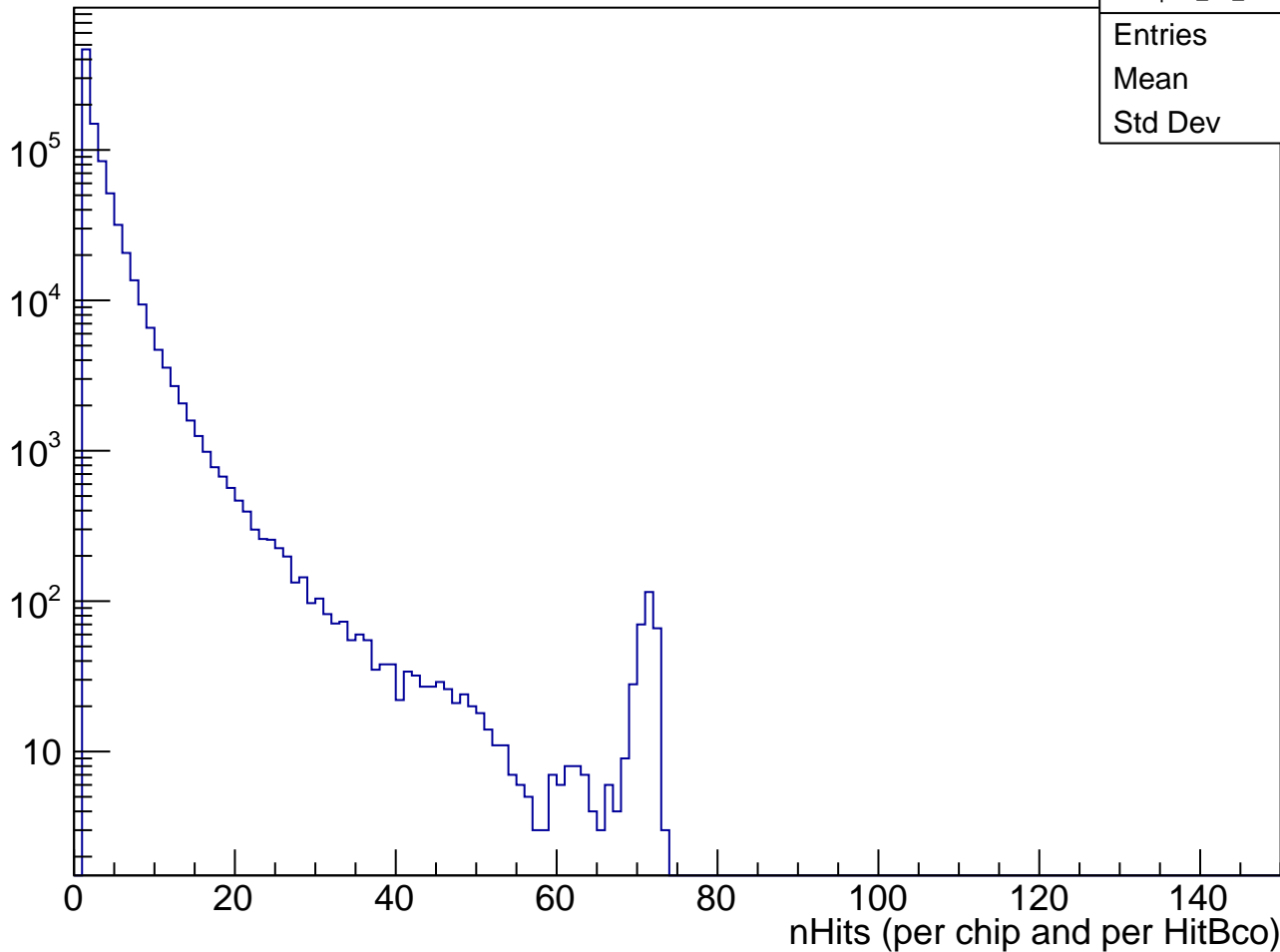
Entries



nChipHit_F0_Fch11_Chip11	
Entries	755683
Mean	2.856
Std Dev	3.357

nChipHit_F0_Fch11_Chip12

Entries



nChipHit_F0_Fch11_Chip12

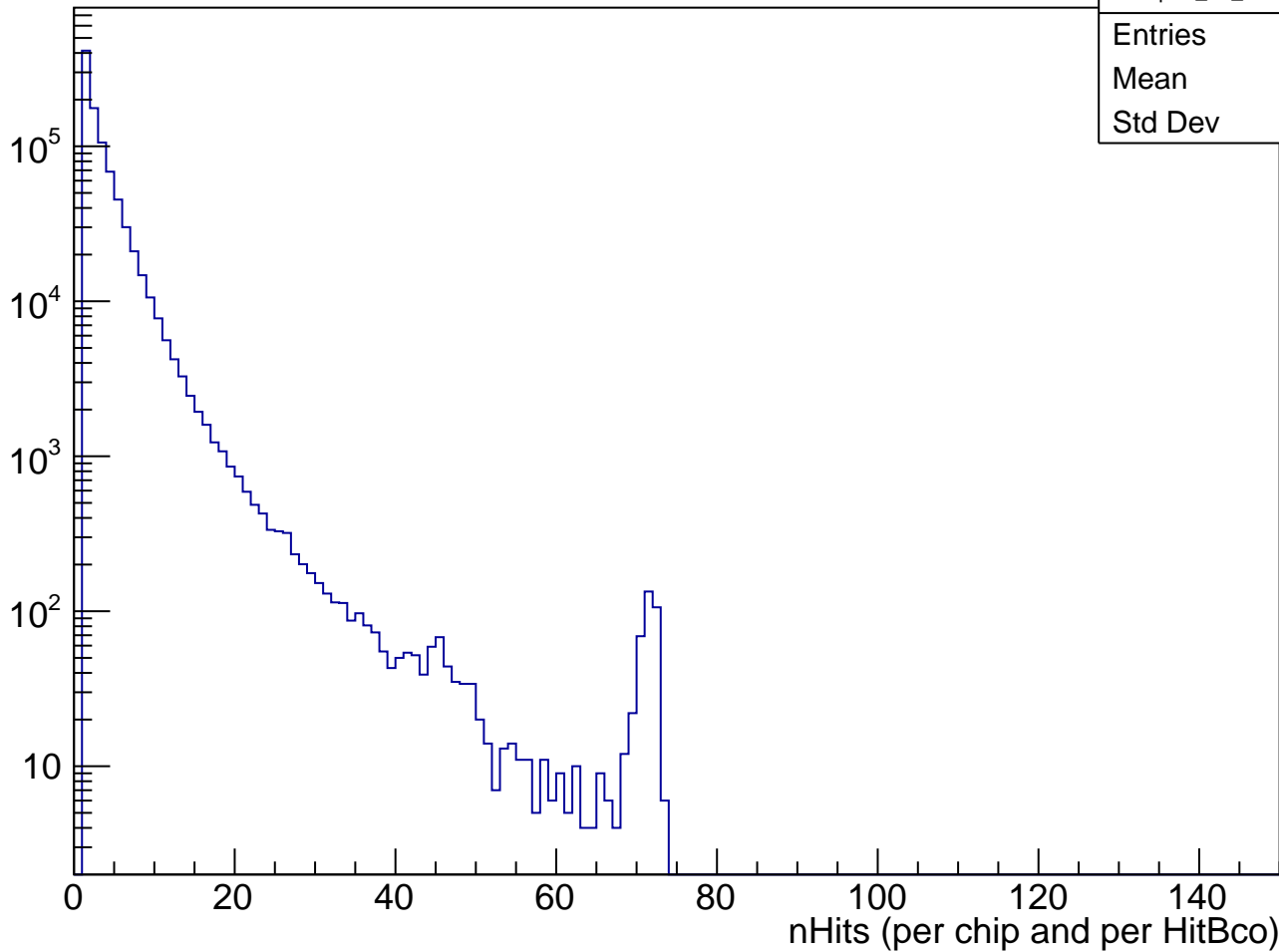
Entries 855836

Mean 2.443

Std Dev 3.141

nChipHit_F0_Fch11_Chip13

Entries



nChipHit_F0_Fch11_Chip13

Entries 922049

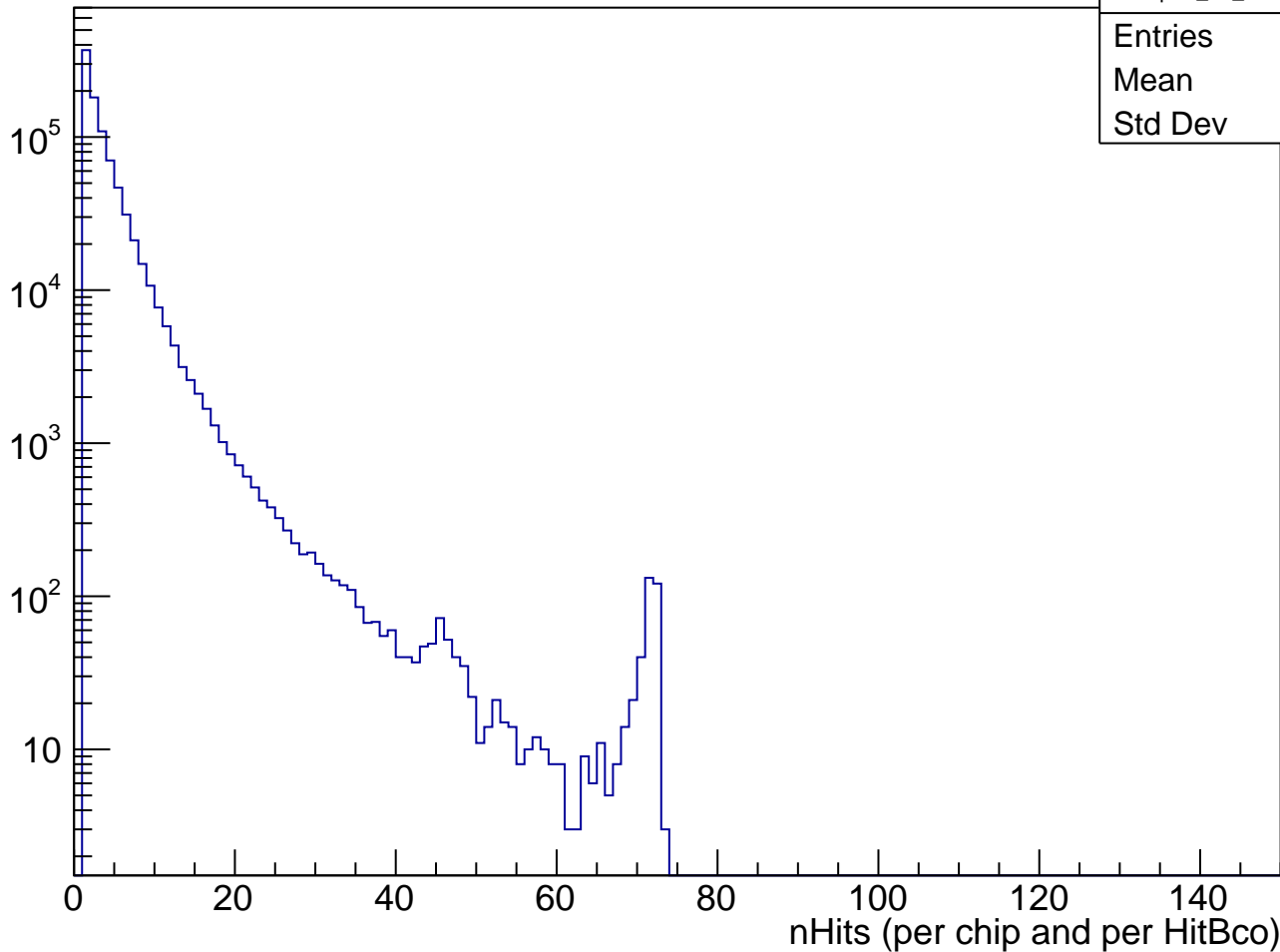
Mean 2.905

Std Dev 3.572

nHits (per chip and per HitBco)

nChipHit_F0_Fch11_Chip14

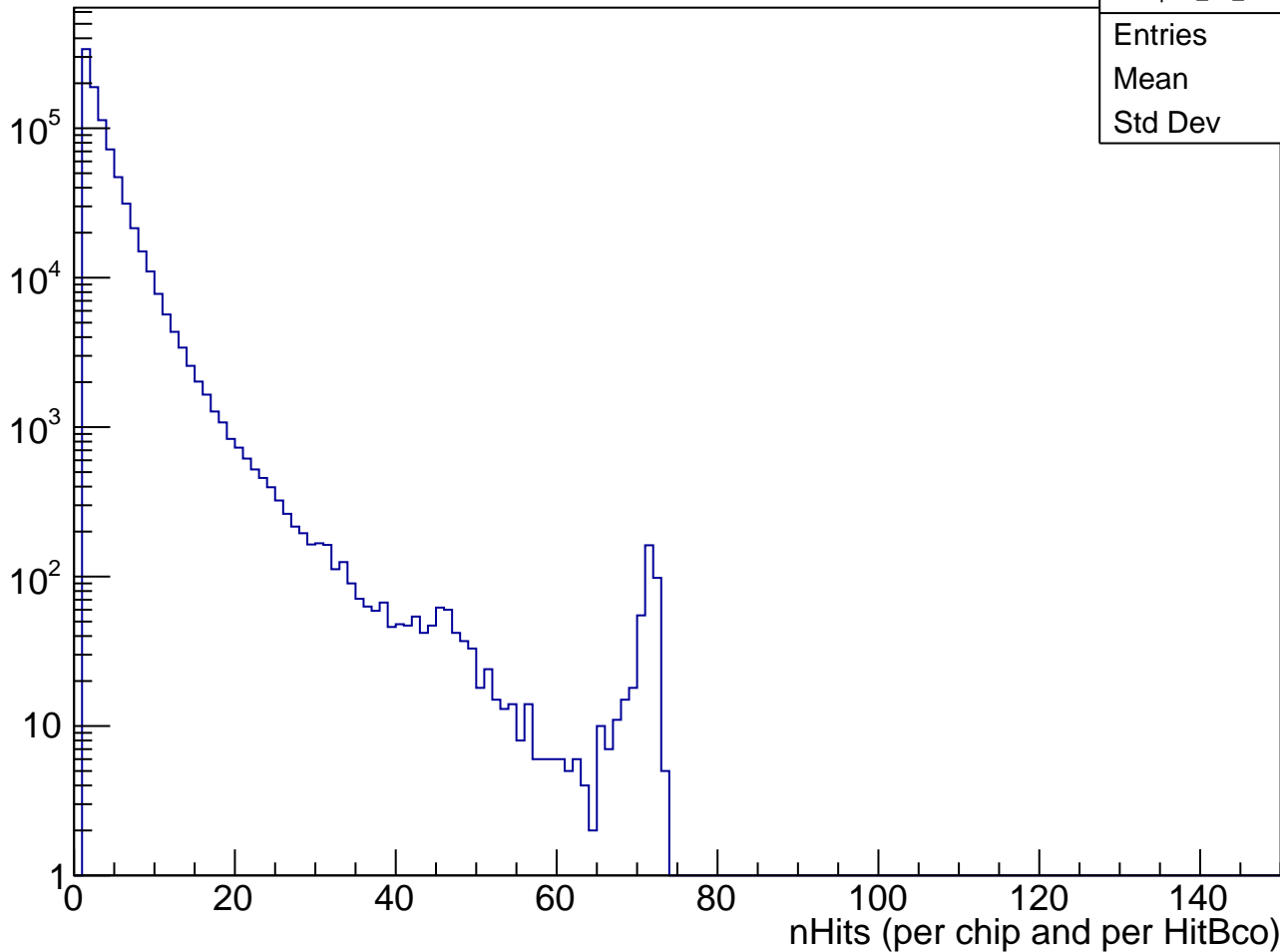
Entries



nChipHit_F0_Fch11_Chip14	
Entries	891071
Mean	3.012
Std Dev	3.612

nChipHit_F0_Fch11_Chip15

Entries



nChipHit_F0_Fch11_Chip15

Entries 874575

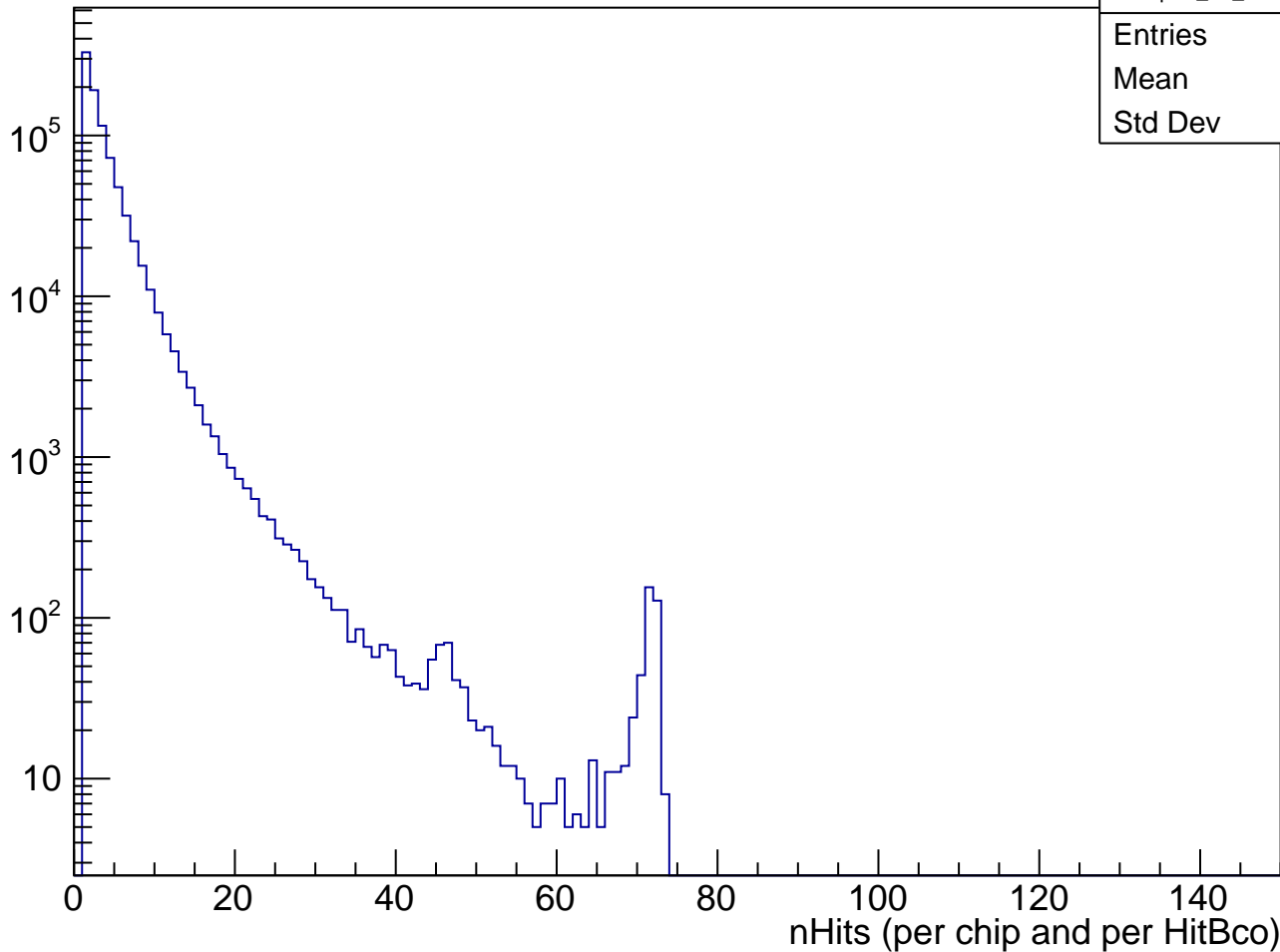
Mean 3.087

Std Dev 3.651

nHits (per chip and per HitBco)

nChipHit_F0_Fch11_Chip16

Entries



nChipHit_F0_Fch11_Chip16

Entries 873342

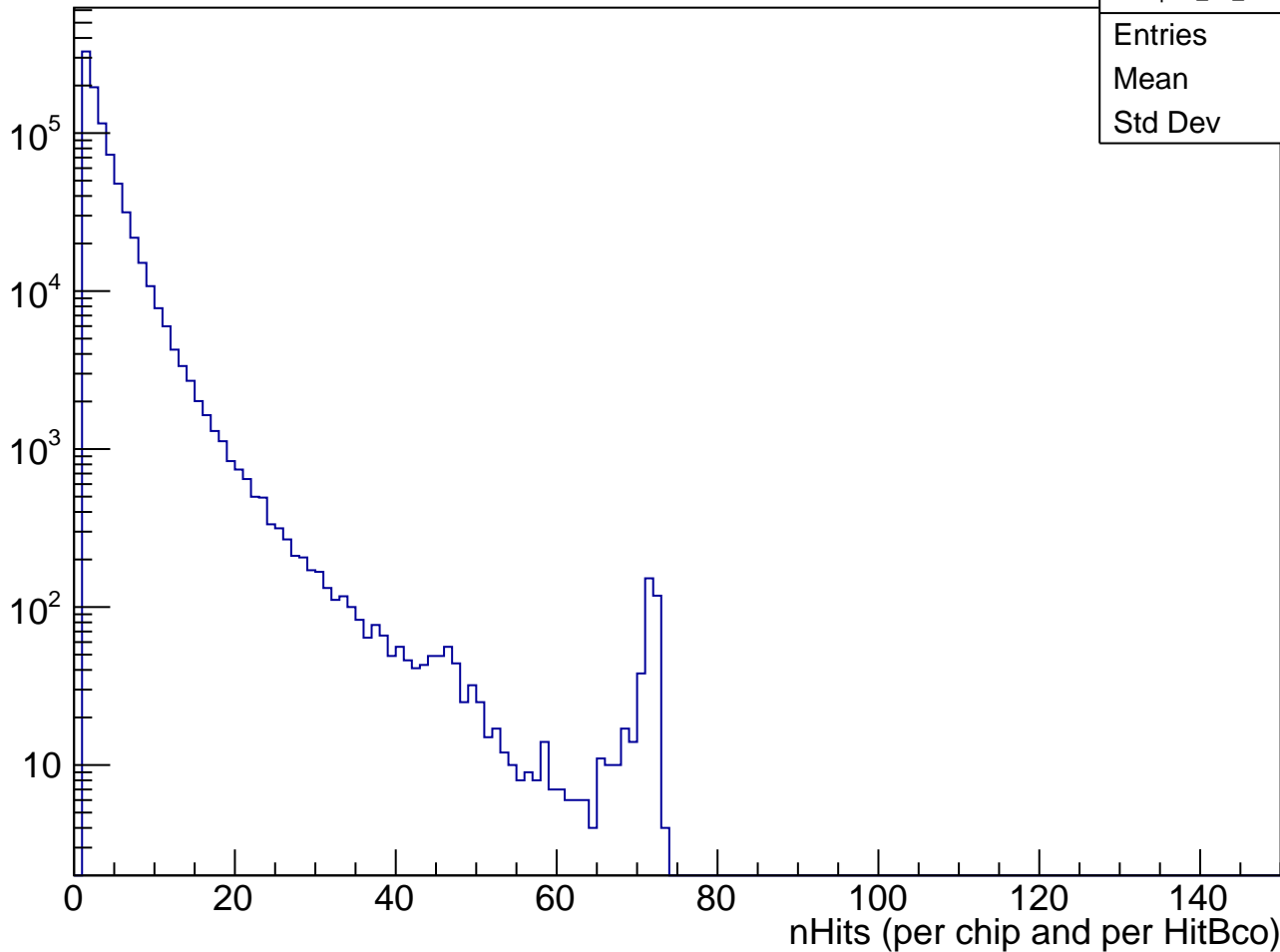
Mean 3.124

Std Dev 3.683

nHits (per chip and per HitBco)

nChipHit_F0_Fch11_Chip17

Entries

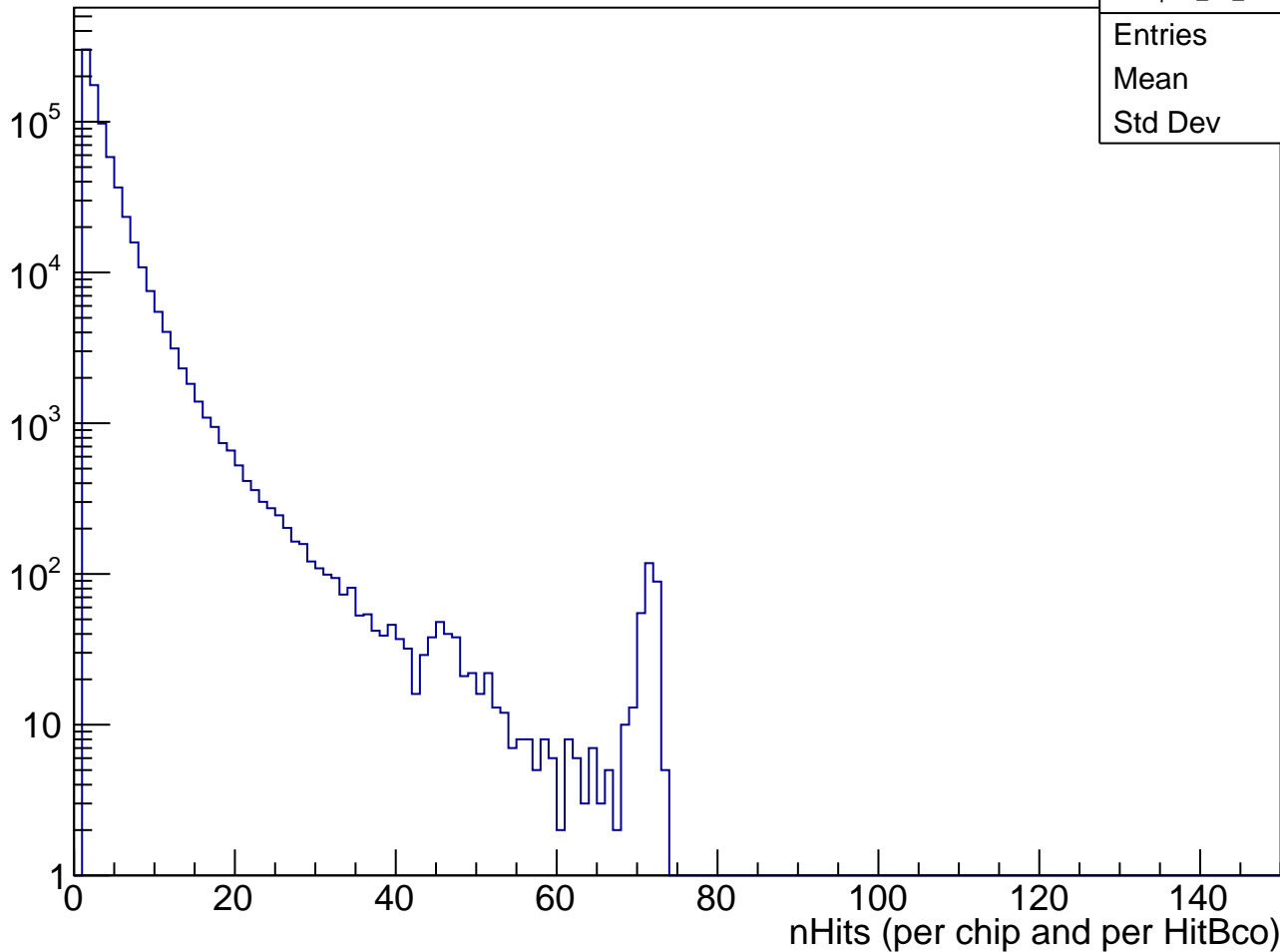


nChipHit_F0_Fch11_Chip17	
Entries	875439
Mean	3.109
Std Dev	3.643

nHits (per chip and per HitBco)

nChipHit_F0_Fch11_Chip18

Entries

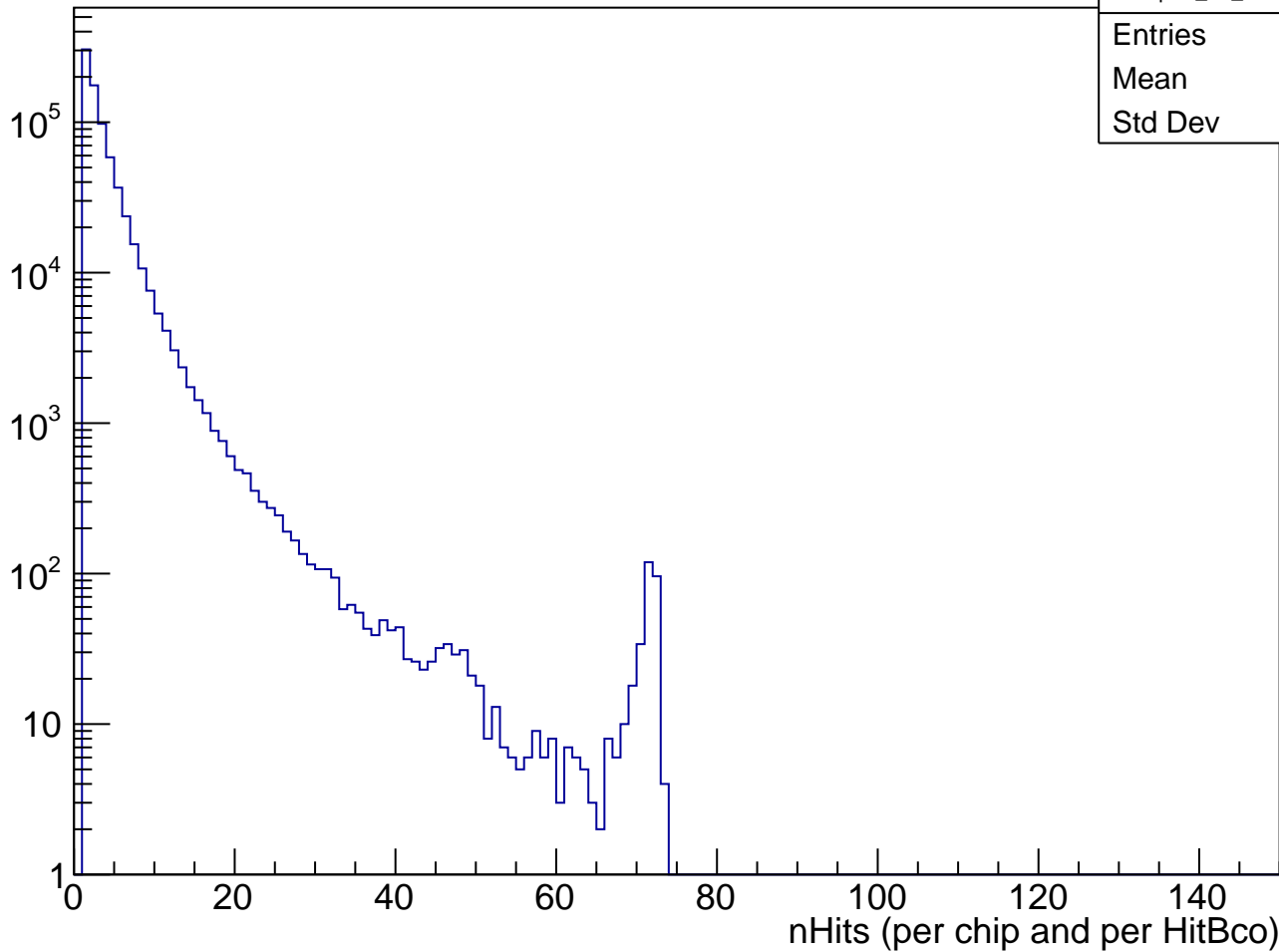


nChipHit_F0_Fch11_Chip18	
Entries	751967
Mean	2.882
Std Dev	3.423

nHits (per chip and per HitBco)

nChipHit_F0_Fch11_Chip19

Entries



nChipHit_F0_Fch11_Chip19

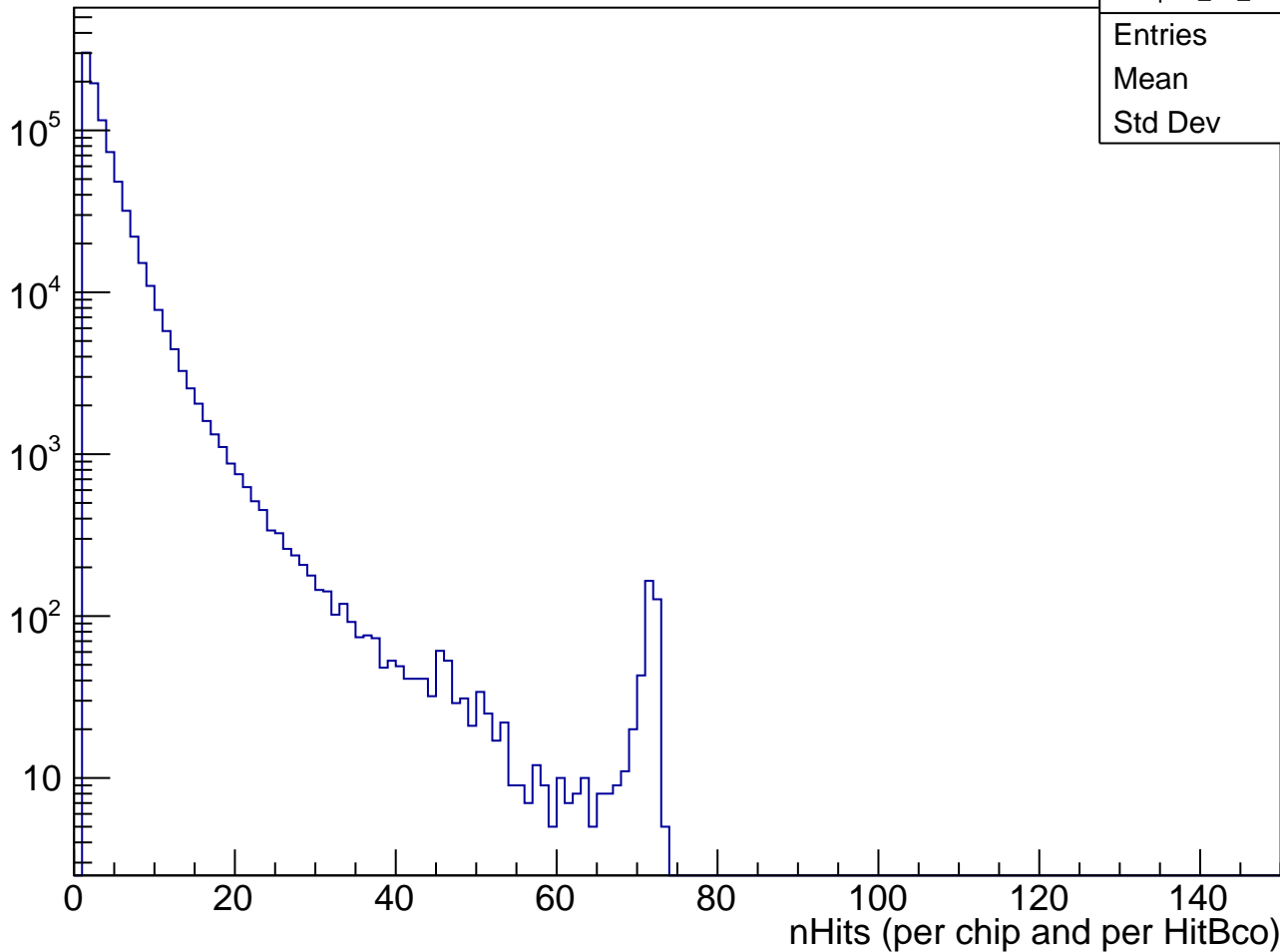
Entries 756190

Mean 2.865

Std Dev 3.378

nChipHit_F0_Fch11_Chip2

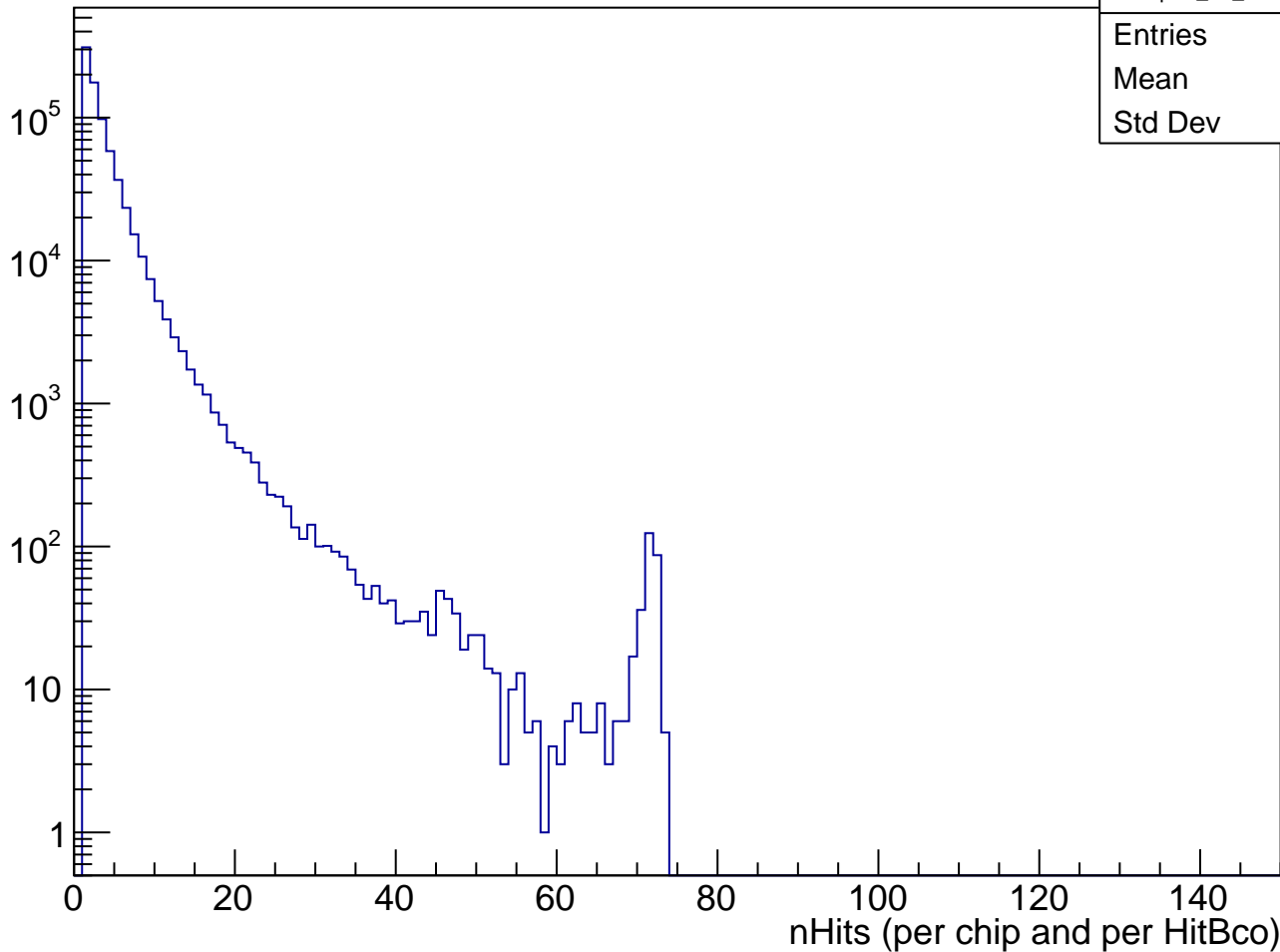
Entries



nChipHit_F0_Fch11_Chip2	
Entries	851308
Mean	3.177
Std Dev	3.69

nChipHit_F0_Fch11_Chip20

Entries

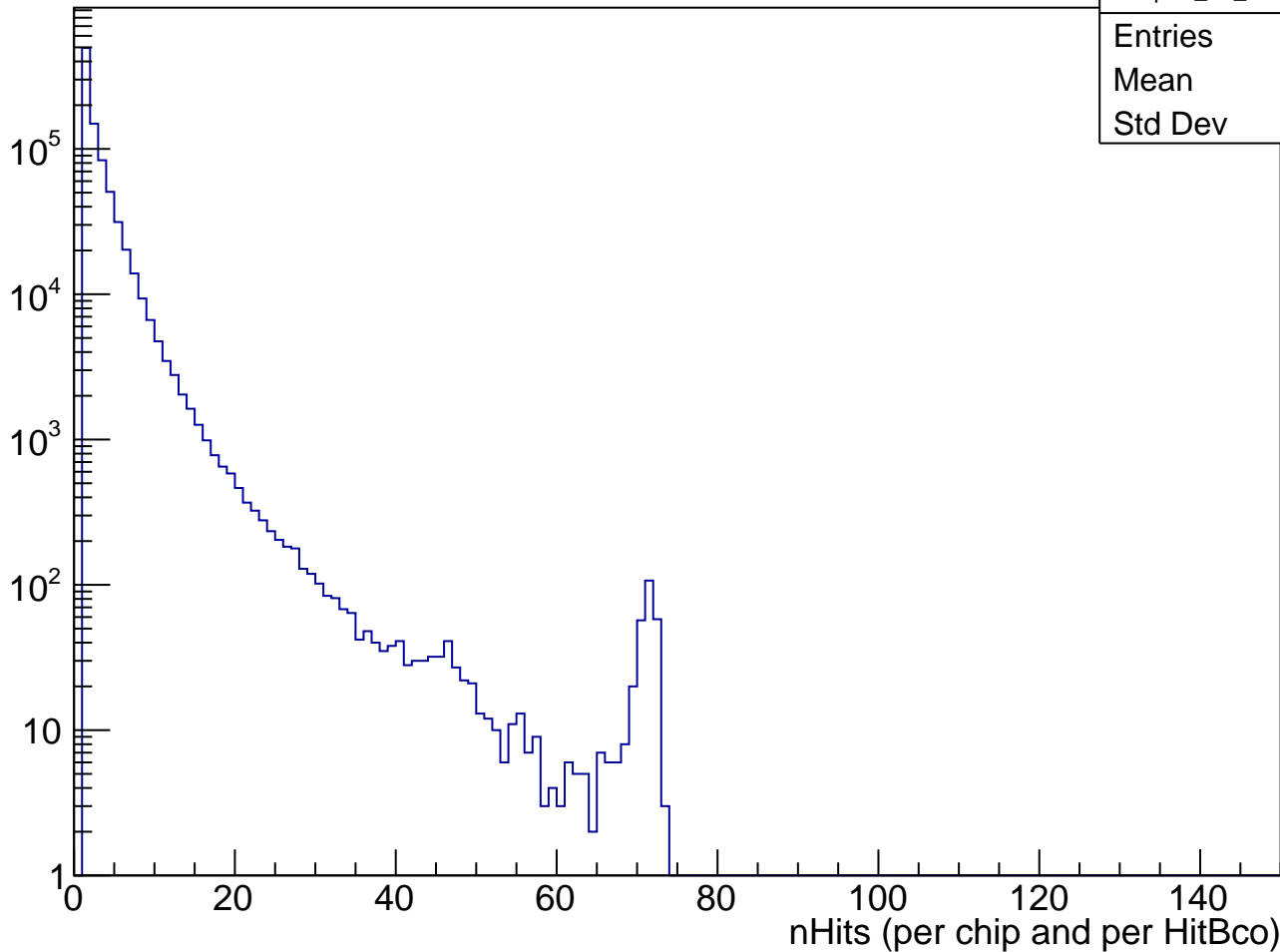


nChipHit_F0_Fch11_Chip20	
Entries	760269
Mean	2.837
Std Dev	3.358

nHits (per chip and per HitBco)

nChipHit_F0_Fch11_Chip21

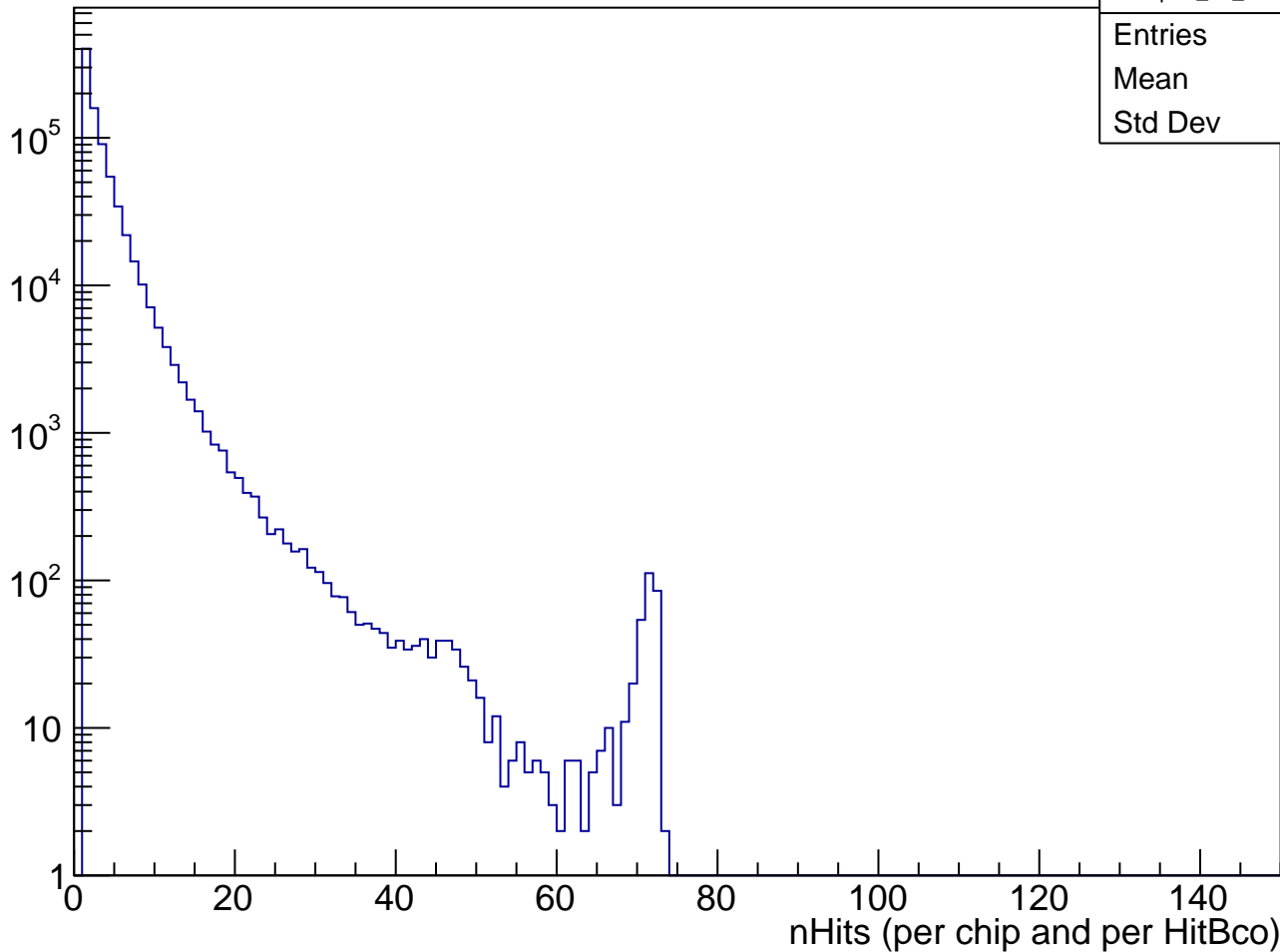
Entries



nChipHit_F0_Fch11_Chip21	
Entries	882831
Mean	2.394
Std Dev	3.085

nChipHit_F0_Fch11_Chip22

Entries



nChipHit_F0_Fch11_Chip22

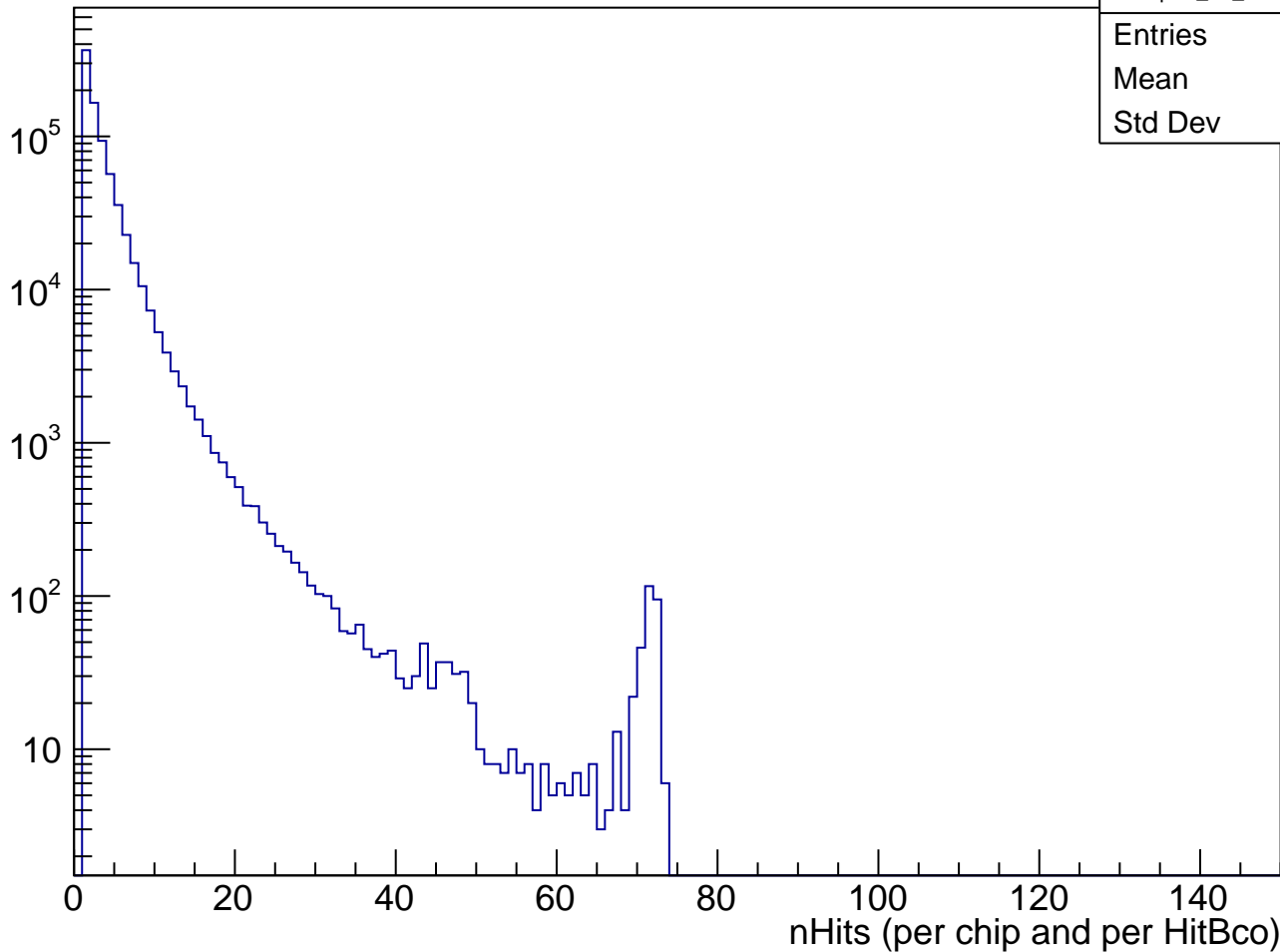
Entries 819078

Mean 2.612

Std Dev 3.257

nChipHit_F0_Fch11_Chip23

Entries



nChipHit_F0_Fch11_Chip23

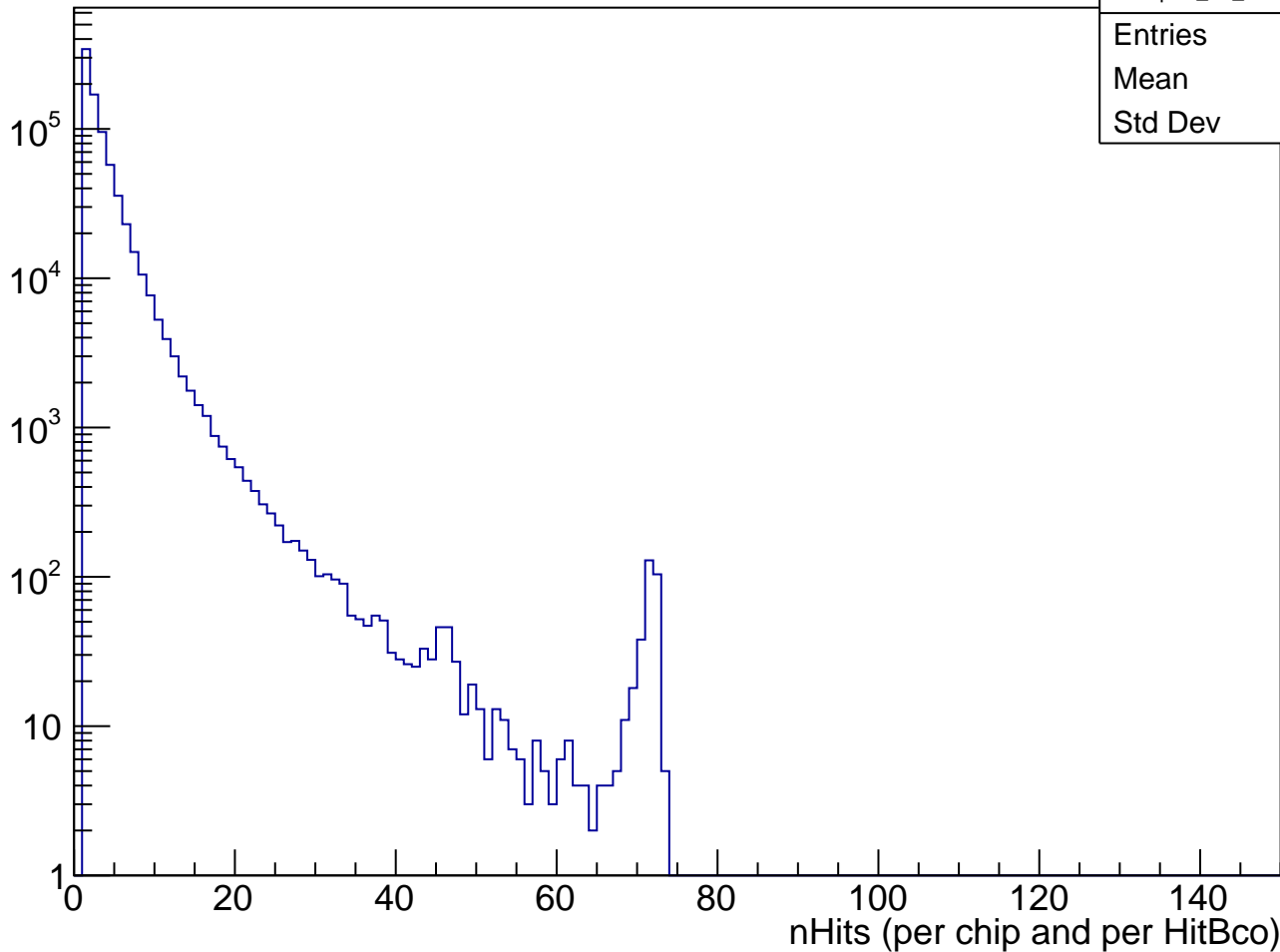
Entries 797556

Mean 2.712

Std Dev 3.318

nChipHit_F0_Fch11_Chip24

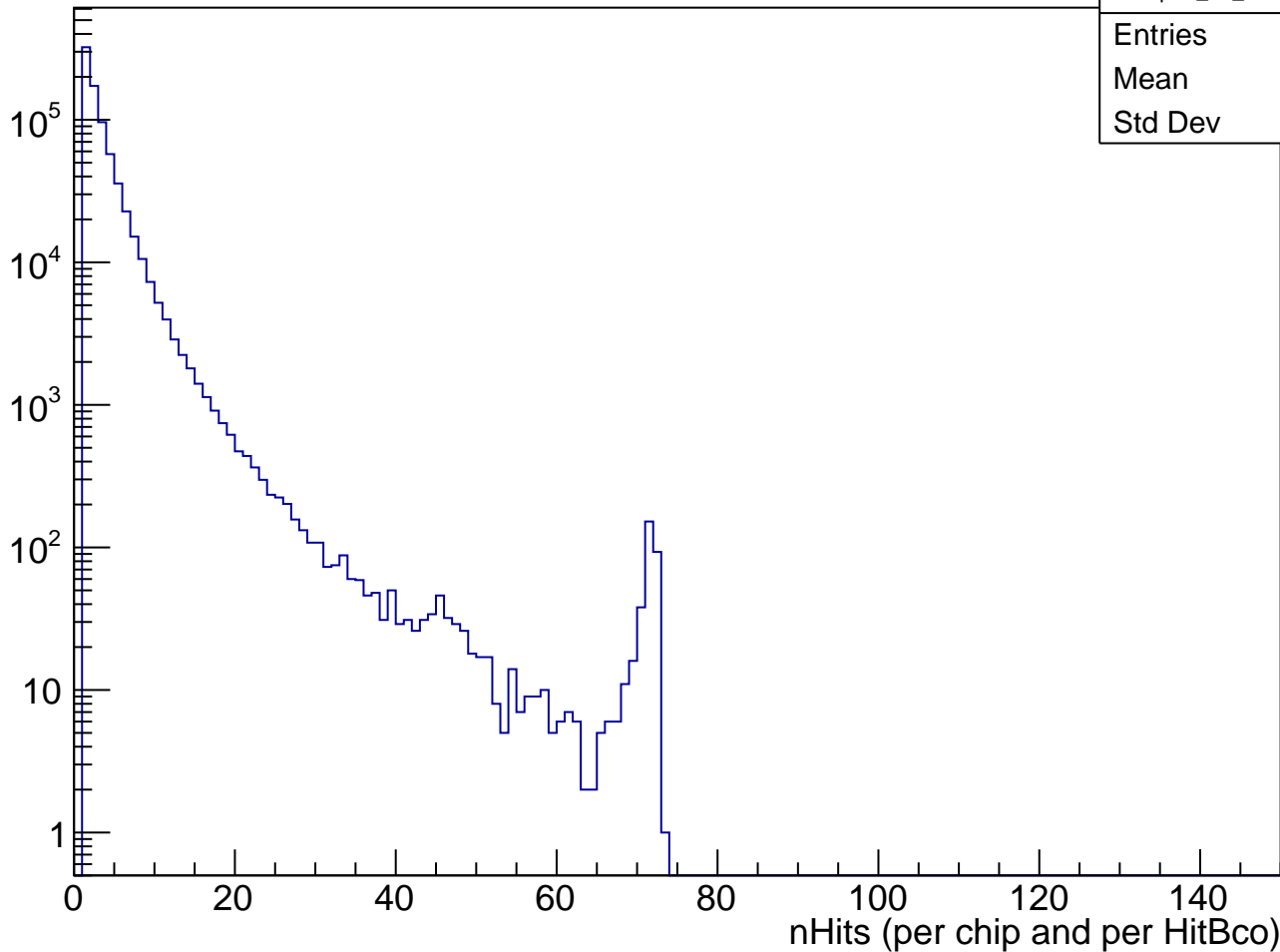
Entries



nChipHit_F0_Fch11_Chip24	
Entries	782694
Mean	2.769
Std Dev	3.354

nChipHit_F0_Fch11_Chip25

Entries



nChipHit_F0_Fch11_Chip25

Entries 765575

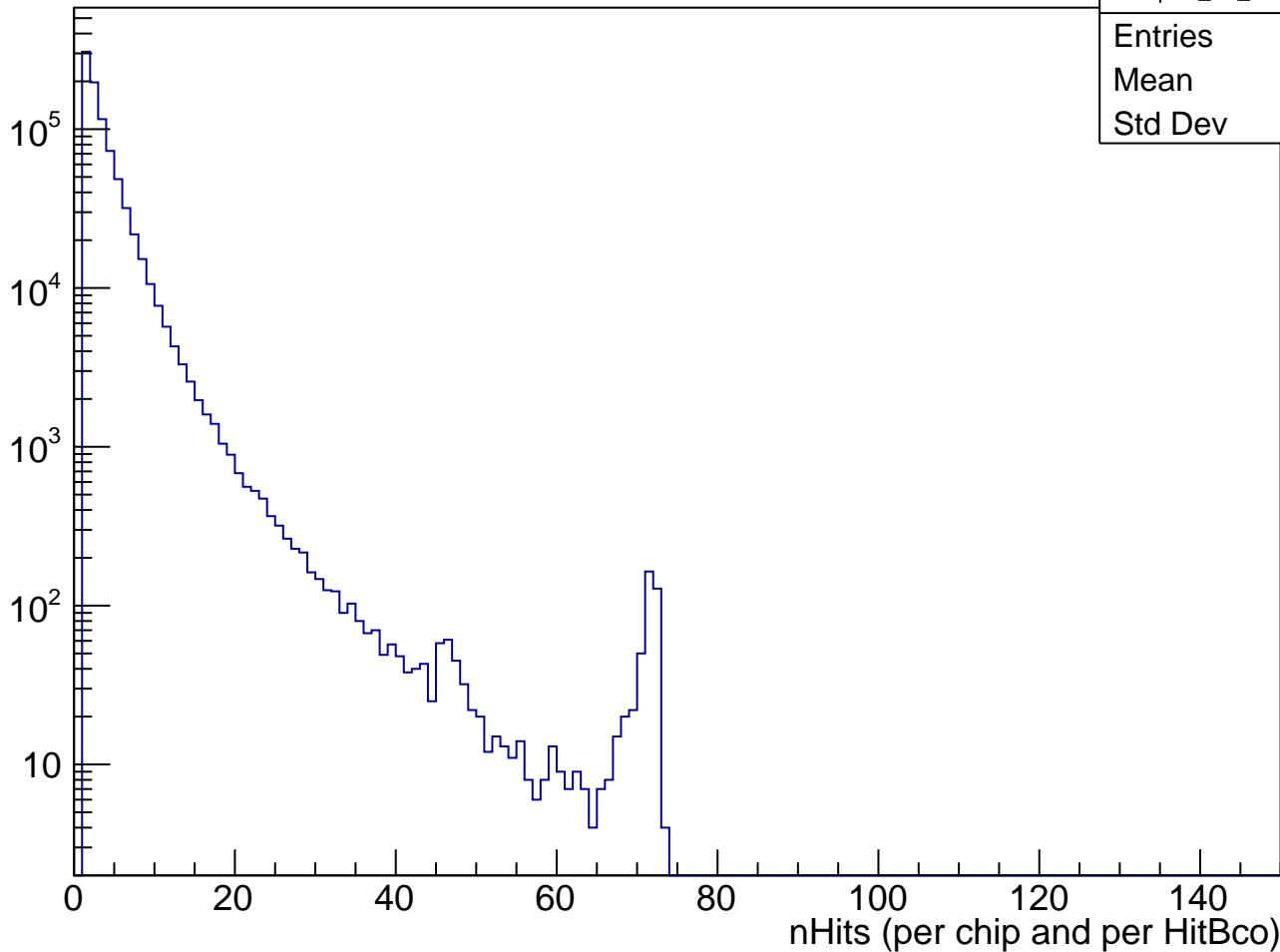
Mean 2.806

Std Dev 3.381

nHits (per chip and per HitBco)

nChipHit_F0_Fch11_Chip3

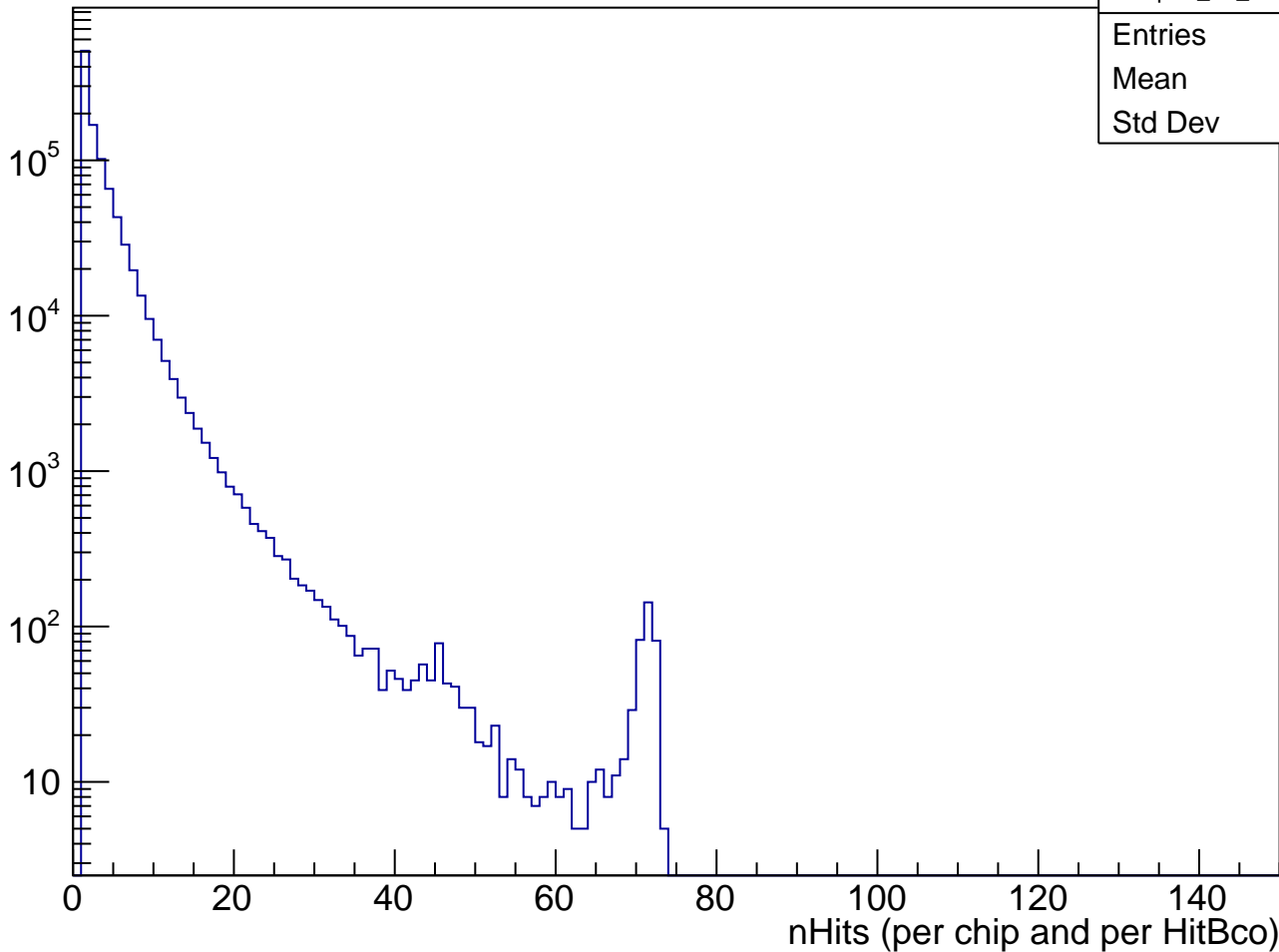
Entries



nChipHit_F0_Fch11_Chip3	
Entries	857061
Mean	3.154
Std Dev	3.678

nChipHit_F0_Fch11_Chip4

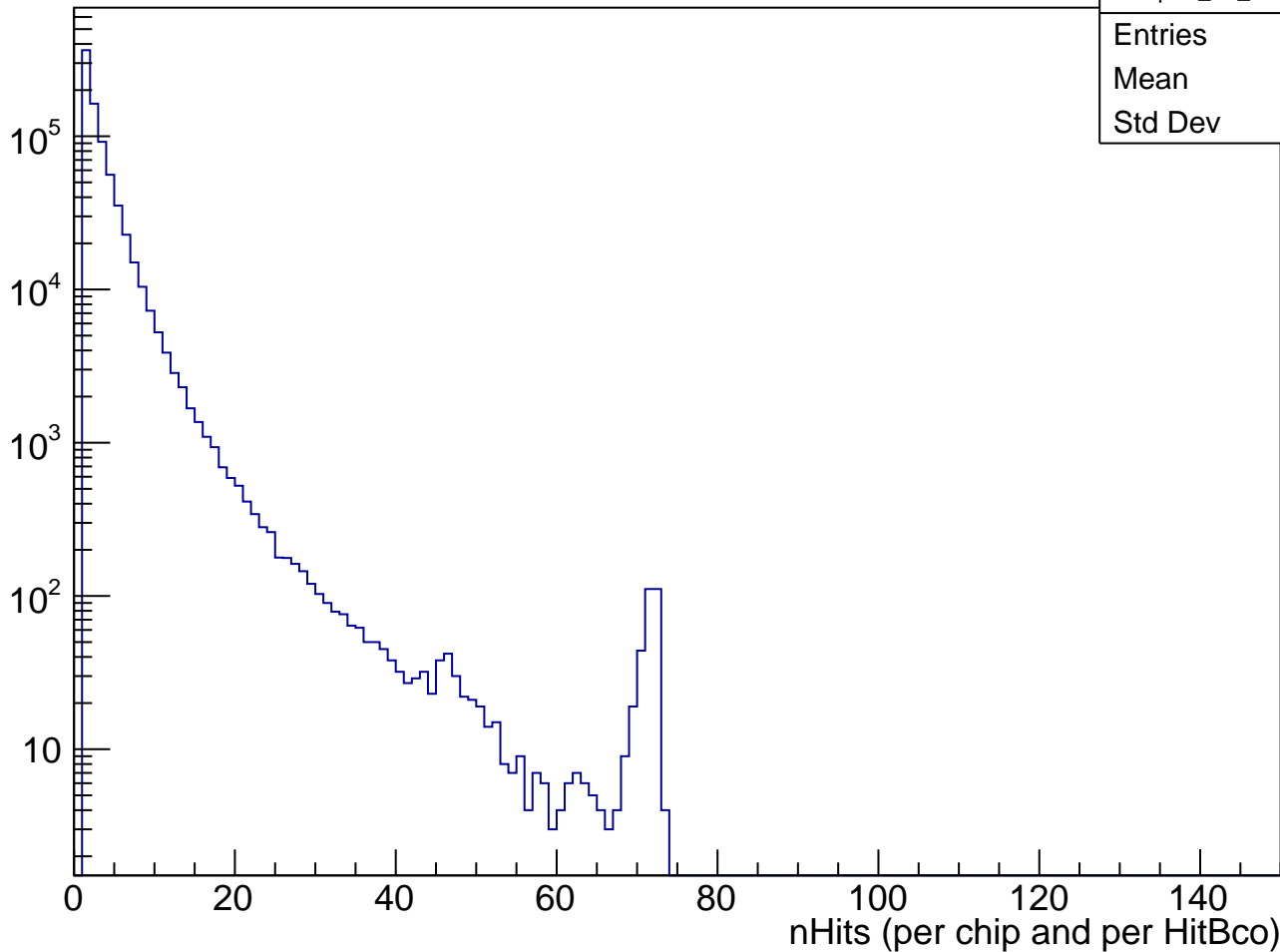
Entries



nChipHit_F0_Fch11_Chip4	
Entries	991143
Mean	2.676
Std Dev	3.429

nChipHit_F0_Fch11_Chip5

Entries

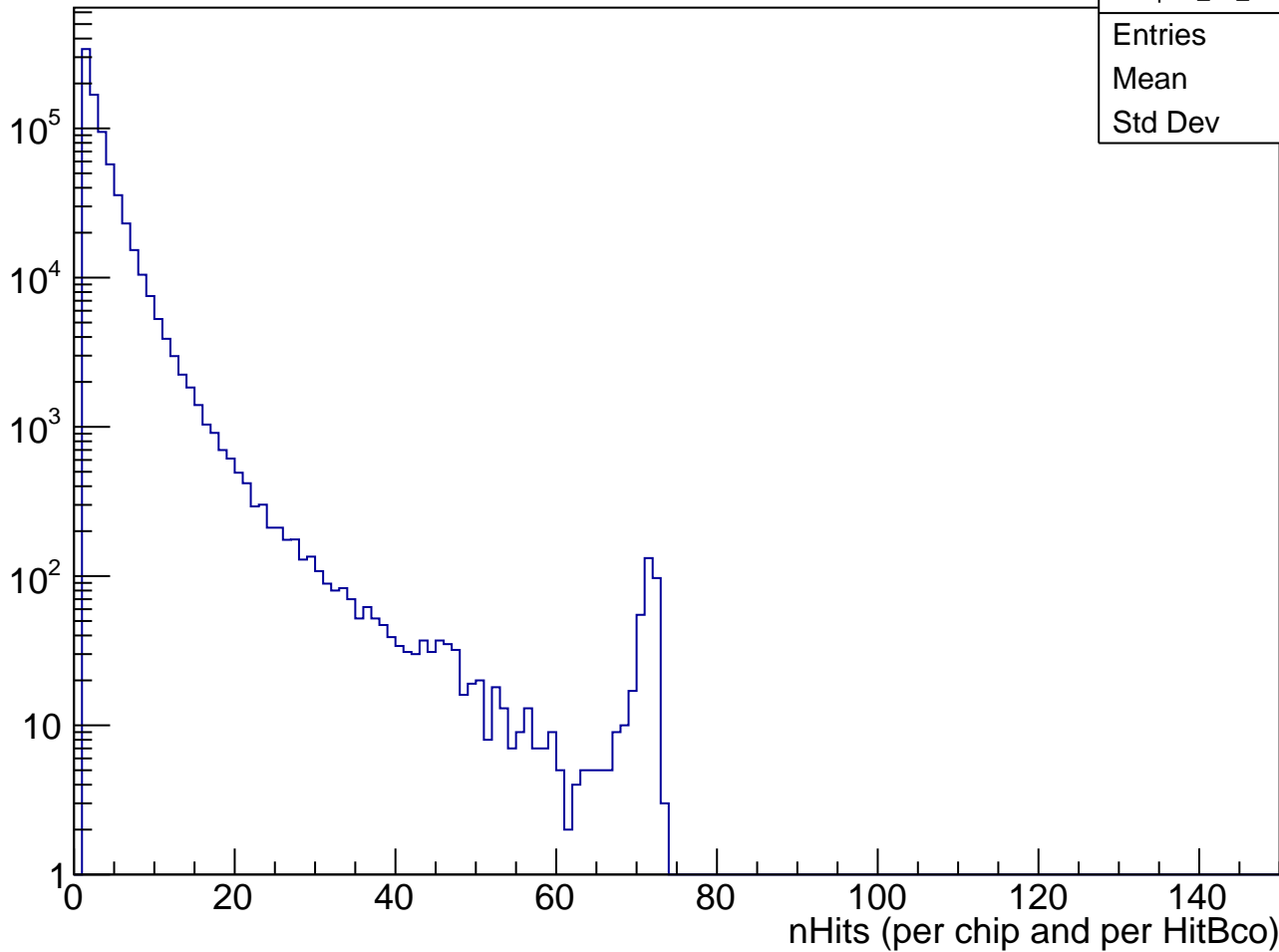


nChipHit_F0_Fch11_Chip5	
Entries	790947
Mean	2.708
Std Dev	3.321

nHits (per chip and per HitBco)

nChipHit_F0_Fch11_Chip6

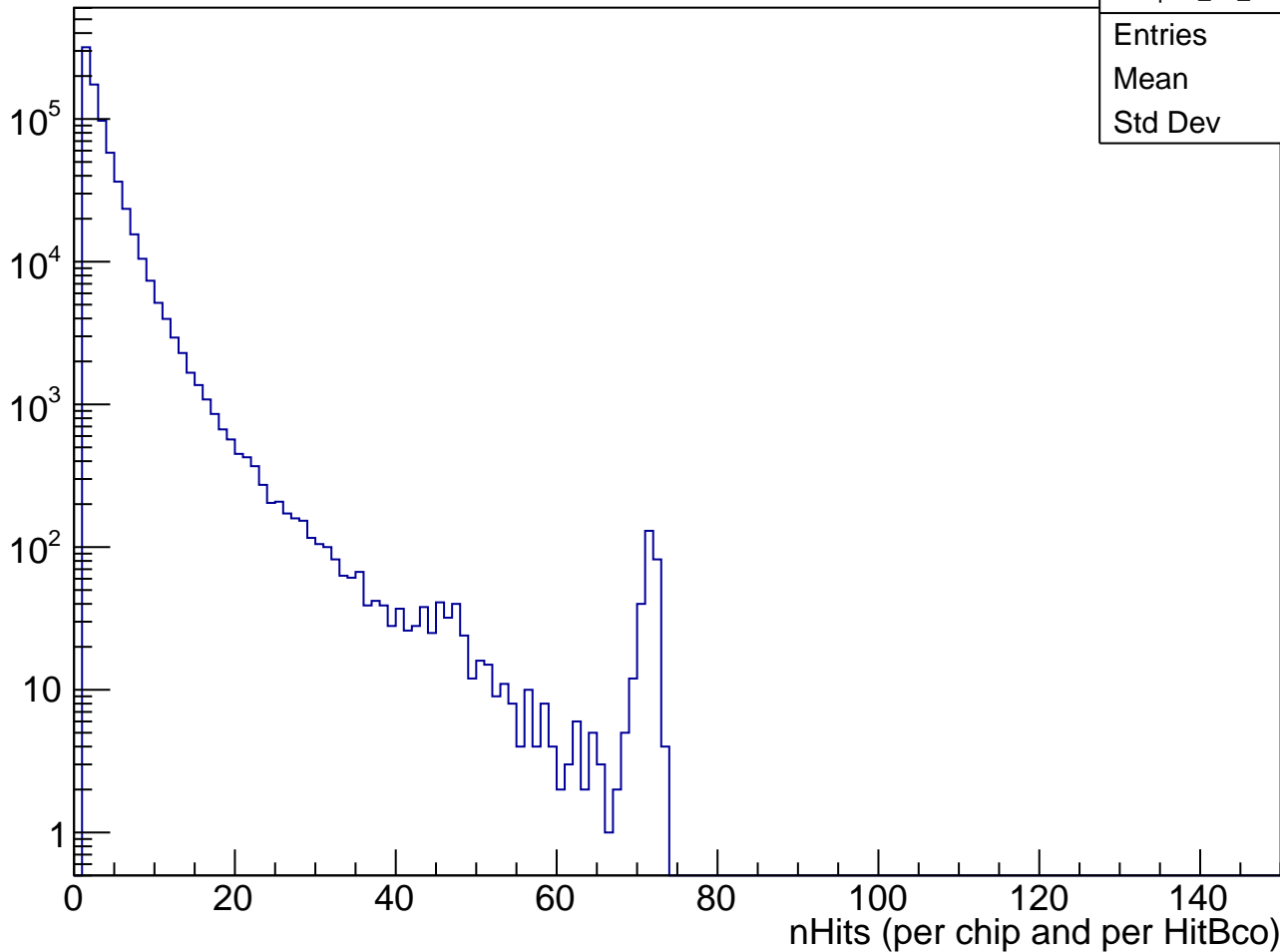
Entries



nChipHit_F0_Fch11_Chip6	
Entries	777363
Mean	2.773
Std Dev	3.374

nChipHit_F0_Fch11_Chip7

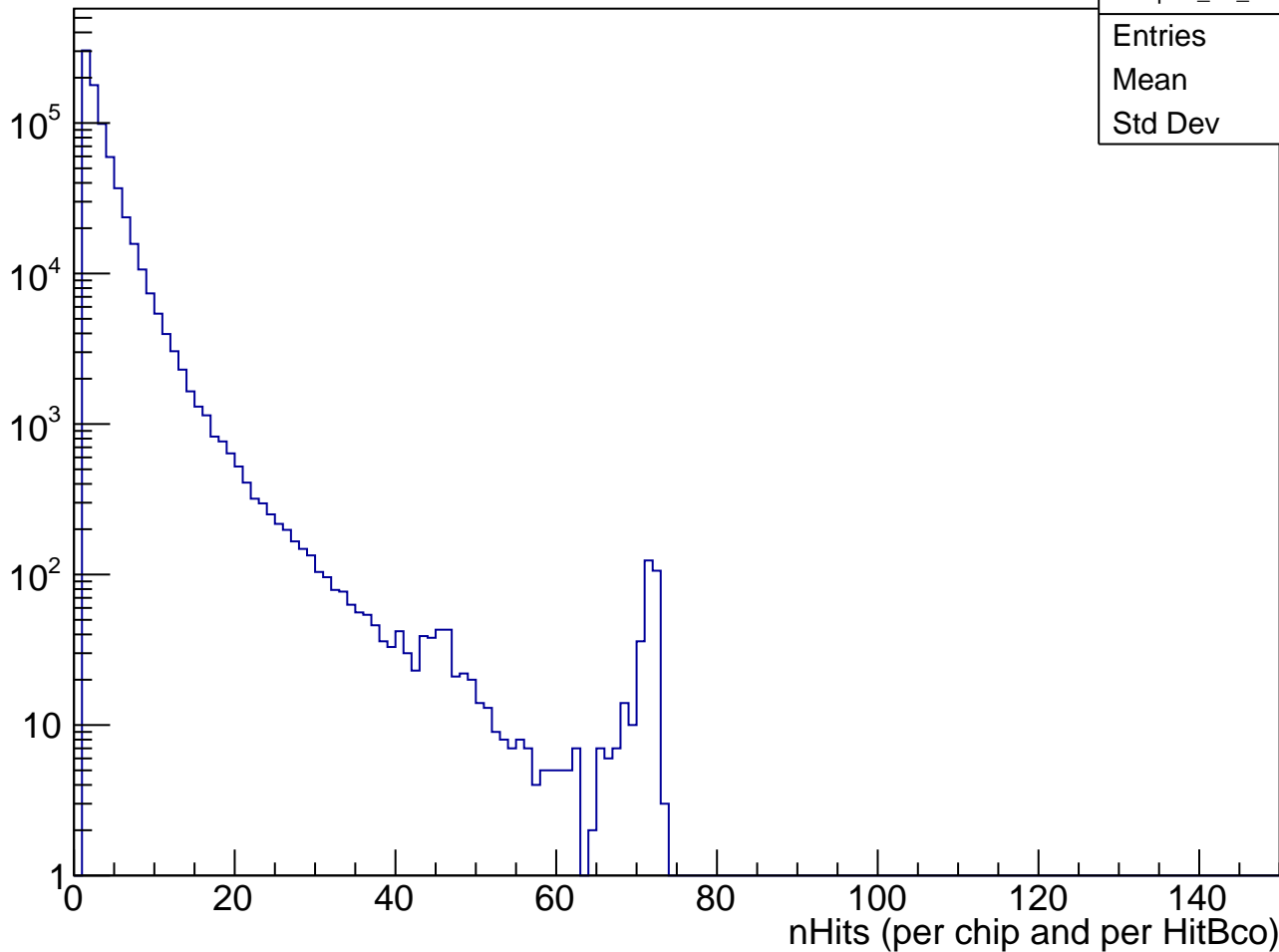
Entries



nChipHit_F0_Fch11_Chip7	
Entries	765486
Mean	2.808
Std Dev	3.305

nChipHit_F0_Fch11_Chip8

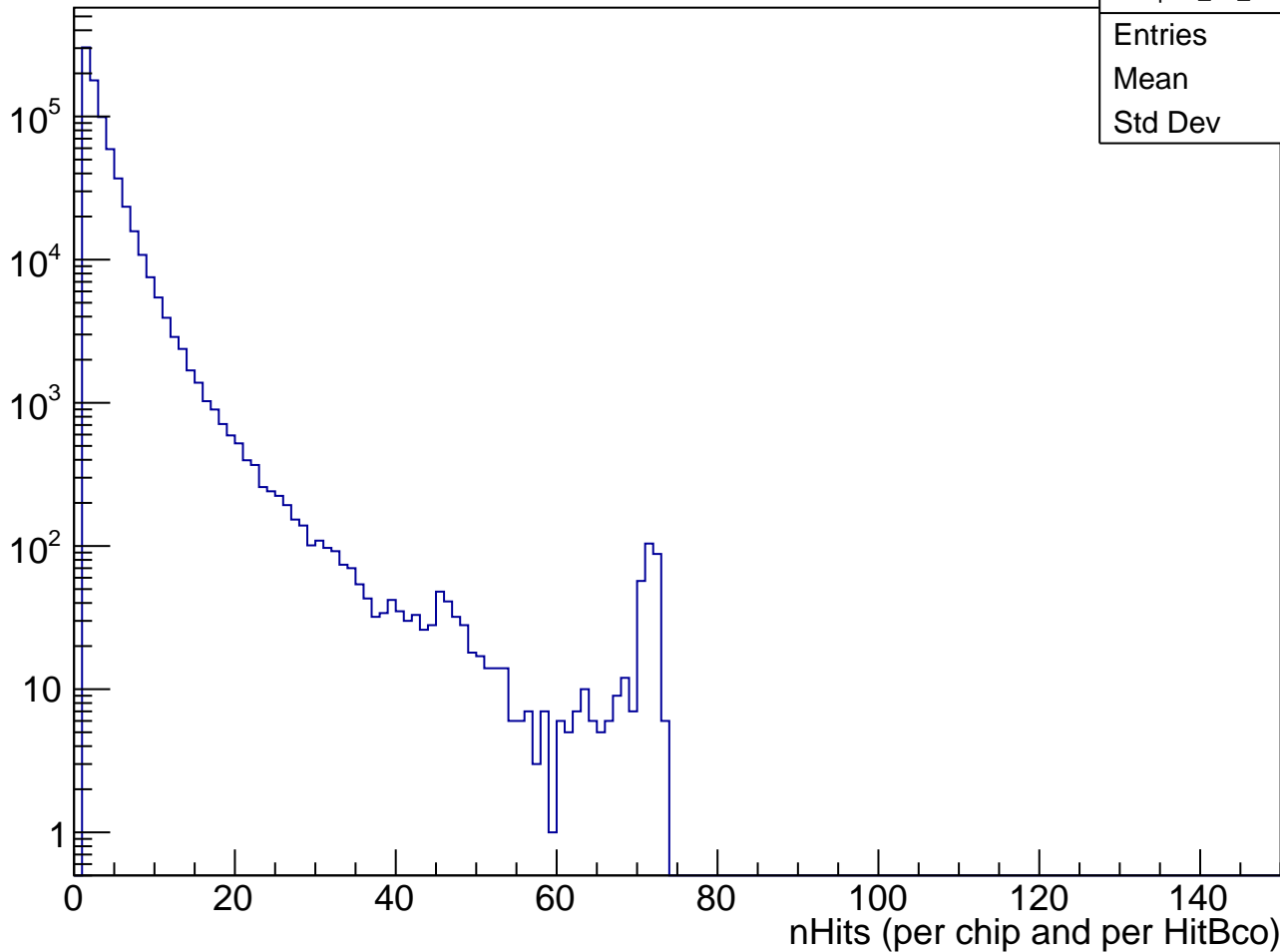
Entries



nChipHit_F0_Fch11_Chip8	
Entries	759635
Mean	2.86
Std Dev	3.369

nChipHit_F0_Fch11_Chip9

Entries

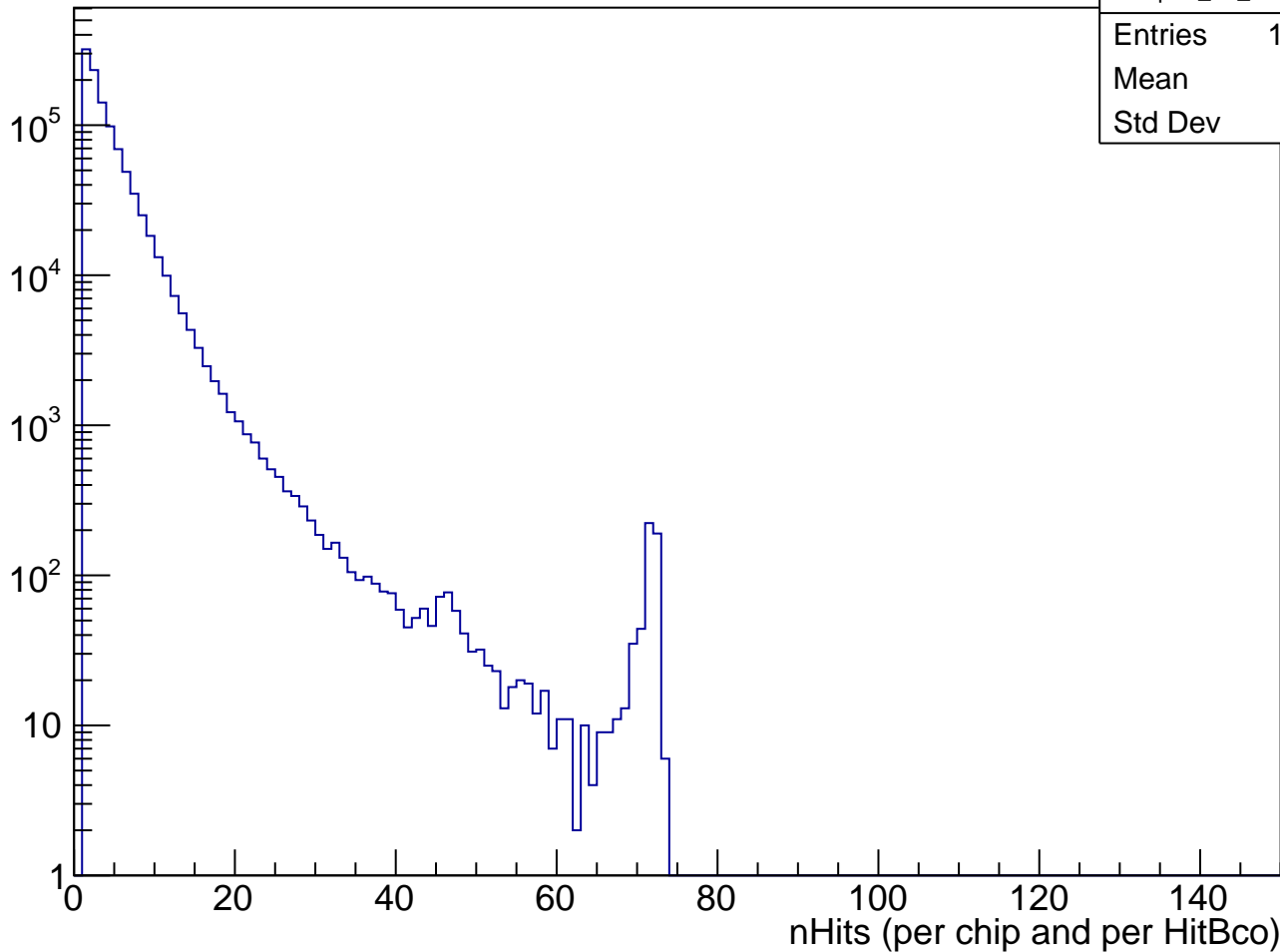


nChipHit_F0_Fch11_Chip9	
Entries	760072
Mean	2.857
Std Dev	3.355

nHits (per chip and per HitBco)

nChipHit_F0_Fch12_Chip0

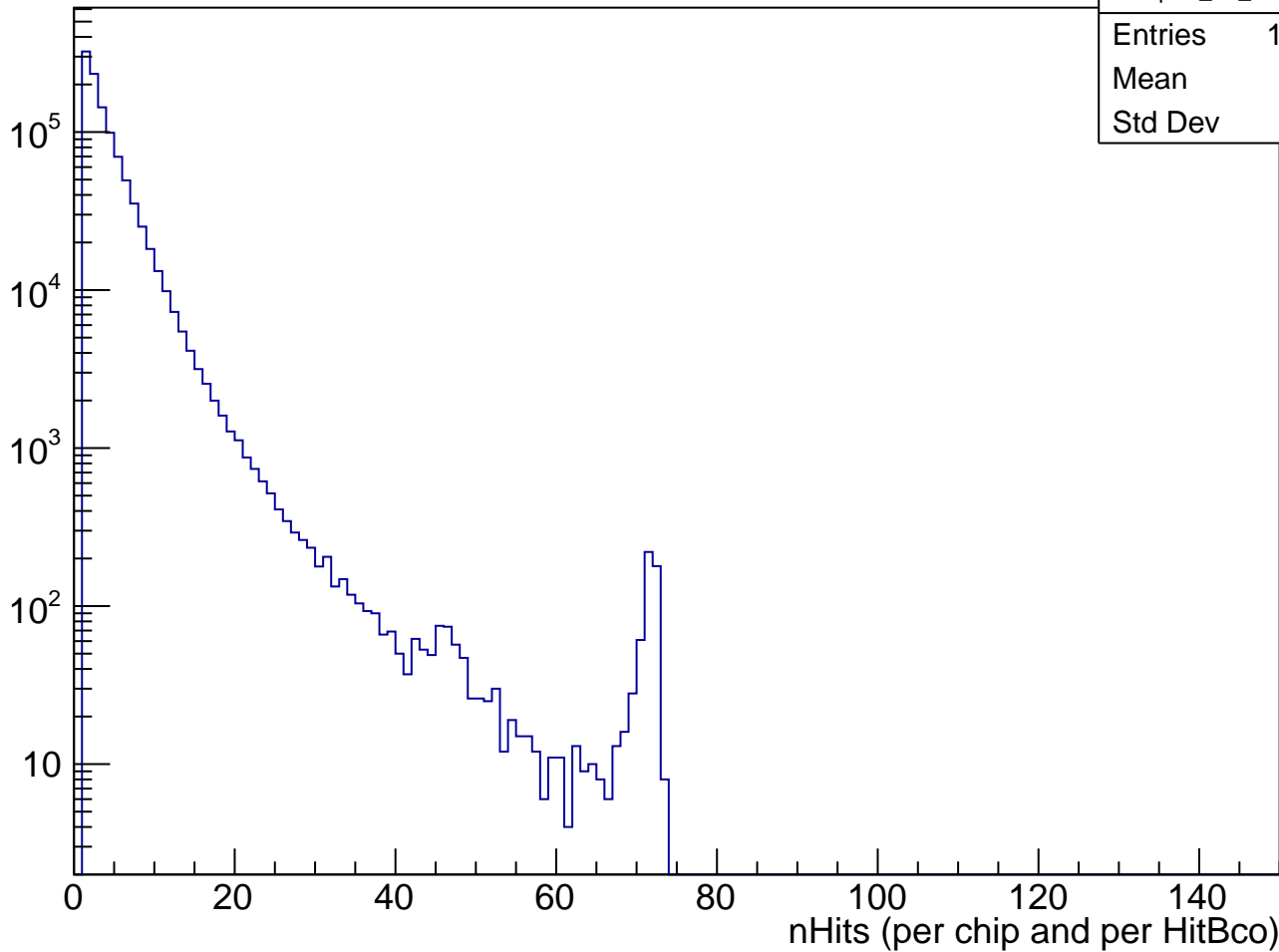
Entries



nChipHit_F0_Fch12_Chip0	
Entries	1048247
Mean	3.558
Std Dev	3.926

nChipHit_F0_Fch12_Chip1

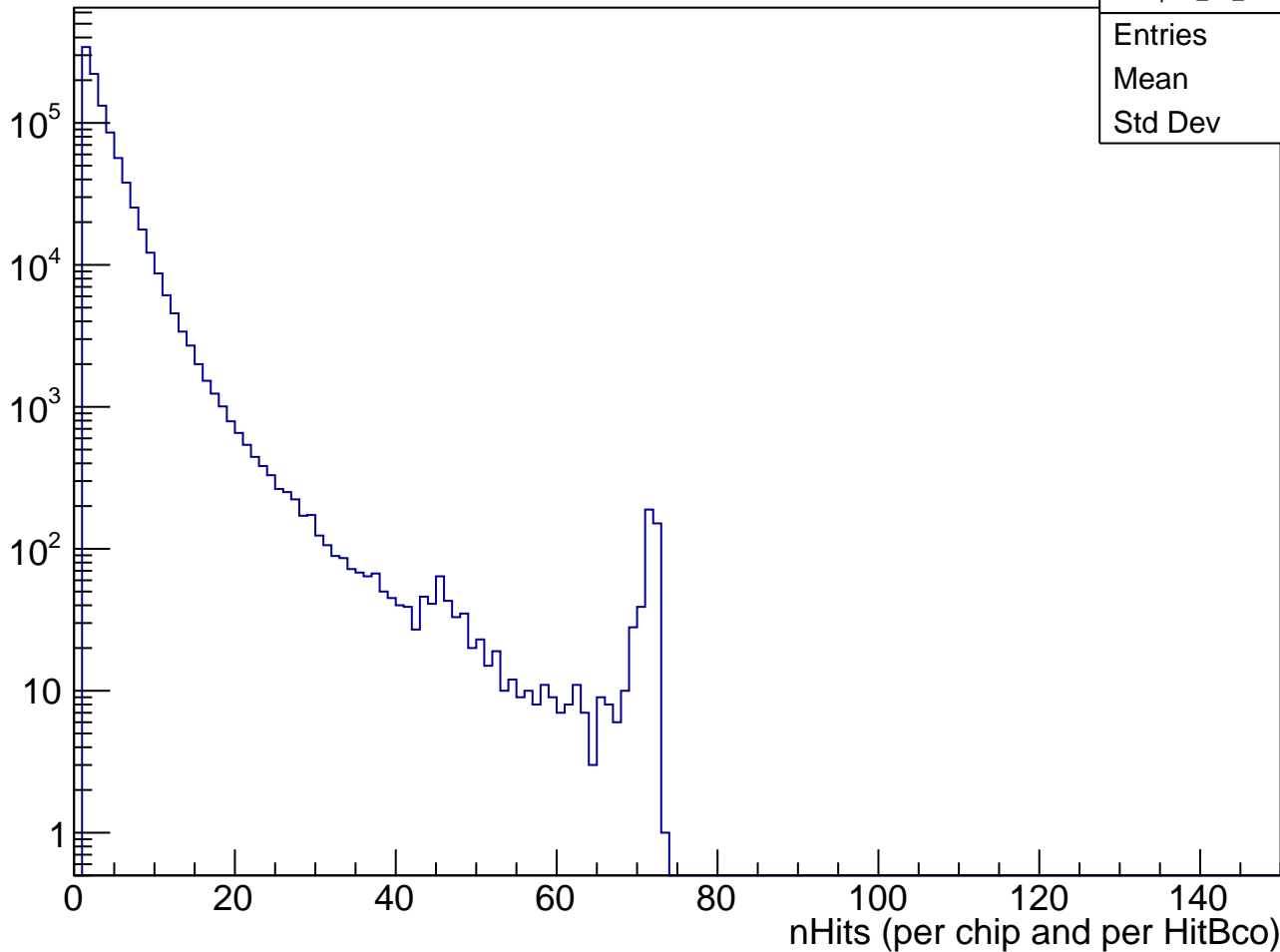
Entries



nChipHit_F0_Fch12_Chip1	
Entries	1055328
Mean	3.548
Std Dev	3.906

nChipHit_F0_Fch12_Chip10

Entries

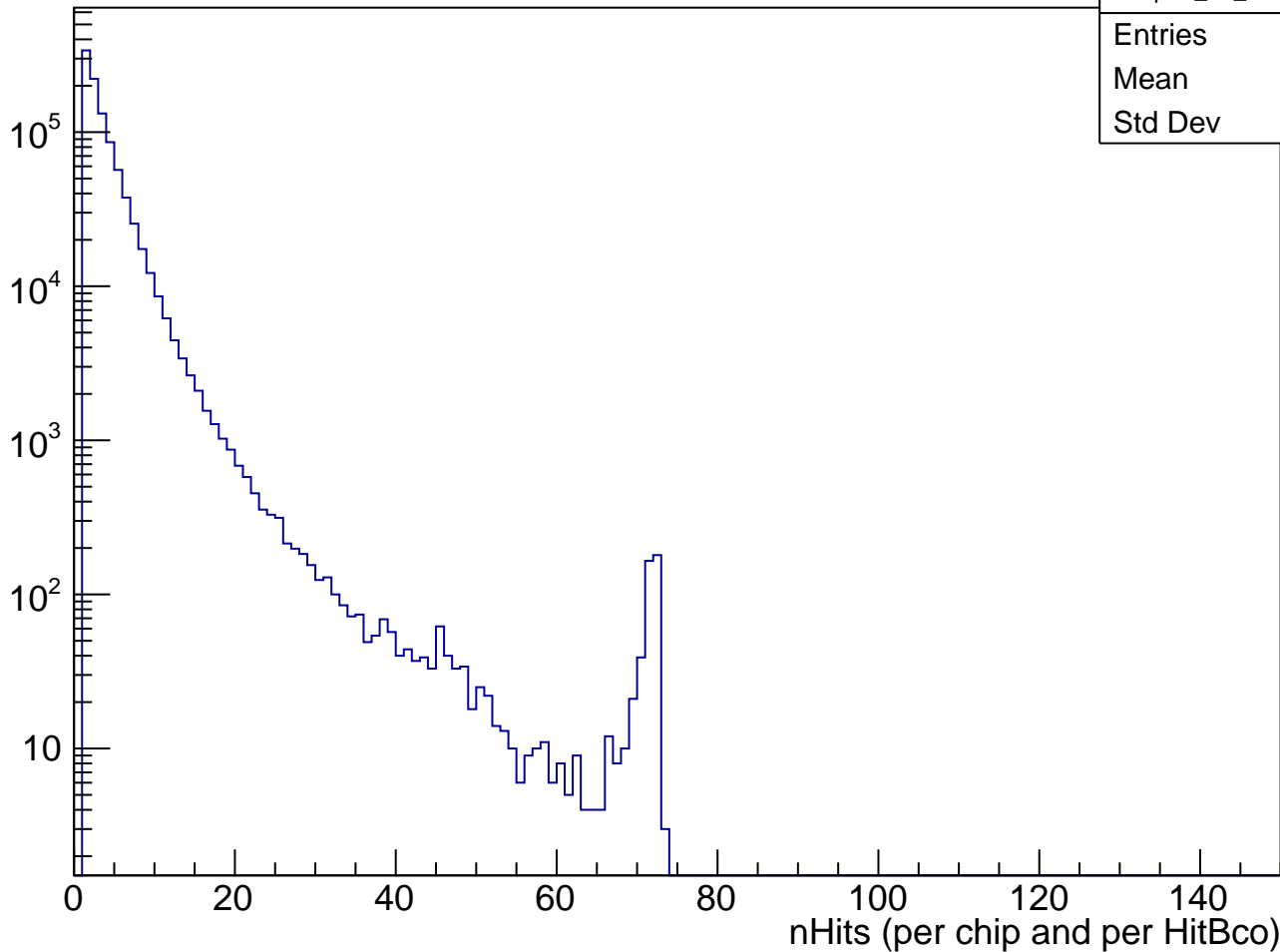


nChipHit_F0_Fch12_Chip10	
Entries	968954
Mean	3.125
Std Dev	3.504

nHits (per chip and per HitBco)

nChipHit_F0_Fch12_Chip11

Entries

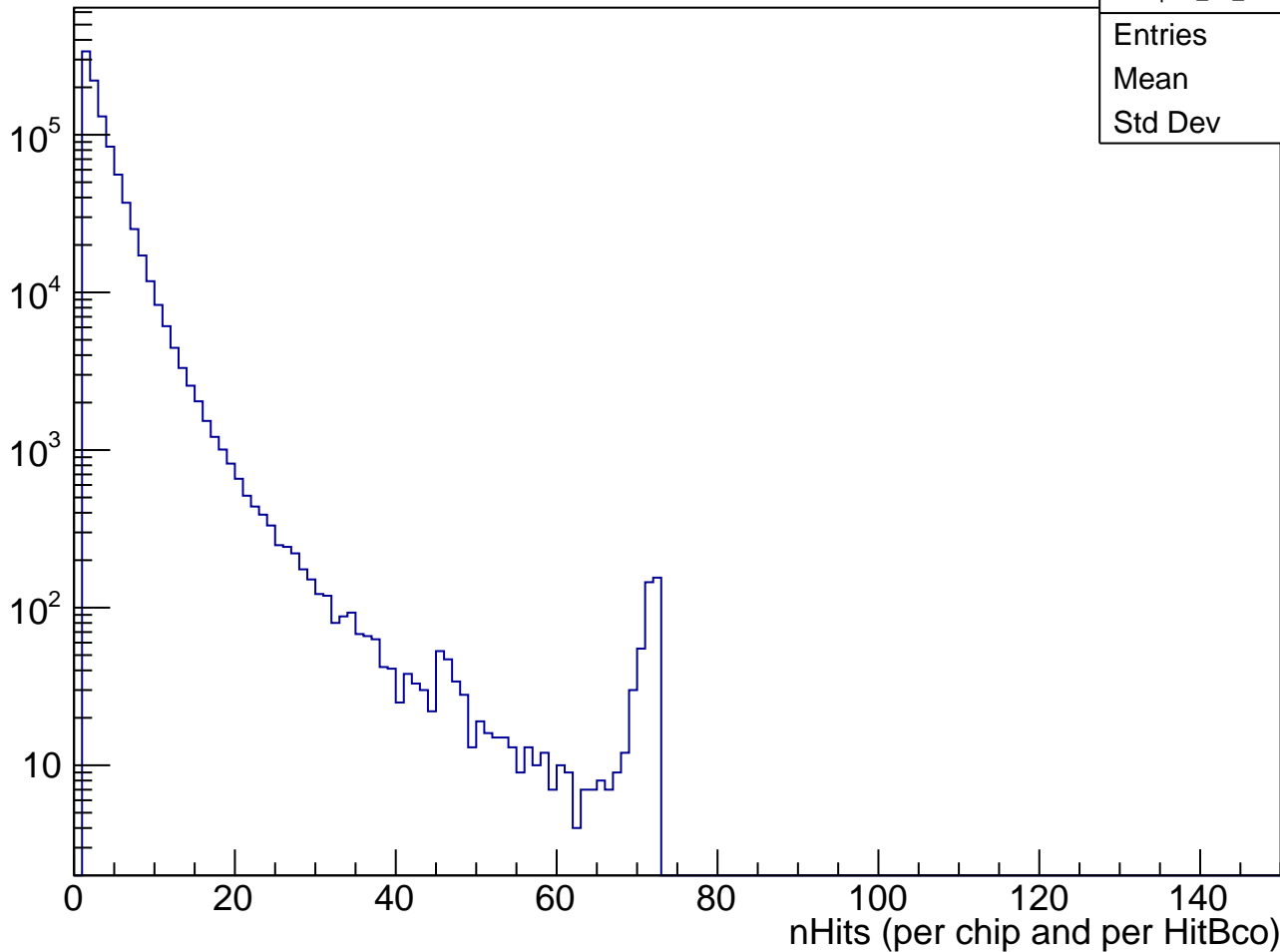


nChipHit_F0_Fch12_Chip11	
Entries	965735
Mean	3.133
Std Dev	3.513

nHits (per chip and per HitBco)

nChipHit_F0_Fch12_Chip12

Entries



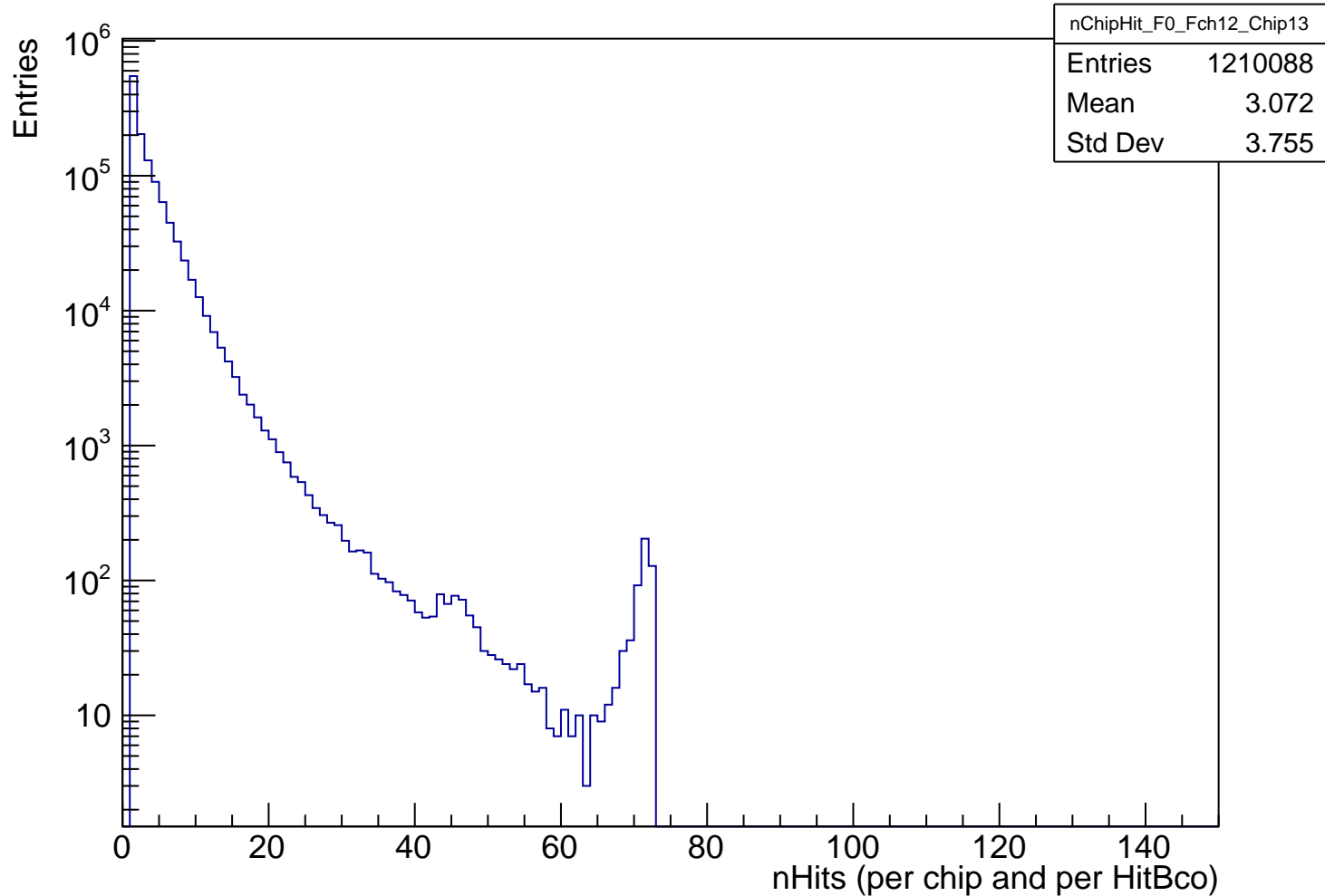
nChipHit_F0_Fch12_Chip12

Entries 957274

Mean 3.113

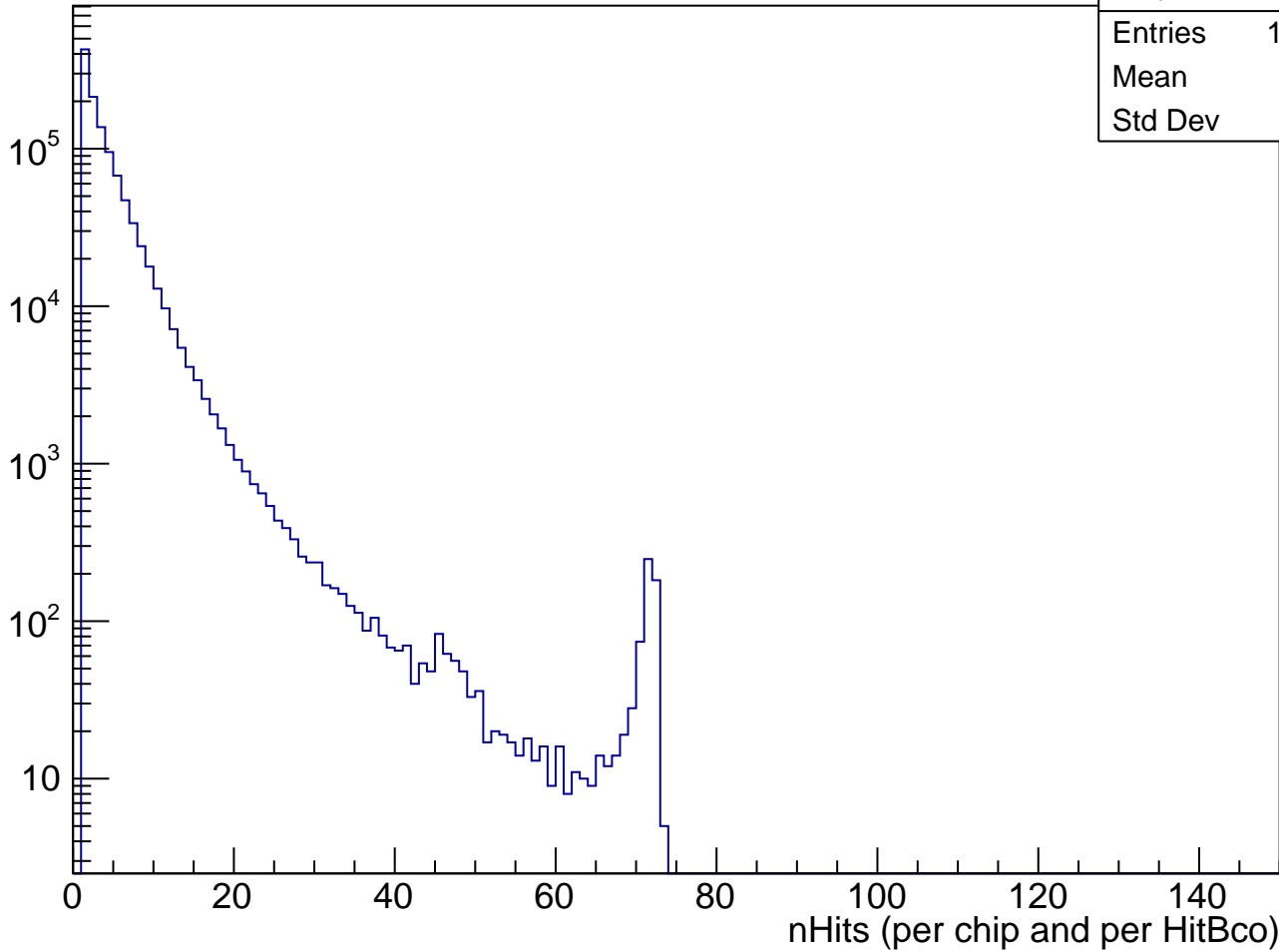
Std Dev 3.477

nChipHit_F0_Fch12_Chip13



nChipHit_F0_Fch12_Chip14

Entries



nChipHit_F0_Fch12_Chip14

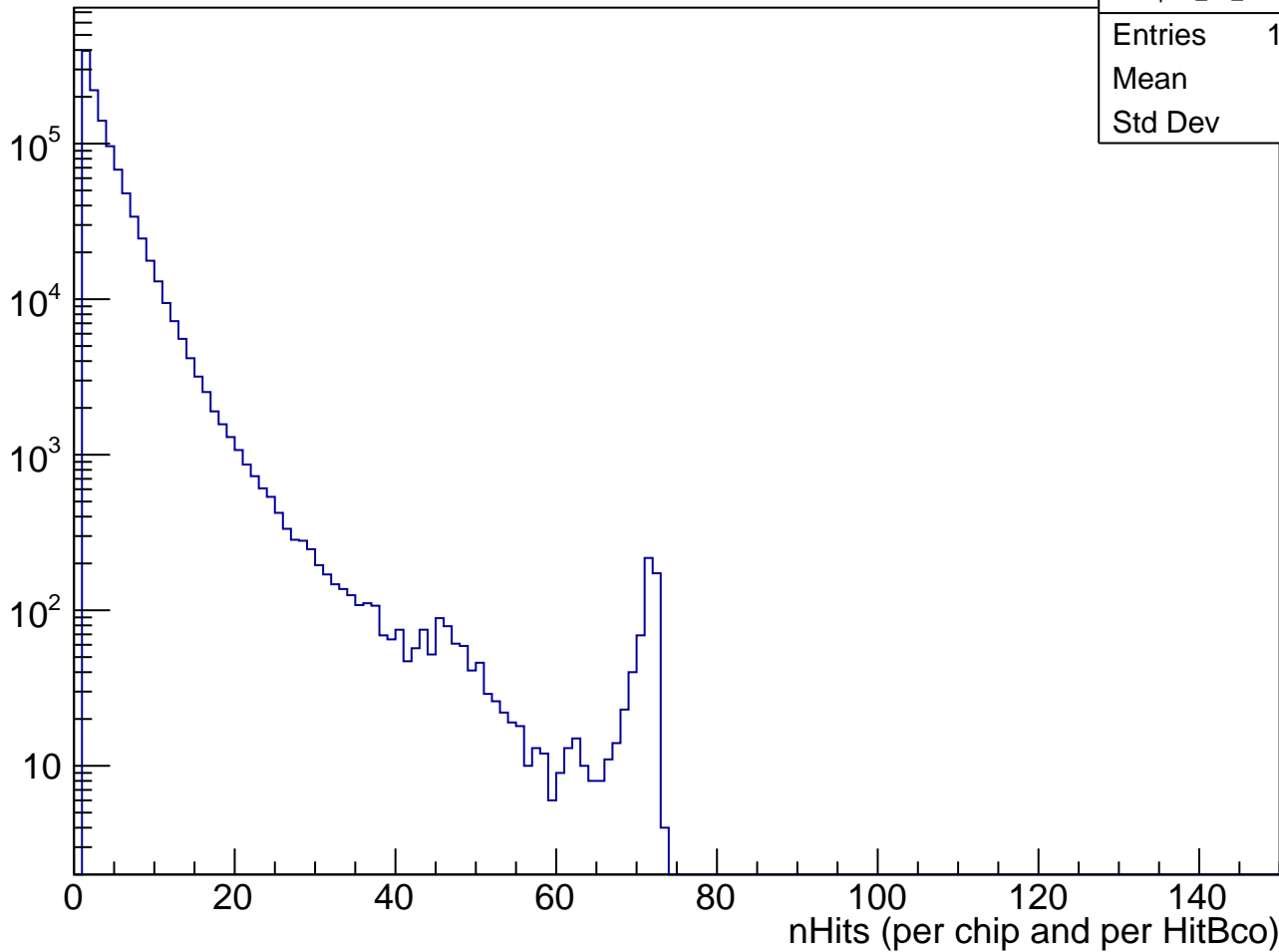
Entries 1121915

Mean 3.332

Std Dev 3.906

nChipHit_F0_Fch12_Chip15

Entries

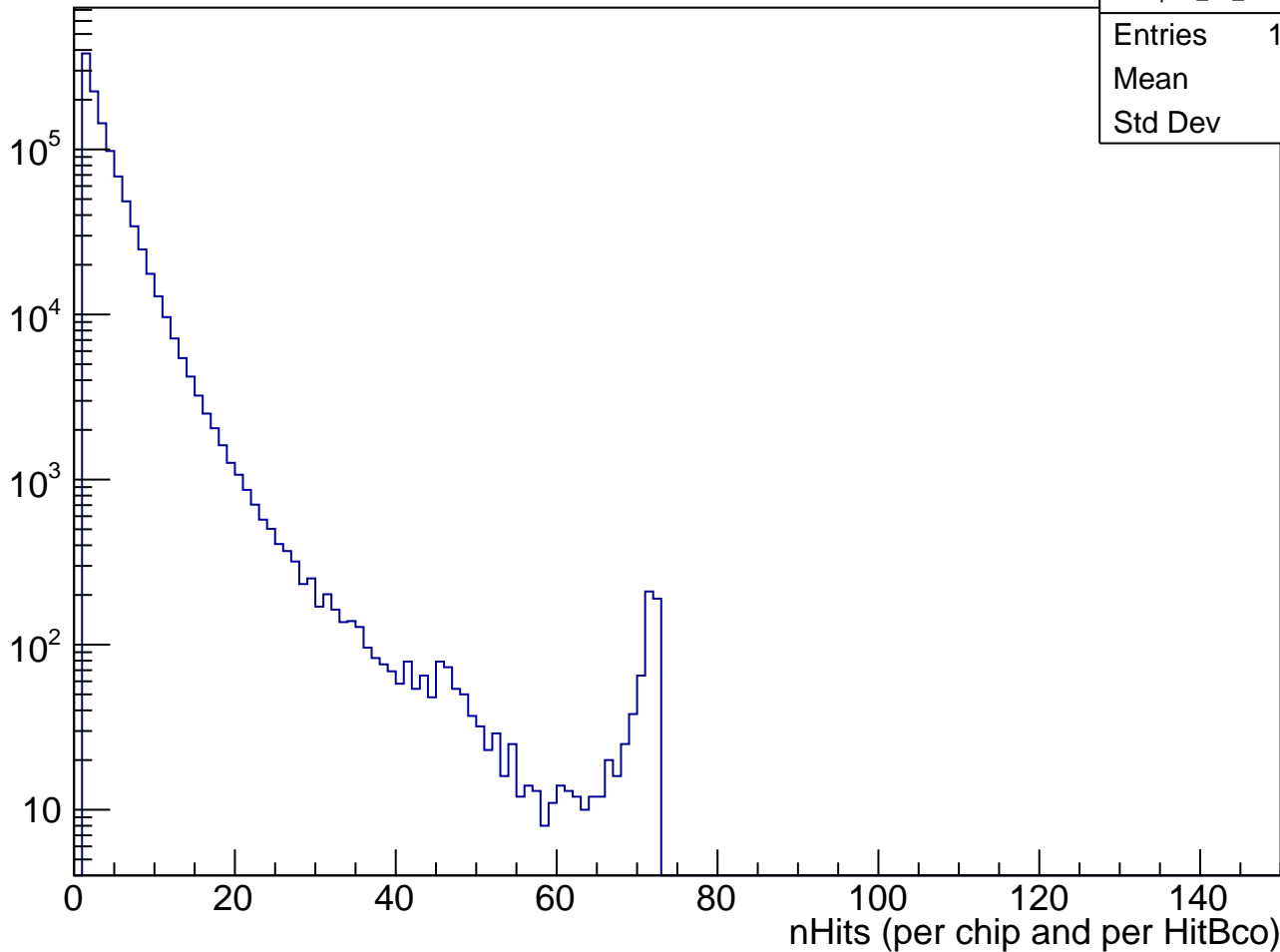


nChipHit_F0_Fch12_Chip15	
Entries	1102699
Mean	3.388
Std Dev	3.903

nHits (per chip and per HitBco)

nChipHit_F0_Fch12_Chip16

Entries

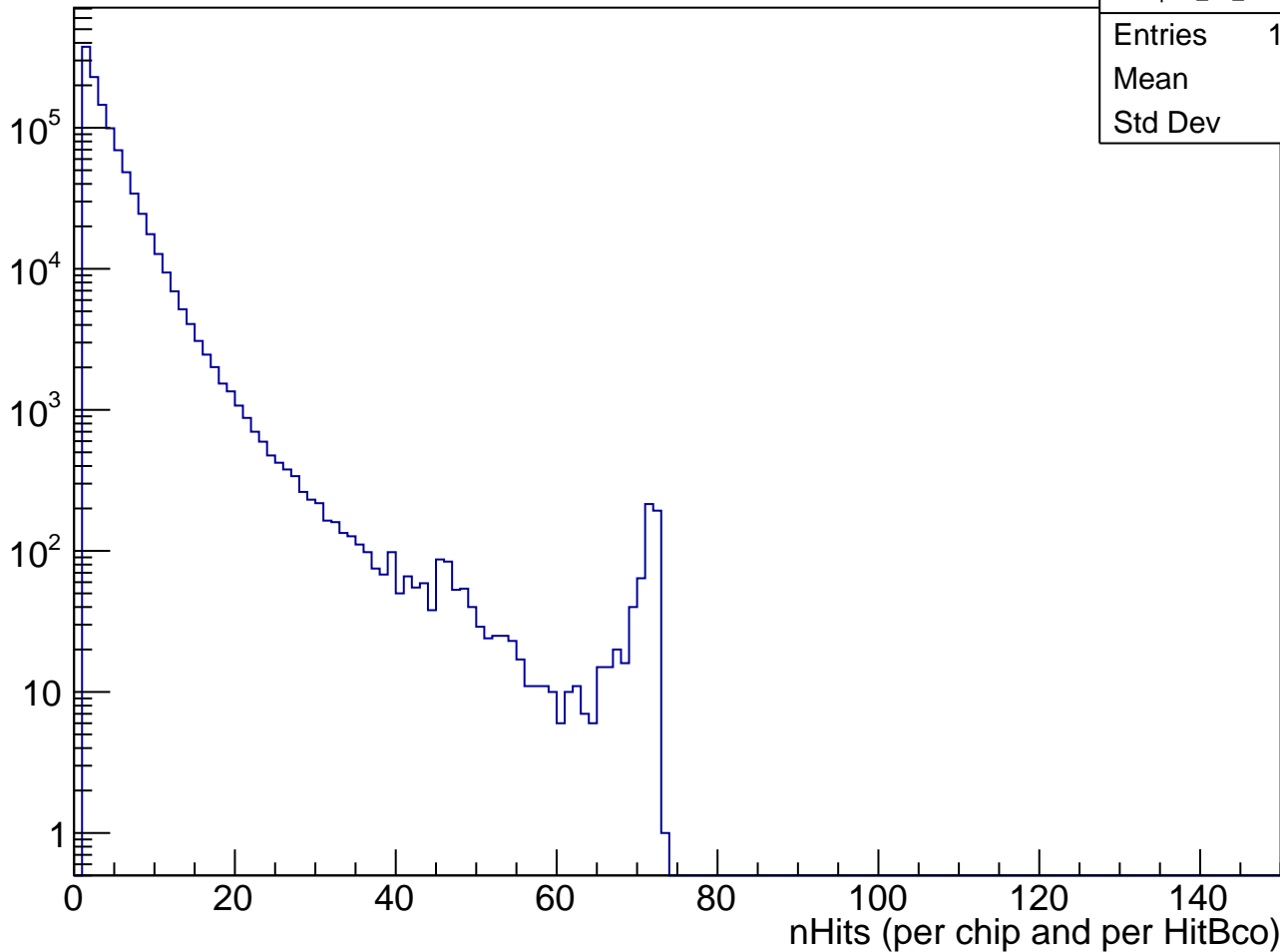


nChipHit_F0_Fch12_Chip16	
Entries	1099244
Mean	3.417
Std Dev	3.901

nHits (per chip and per HitBco)

nChipHit_F0_Fch12_Chip17

Entries

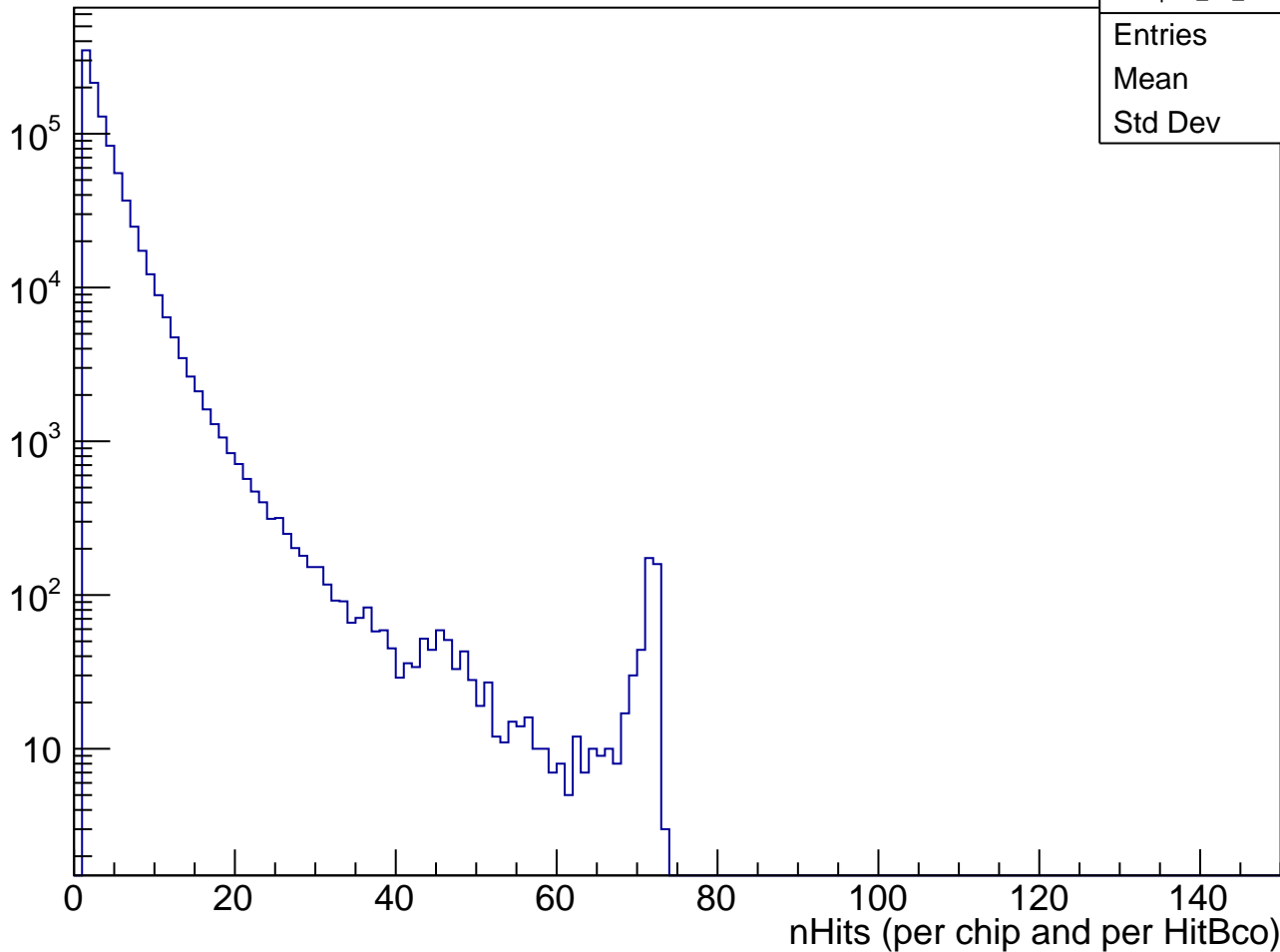


nChipHit_F0_Fch12_Chip17	
Entries	1099754
Mean	3.414
Std Dev	3.88

nHits (per chip and per HitBco)

nChipHit_F0_Fch12_Chip18

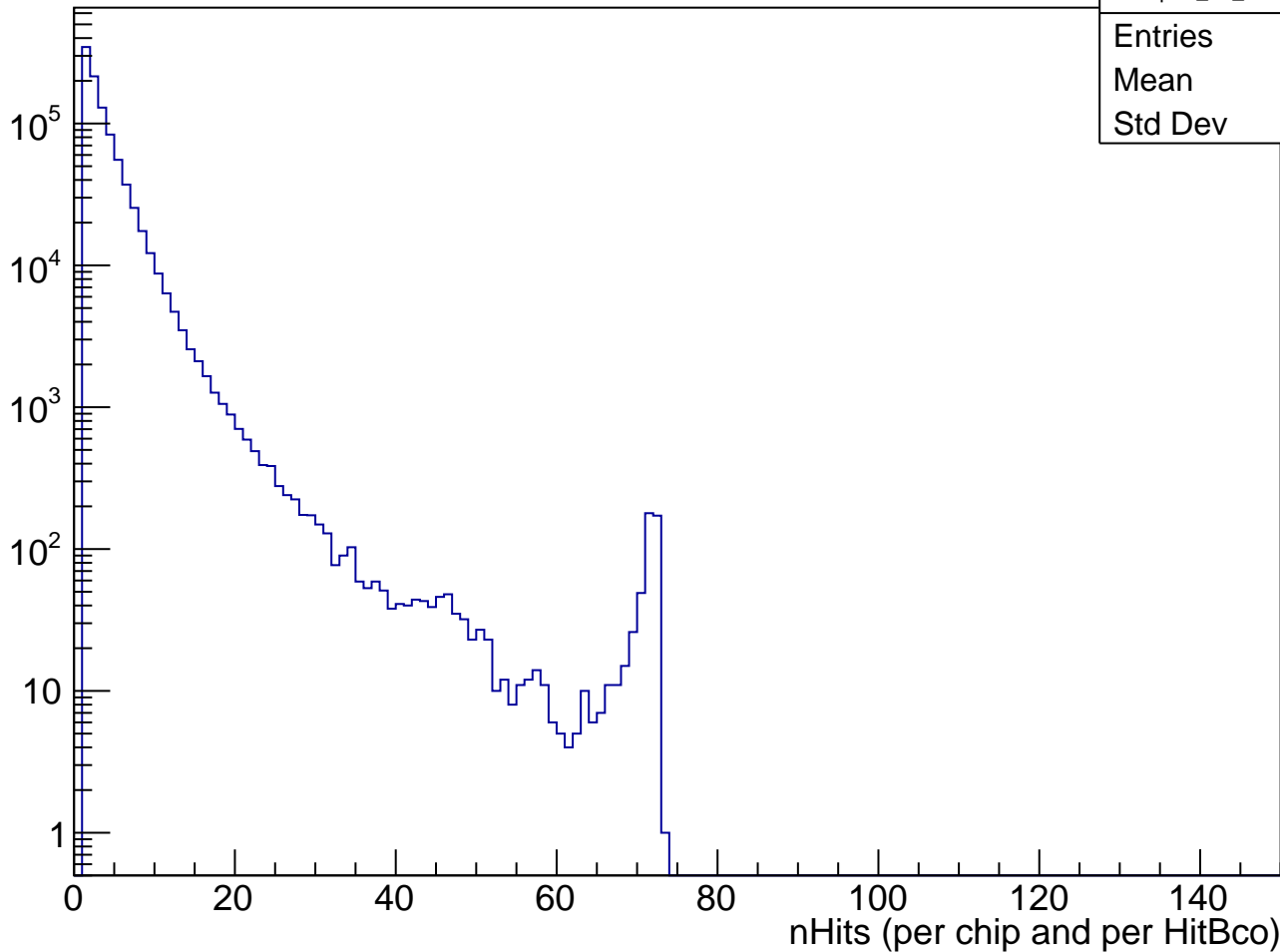
Entries



nChipHit_F0_Fch12_Chip18	
Entries	961723
Mean	3.125
Std Dev	3.568

nChipHit_F0_Fch12_Chip19

Entries



nChipHit_F0_Fch12_Chip19

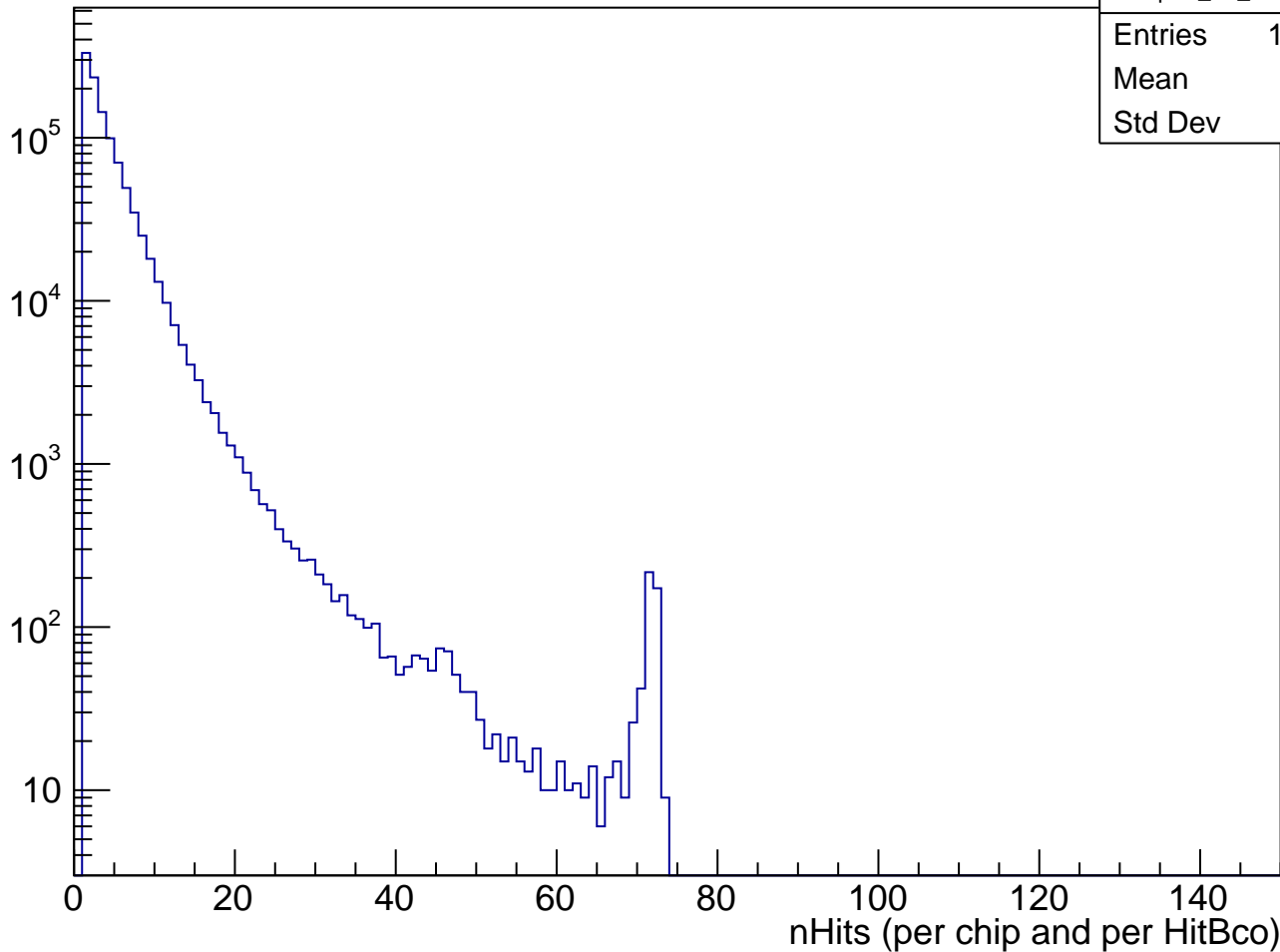
Entries 960141

Mean 3.133

Std Dev 3.568

nChipHit_F0_Fch12_Chip2

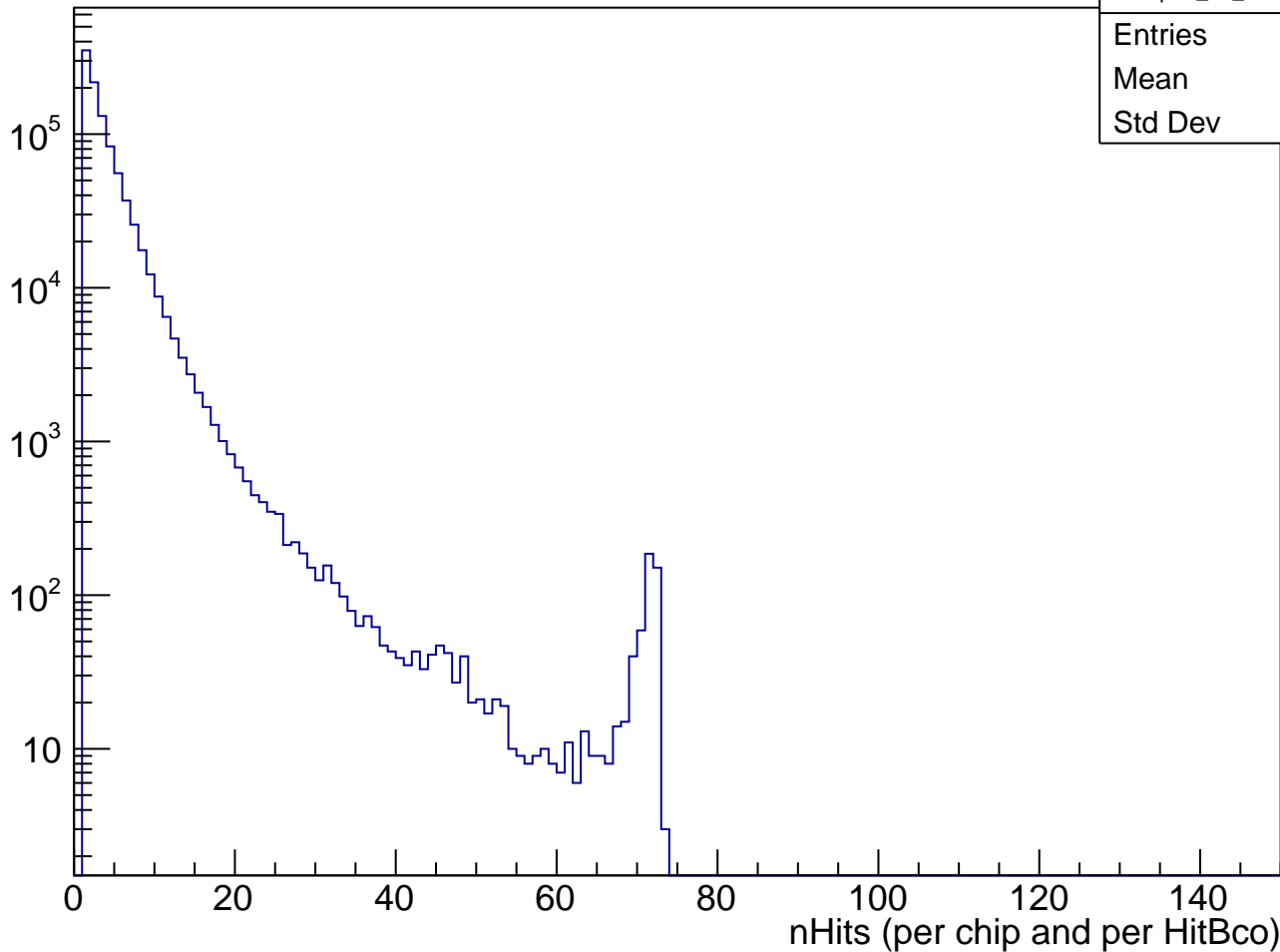
Entries



nChipHit_F0_Fch12_Chip2	
Entries	1063642
Mean	3.521
Std Dev	3.888

nChipHit_F0_Fch12_Chip20

Entries



nChipHit_F0_Fch12_Chip20

Entries 969676

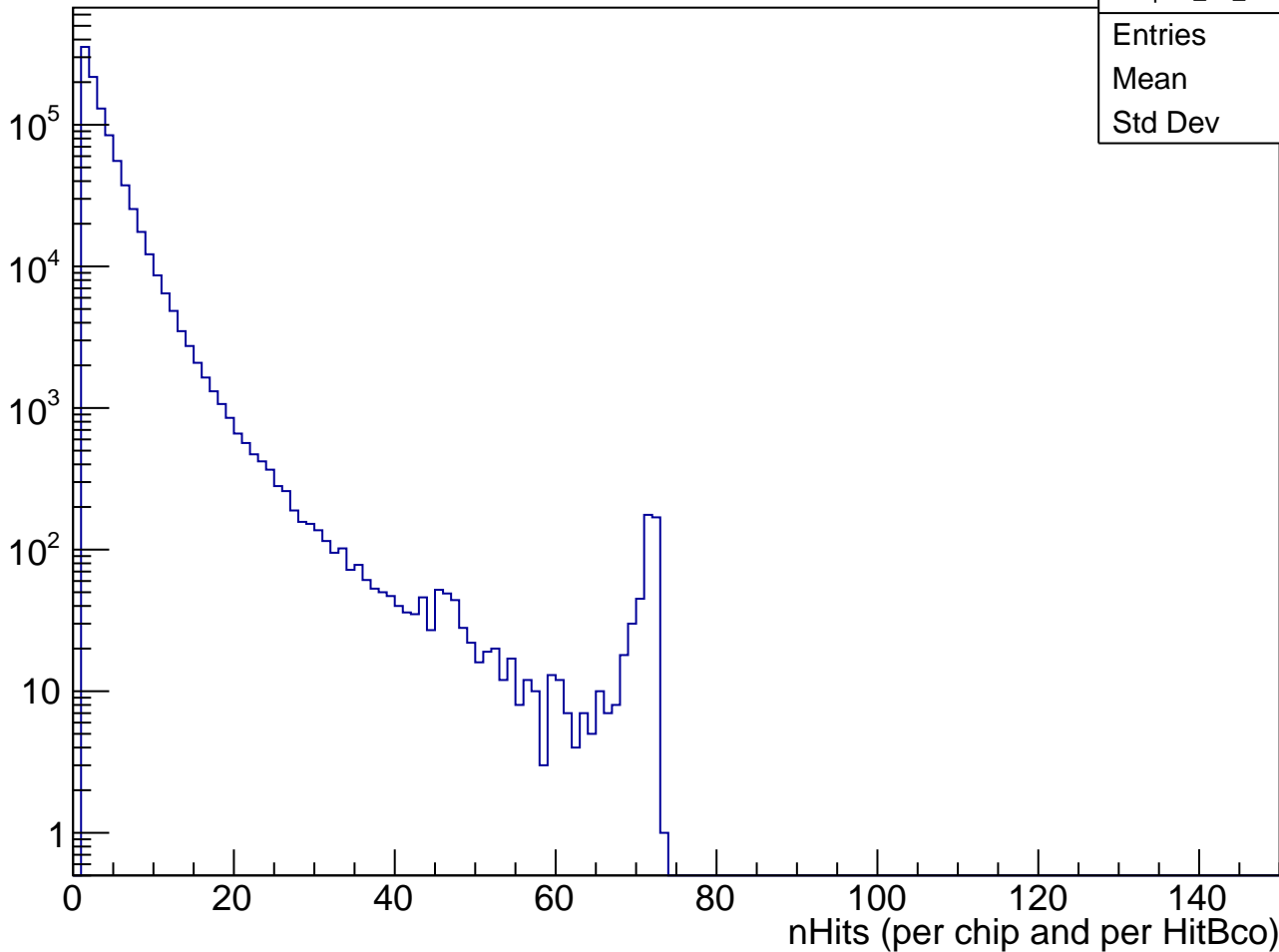
Mean 3.122

Std Dev 3.567

nHits (per chip and per HitBco)

nChipHit_F0_Fch12_Chip21

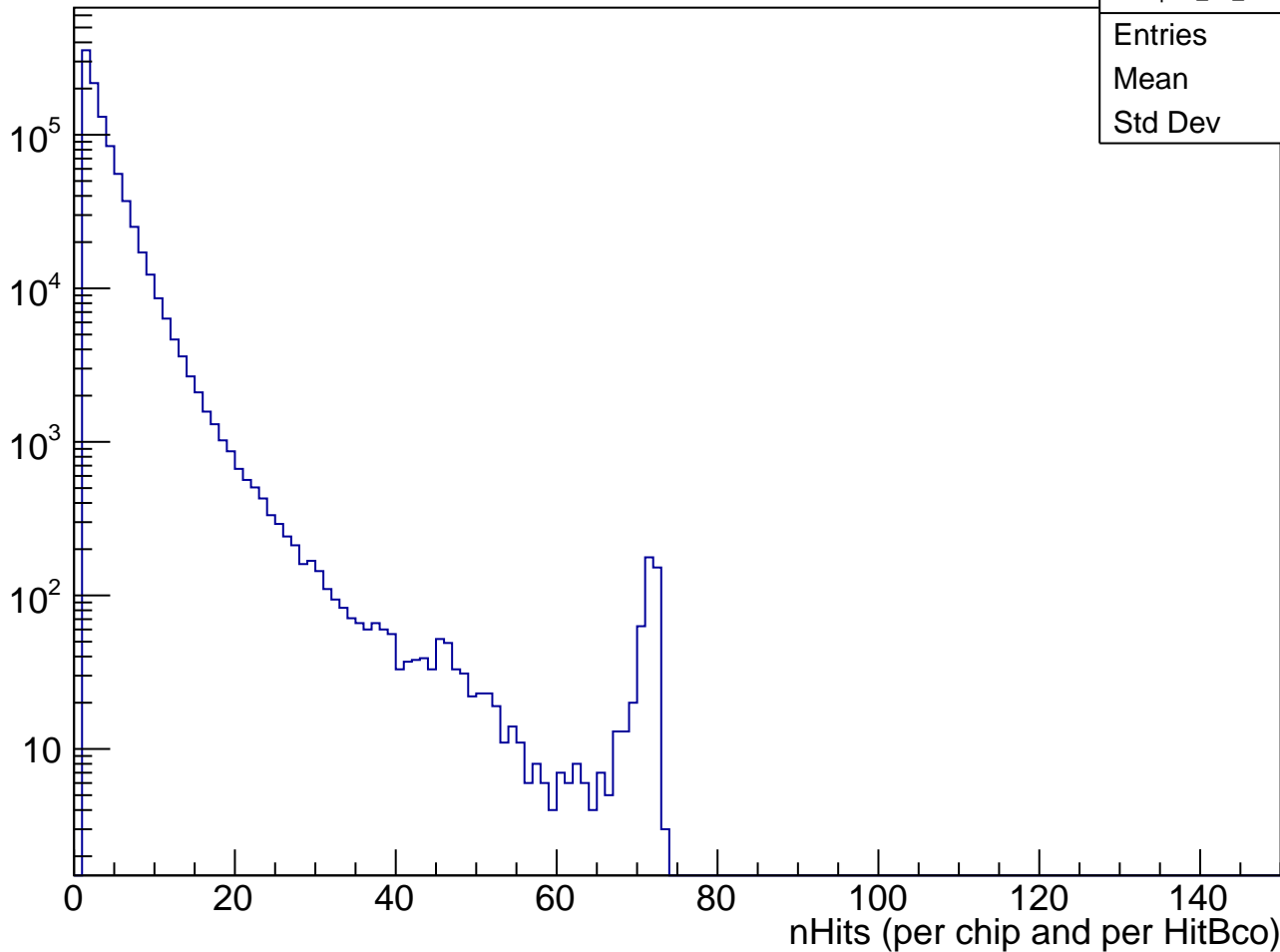
Entries



nChipHit_F0_Fch12_Chip21	
Entries	973308
Mean	3.113
Std Dev	3.541

nChipHit_F0_Fch12_Chip22

Entries



nChipHit_F0_Fch12_Chip22

Entries 973098

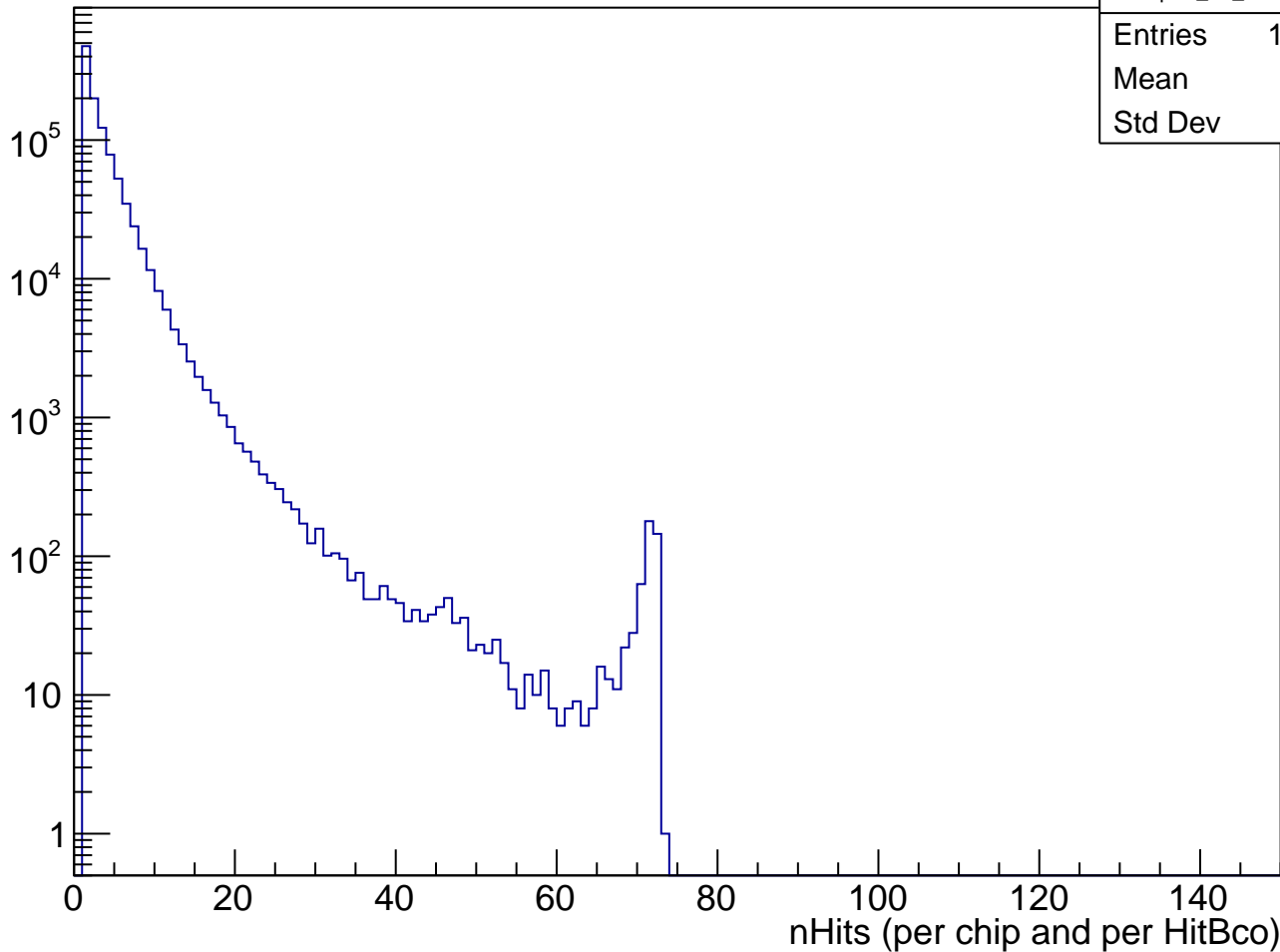
Mean 3.104

Std Dev 3.524

nHits (per chip and per HitBco)

nChipHit_F0_Fch12_Chip23

Entries



nChipHit_F0_Fch12_Chip23

Entries 1052019

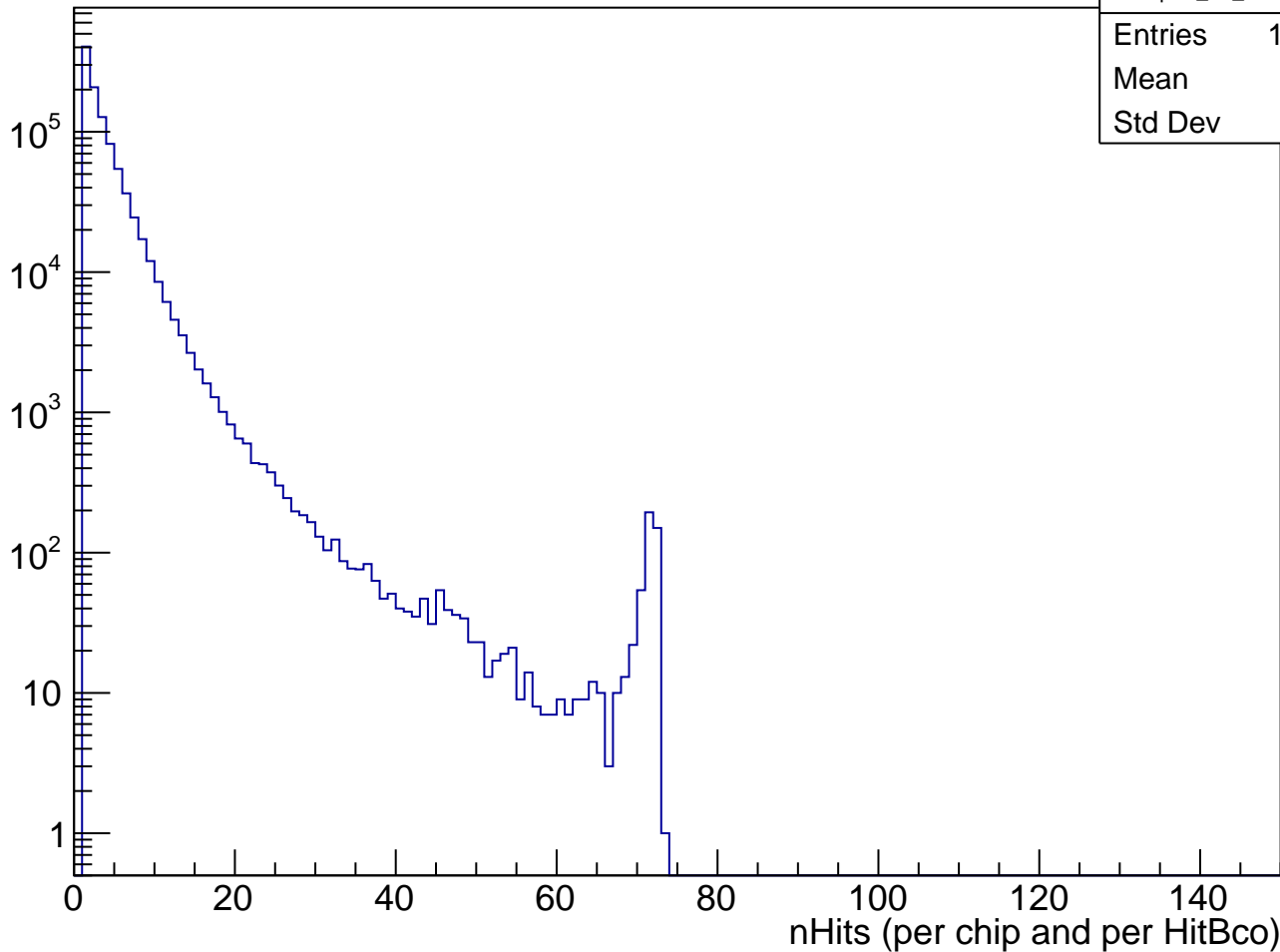
Mean 2.843

Std Dev 3.441

nHits (per chip and per HitBco)

nChipHit_F0_Fch12_Chip24

Entries



nChipHit_F0_Fch12_Chip24

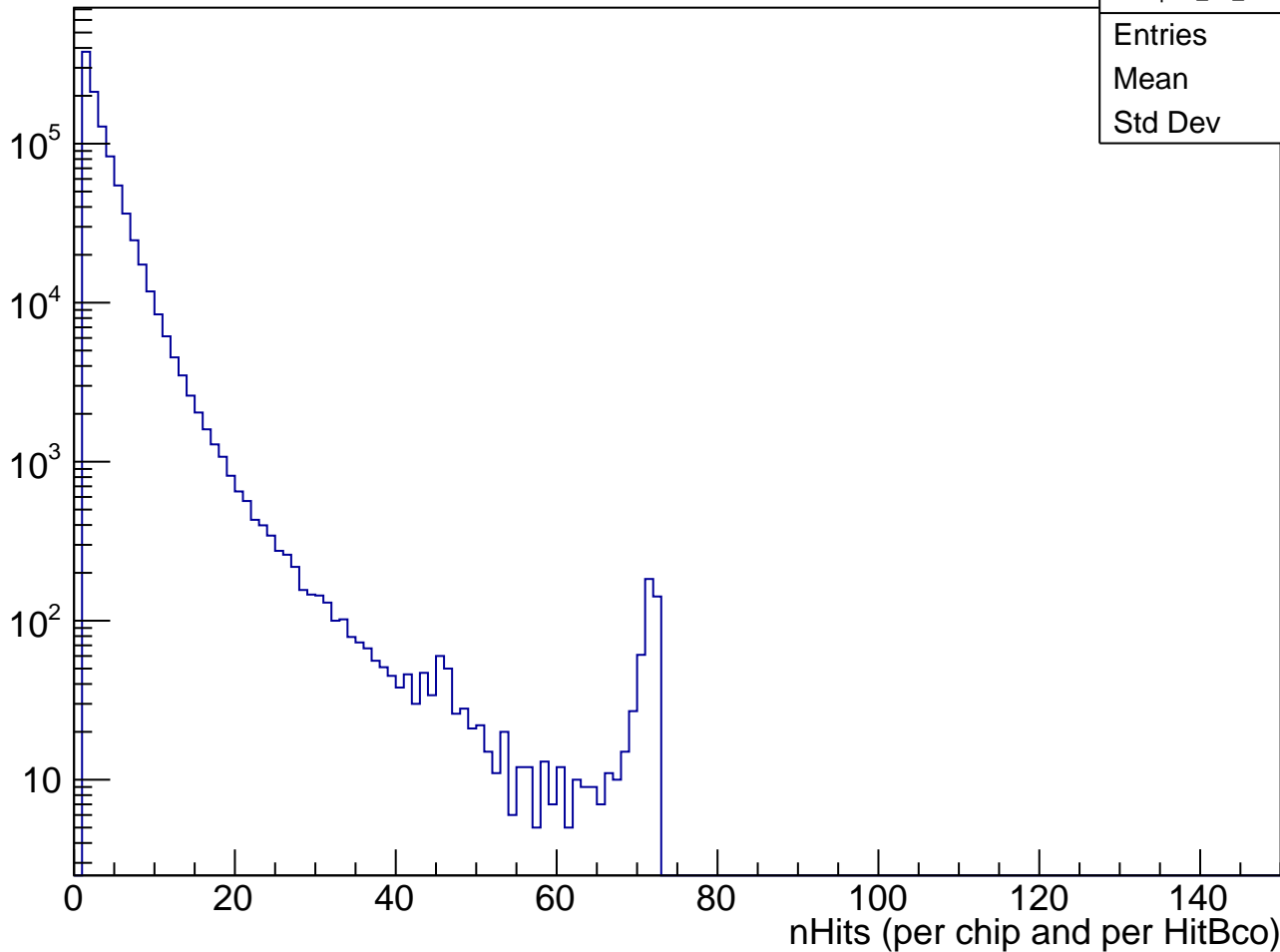
Entries 1004489

Mean 2.998

Std Dev 3.509

nChipHit_F0_Fch12_Chip25

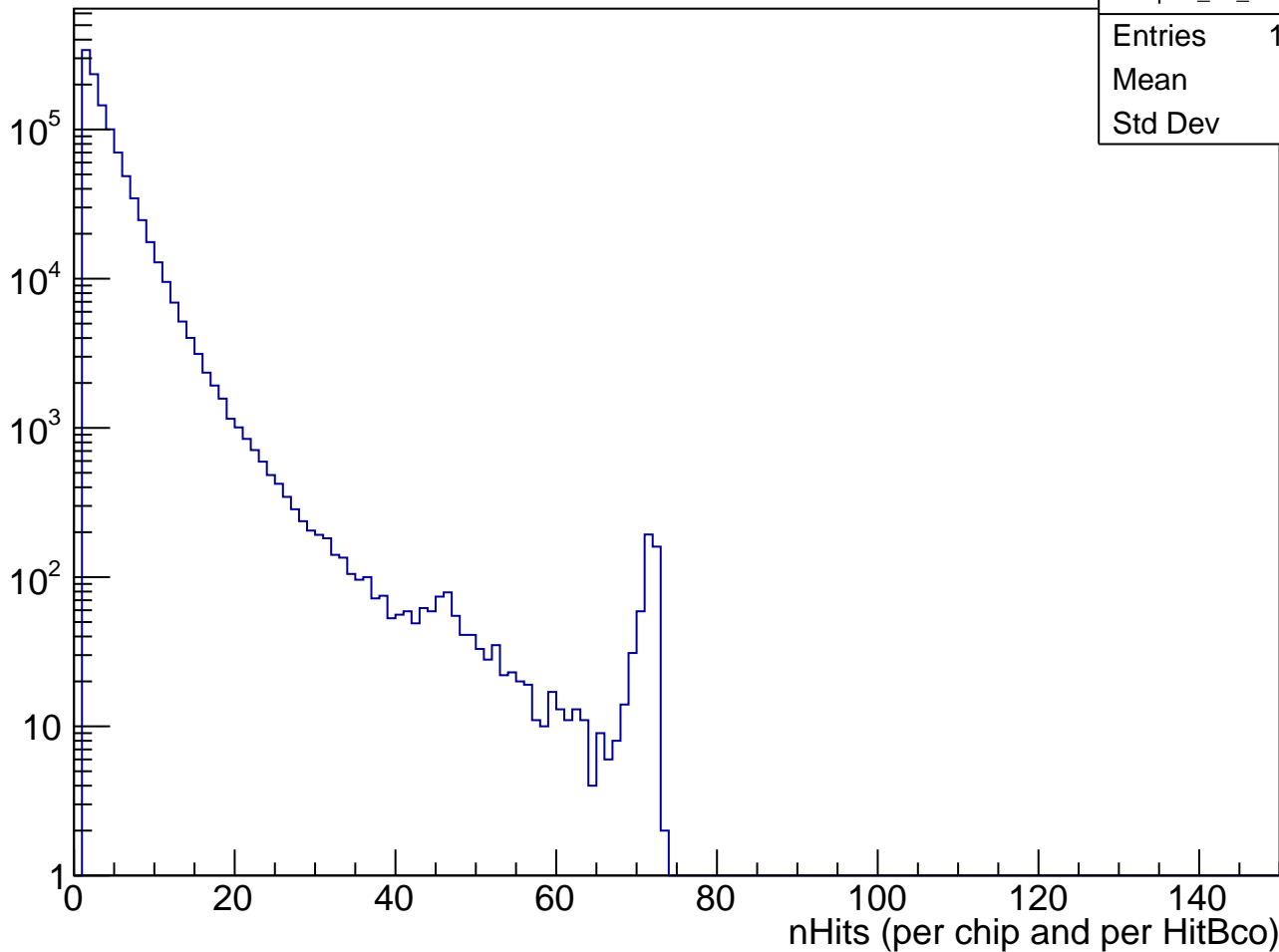
Entries



nChipHit_F0_Fch12_Chip25	
Entries	983600
Mean	3.045
Std Dev	3.513

nChipHit_F0_Fch12_Chip3

Entries



nChipHit_F0_Fch12_Chip3

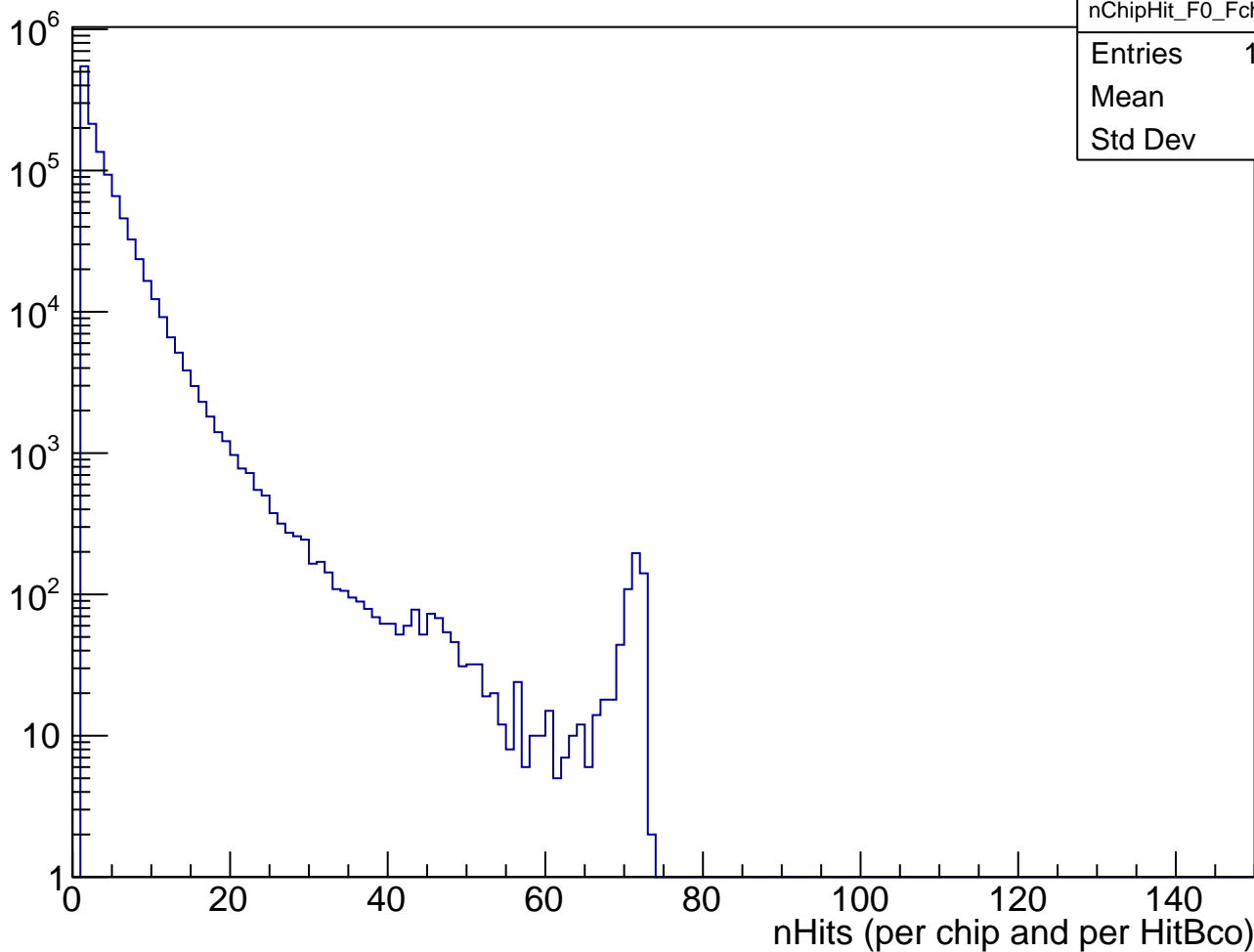
Entries 1072460

Mean 3.473

Std Dev 3.835

nChipHit_F0_Fch12_Chip4

Entries

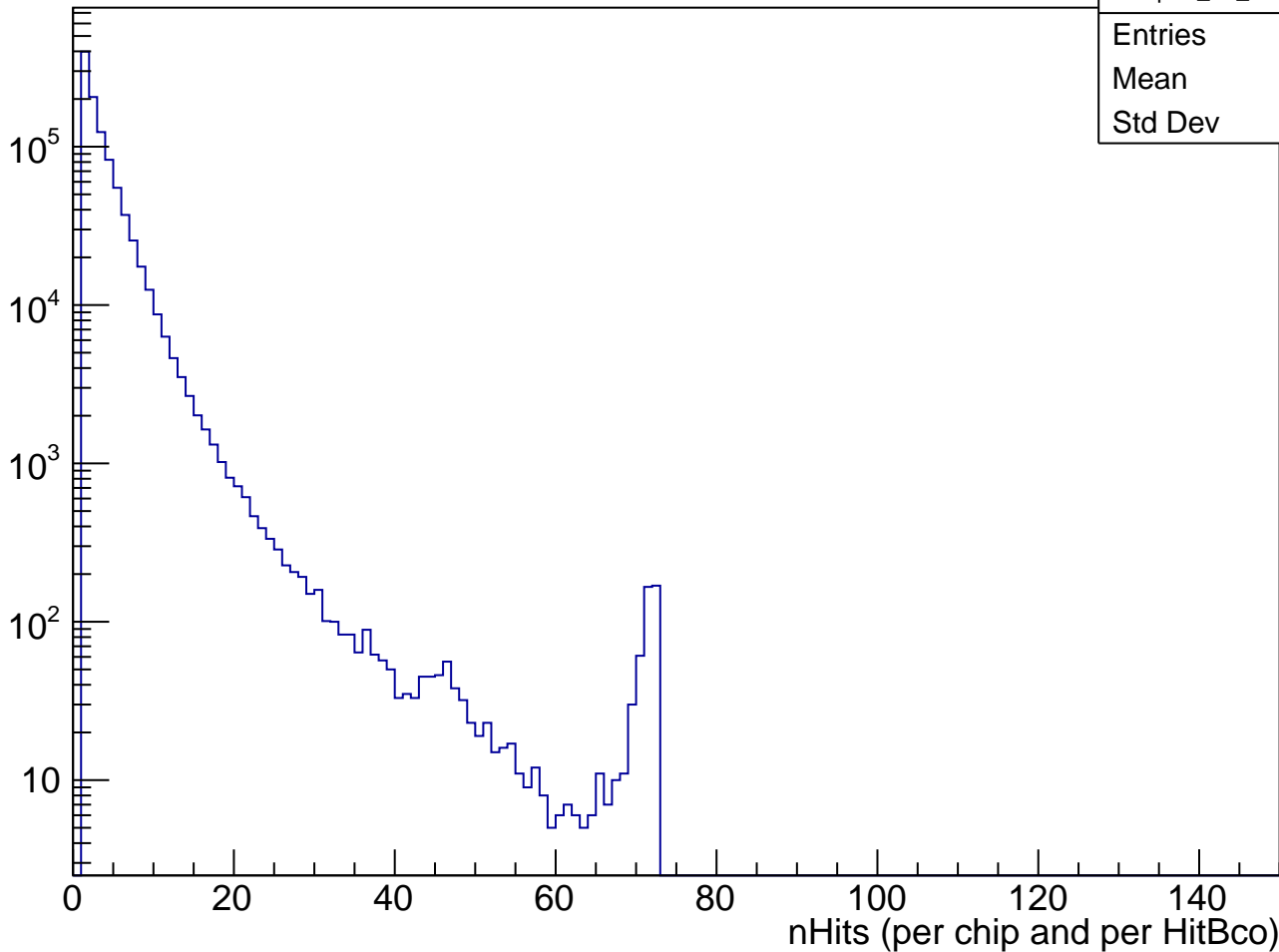


nChipHit_F0_Fch12_Chip4	
Entries	1228190
Mean	3.043
Std Dev	3.672

nHits (per chip and per HitBco)

nChipHit_F0_Fch12_Chip5

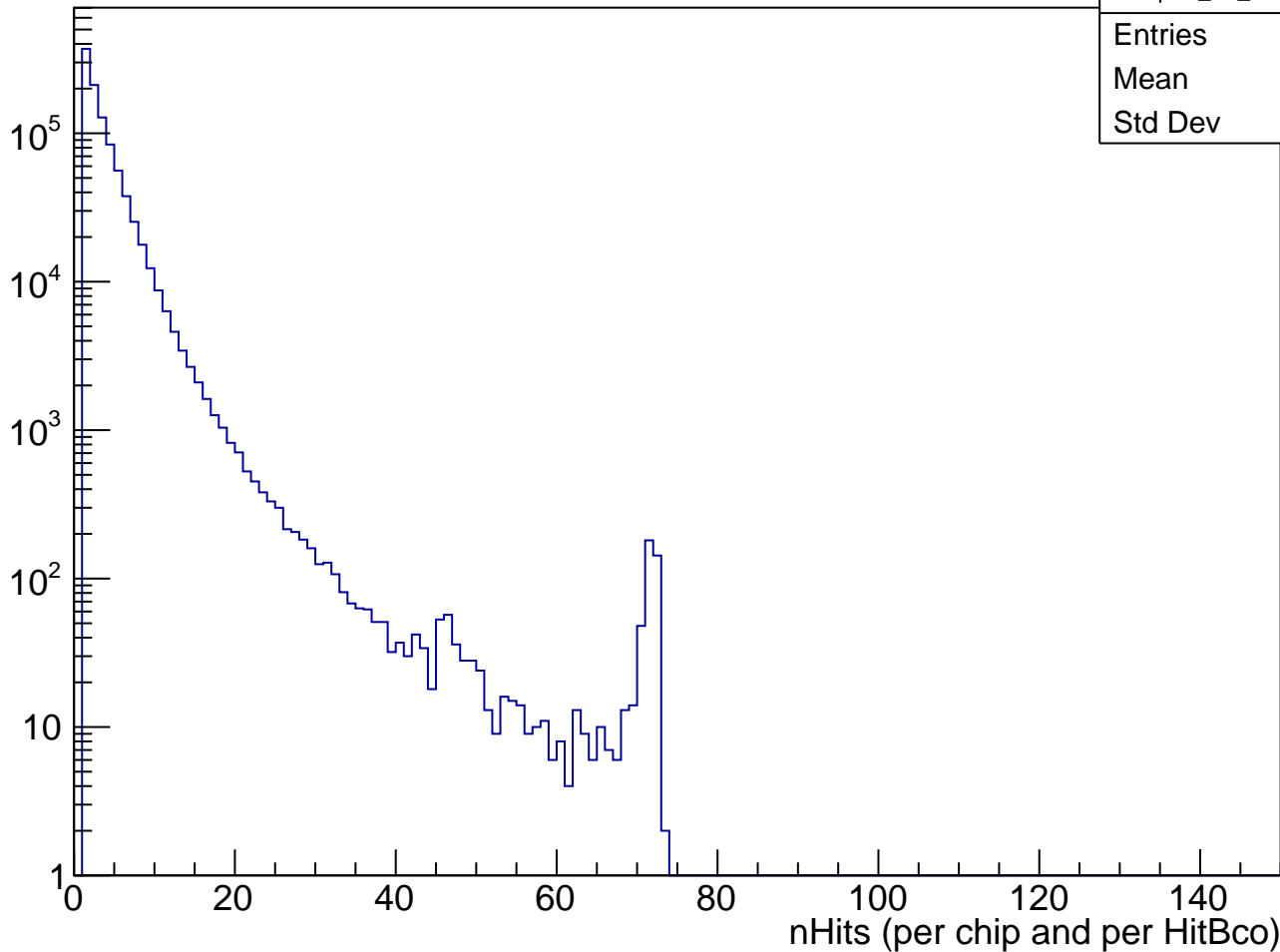
Entries



nChipHit_F0_Fch12_Chip5	
Entries	996388
Mean	3.029
Std Dev	3.527

nChipHit_F0_Fch12_Chip6

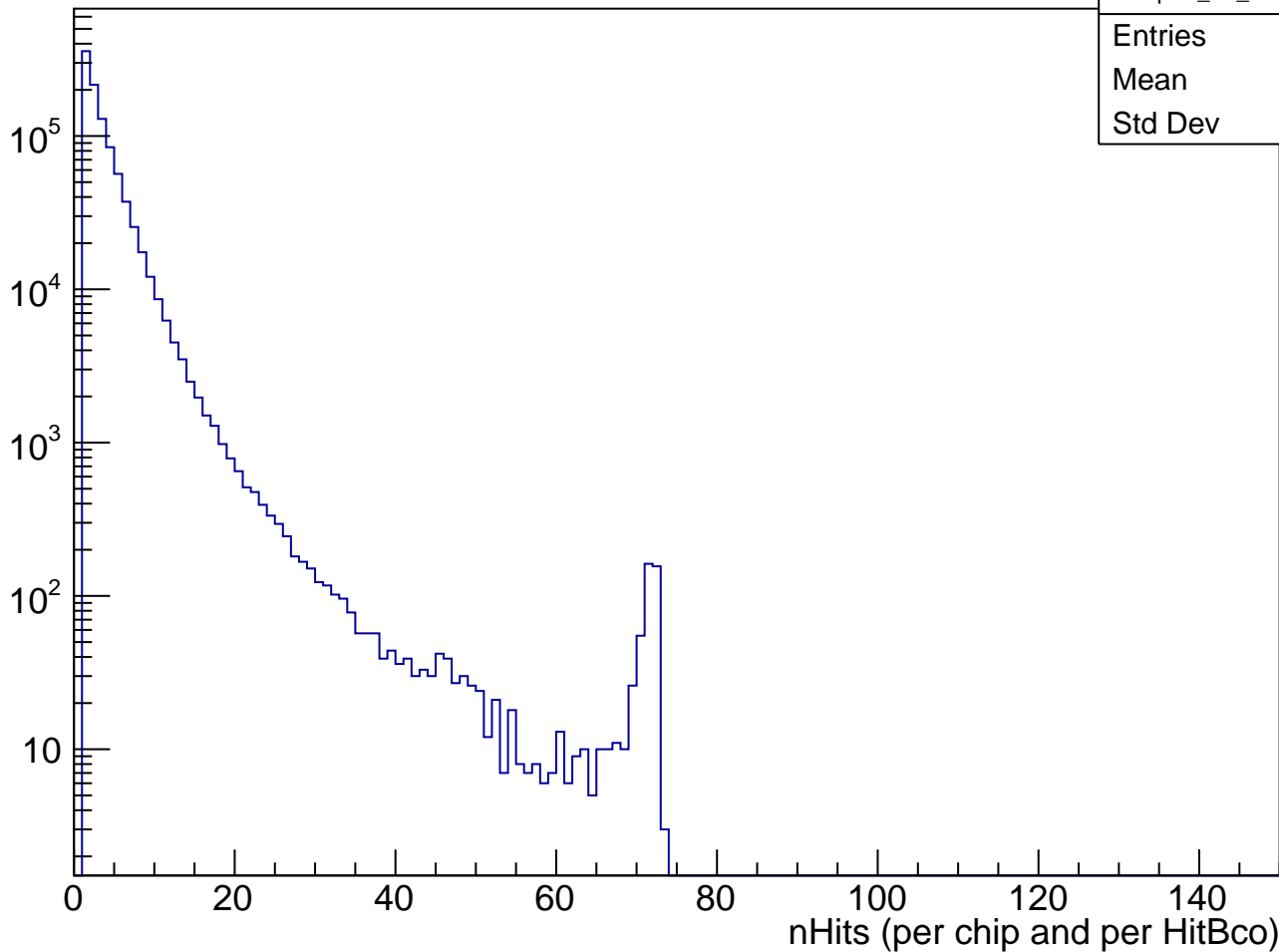
Entries



nChipHit_F0_Fch12_Chip6	
Entries	980788
Mean	3.077
Std Dev	3.492

nChipHit_F0_Fch12_Chip7

Entries

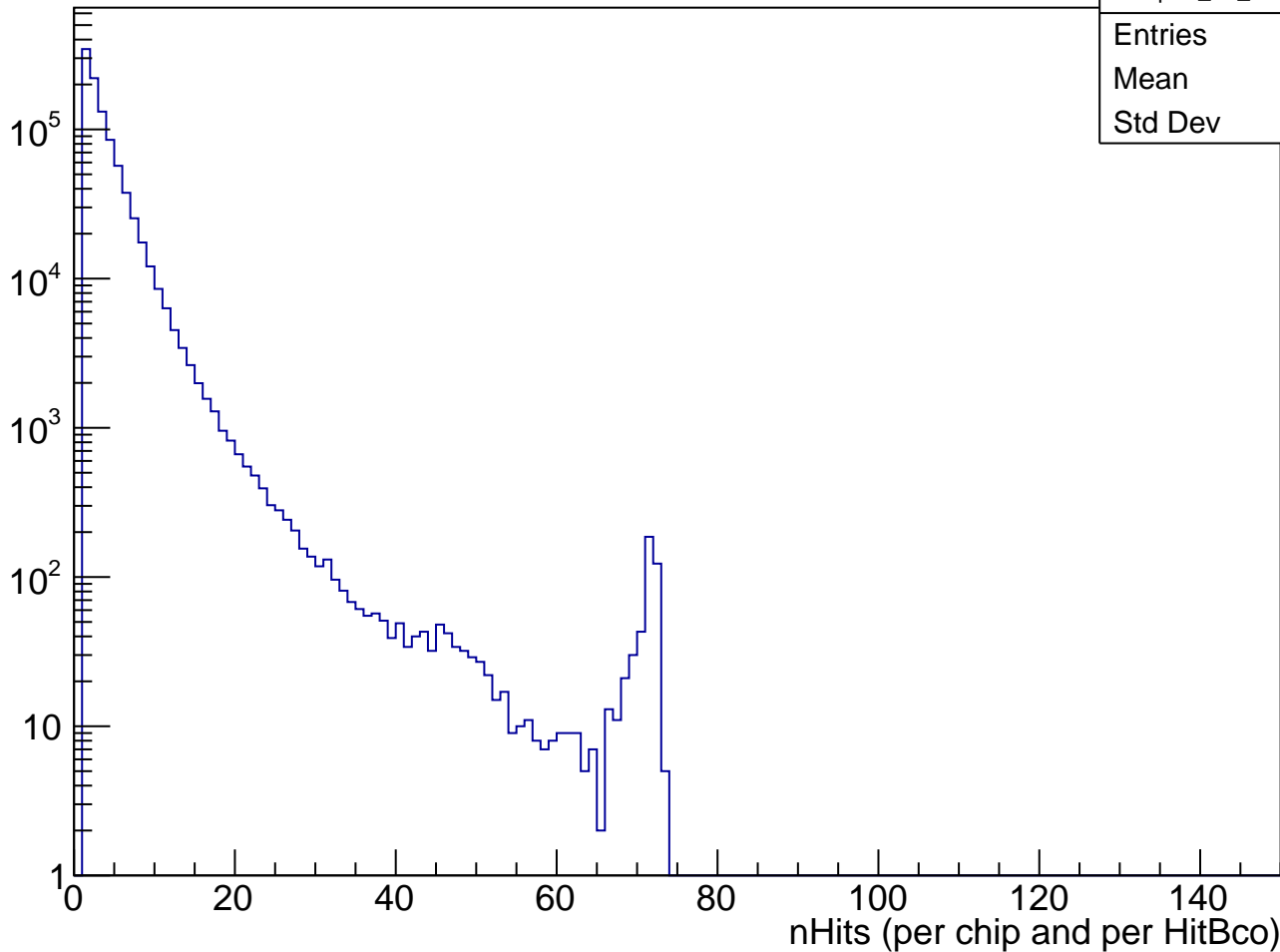


nChipHit_F0_Fch12_Chip7	
Entries	973219
Mean	3.089
Std Dev	3.481

nHits (per chip and per HitBco)

nChipHit_F0_Fch12_Chip8

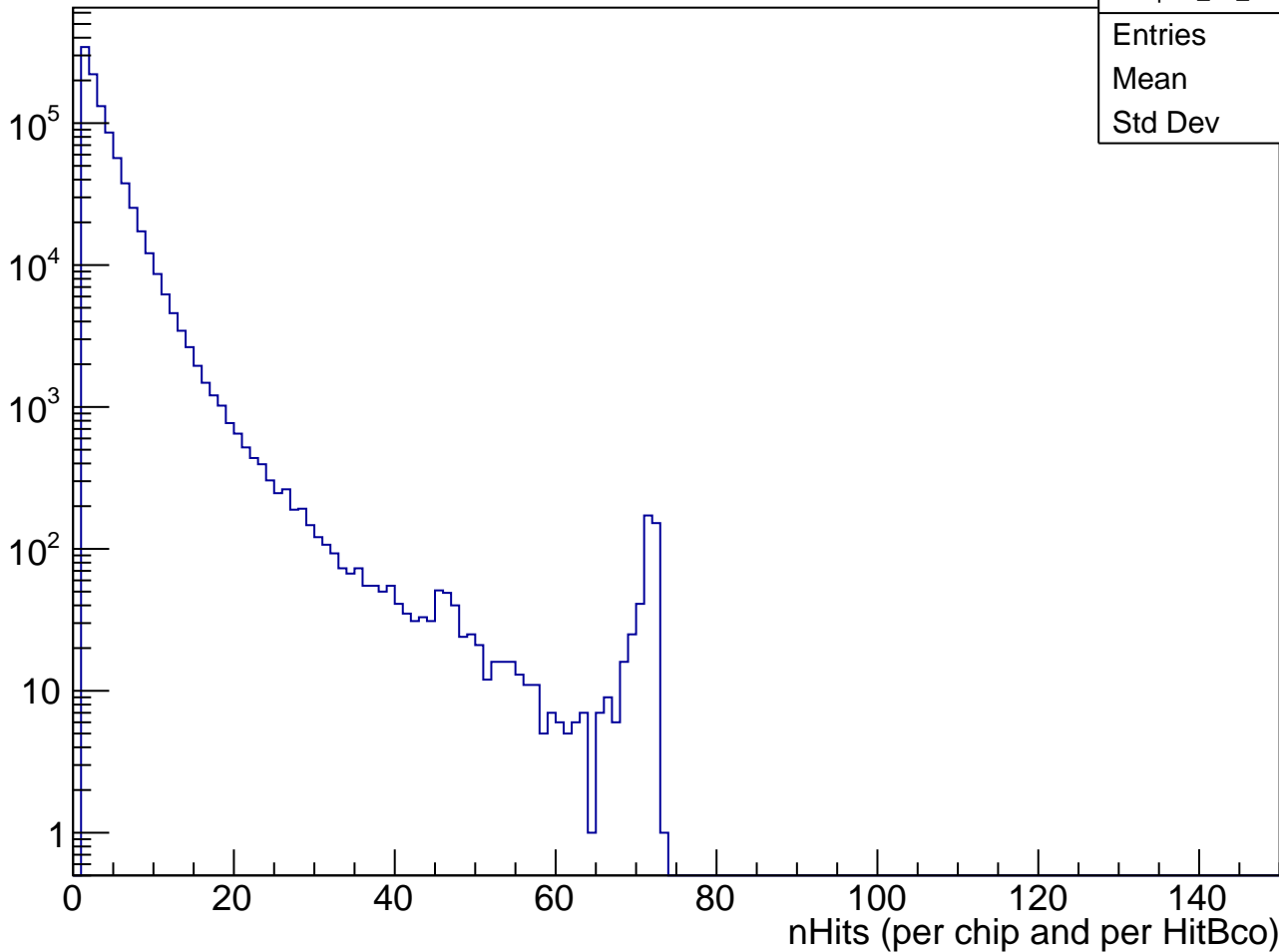
Entries



nChipHit_F0_Fch12_Chip8	
Entries	969378
Mean	3.115
Std Dev	3.493

nChipHit_F0_Fch12_Chip9

Entries

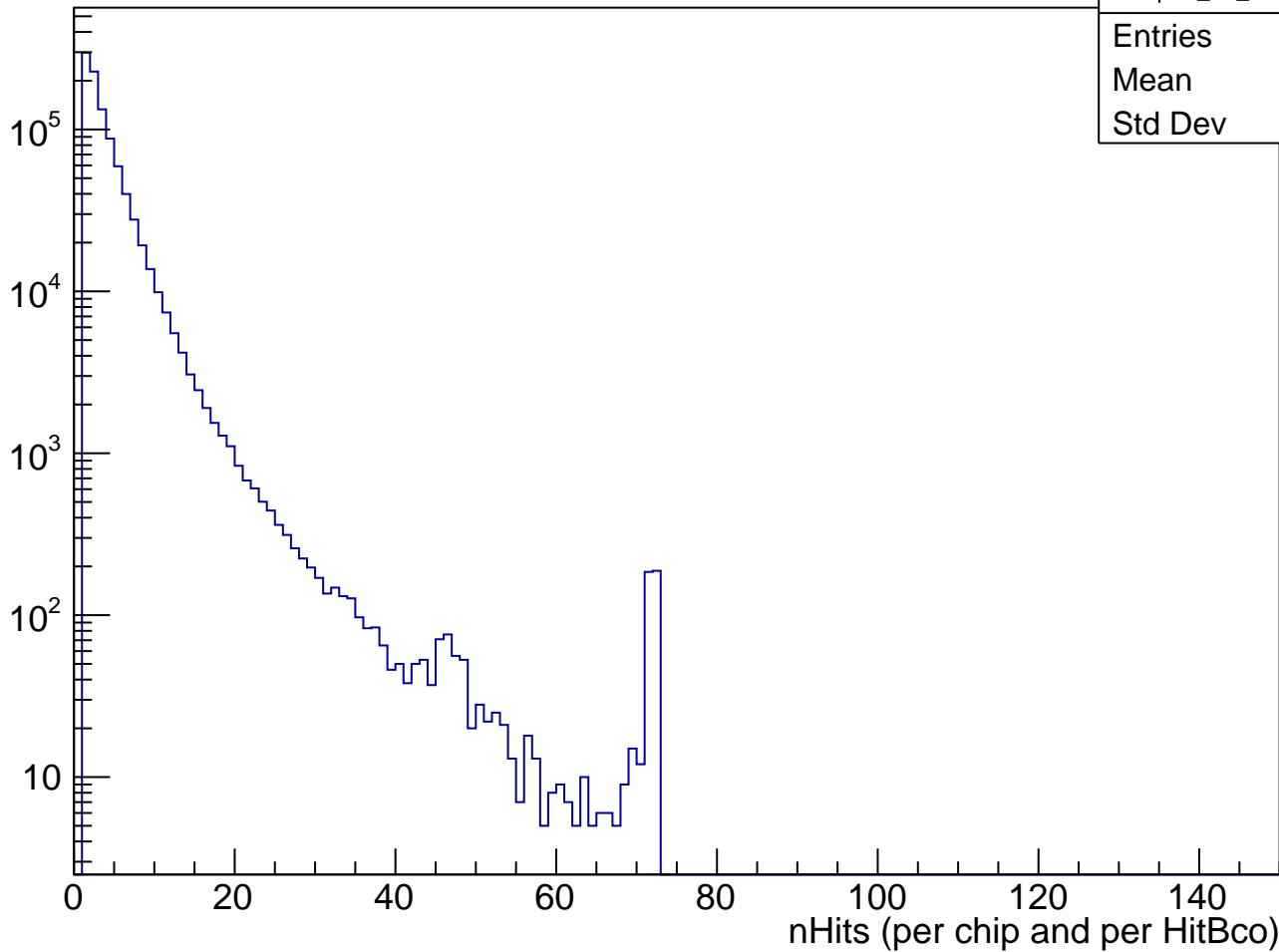


nChipHit_F0_Fch12_Chip9	
Entries	968673
Mean	3.112
Std Dev	3.471

nHits (per chip and per HitBco)

nChipHit_F0_Fch13_Chip0

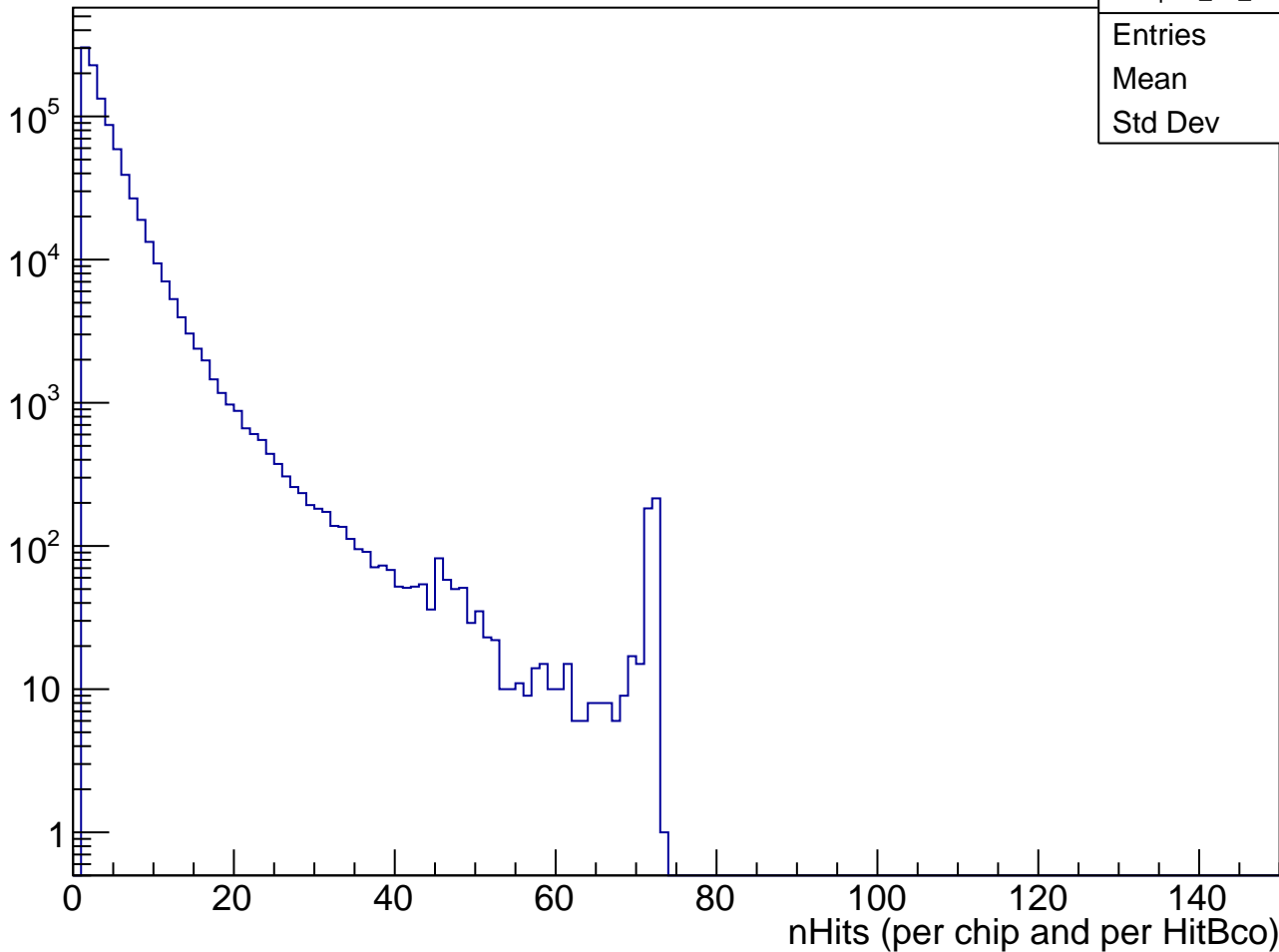
Entries



nChipHit_F0_Fch13_Chip0	
Entries	951851
Mean	3.357
Std Dev	3.75

nChipHit_F0_Fch13_Chip1

Entries

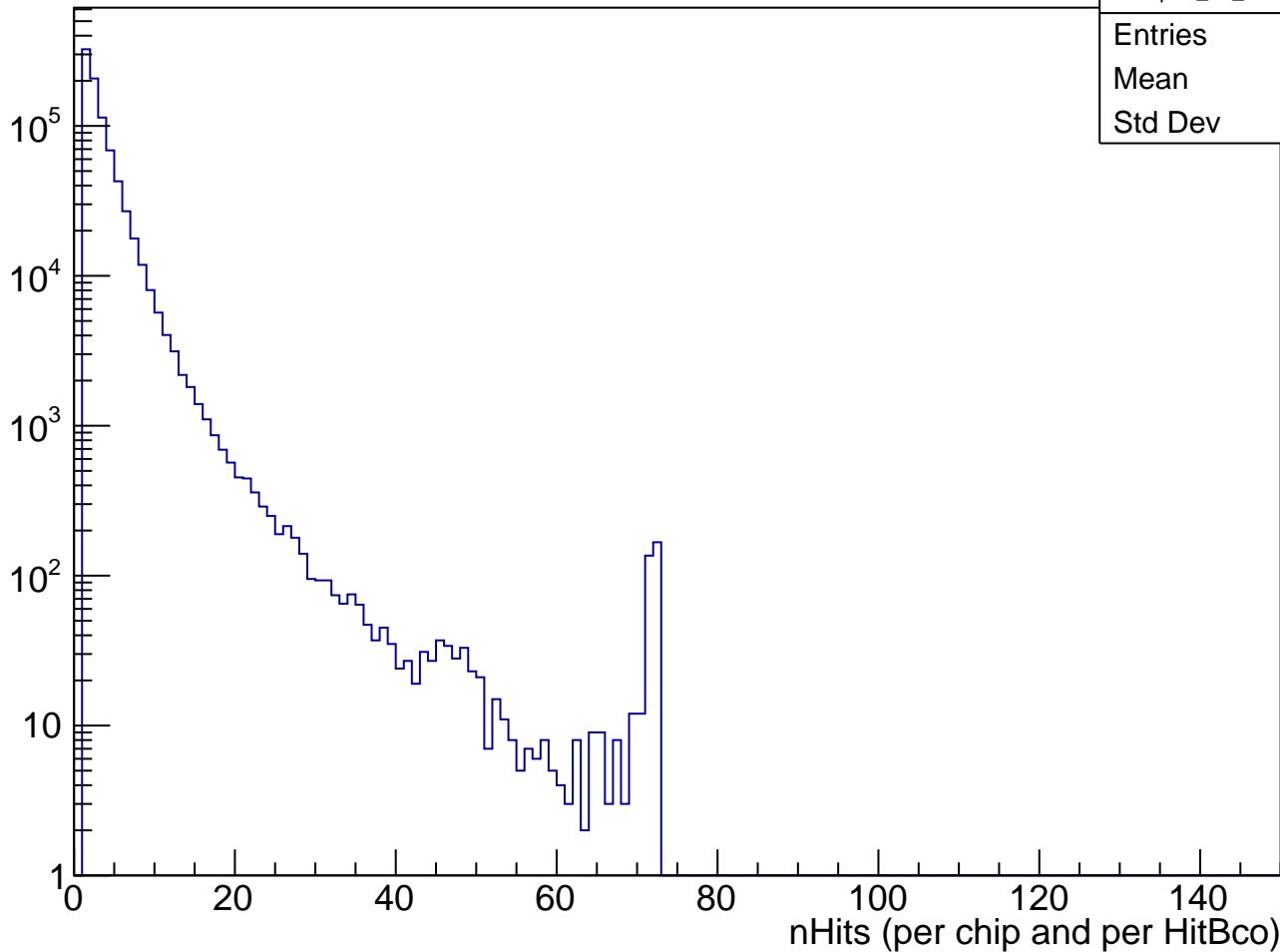


nChipHit_F0_Fch13_Chip1	
Entries	952064
Mean	3.325
Std Dev	3.766

nHits (per chip and per HitBco)

nChipHit_F0_Fch13_Chip10

Entries



nChipHit_F0_Fch13_Chip10

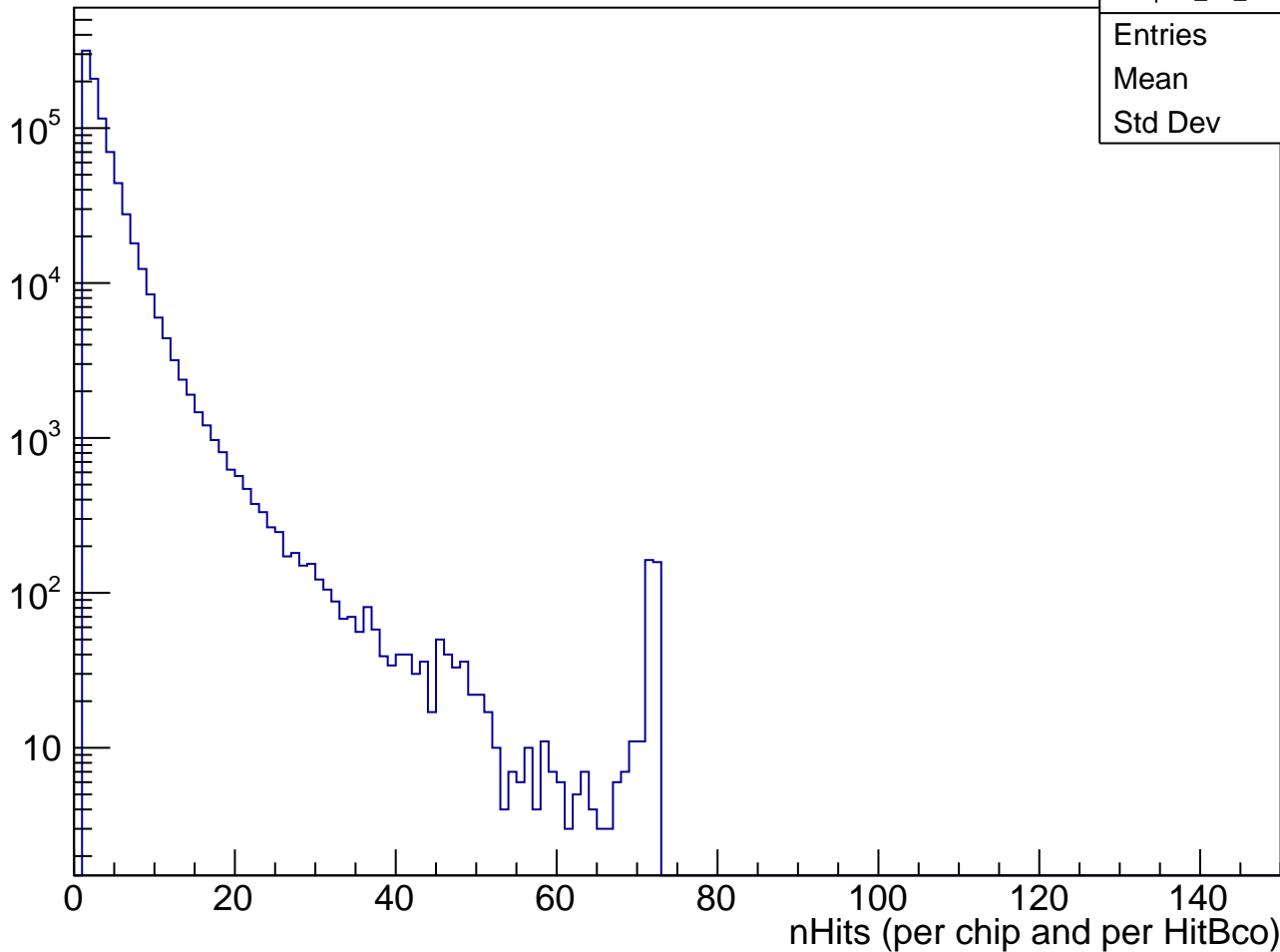
Entries 847192

Mean 2.853

Std Dev 3.269

nChipHit_F0_Fch13_Chip11

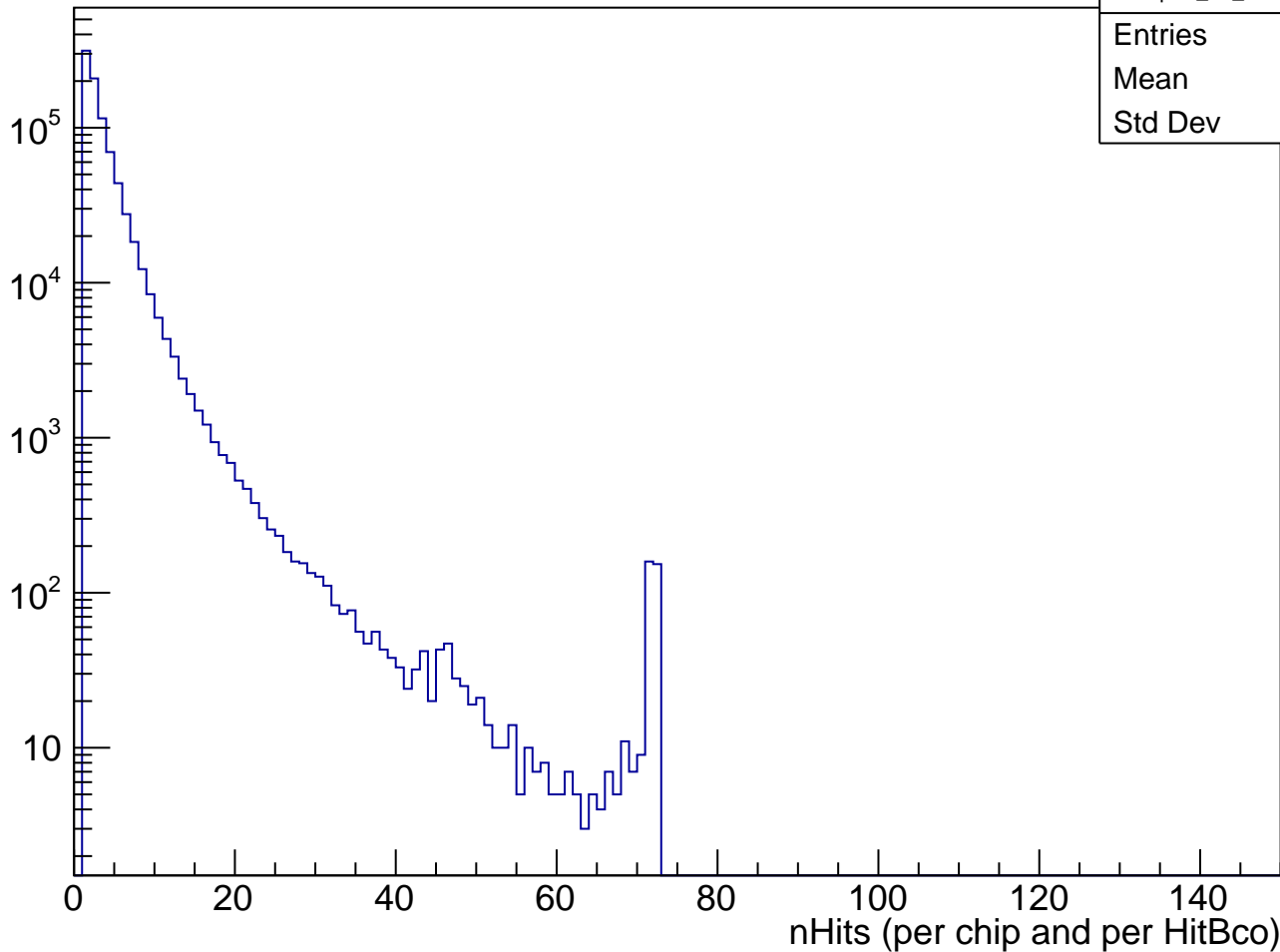
Entries



nChipHit_F0_Fch13_Chip11	
Entries	847920
Mean	2.919
Std Dev	3.367

nChipHit_F0_Fch13_Chip12

Entries



nChipHit_F0_Fch13_Chip12

Entries 844724

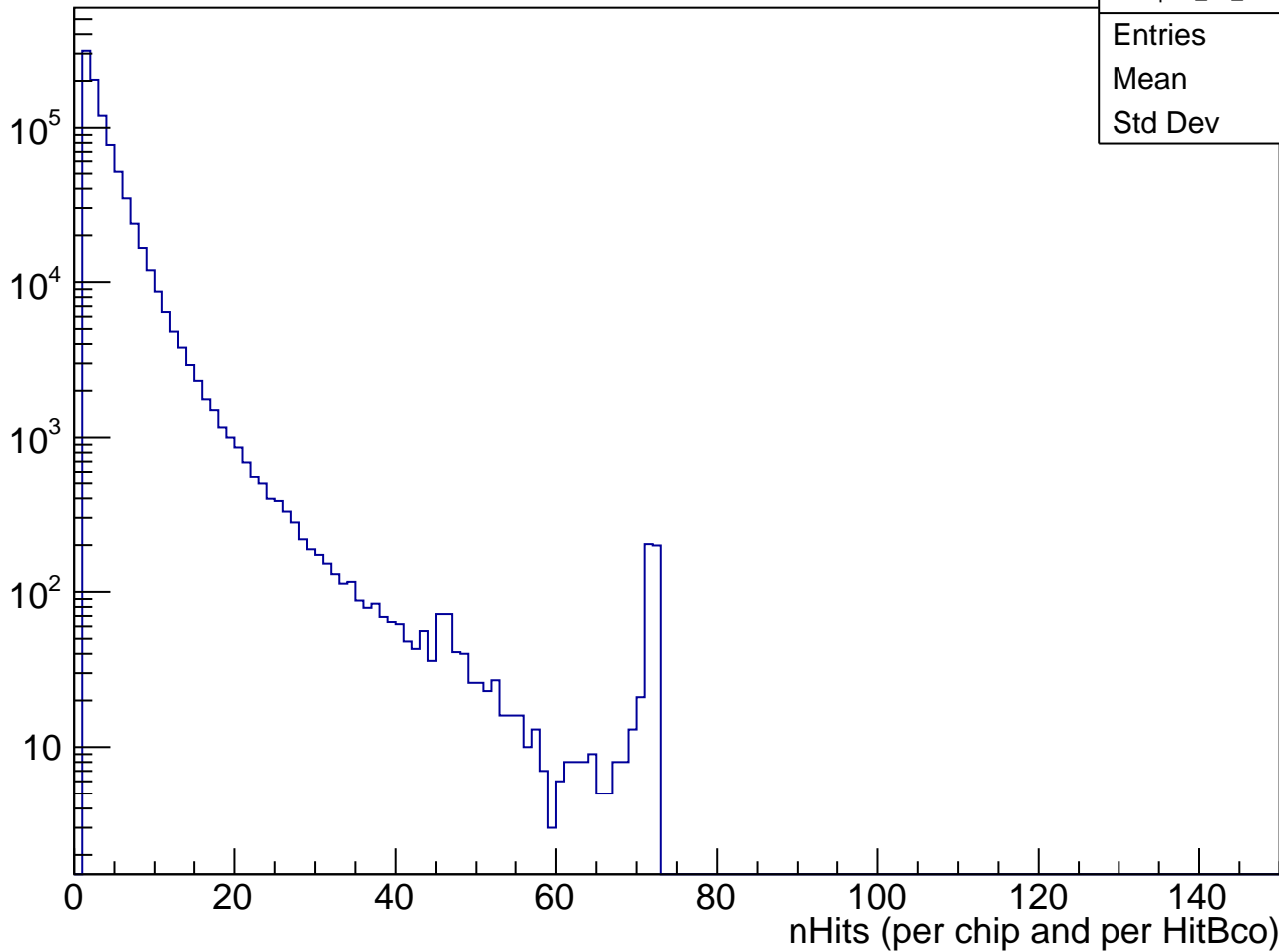
Mean 2.92

Std Dev 3.351

nHits (per chip and per HitBco)

nChipHit_F0_Fch13_Chip13

Entries



nChipHit_F0_Fch13_Chip13

Entries 892692

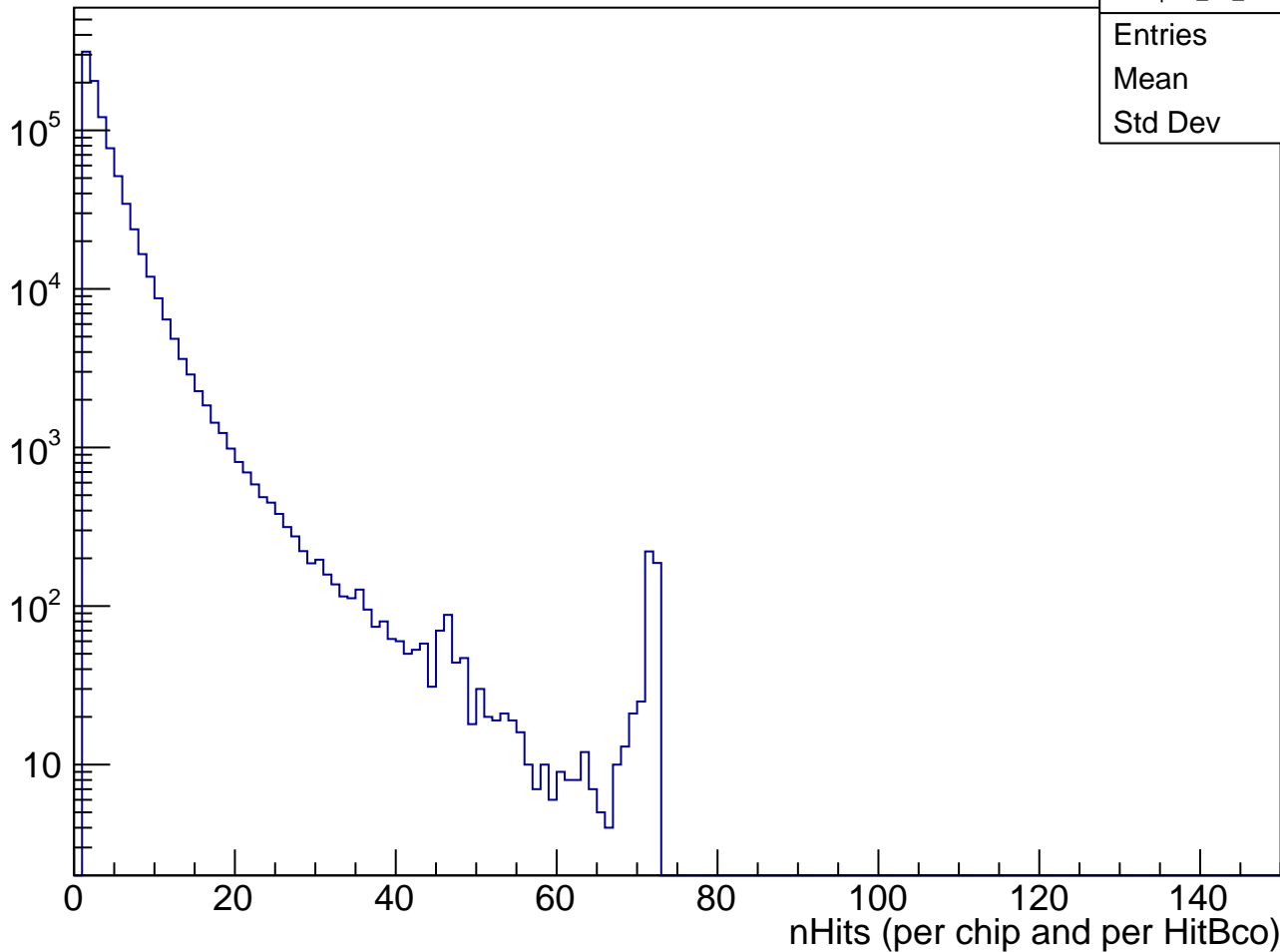
Mean 3.246

Std Dev 3.814

nHits (per chip and per HitBco)

nChipHit_F0_Fch13_Chip14

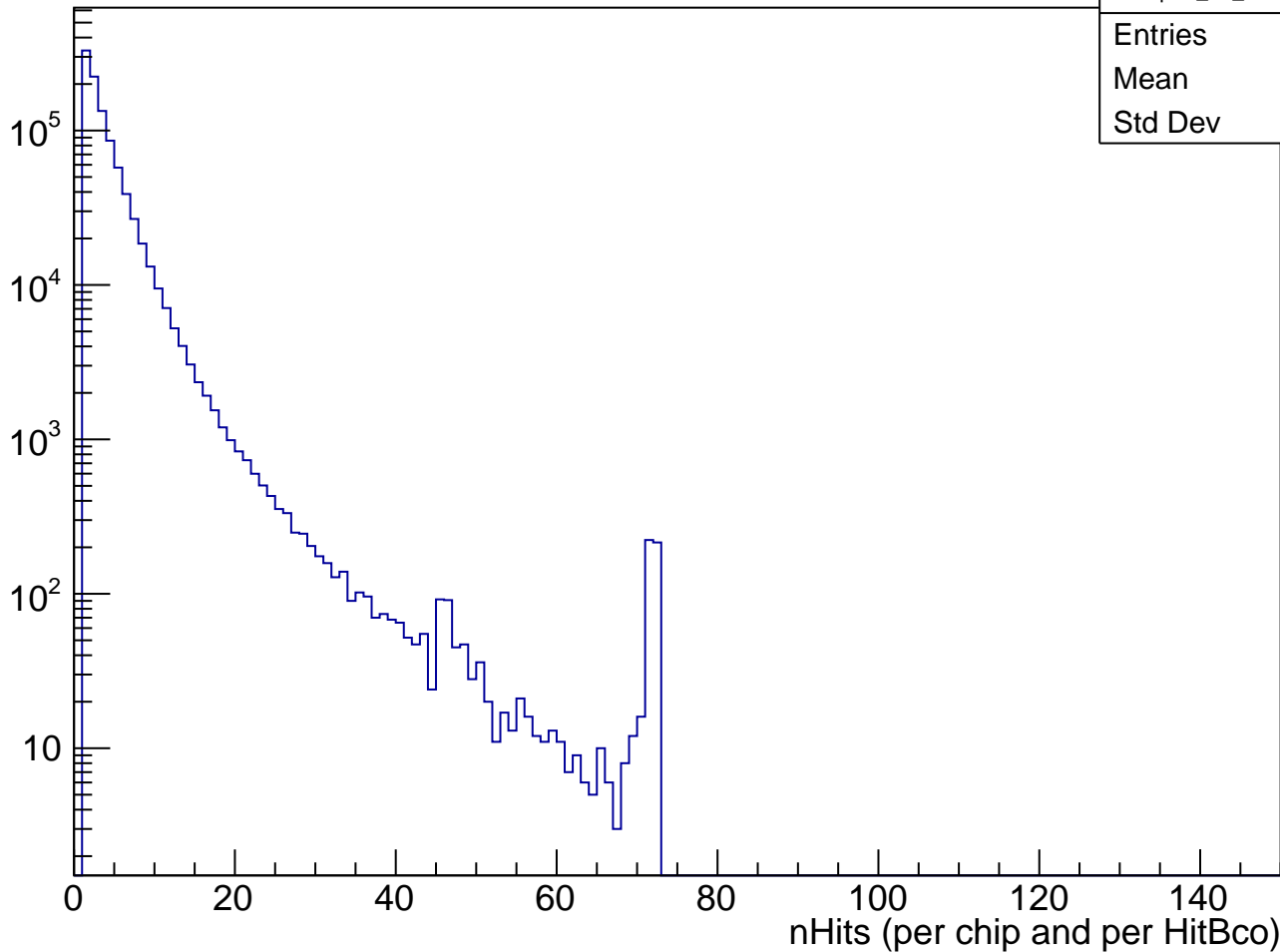
Entries



nChipHit_F0_Fch13_Chip14	
Entries	895749
Mean	3.245
Std Dev	3.842

nChipHit_F0_Fch13_Chip15

Entries



nChipHit_F0_Fch13_Chip15

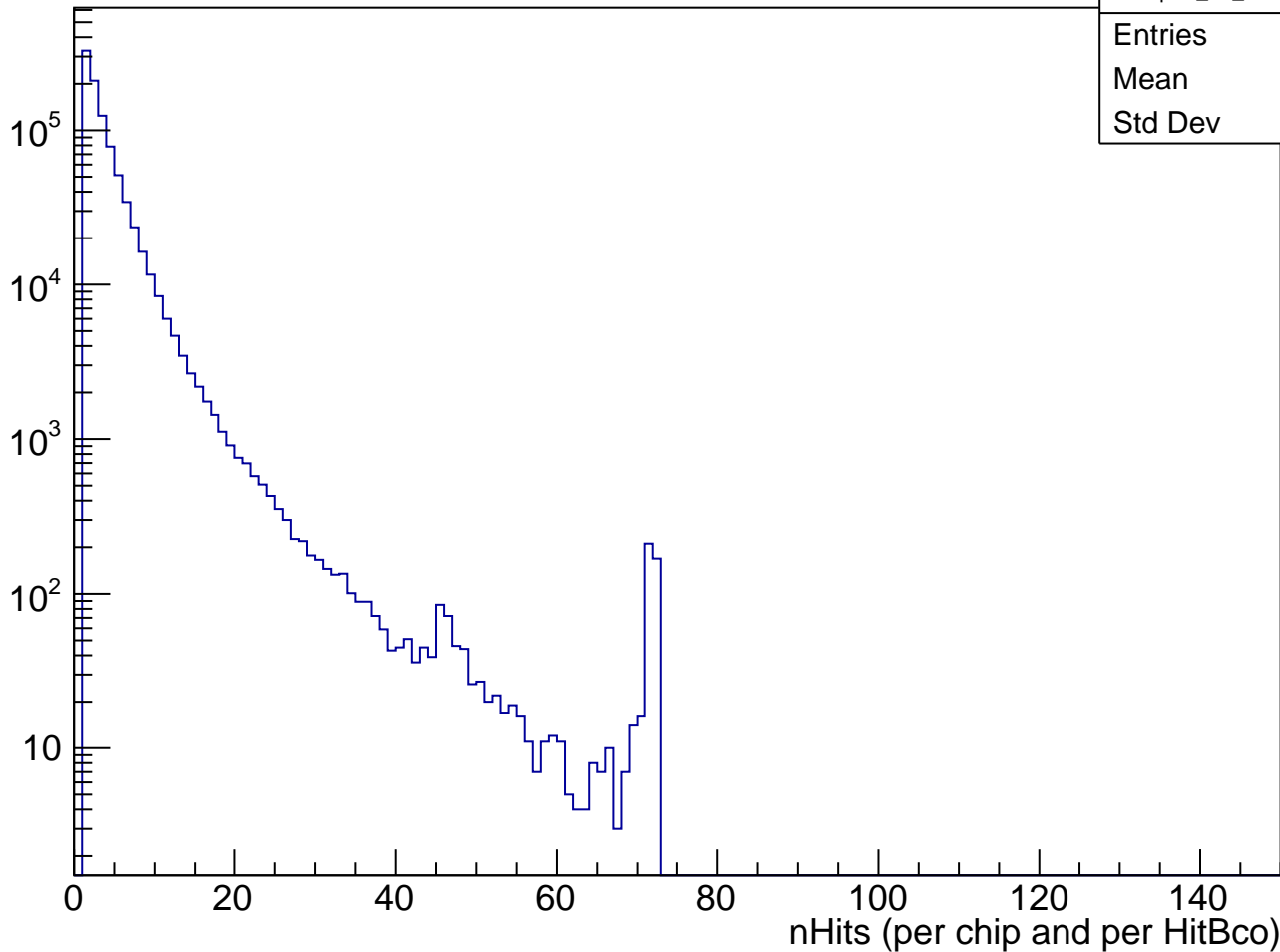
Entries 971426

Mean 3.265

Std Dev 3.77

nChipHit_F0_Fch13_Chip16

Entries



nChipHit_F0_Fch13_Chip16

Entries 915840

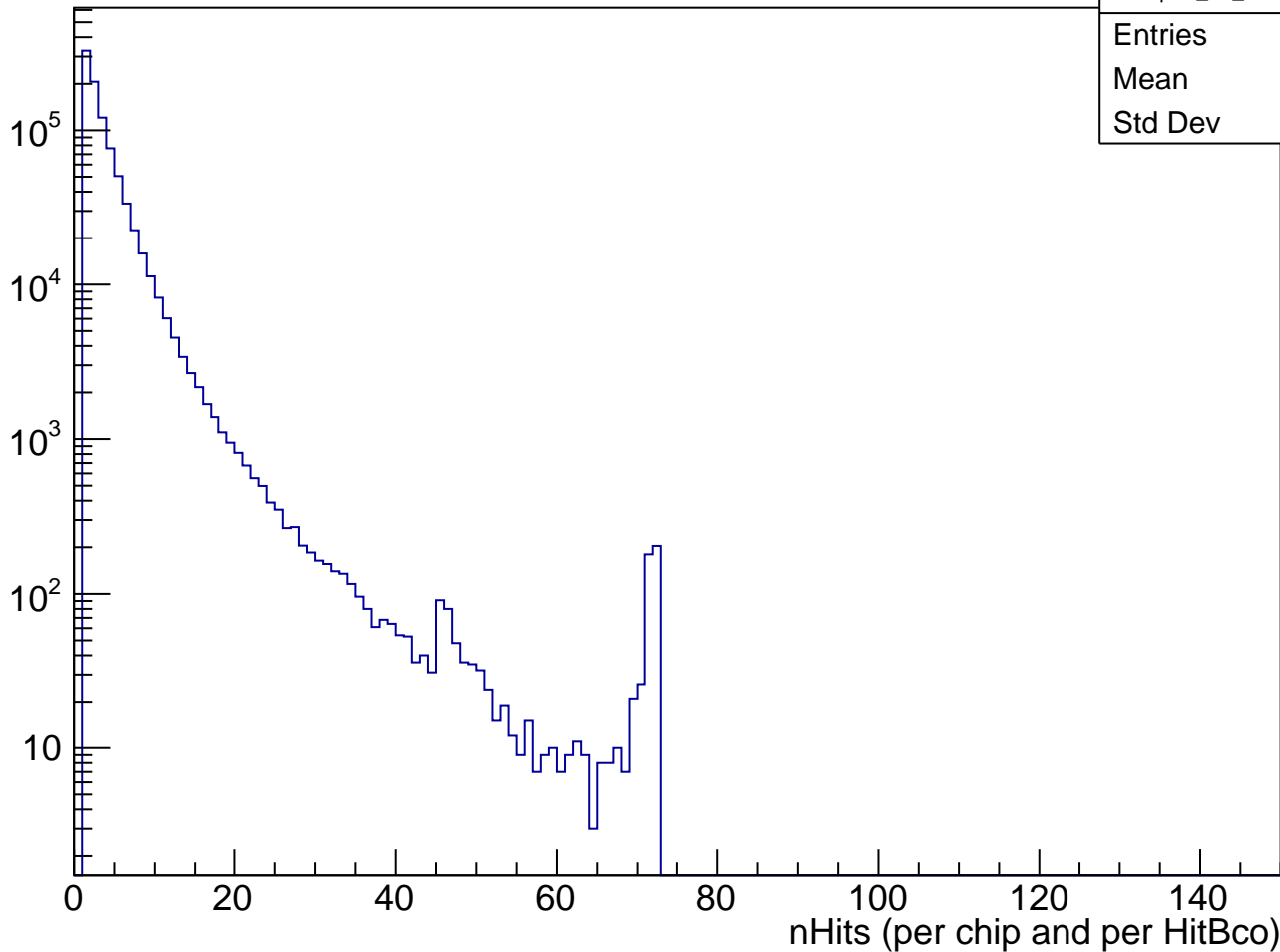
Mean 3.168

Std Dev 3.712

nHits (per chip and per HitBco)

nChipHit_F0_Fch13_Chip17

Entries



nChipHit_F0_Fch13_Chip17

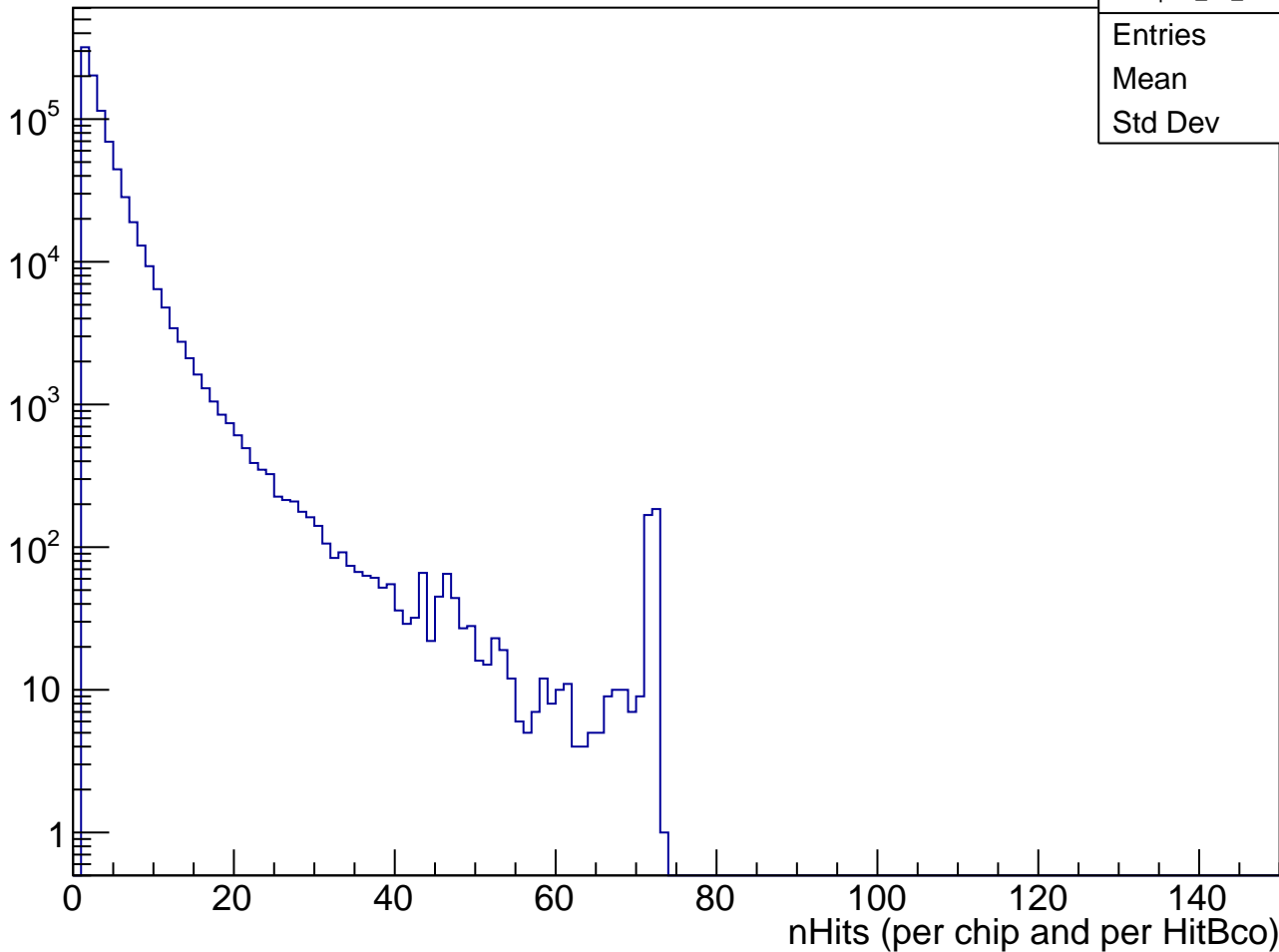
Entries 903479

Mean 3.158

Std Dev 3.751

nChipHit_F0_Fch13_Chip18

Entries



nChipHit_F0_Fch13_Chip18

Entries 849247

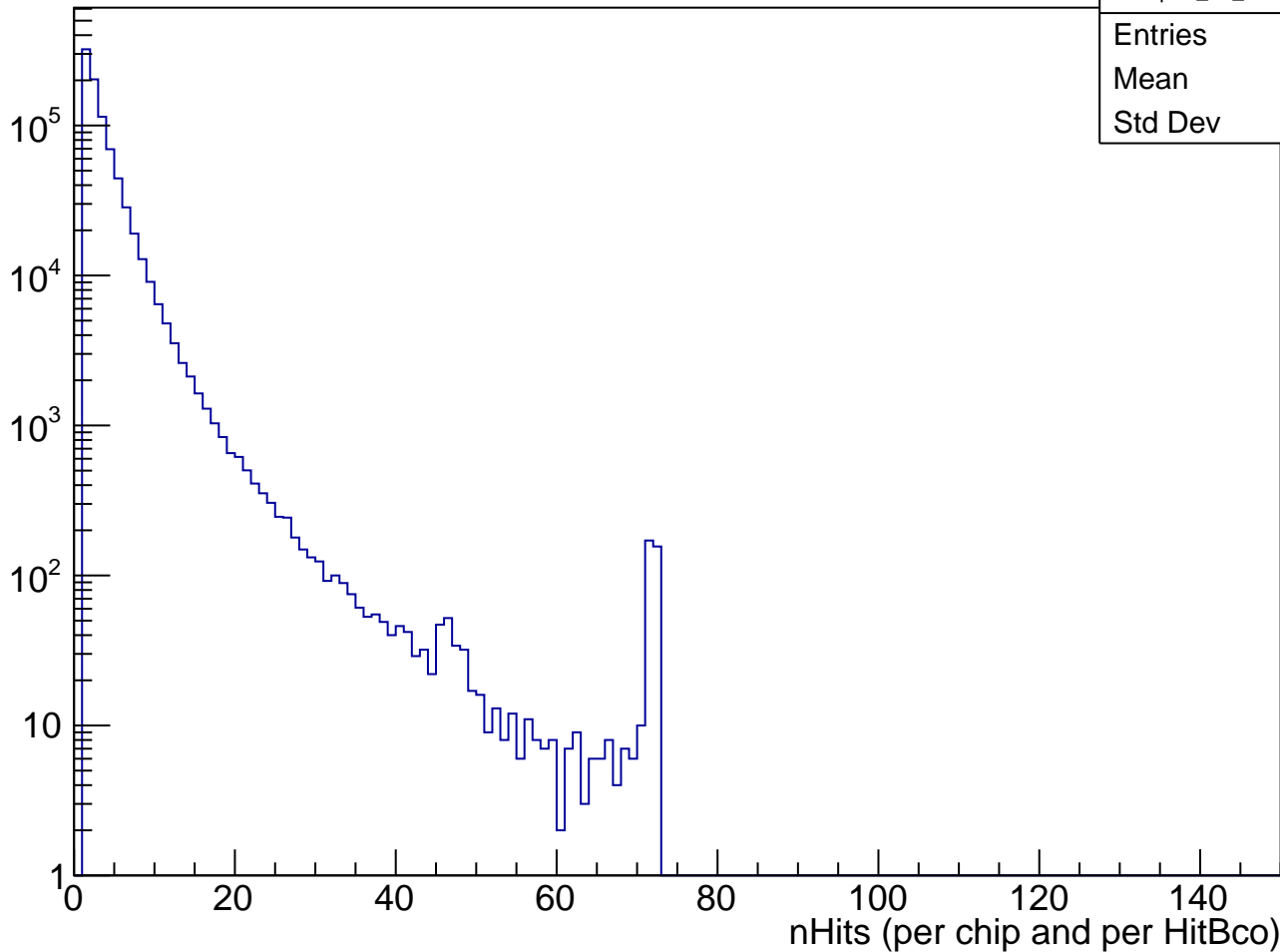
Mean 2.977

Std Dev 3.511

nHits (per chip and per HitBco)

nChipHit_F0_Fch13_Chip19

Entries



nChipHit_F0_Fch13_Chip19

Entries 852494

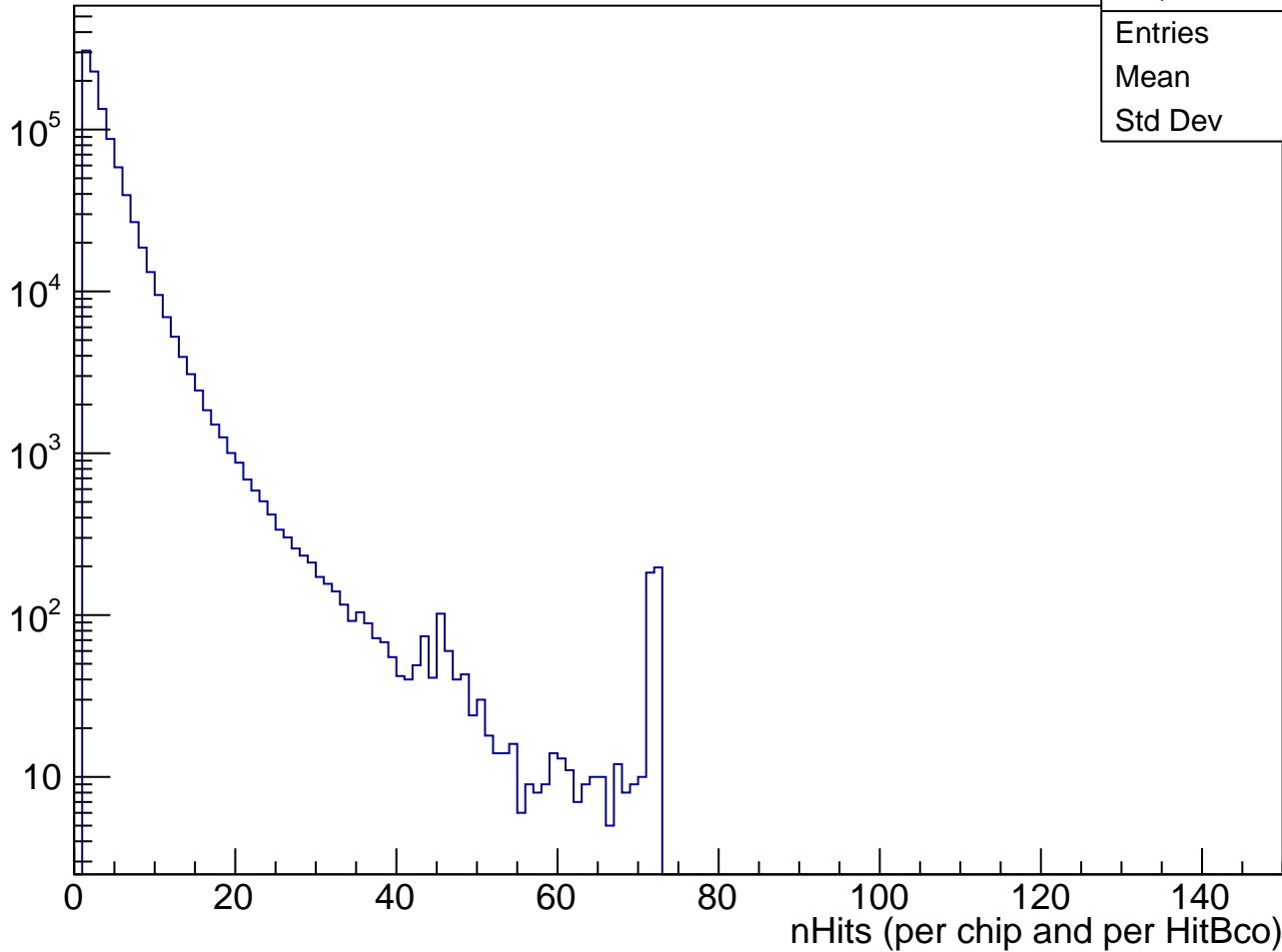
Mean 2.955

Std Dev 3.43

nHits (per chip and per HitBco)

nChipHit_F0_Fch13_Chip2

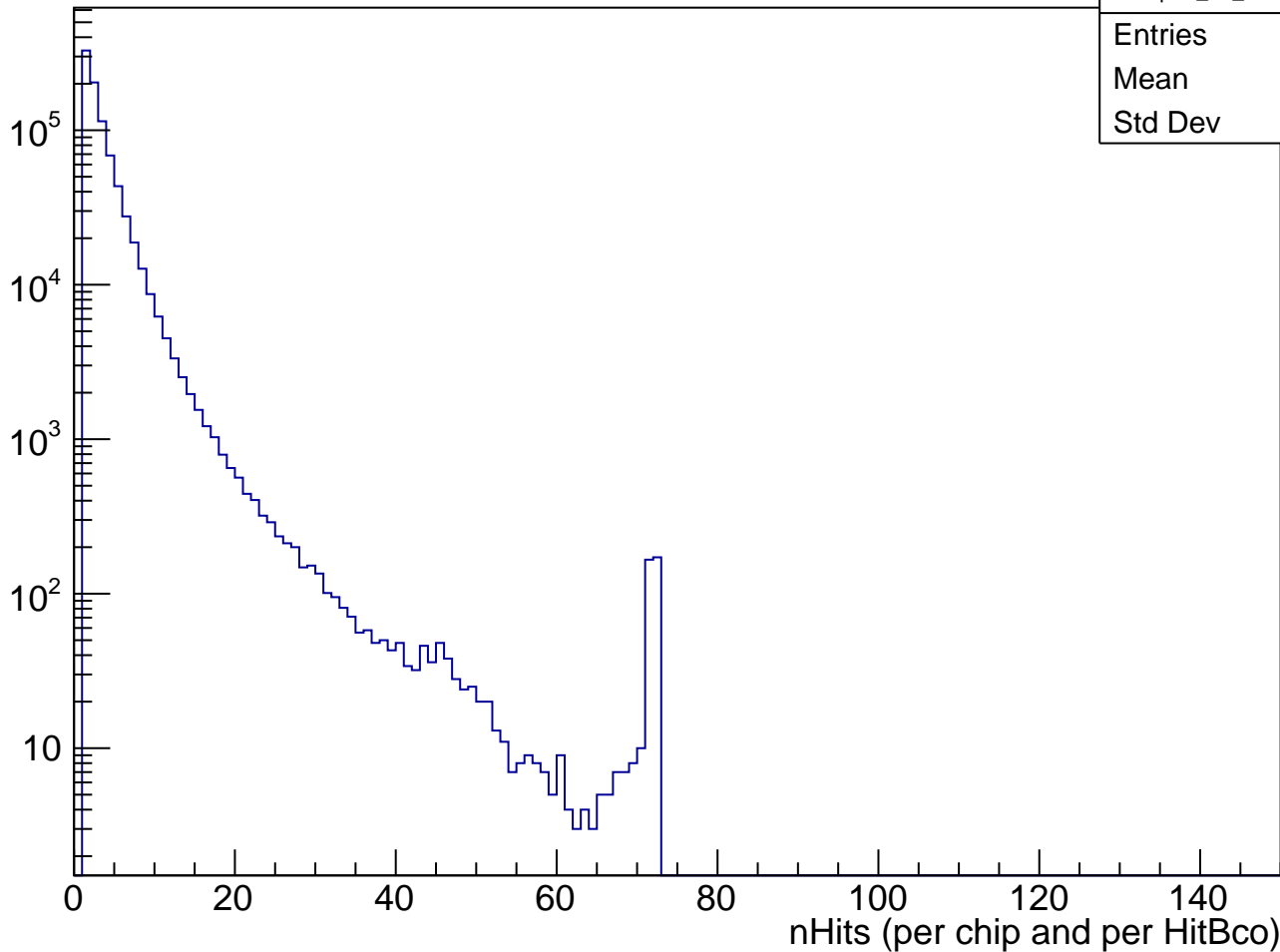
Entries



nChipHit_F0_Fch13_Chip2	
Entries	957162
Mean	3.305
Std Dev	3.723

nChipHit_F0_Fch13_Chip20

Entries



nChipHit_F0_Fch13_Chip20

Entries 854617

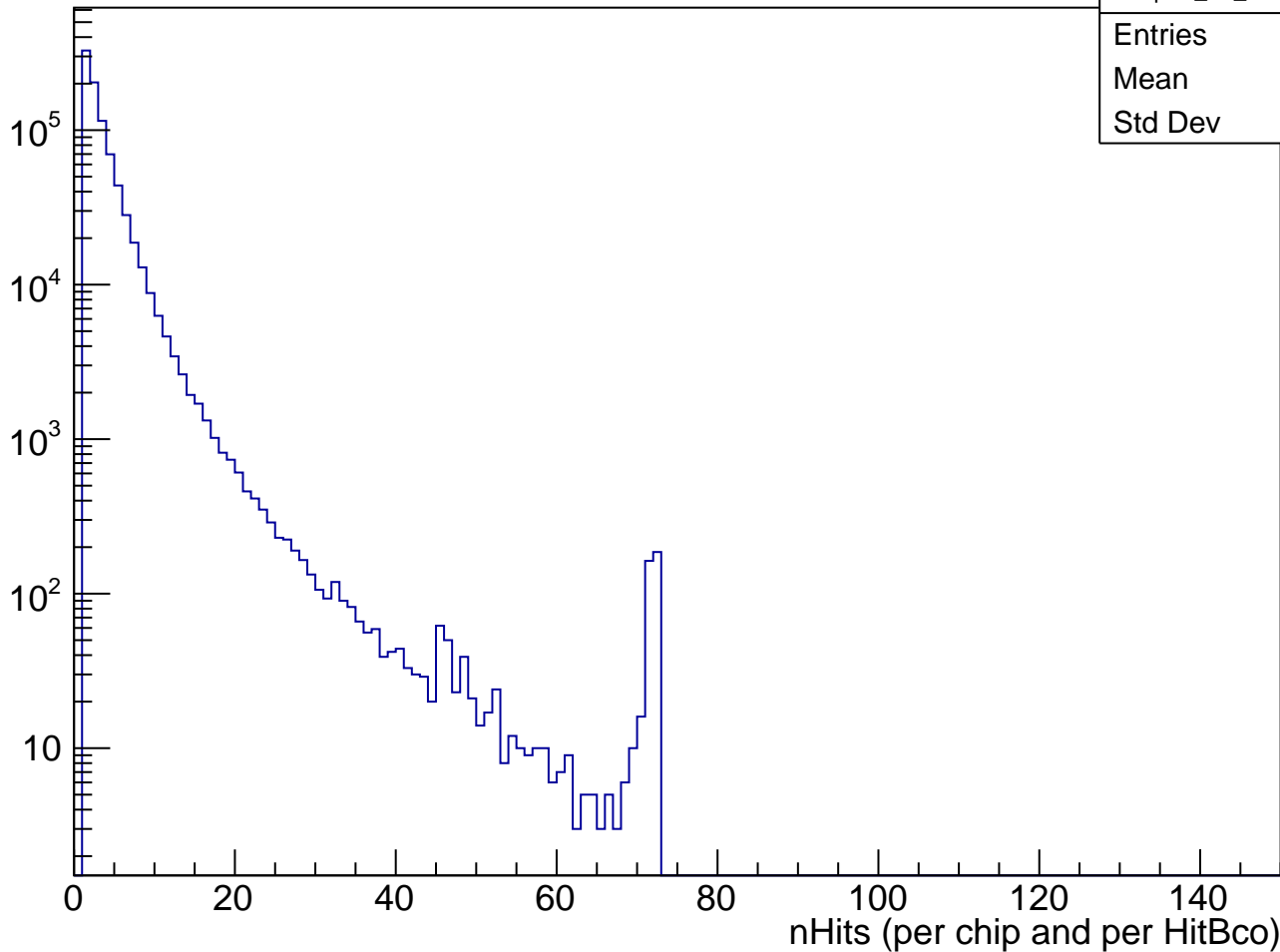
Mean 2.914

Std Dev 3.407

nHits (per chip and per HitBco)

nChipHit_F0_Fch13_Chip21

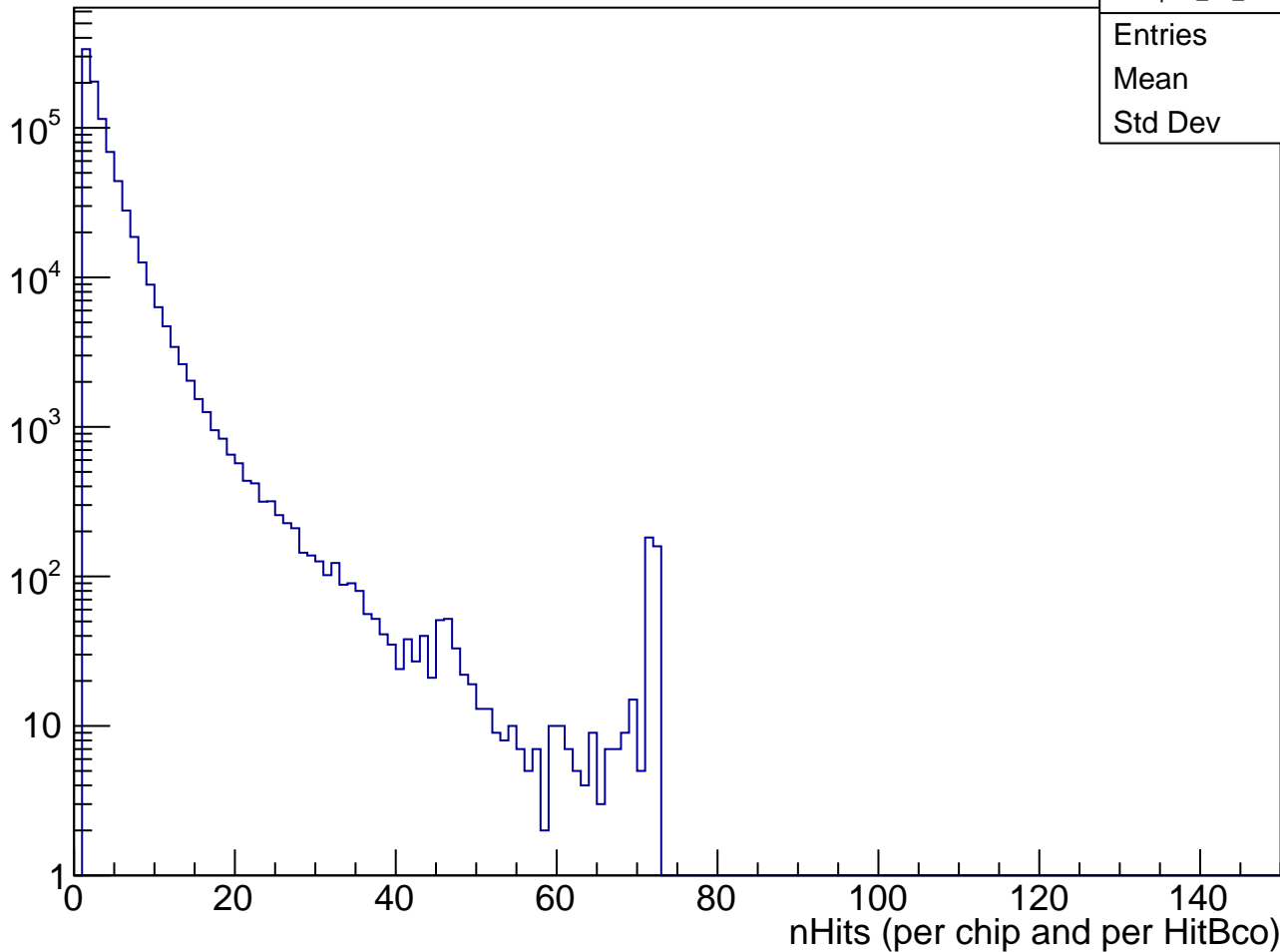
Entries



nChipHit_F0_Fch13_Chip21	
Entries	857664
Mean	2.936
Std Dev	3.446

nChipHit_F0_Fch13_Chip22

Entries



nChipHit_F0_Fch13_Chip22

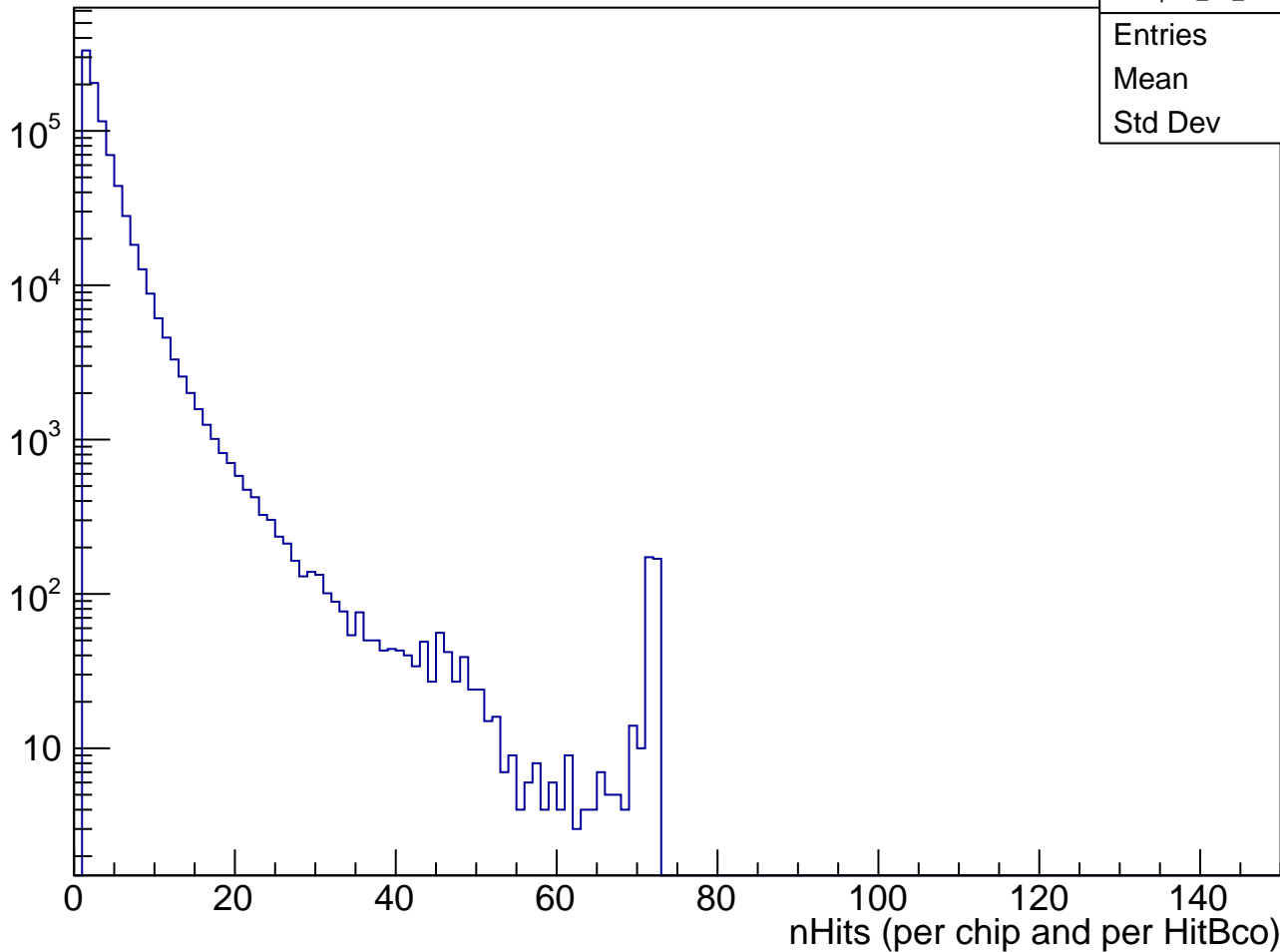
Entries 864923

Mean 2.908

Std Dev 3.409

nChipHit_F0_Fch13_Chip23

Entries



nChipHit_F0_Fch13_Chip23

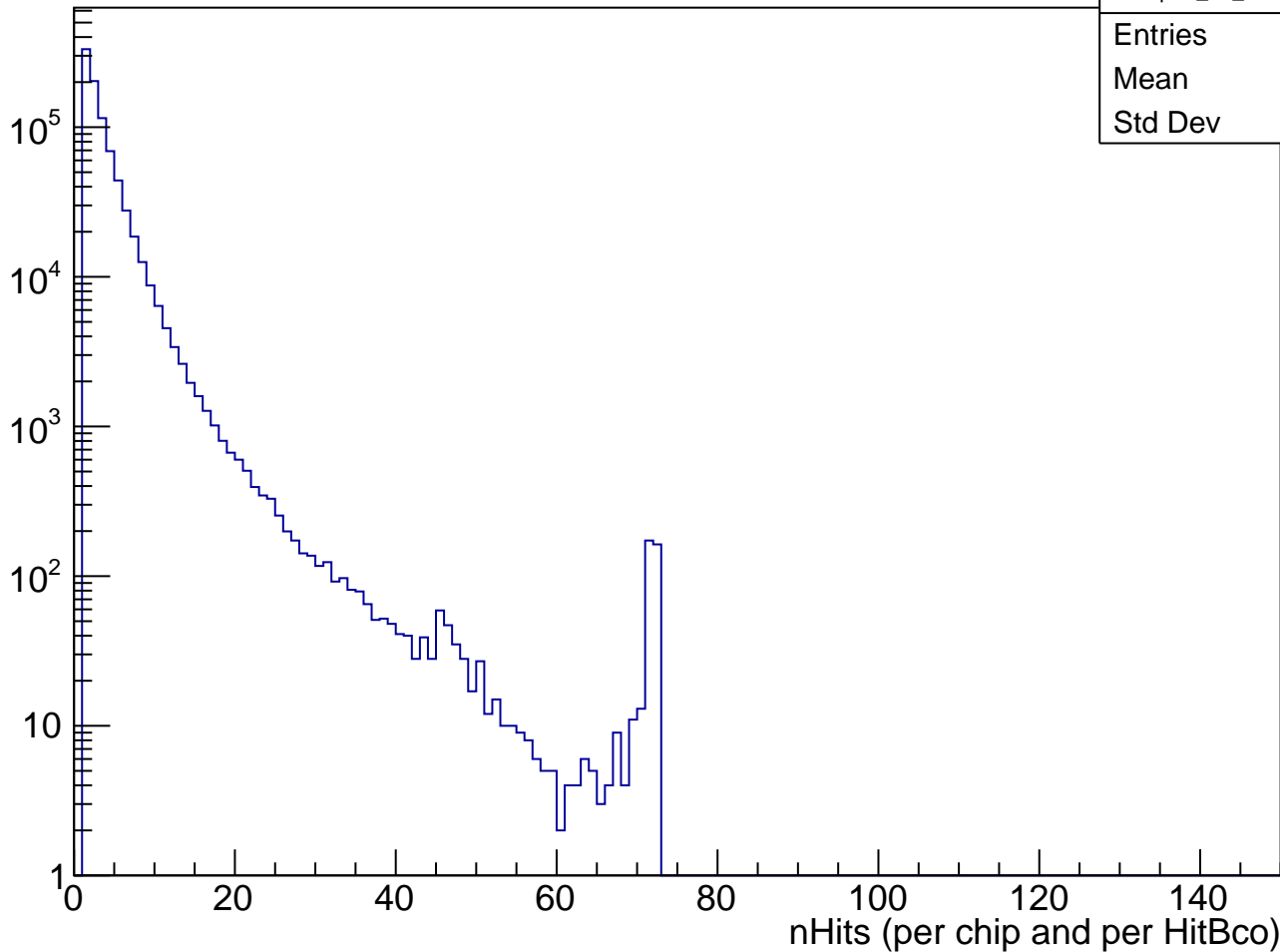
Entries 861637

Mean 2.911

Std Dev 3.405

nChipHit_F0_Fch13_Chip24

Entries



nChipHit_F0_Fch13_Chip24

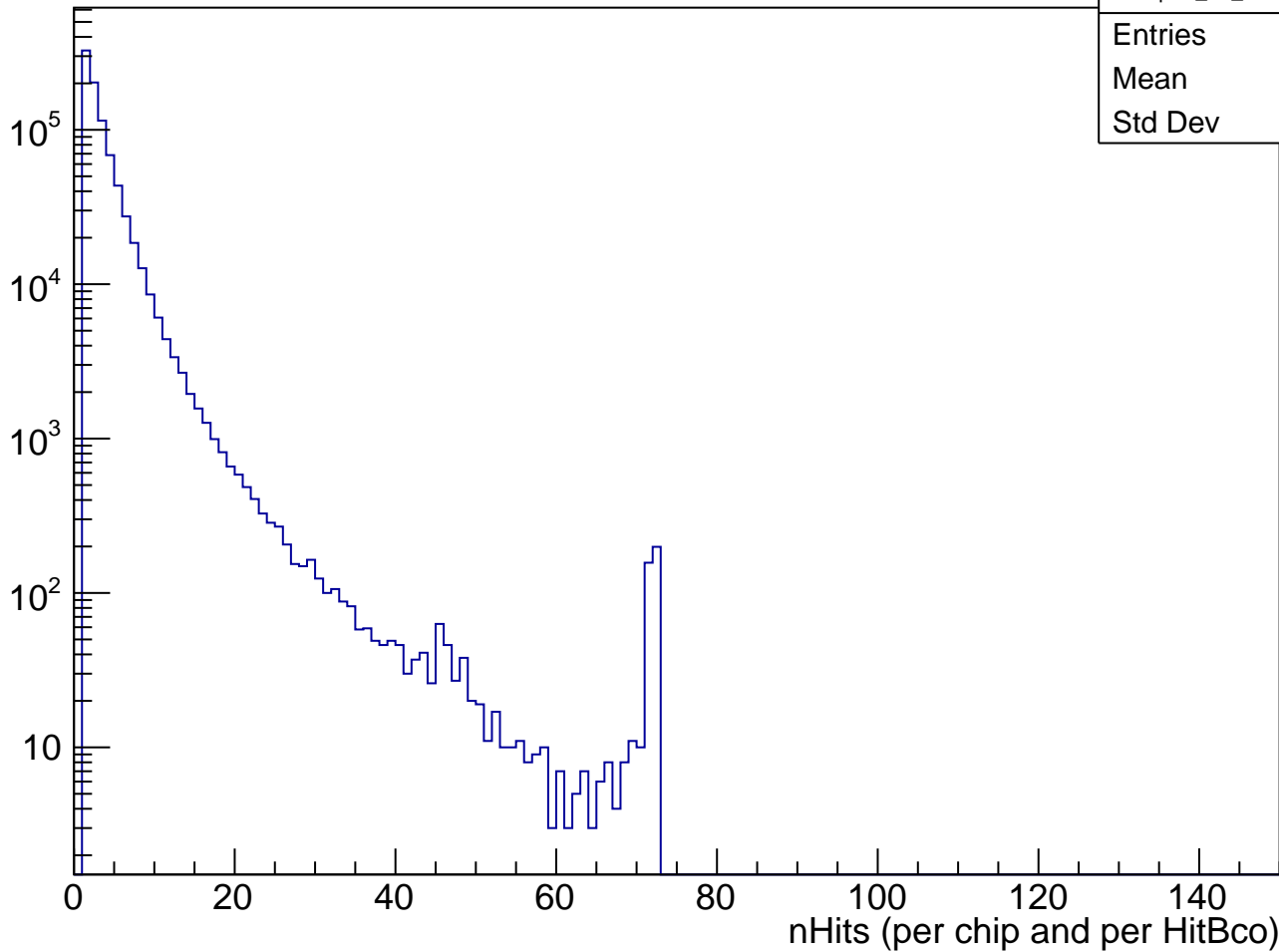
Entries 860035

Mean 2.917

Std Dev 3.423

nChipHit_F0_Fch13_Chip25

Entries

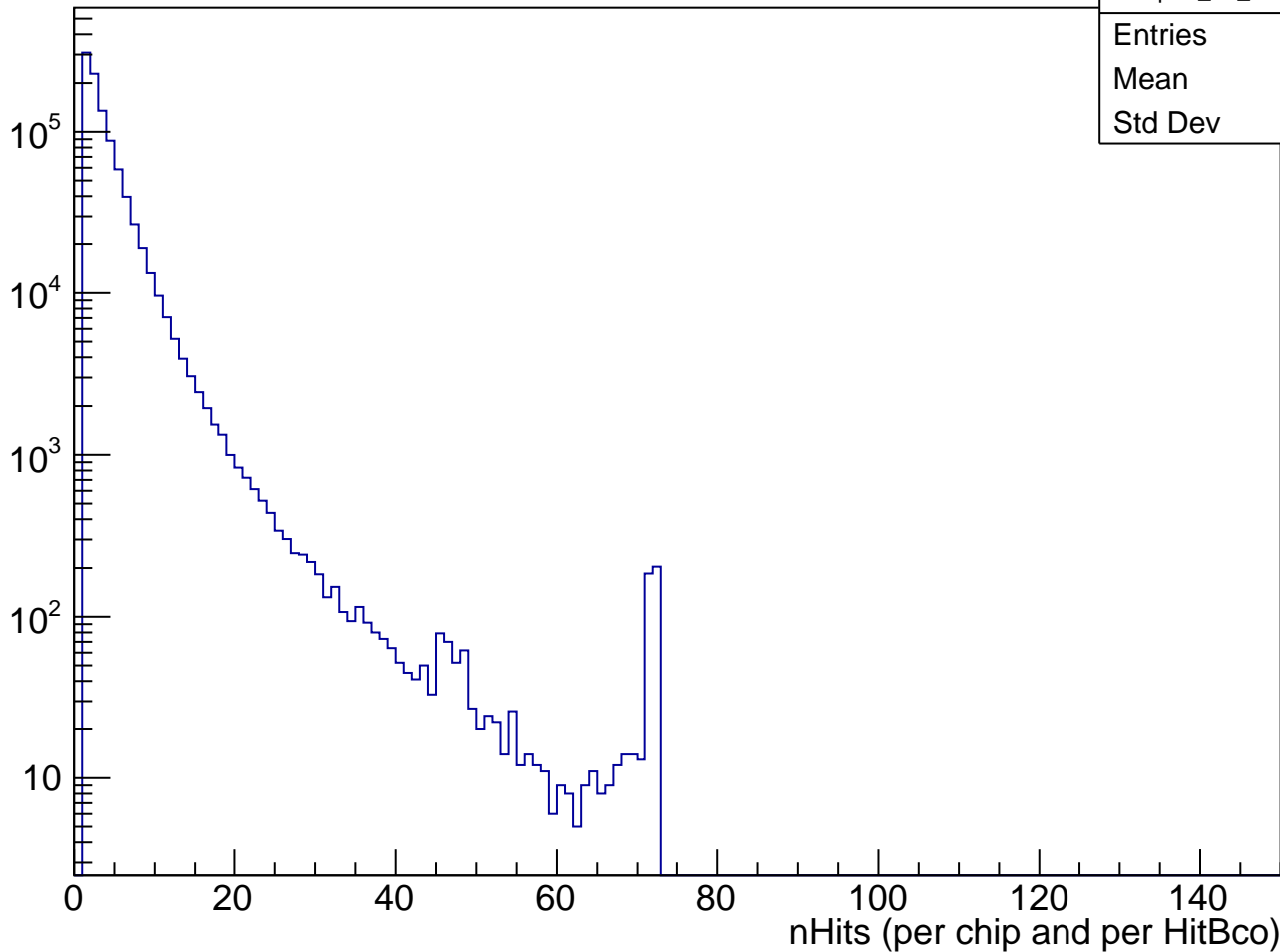


nChipHit_F0_Fch13_Chip25	
Entries	851624
Mean	2.922
Std Dev	3.445

nHits (per chip and per HitBco)

nChipHit_F0_Fch13_Chip3

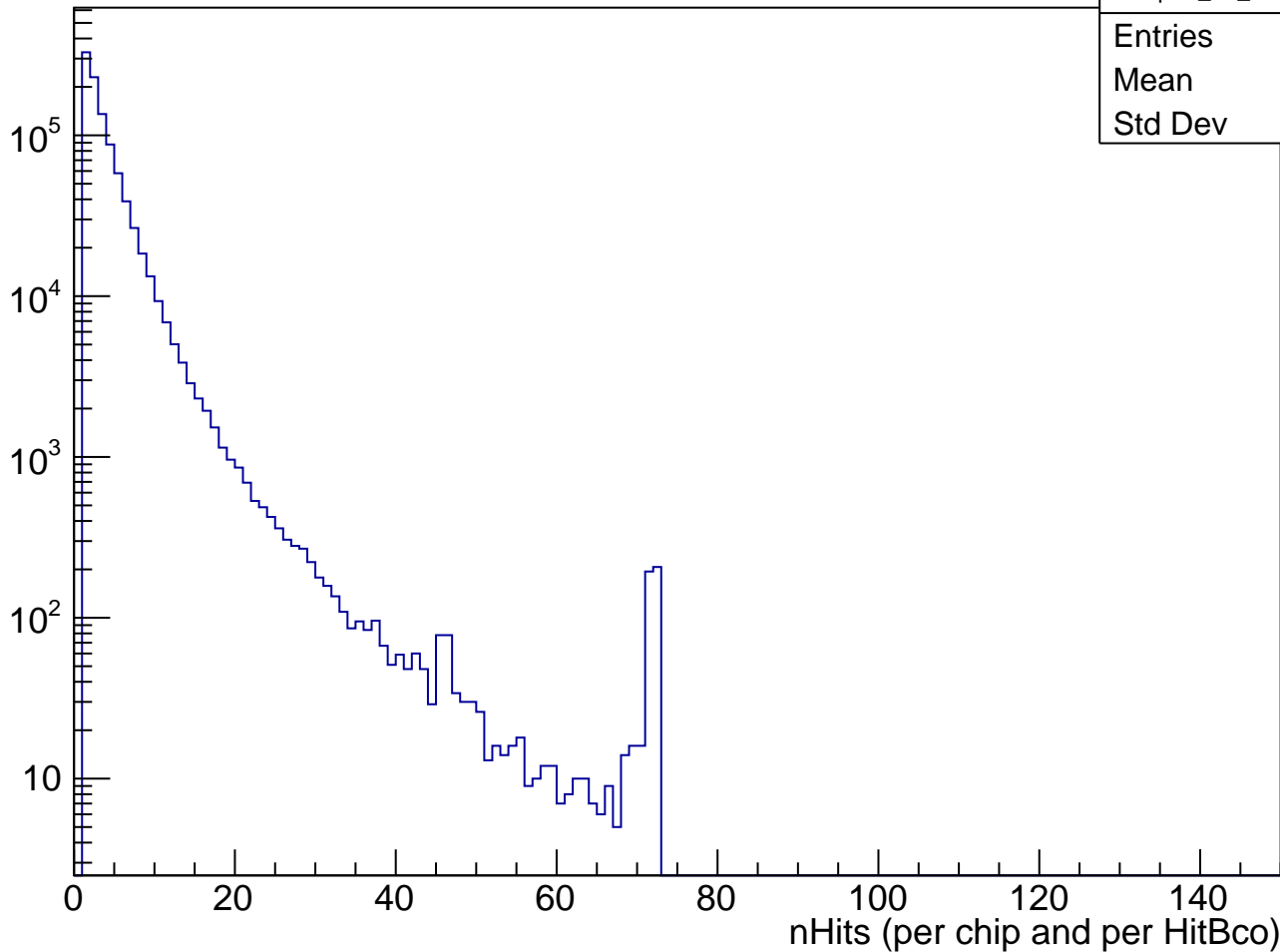
Entries



nChipHit_F0_Fch13_Chip3	
Entries	961058
Mean	3.315
Std Dev	3.752

nChipHit_F0_Fch13_Chip4

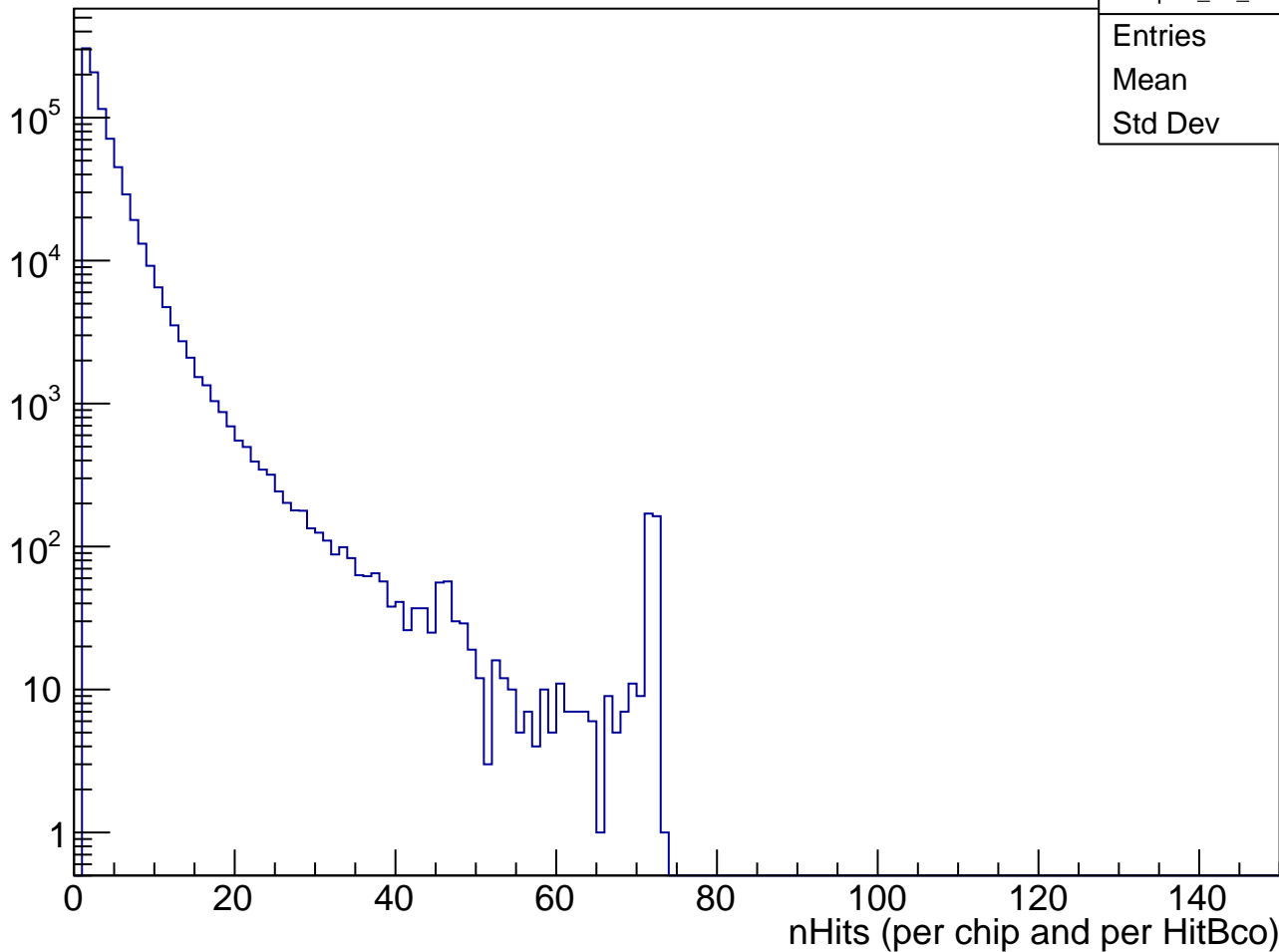
Entries



nChipHit_F0_Fch13_Chip4	
Entries	979121
Mean	3.244
Std Dev	3.7

nChipHit_F0_Fch13_Chip5

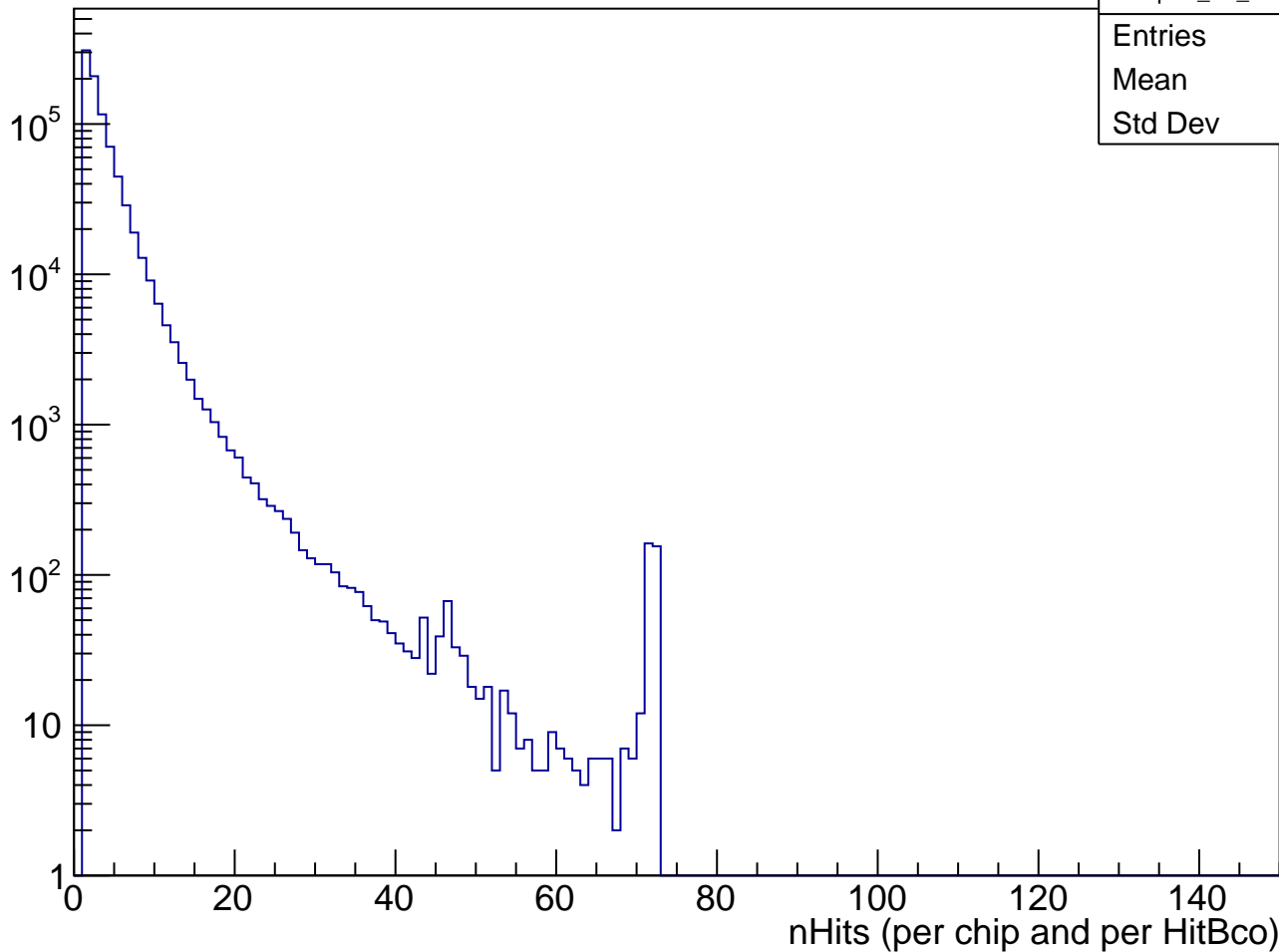
Entries



nChipHit_F0_Fch13_Chip5	
Entries	844079
Mean	3.001
Std Dev	3.457

nChipHit_F0_Fch13_Chip6

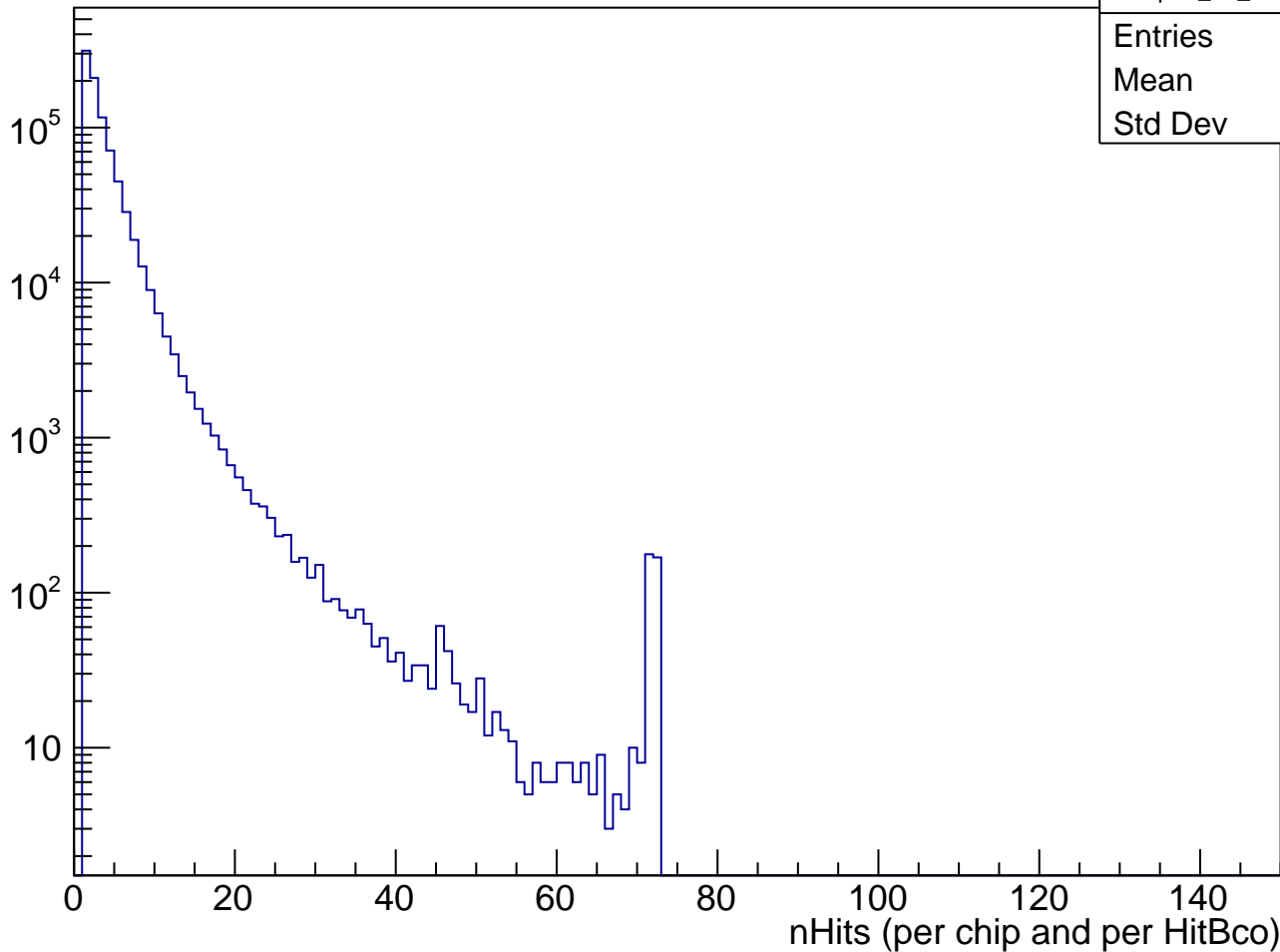
Entries



nChipHit_F0_Fch13_Chip6	
Entries	847100
Mean	2.974
Std Dev	3.419

nChipHit_F0_Fch13_Chip7

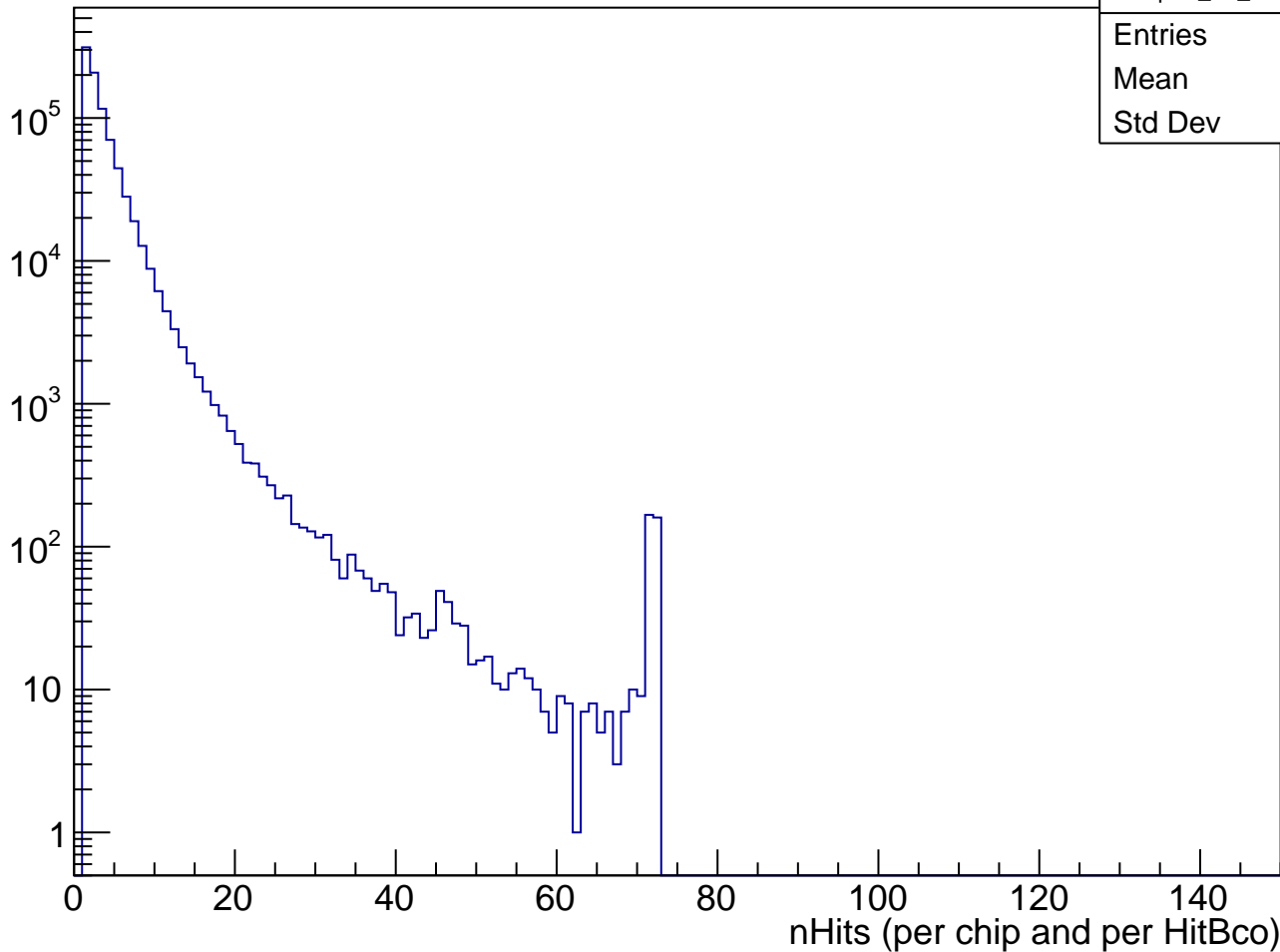
Entries



nChipHit_F0_Fch13_Chip7	
Entries	852371
Mean	2.957
Std Dev	3.416

nChipHit_F0_Fch13_Chip8

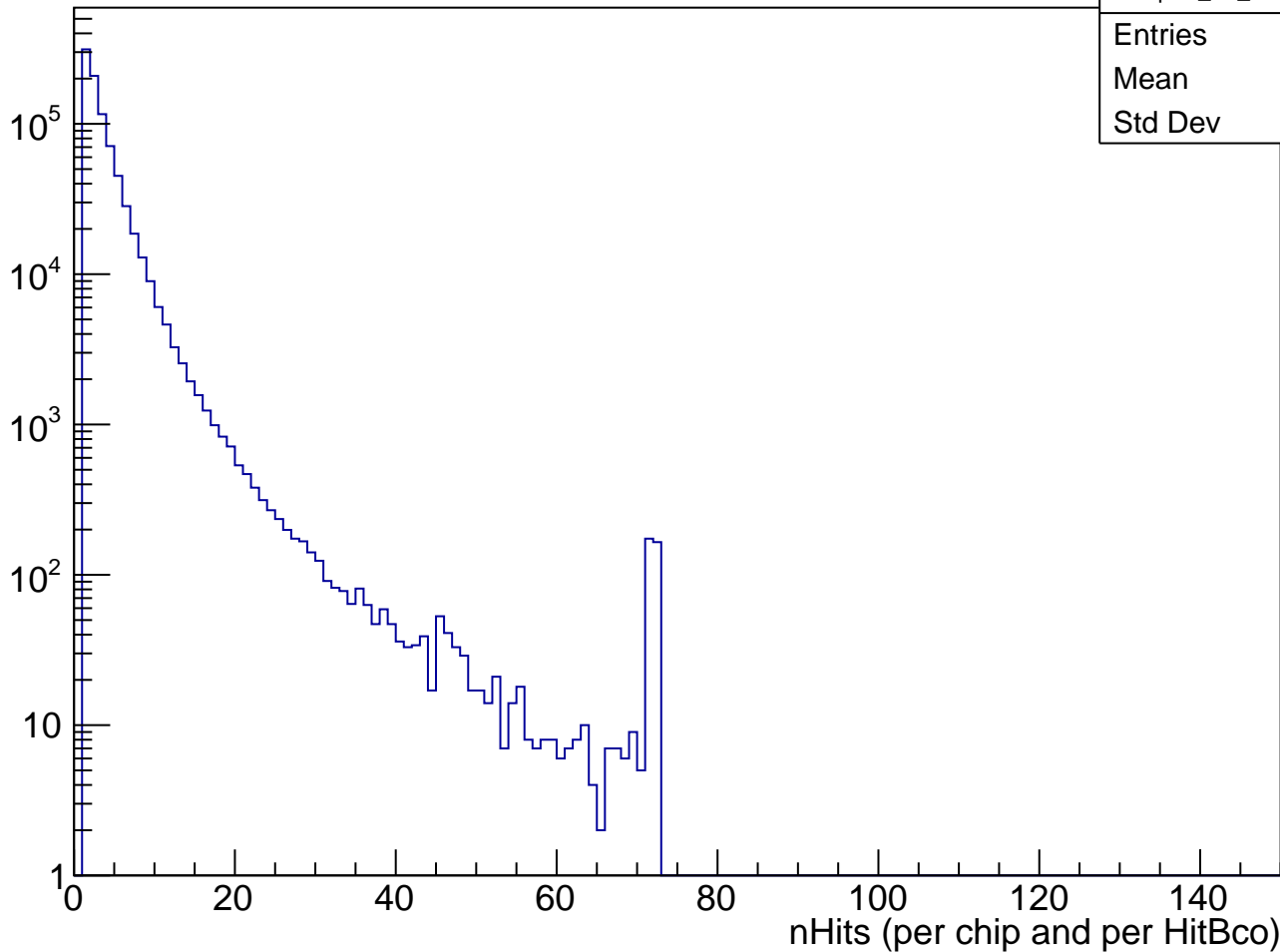
Entries



nChipHit_F0_Fch13_Chip8	
Entries	847715
Mean	2.943
Std Dev	3.38

nChipHit_F0_Fch13_Chip9

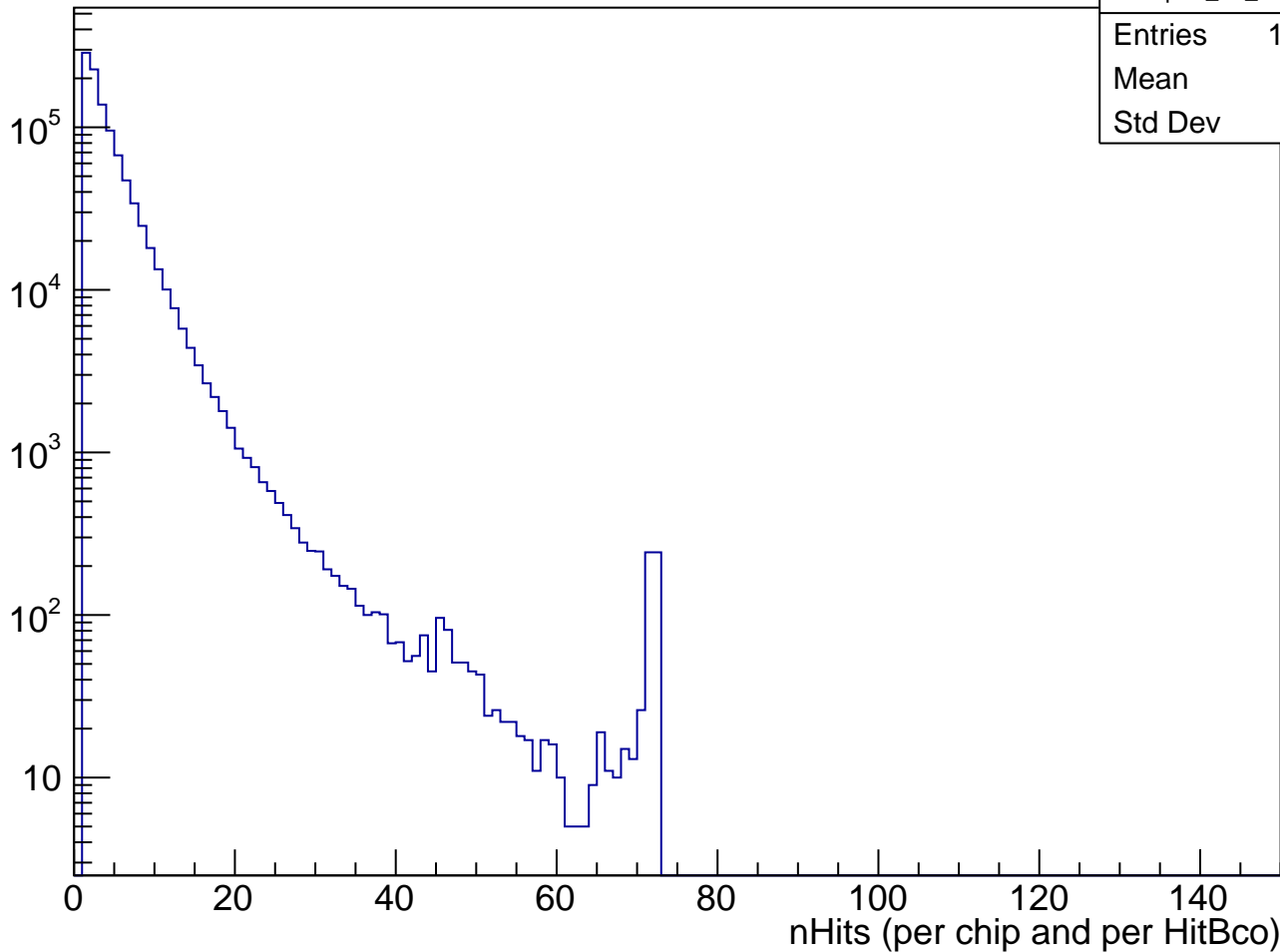
Entries



nChipHit_F0_Fch13_Chip9	
Entries	851047
Mean	2.954
Std Dev	3.412

nChipHit_F0_Fch1_Chip0

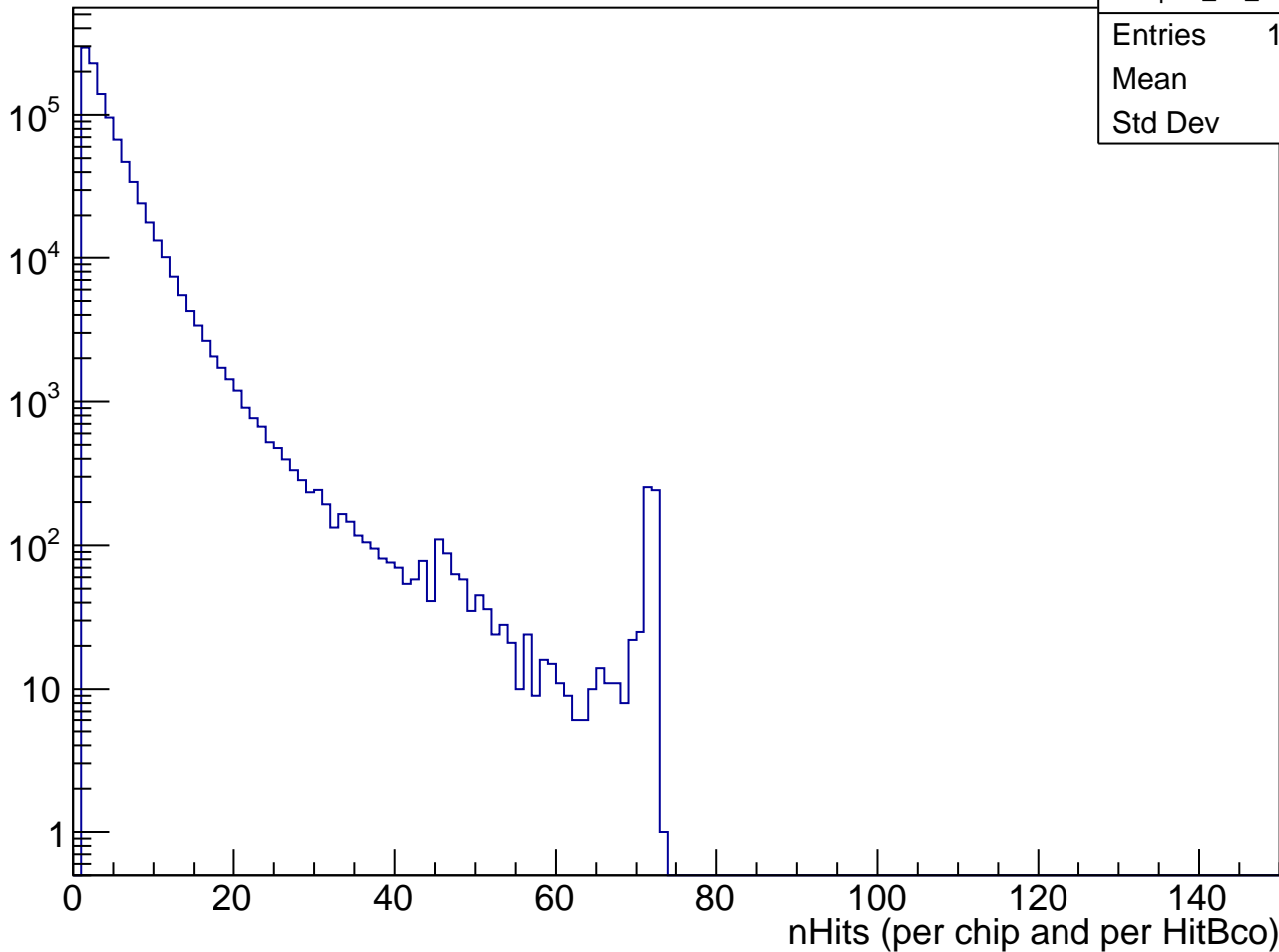
Entries



nChipHit_F0_Fch1_Chip0	
Entries	1000442
Mean	3.681
Std Dev	4.1

nChipHit_F0_Fch1_Chip1

Entries

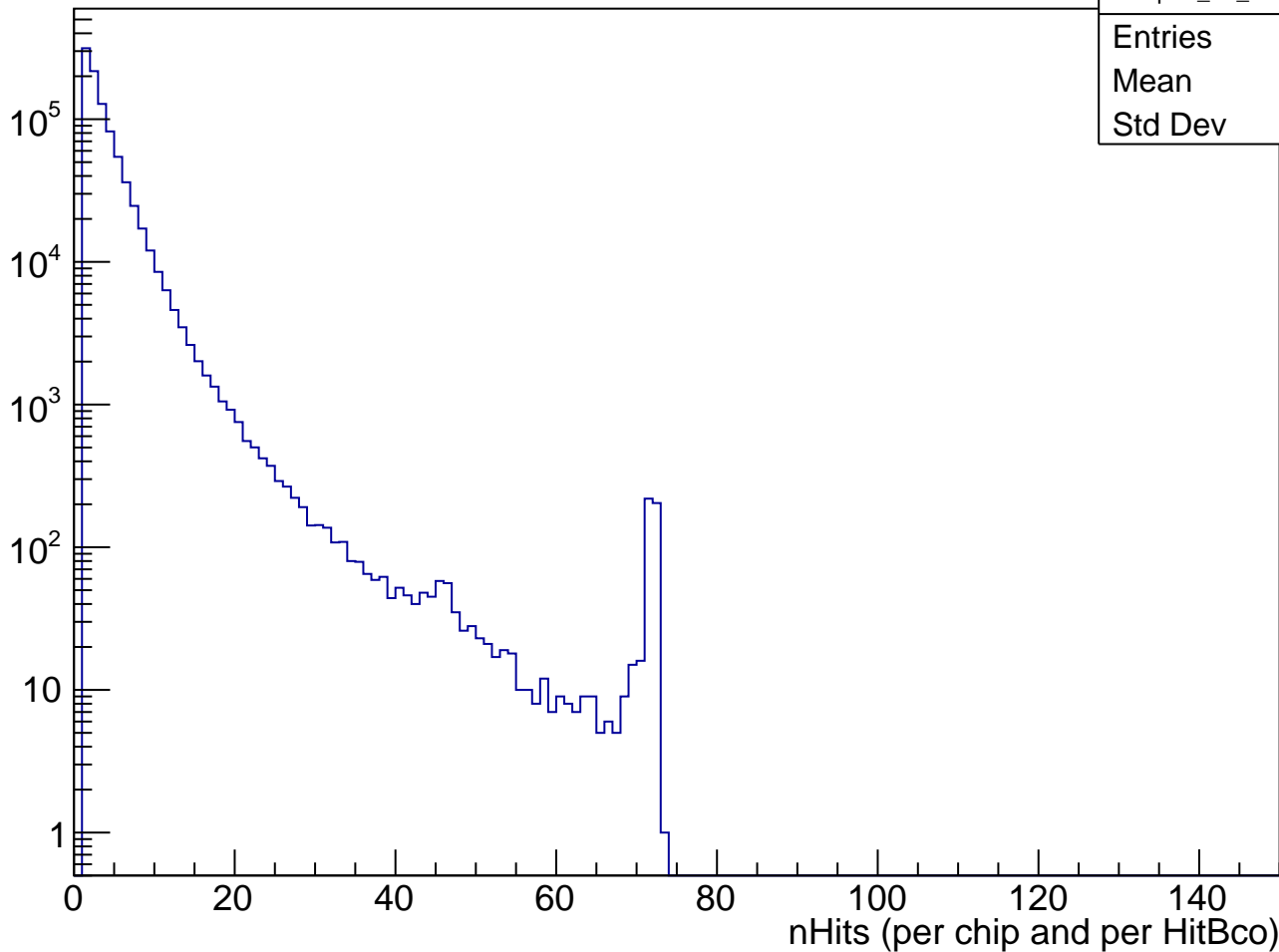


nChipHit_F0_Fch1_Chip1	
Entries	1008825
Mean	3.647
Std Dev	4.084

nHits (per chip and per HitBco)

nChipHit_F0_Fch1_Chip10

Entries

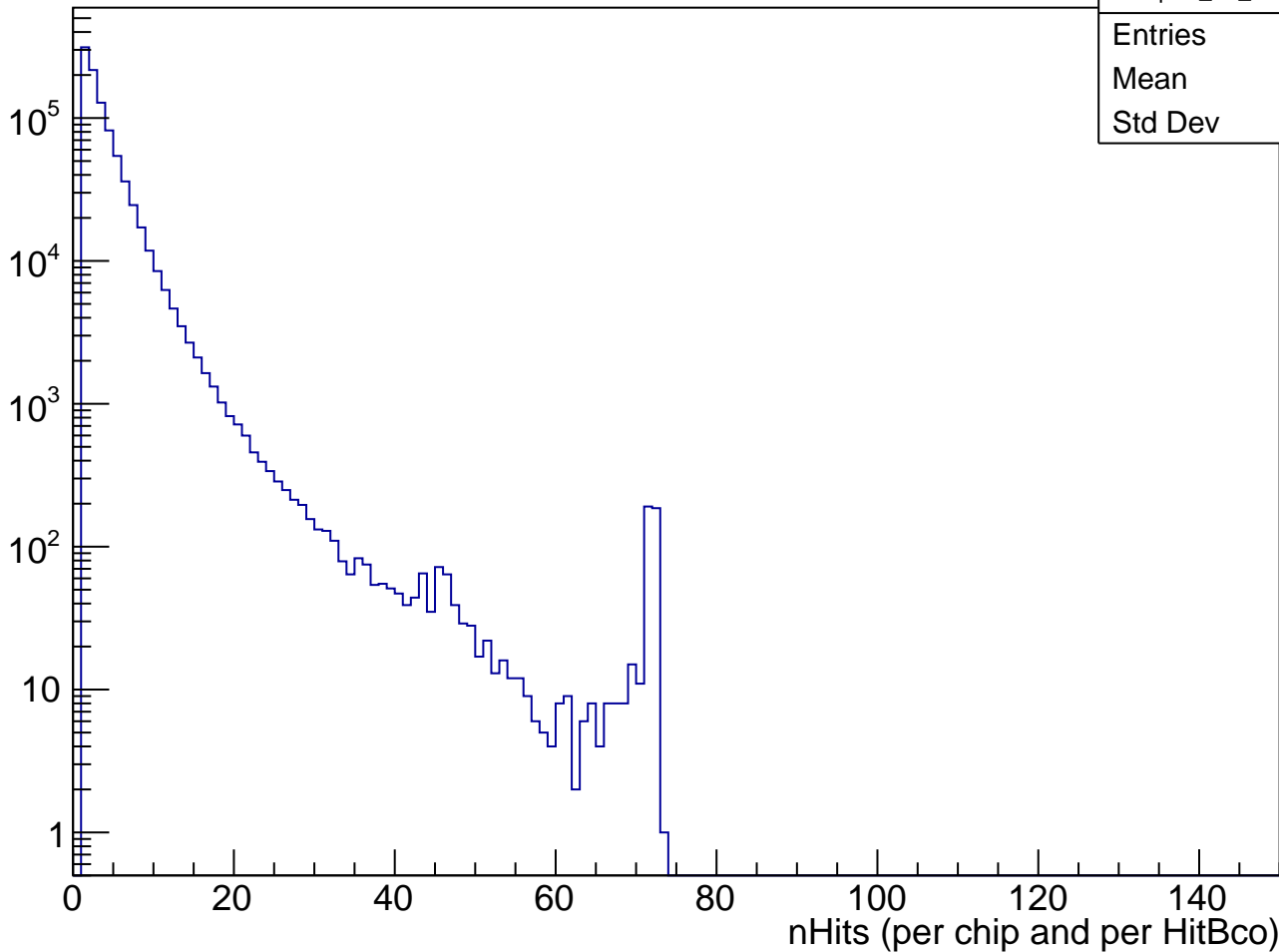


nChipHit_F0_Fch1_Chip10	
Entries	924201
Mean	3.196
Std Dev	3.651

nHits (per chip and per HitBco)

nChipHit_F0_Fch1_Chip11

Entries

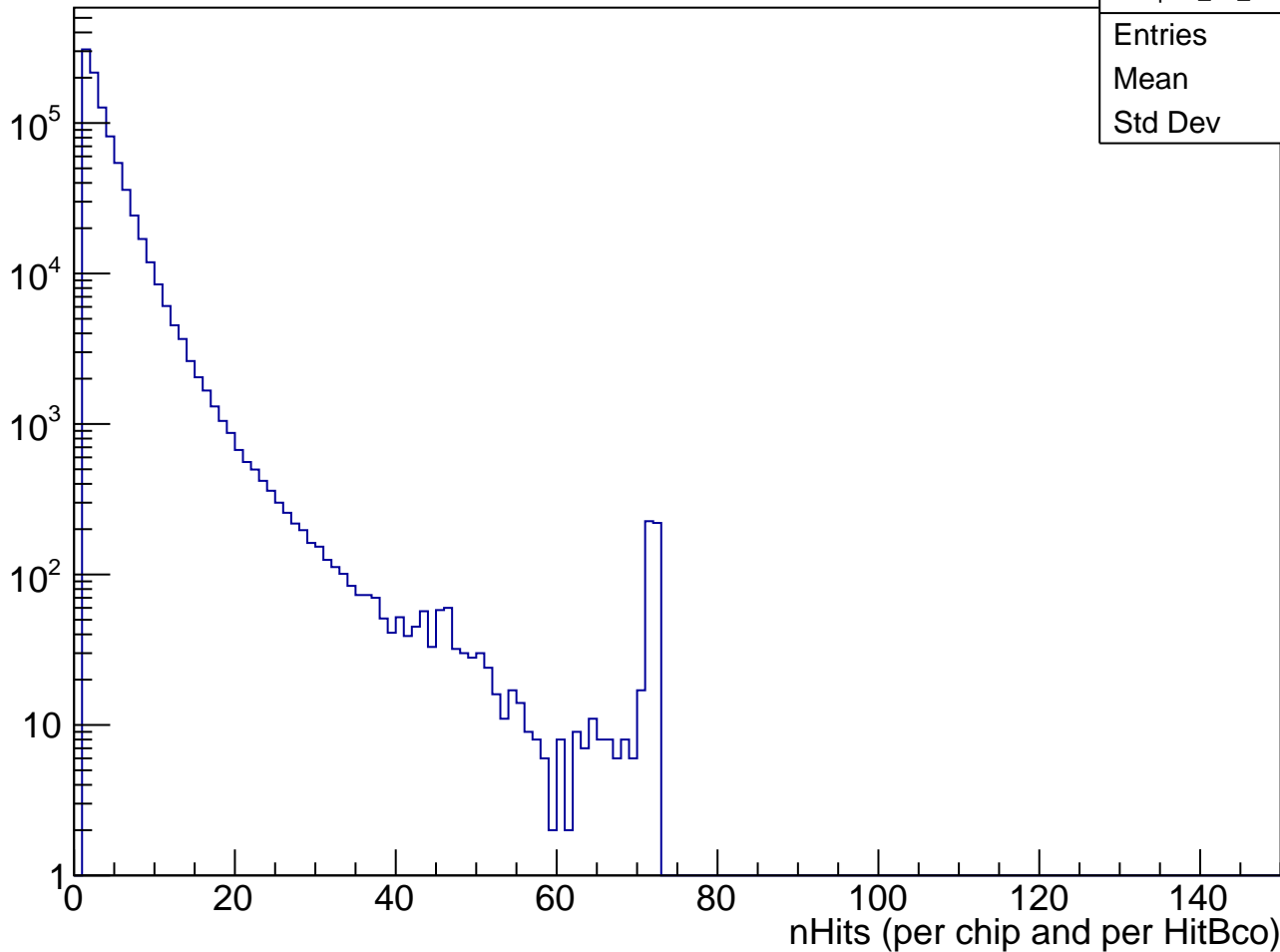


nChipHit_F0_Fch1_Chip11	
Entries	921140
Mean	3.19
Std Dev	3.598

nHits (per chip and per HitBco)

nChipHit_F0_Fch1_Chip12

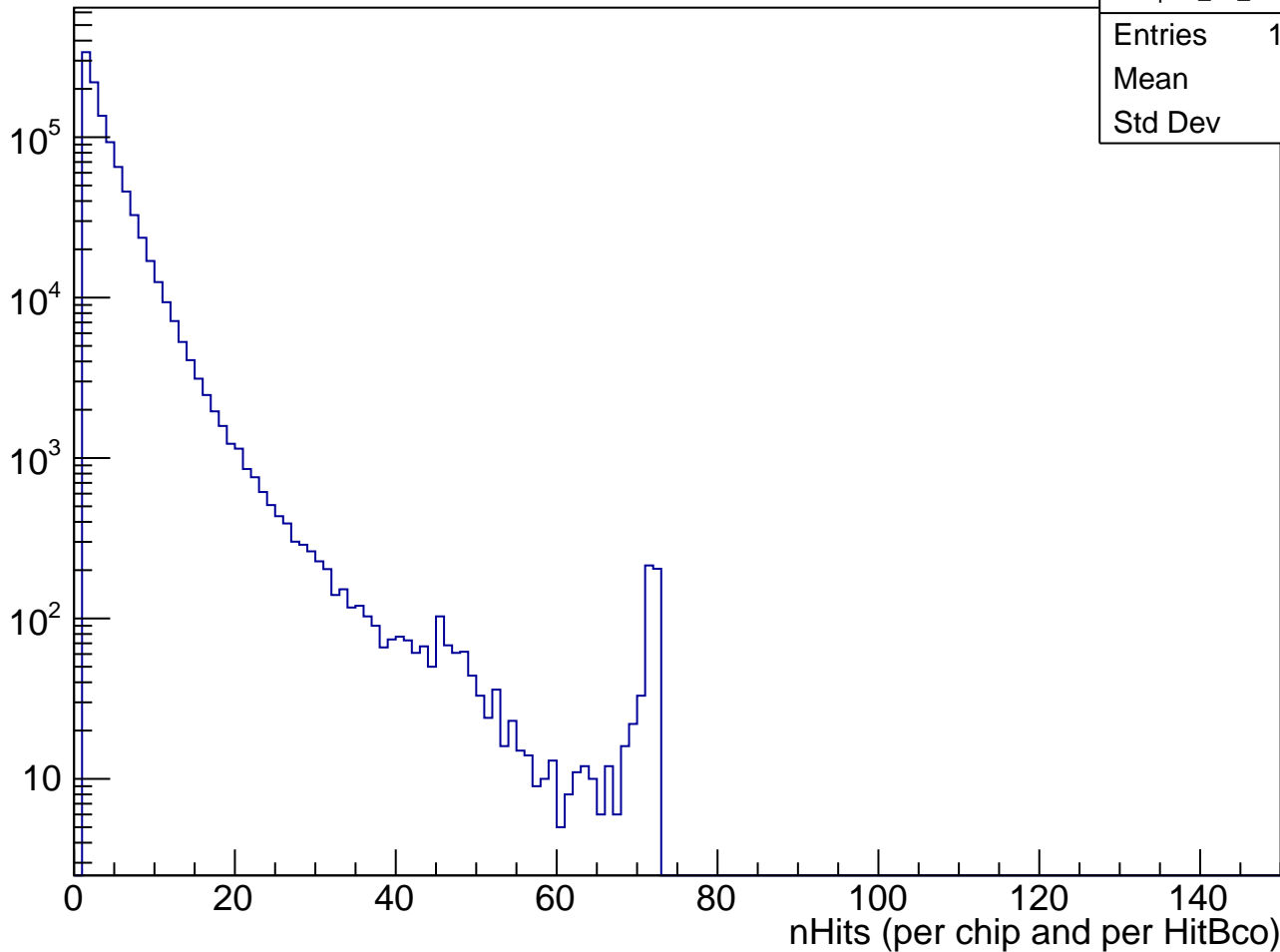
Entries



nChipHit_F0_Fch1_Chip12	
Entries	914312
Mean	3.206
Std Dev	3.667

nChipHit_F0_Fch1_Chip13

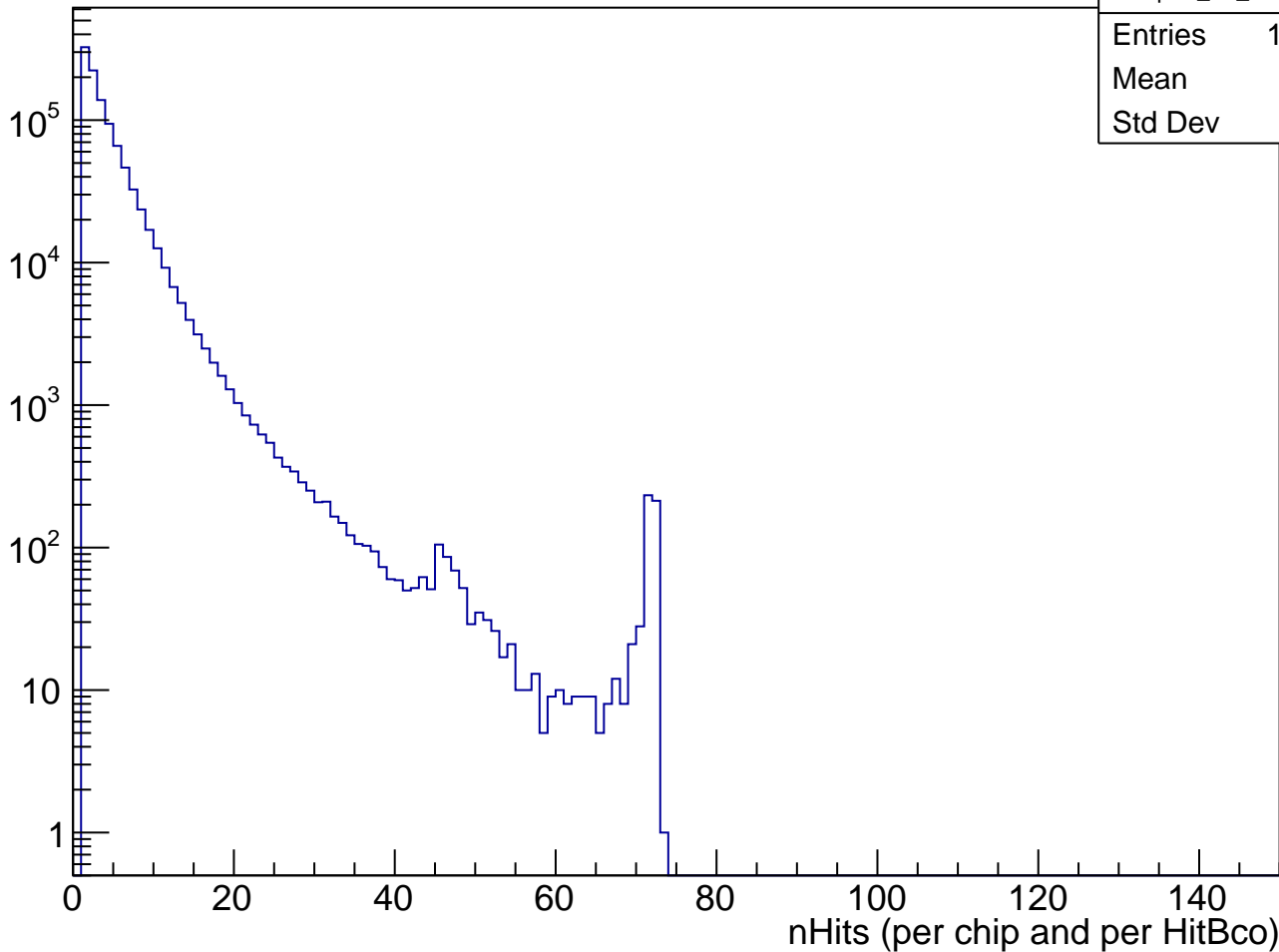
Entries



nChipHit_F0_Fch1_Chip13	
Entries	1029089
Mean	3.49
Std Dev	3.97

nChipHit_F0_Fch1_Chip14

Entries

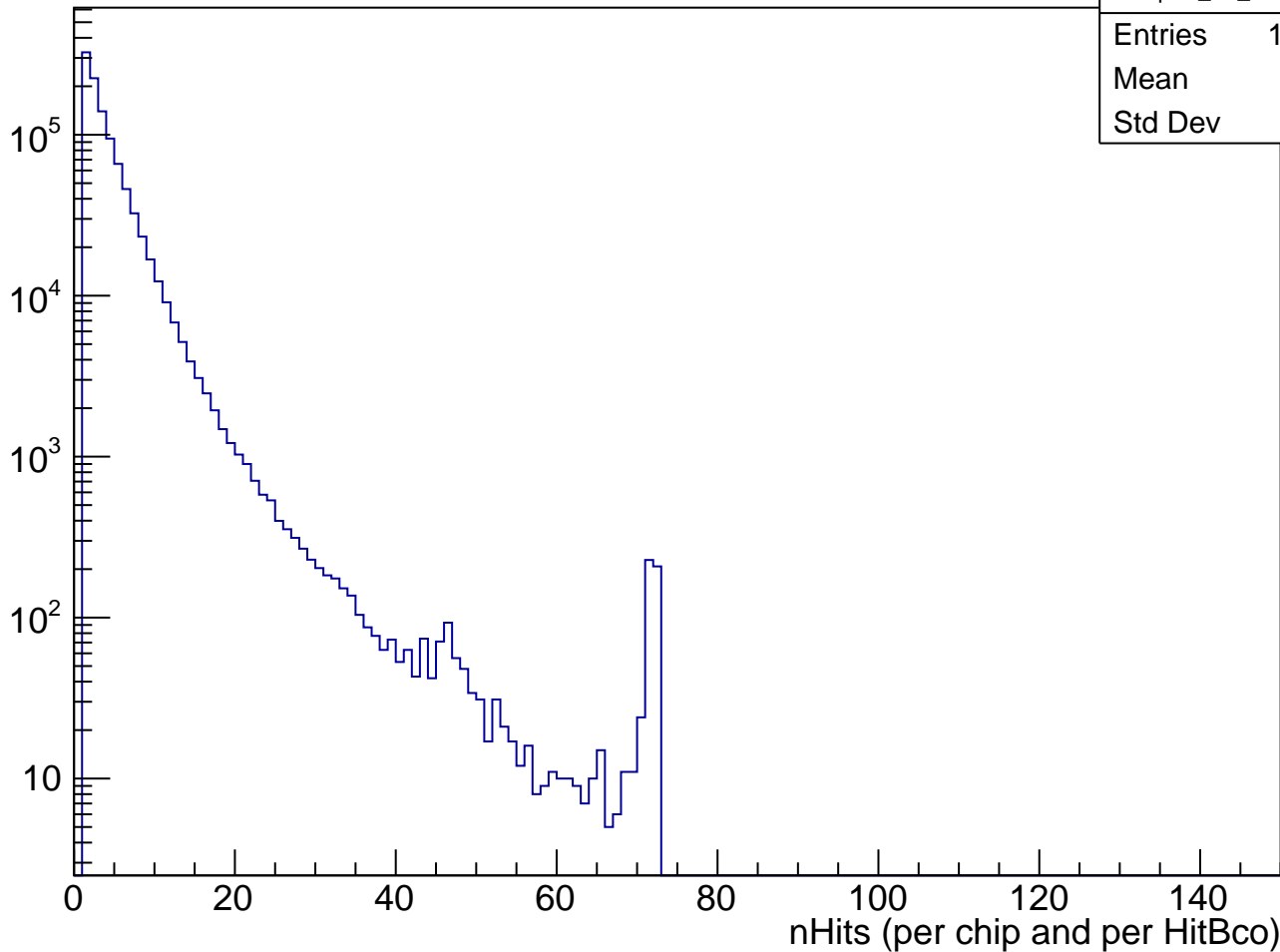


nChipHit_F0_Fch1_Chip14	
Entries	1022575
Mean	3.514
Std Dev	3.961

nHits (per chip and per HitBco)

nChipHit_F0_Fch1_Chip15

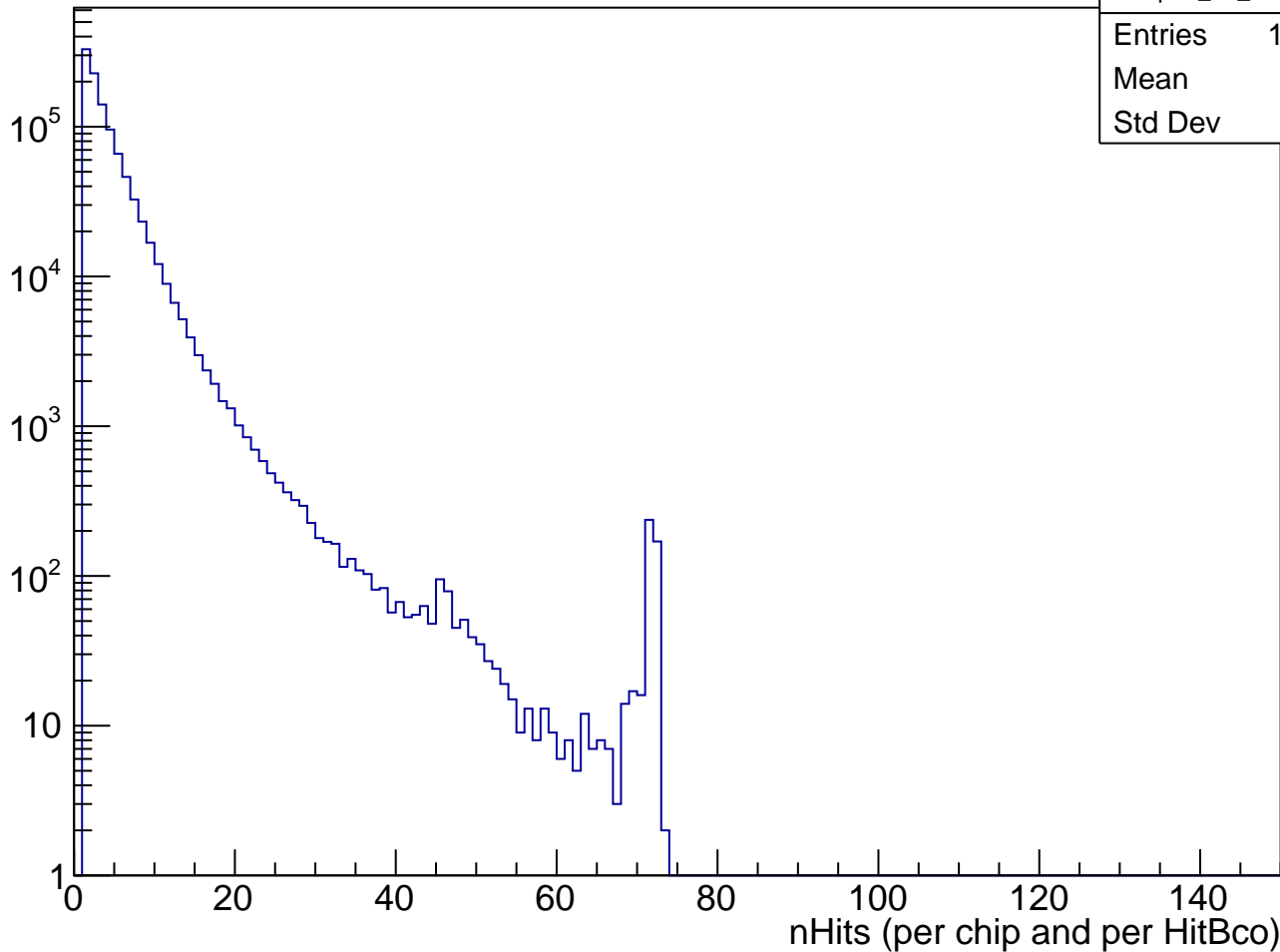
Entries



nChipHit_F0_Fch1_Chip15	
Entries	1023706
Mean	3.493
Std Dev	3.913

nChipHit_F0_Fch1_Chip16

Entries

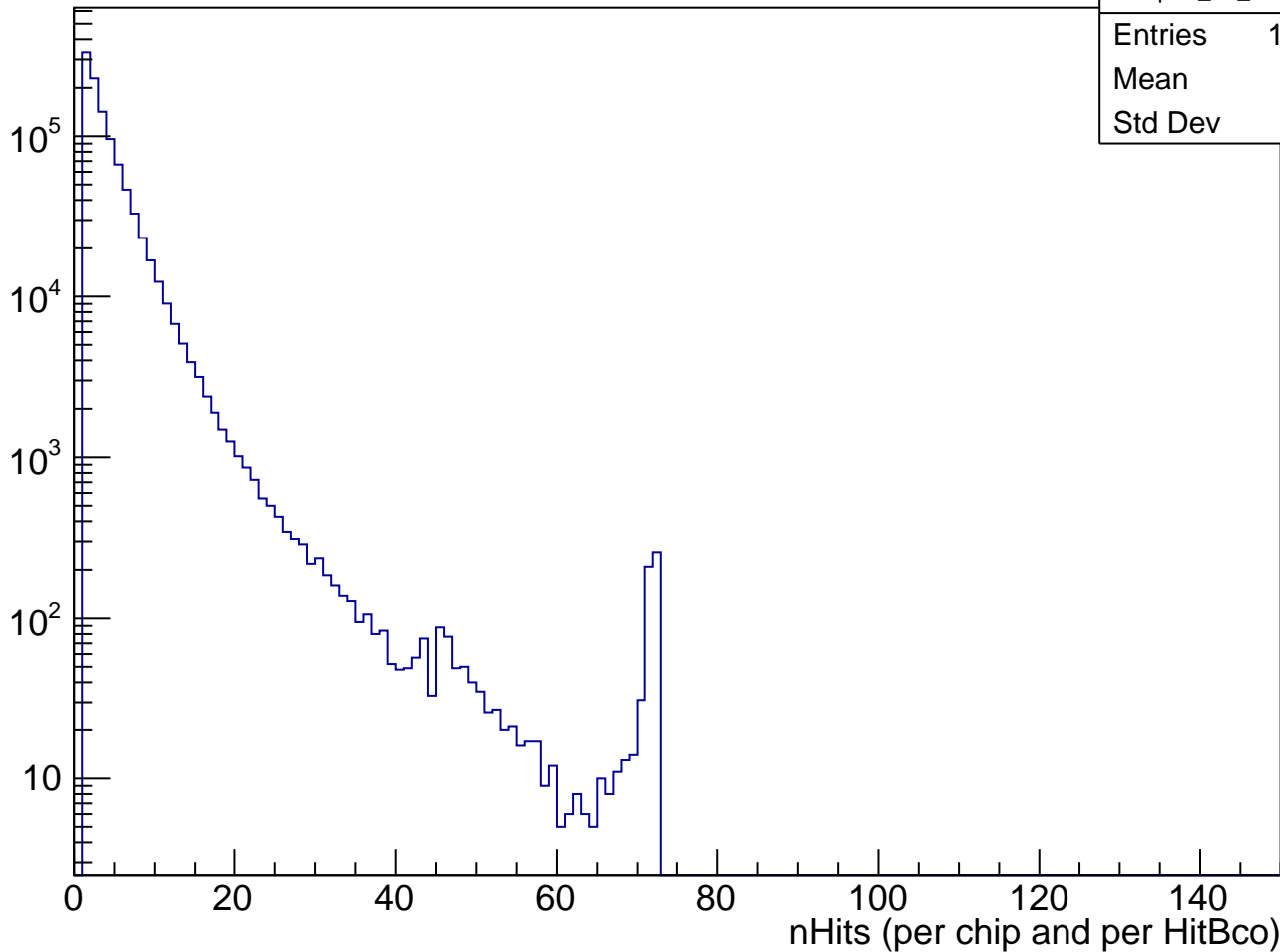


nChipHit_F0_Fch1_Chip16	
Entries	1032465
Mean	3.47
Std Dev	3.868

nHits (per chip and per HitBco)

nChipHit_F0_Fch1_Chip17

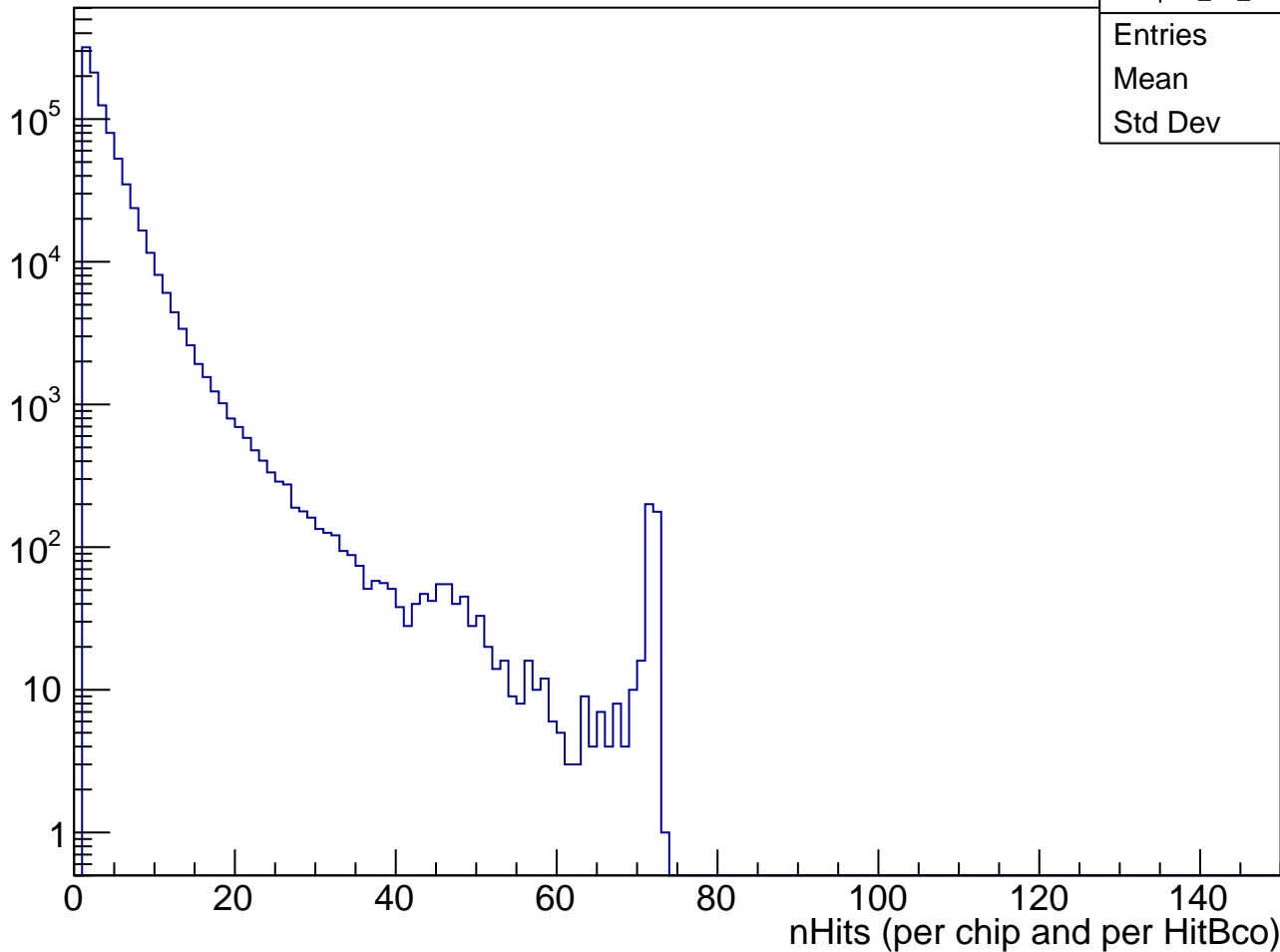
Entries



nChipHit_F0_Fch1_Chip17	
Entries	1040666
Mean	3.473
Std Dev	3.911

nChipHit_F0_Fch1_Chip18

Entries

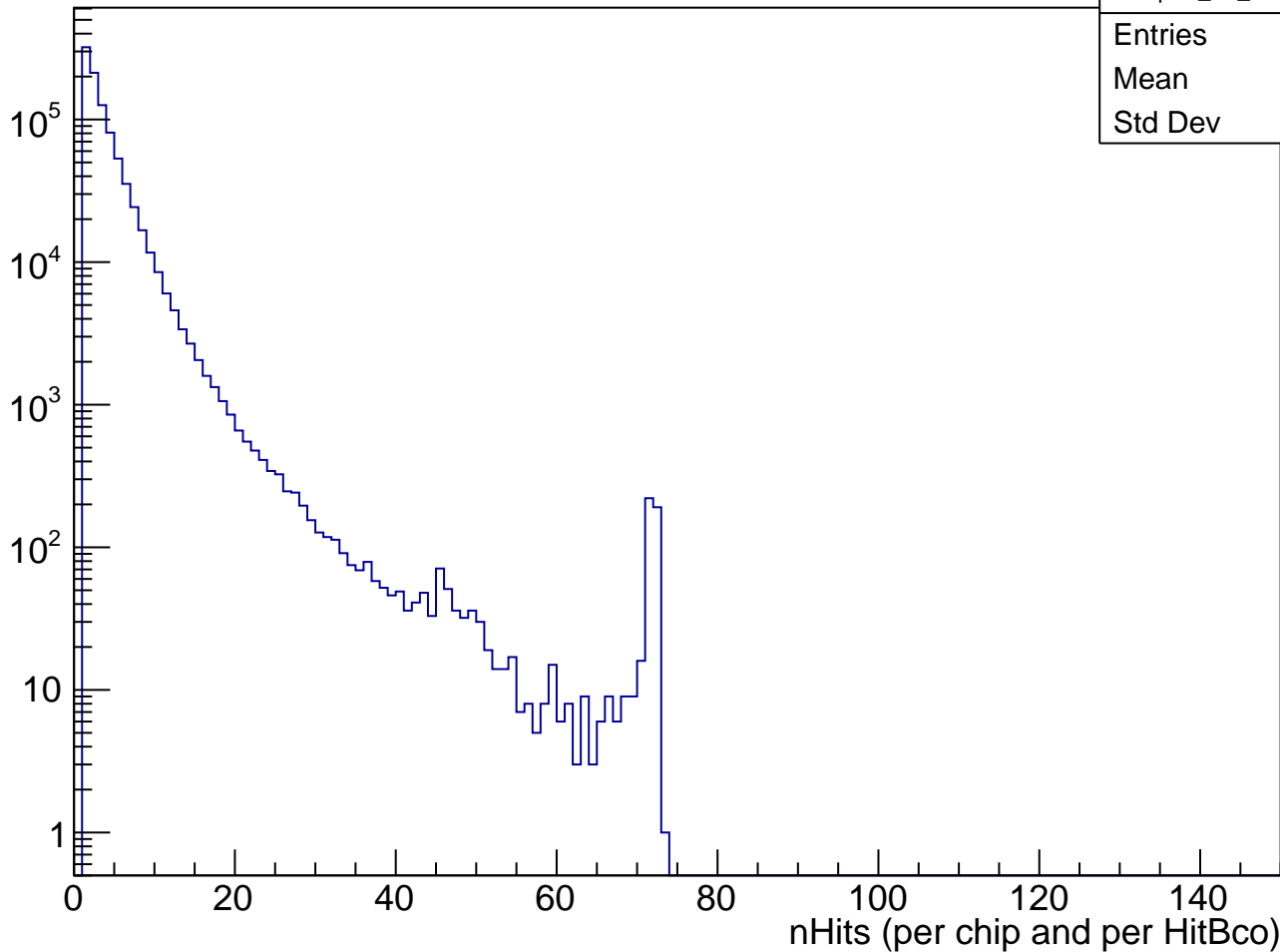


nChipHit_F0_Fch1_Chip18	
Entries	911696
Mean	3.151
Std Dev	3.588

nHits (per chip and per HitBco)

nChipHit_F0_Fch1_Chip19

Entries

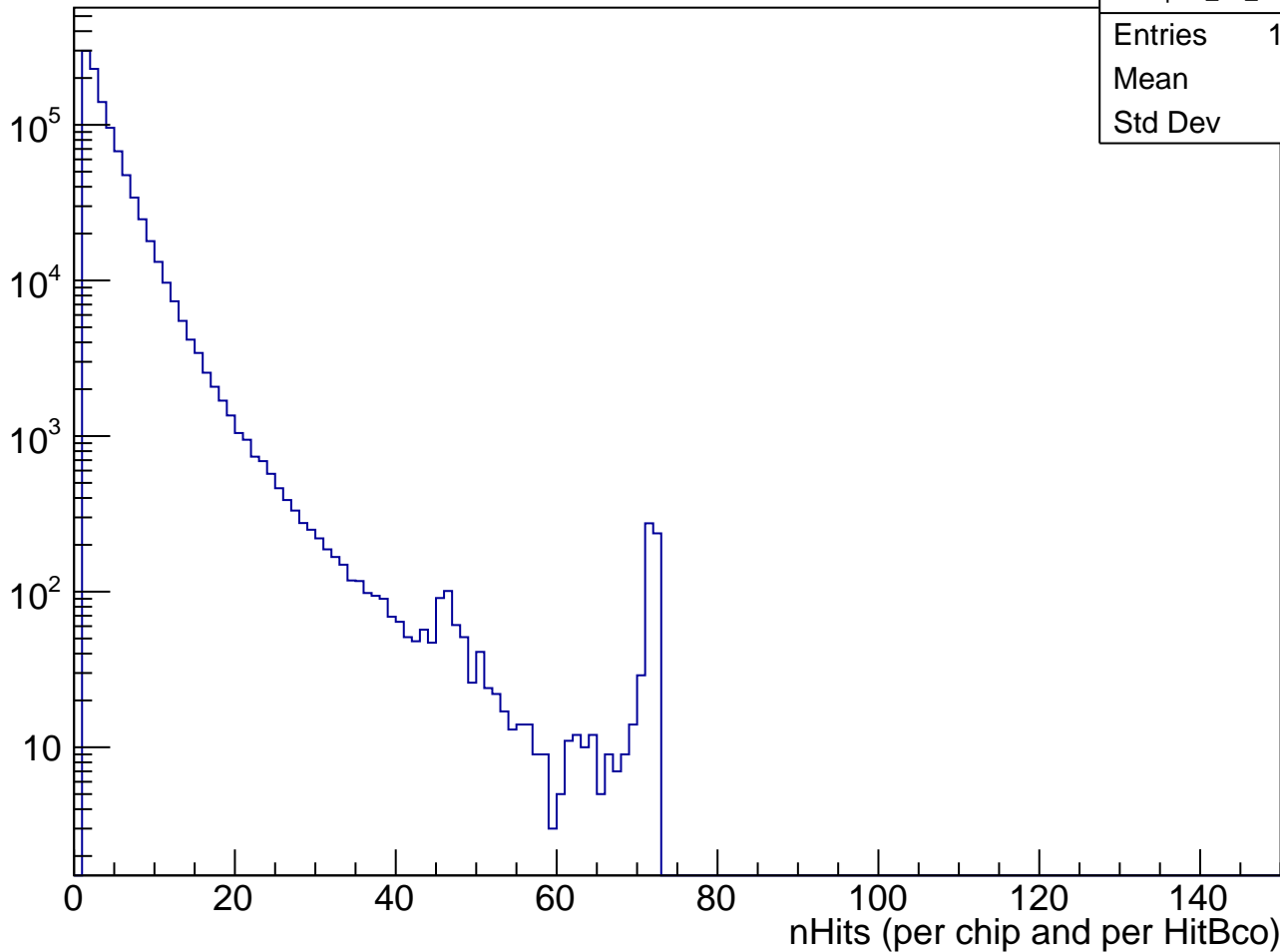


nChipHit_F0_Fch1_Chip19	
Entries	919639
Mean	3.165
Std Dev	3.625

nHits (per chip and per HitBco)

nChipHit_F0_Fch1_Chip2

Entries



nChipHit_F0_Fch1_Chip2

Entries 1015208

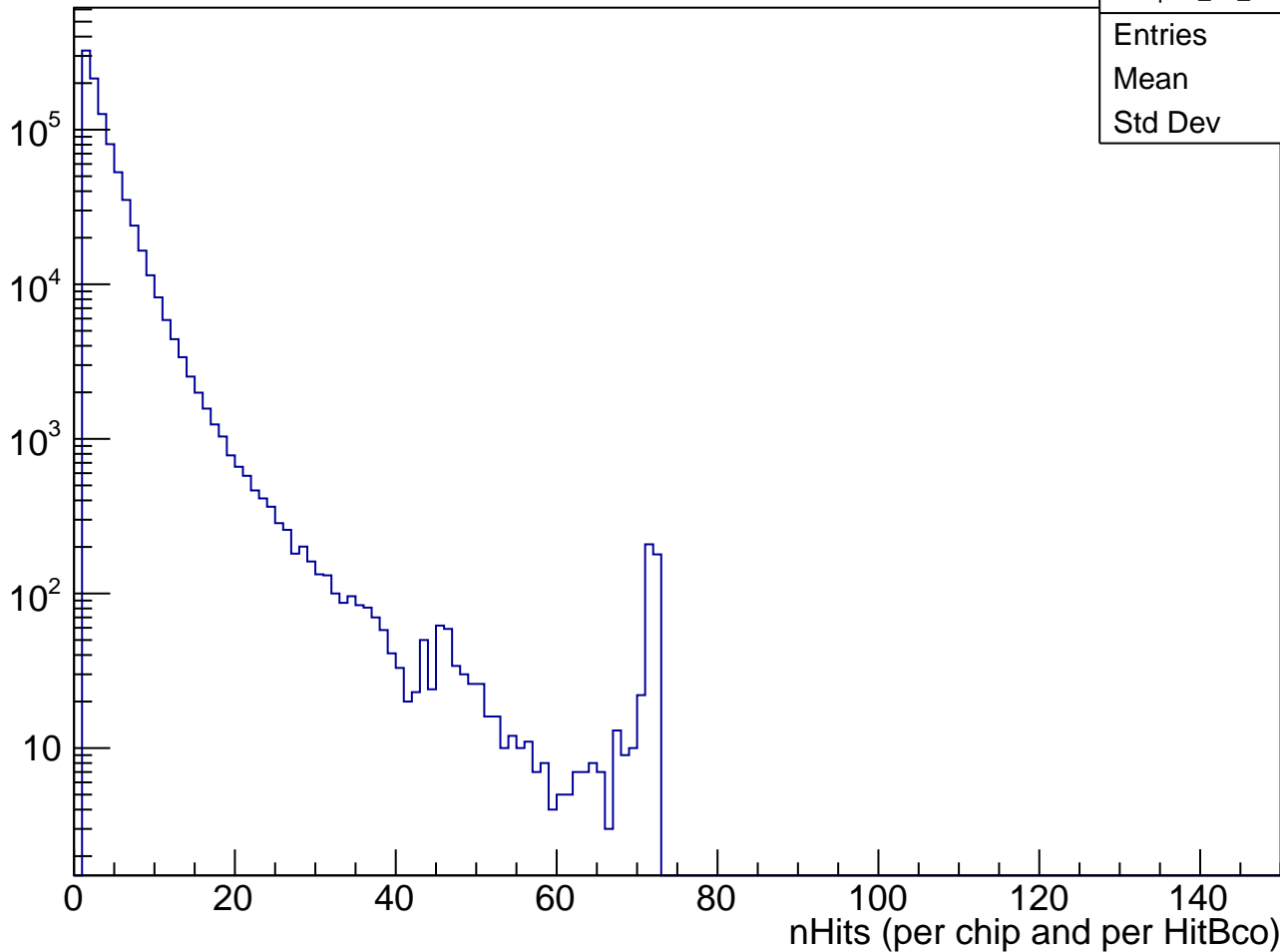
Mean 3.622

Std Dev 4.035

nHits (per chip and per HitBco)

nChipHit_F0_Fch1_Chip20

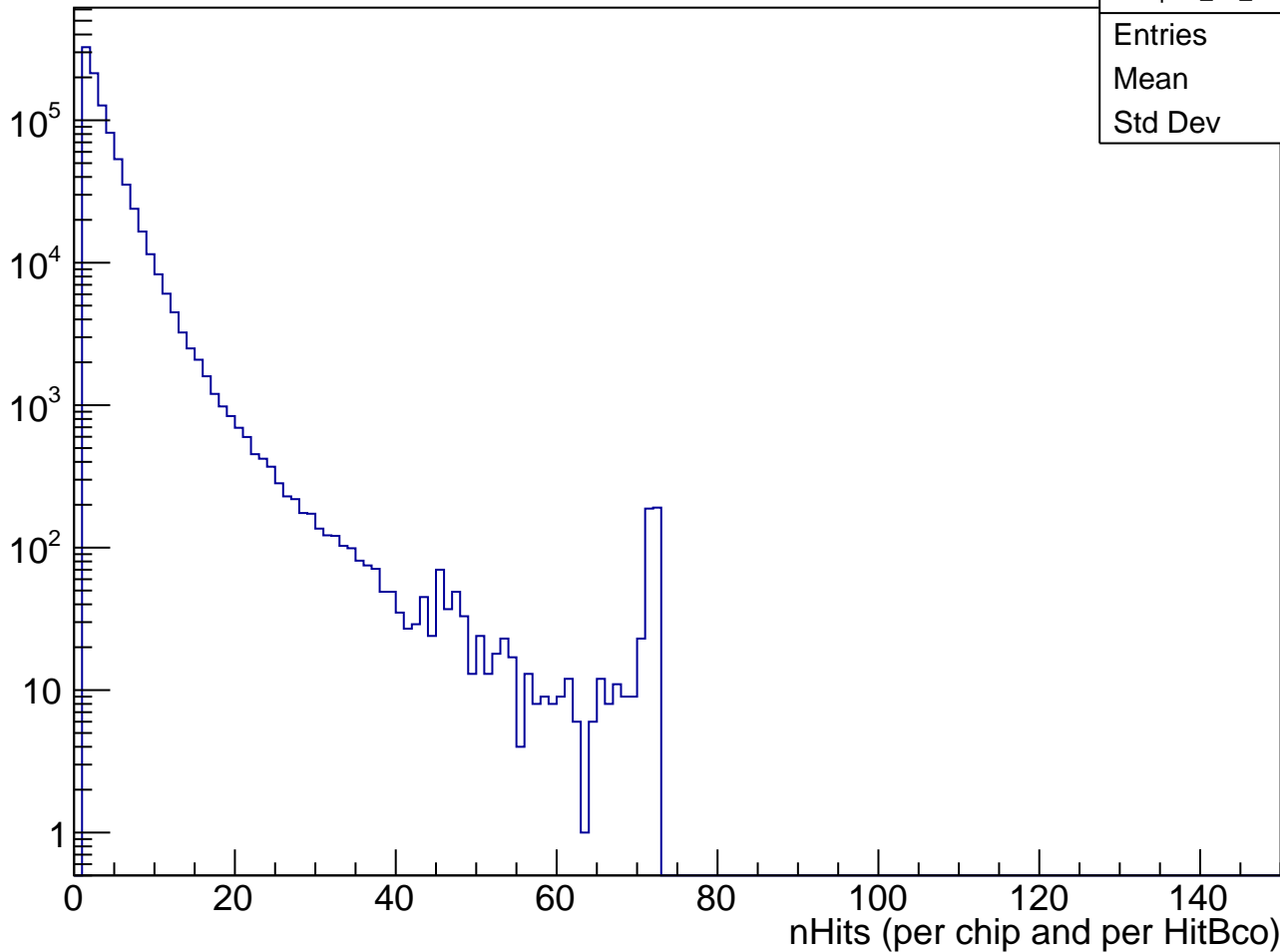
Entries



nChipHit_F0_Fch1_Chip20	
Entries	922808
Mean	3.133
Std Dev	3.572

nChipHit_F0_Fch1_Chip21

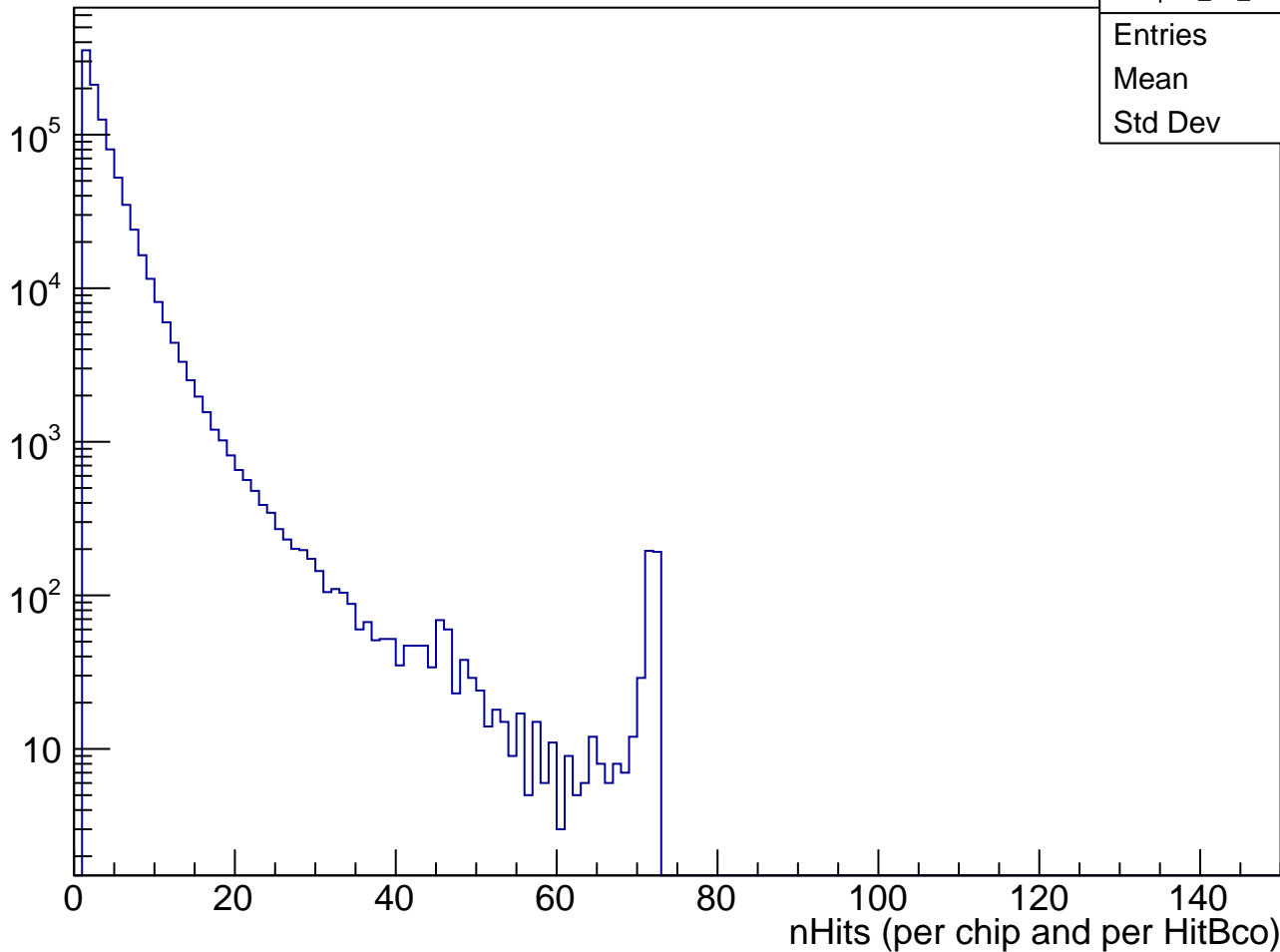
Entries



nChipHit_F0_Fch1_Chip21	
Entries	925848
Mean	3.139
Std Dev	3.581

nChipHit_F0_Fch1_Chip22

Entries

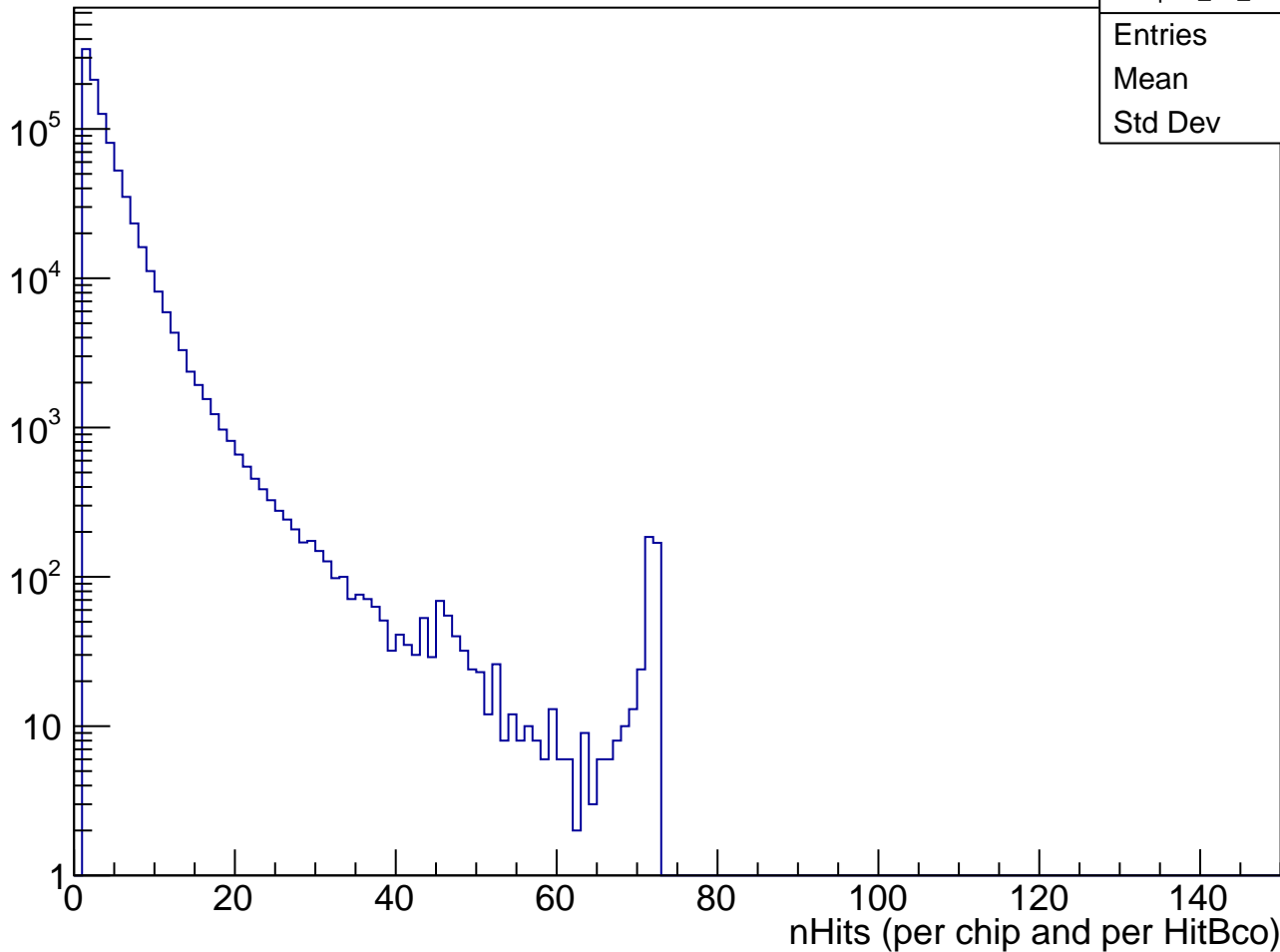


nChipHit_F0_Fch1_Chip22	
Entries	947106
Mean	3.069
Std Dev	3.559

nHits (per chip and per HitBco)

nChipHit_F0_Fch1_Chip23

Entries

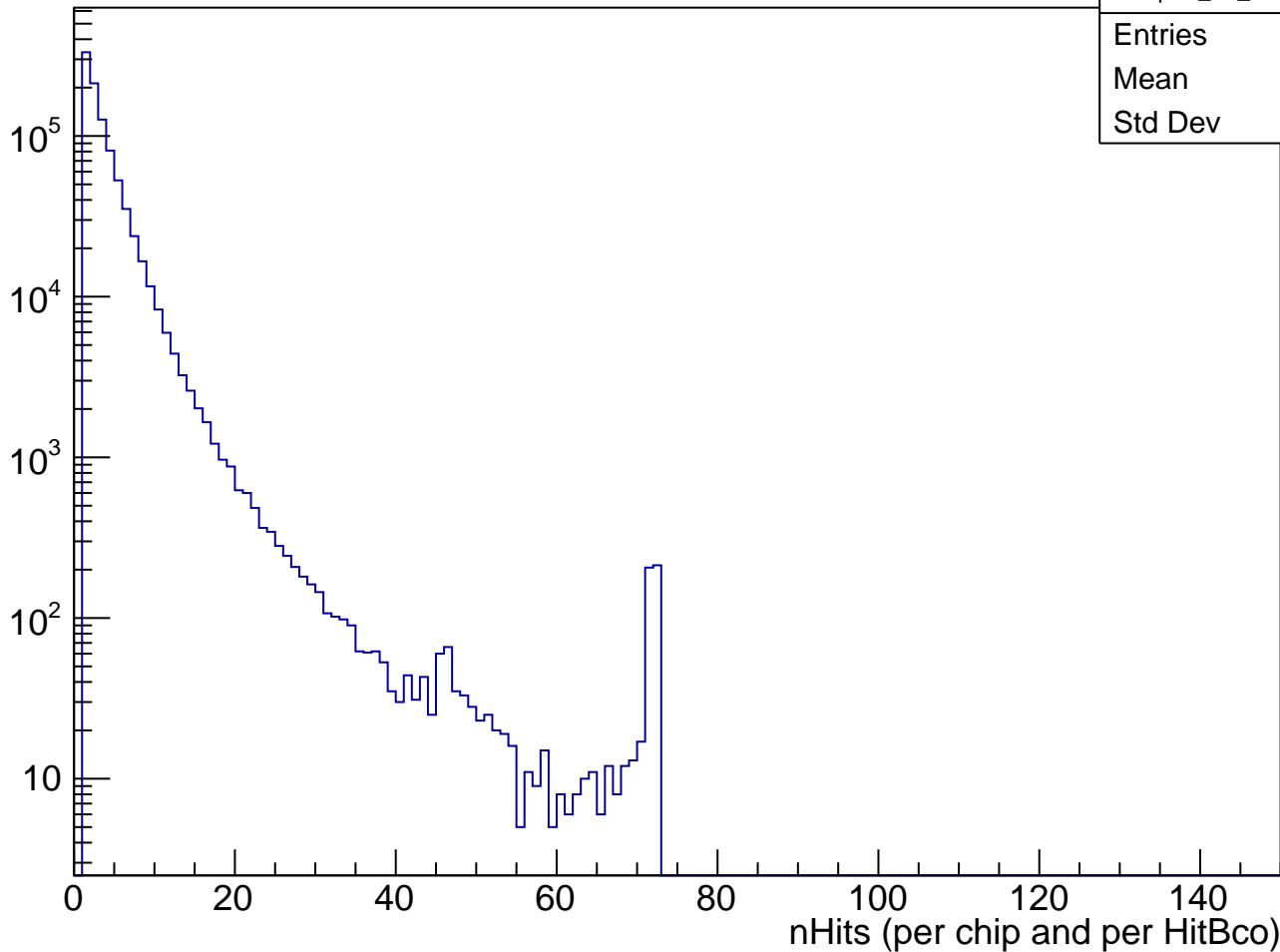


nChipHit_F0_Fch1_Chip23	
Entries	937292
Mean	3.077
Std Dev	3.517

nHits (per chip and per HitBco)

nChipHit_F0_Fch1_Chip24

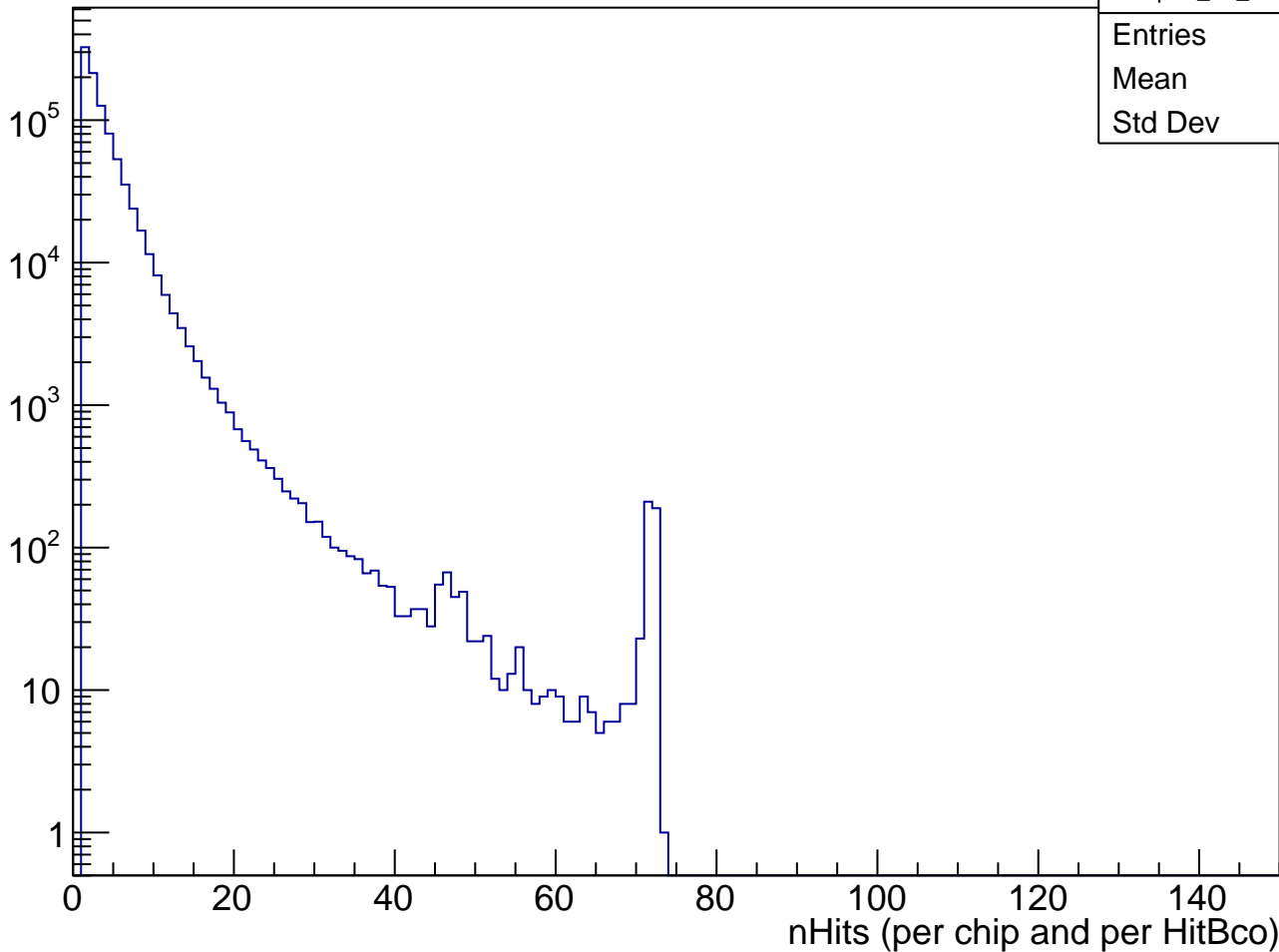
Entries



nChipHit_F0_Fch1_Chip24	
Entries	928958
Mean	3.126
Std Dev	3.604

nChipHit_F0_Fch1_Chip25

Entries

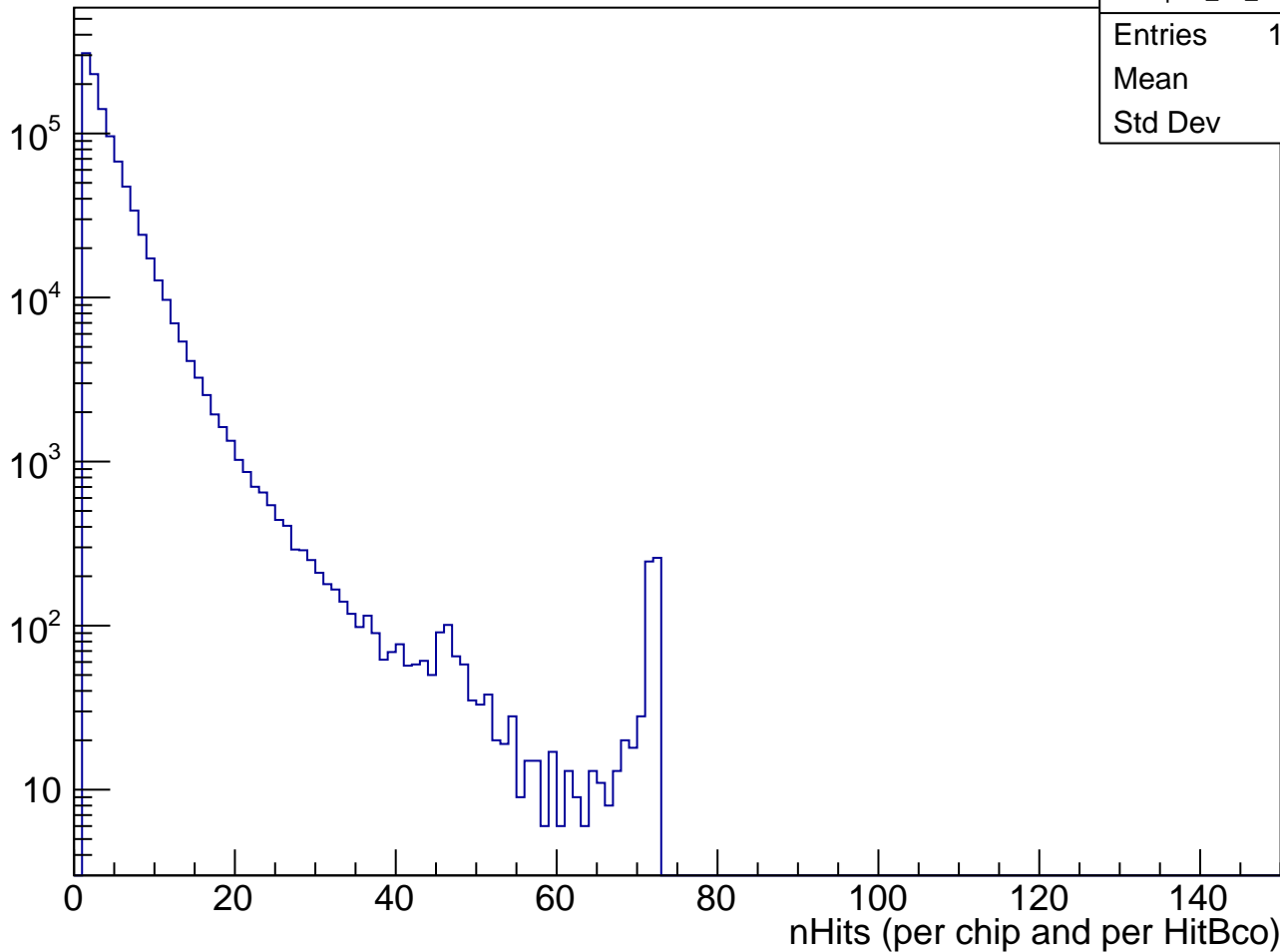


nChipHit_F0_Fch1_Chip25	
Entries	923503
Mean	3.146
Std Dev	3.611

nHits (per chip and per HitBco)

nChipHit_F0_Fch1_Chip3

Entries



nChipHit_F0_Fch1_Chip3

Entries 1023704

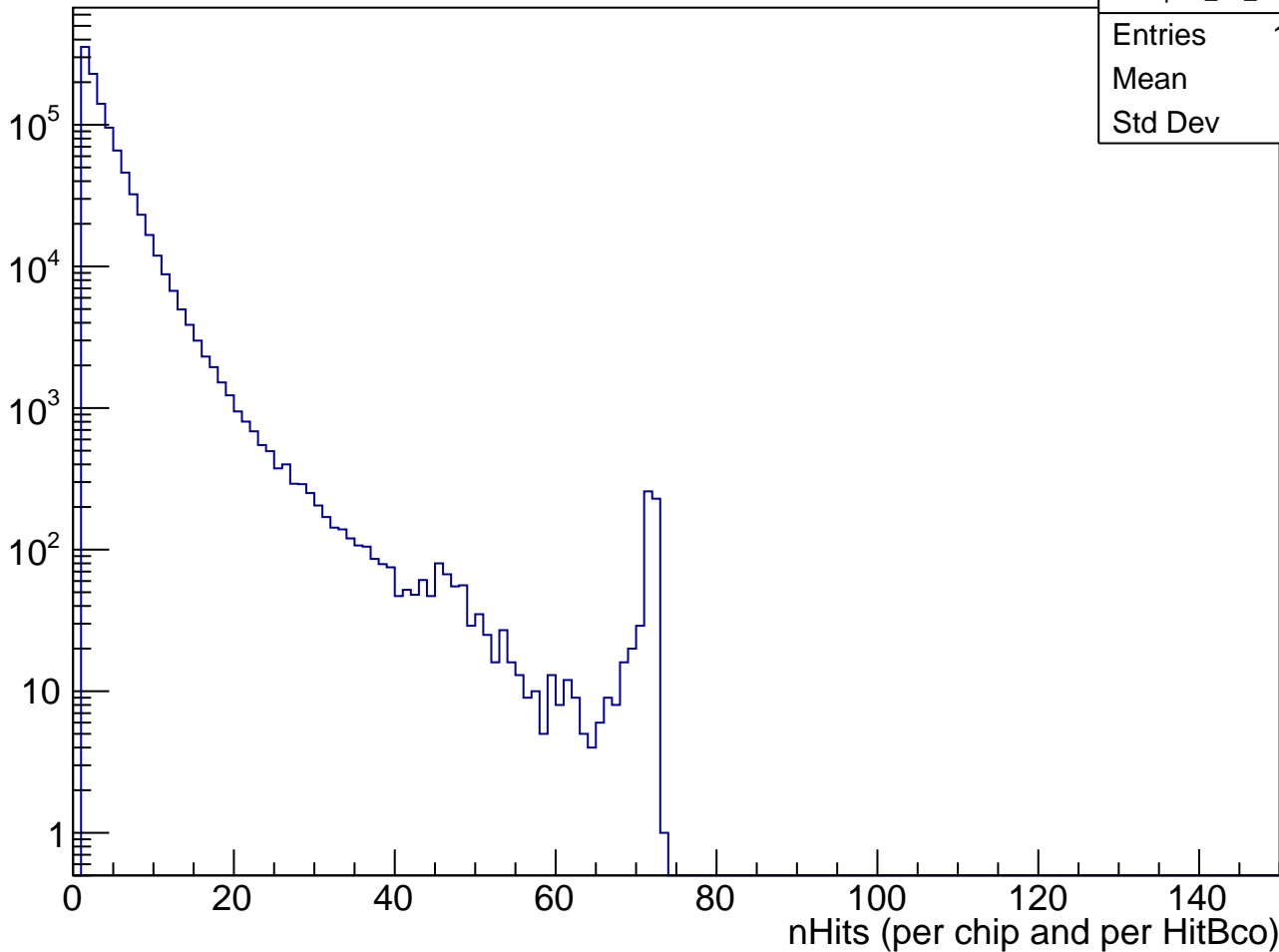
Mean 3.574

Std Dev 4.019

nHits (per chip and per HitBco)

nChipHit_F0_Fch1_Chip4

Entries



nChipHit_F0_Fch1_Chip4

Entries 1058191

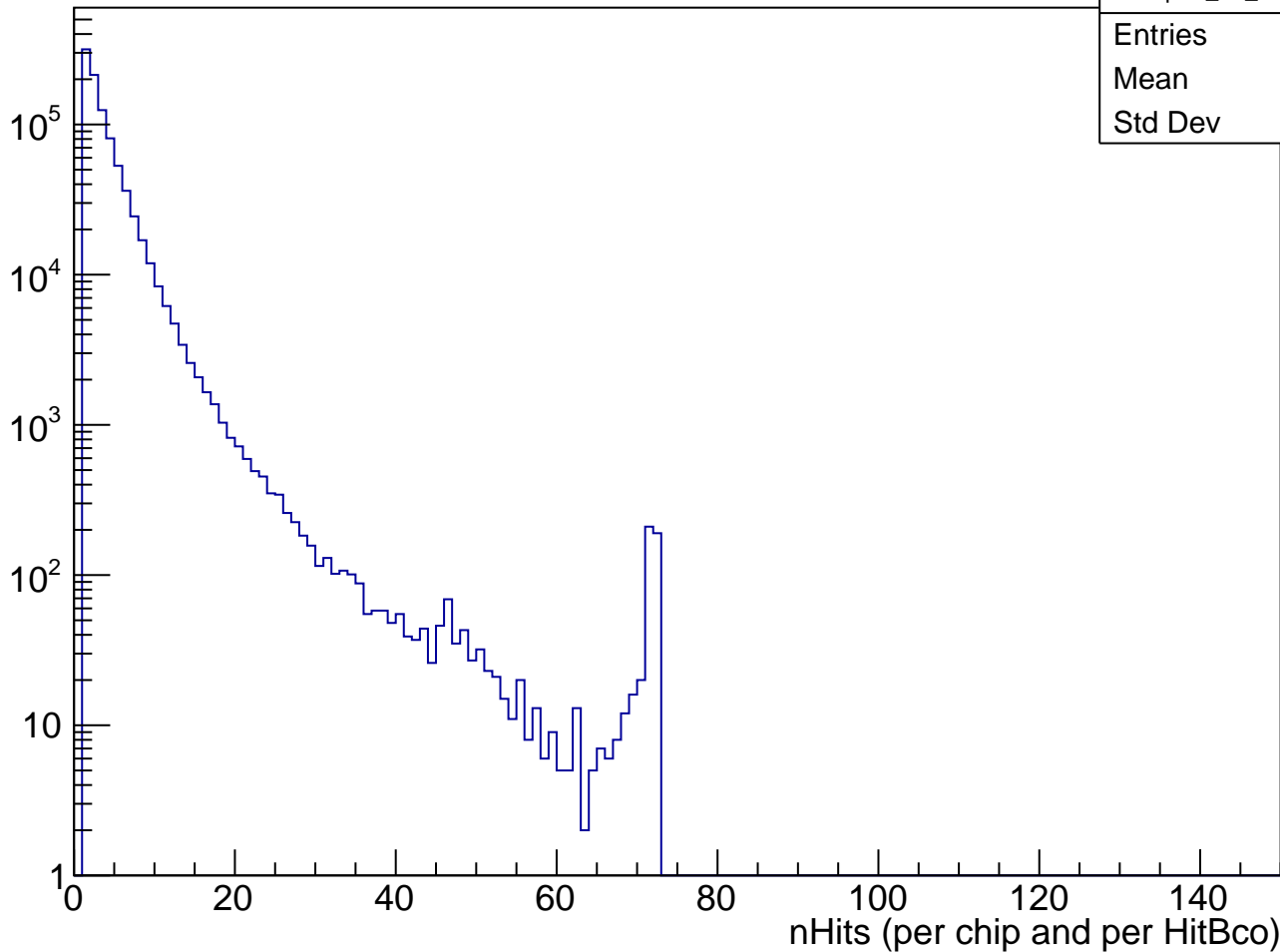
Mean 3.403

Std Dev 3.878

nHits (per chip and per HitBco)

nChipHit_F0_Fch1_Chip5

Entries



nChipHit_F0_Fch1_Chip5

Entries 916037

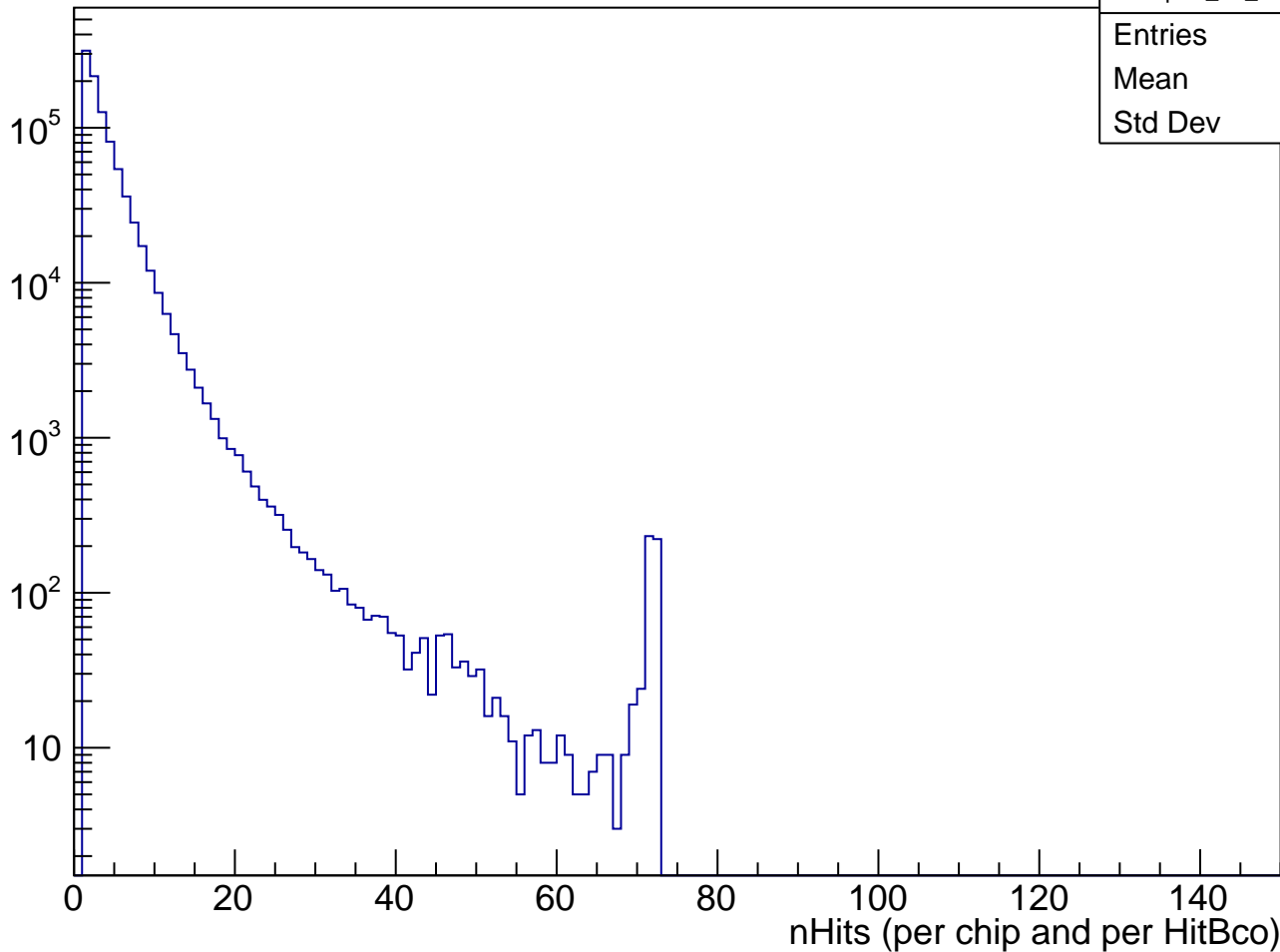
Mean 3.188

Std Dev 3.652

nHits (per chip and per HitBco)

nChipHit_F0_Fch1_Chip6

Entries



nChipHit_F0_Fch1_Chip6

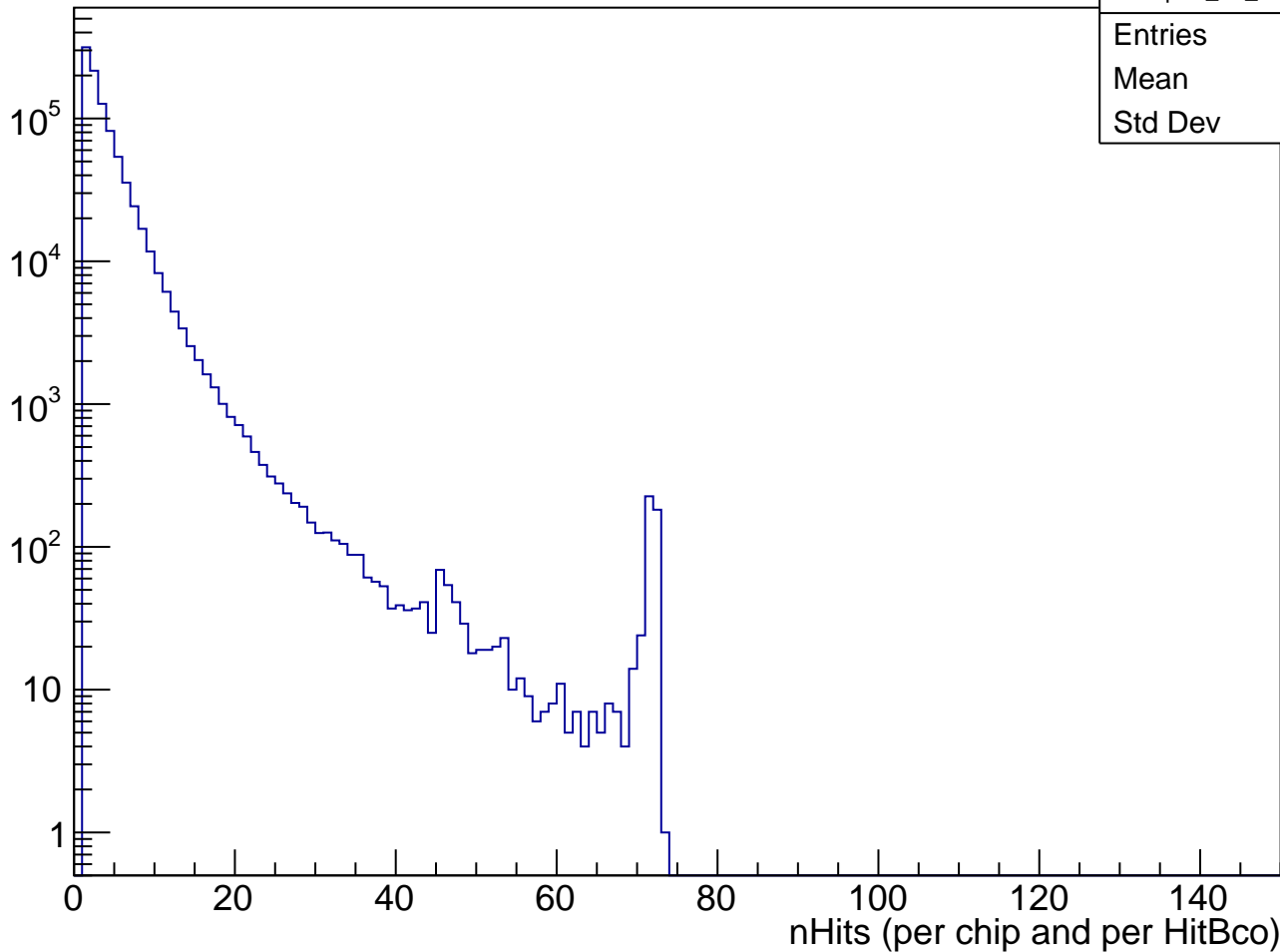
Entries 919605

Mean 3.202

Std Dev 3.688

nChipHit_F0_Fch1_Chip7

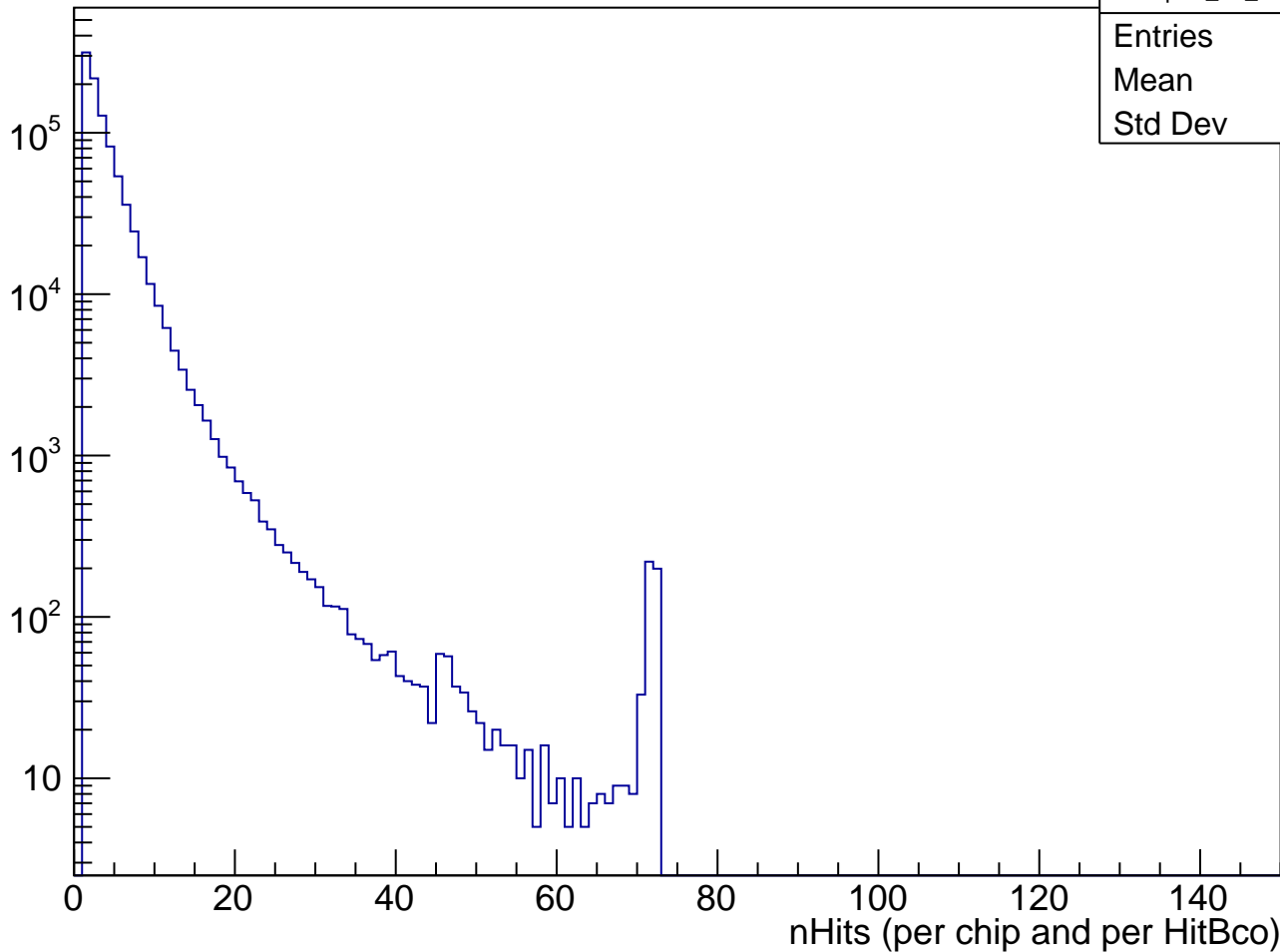
Entries



nChipHit_F0_Fch1_Chip7	
Entries	918976
Mean	3.169
Std Dev	3.6

nChipHit_F0_Fch1_Chip8

Entries



nChipHit_F0_Fch1_Chip8

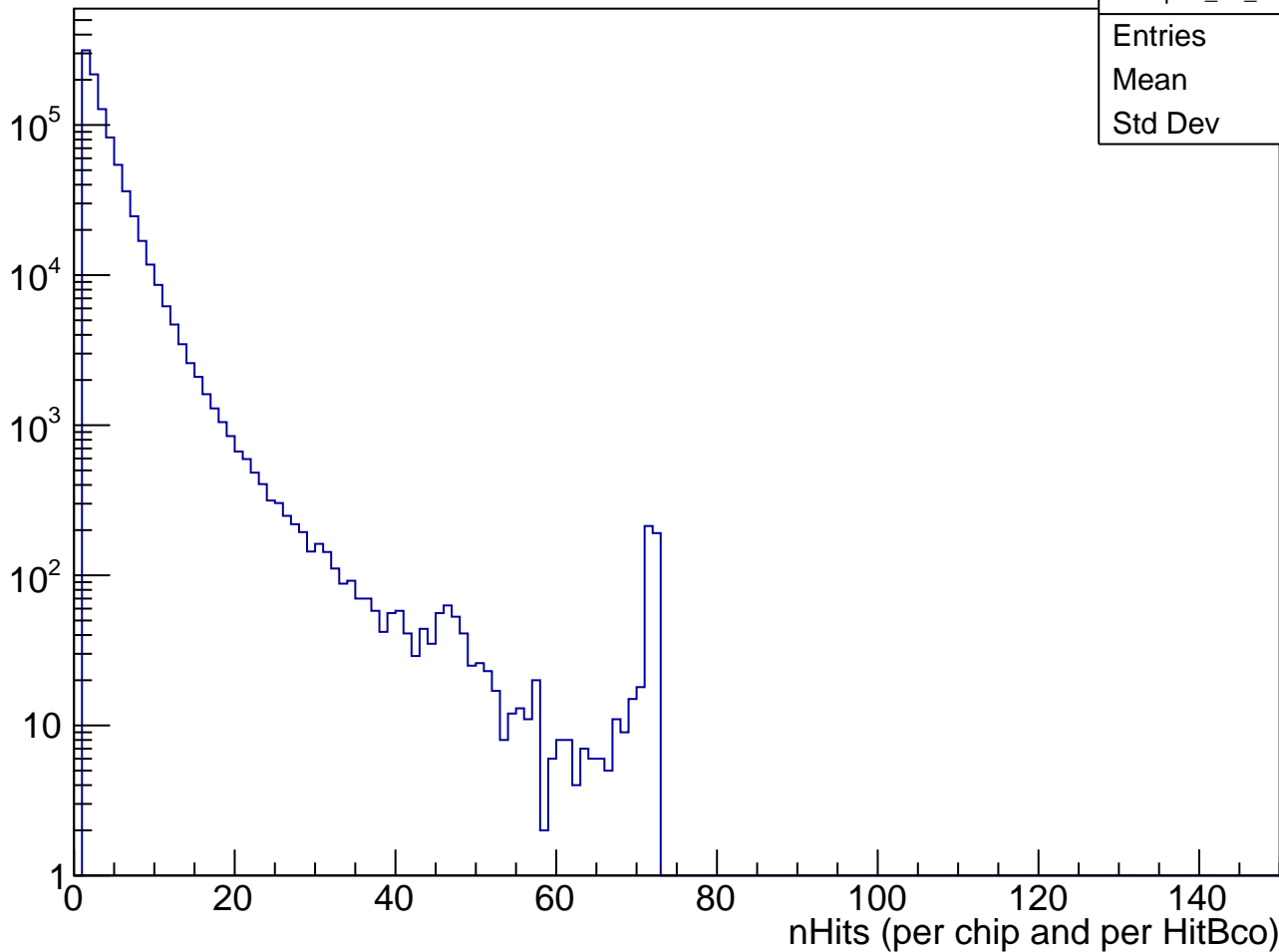
Entries 922081

Mean 3.18

Std Dev 3.635

nChipHit_F0_Fch1_Chip9

Entries



nChipHit_F0_Fch1_Chip9

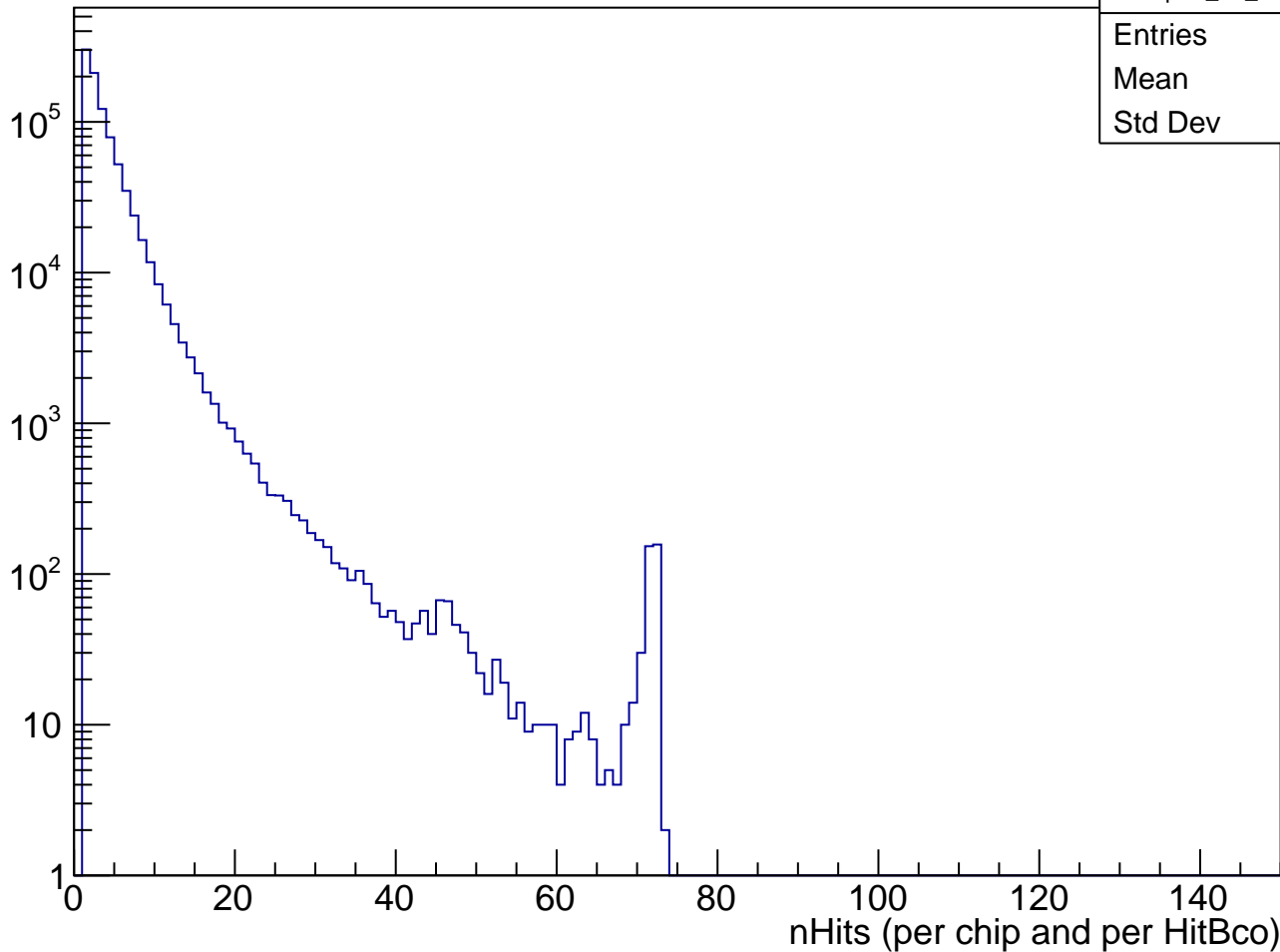
Entries 923734

Mean 3.188

Std Dev 3.628

nChipHit_F0_Fch2_Chip0

Entries

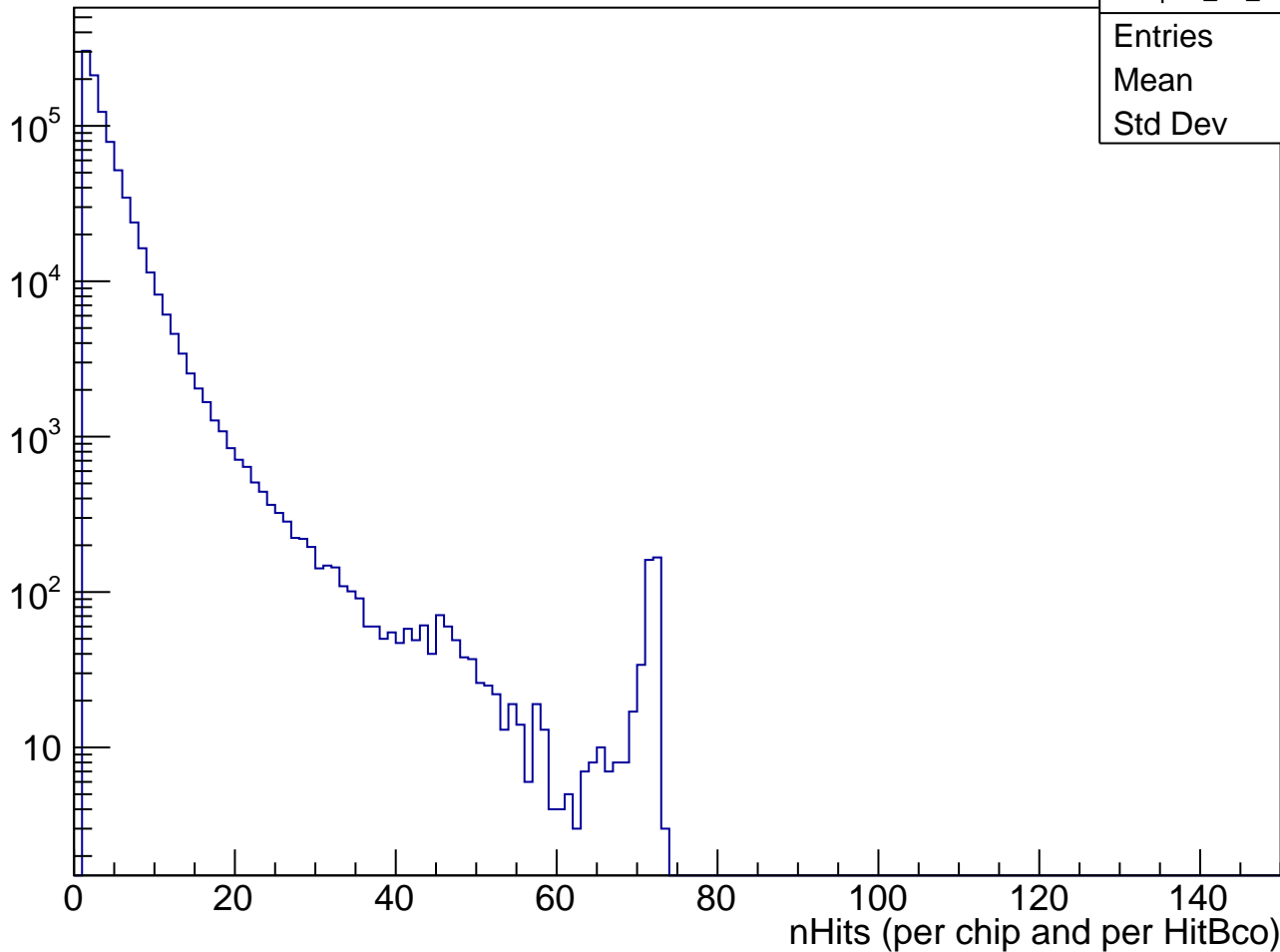


nChipHit_F0_Fch2_Chip0	
Entries	892188
Mean	3.219
Std Dev	3.675

nHits (per chip and per HitBco)

nChipHit_F0_Fch2_Chip1

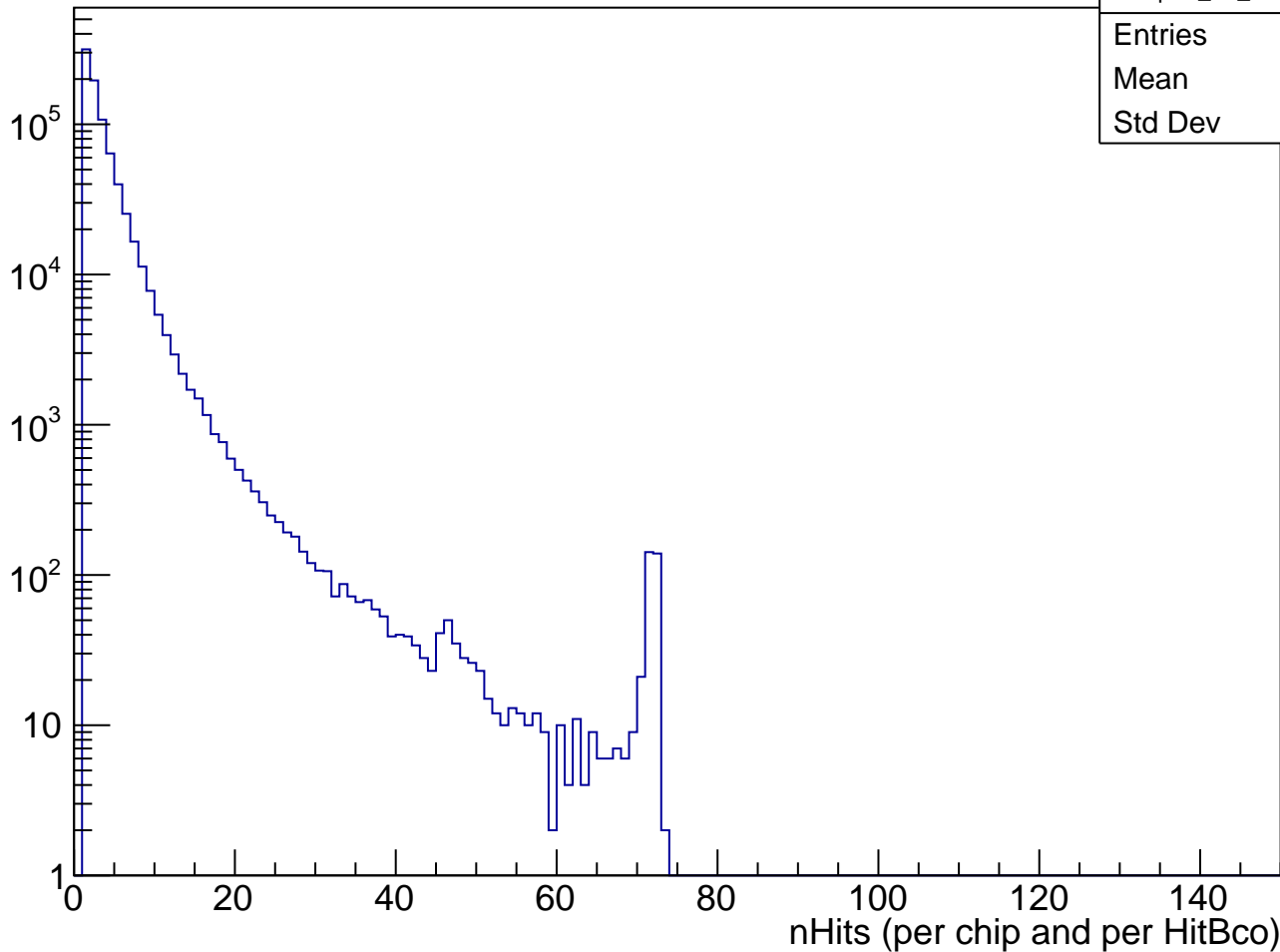
Entries



nChipHit_F0_Fch2_Chip1	
Entries	893501
Mean	3.204
Std Dev	3.682

nChipHit_F0_Fch2_Chip10

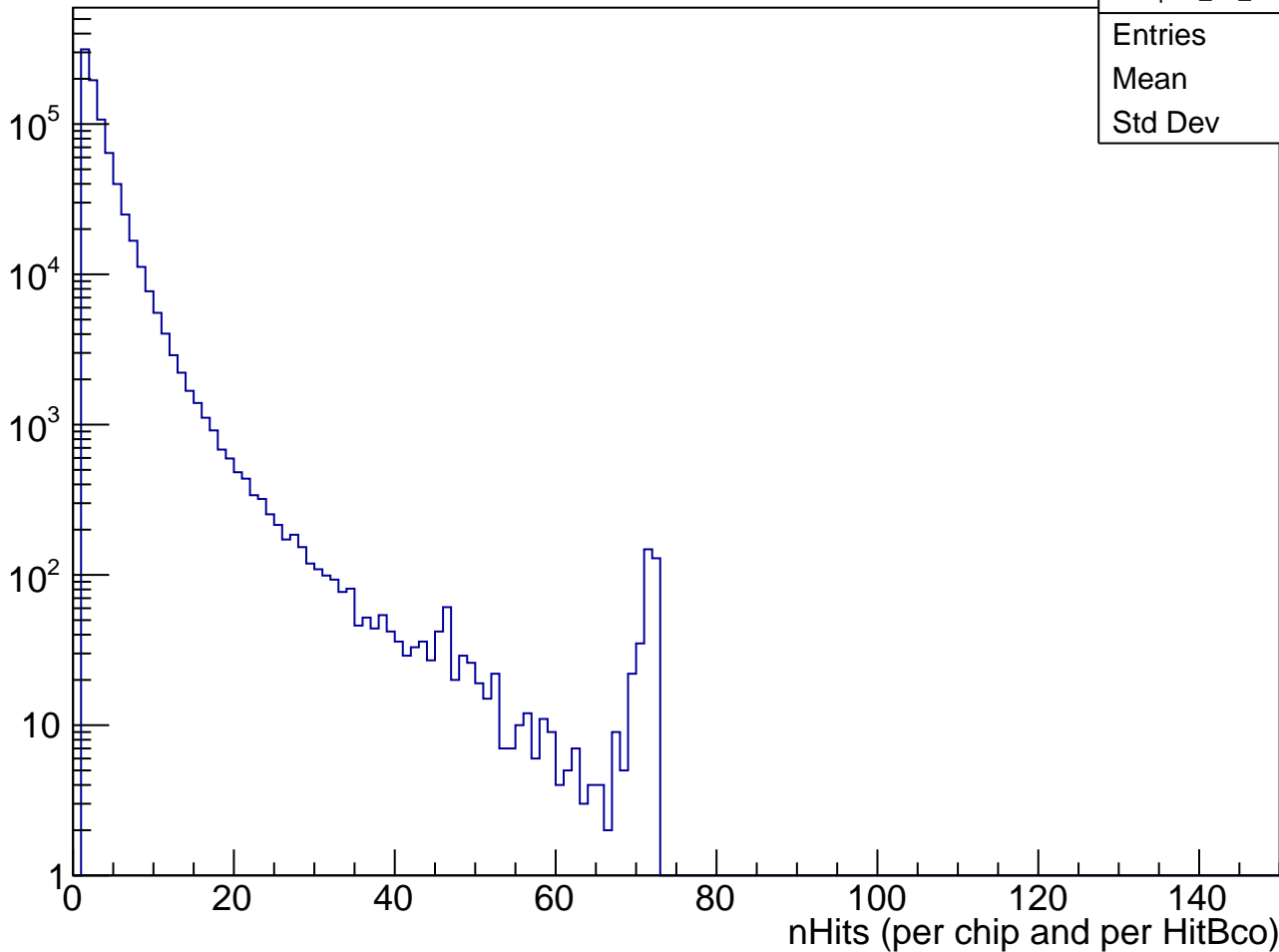
Entries



nChipHit_F0_Fch2_Chip10	
Entries	808898
Mean	2.861
Std Dev	3.376

nChipHit_F0_Fch2_Chip11

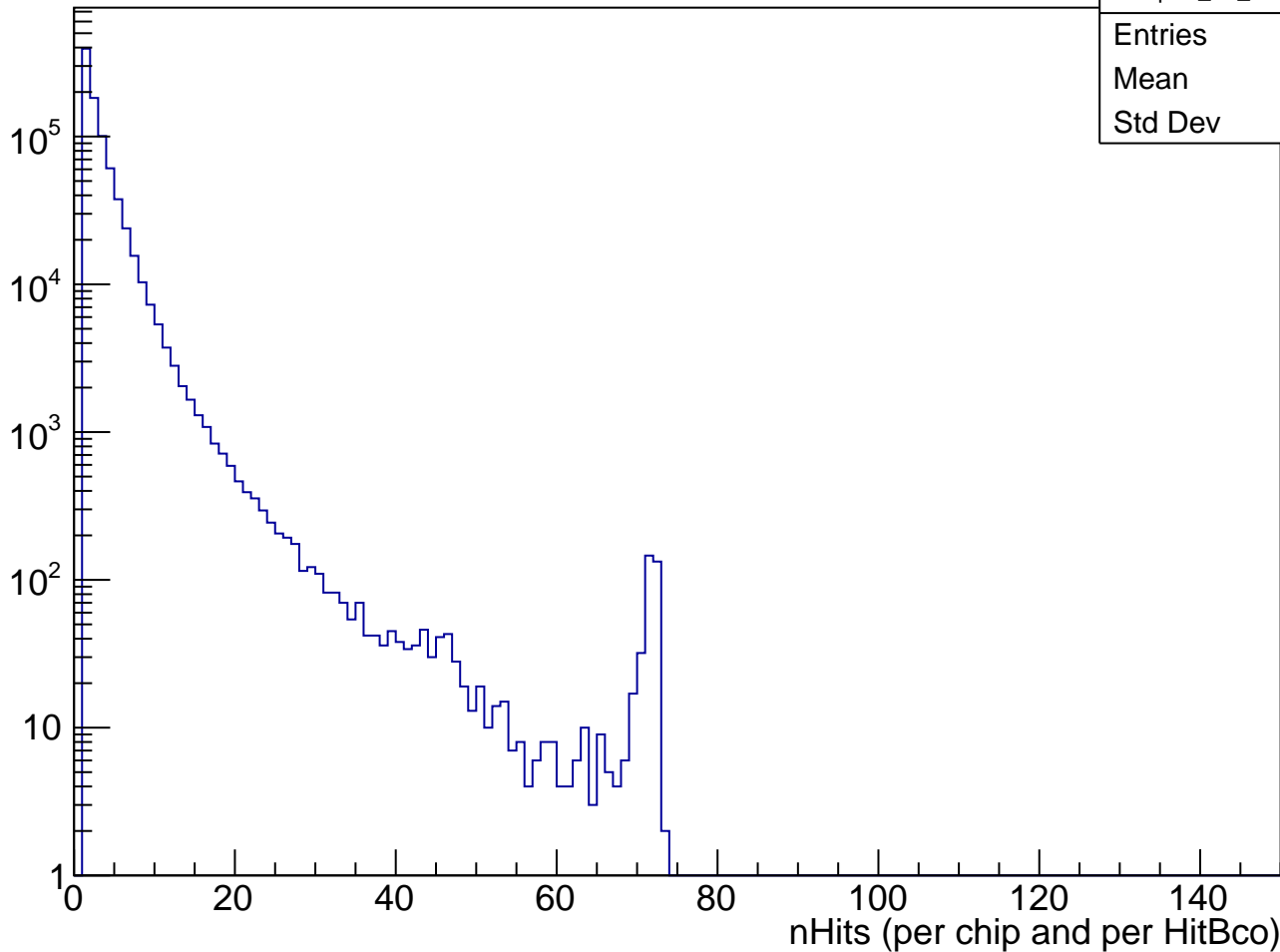
Entries



nChipHit_F0_Fch2_Chip11	
Entries	807002
Mean	2.86
Std Dev	3.368

nChipHit_F0_Fch2_Chip12

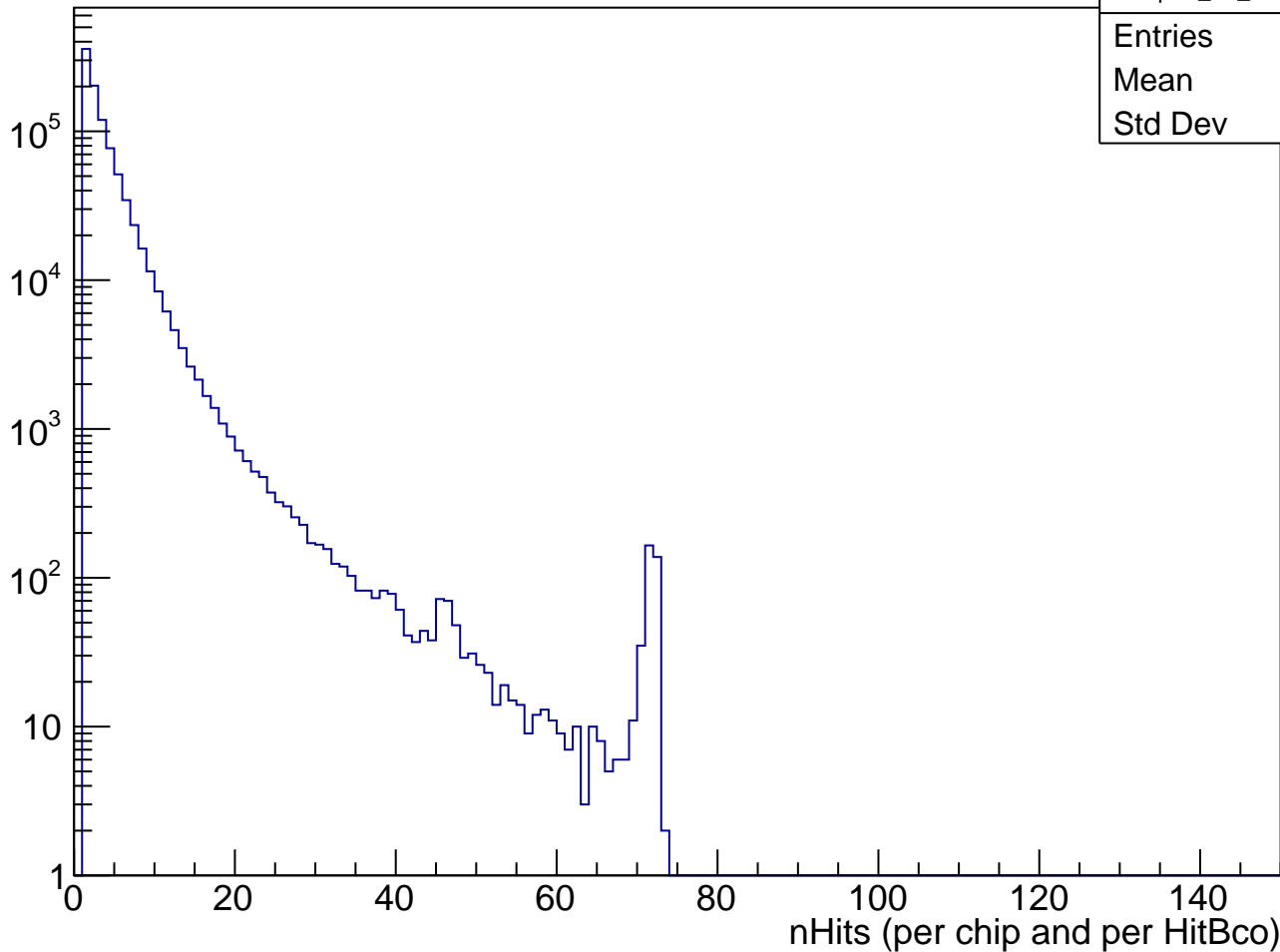
Entries



nChipHit_F0_Fch2_Chip12	
Entries	856058
Mean	2.656
Std Dev	3.248

nChipHit_F0_Fch2_Chip13

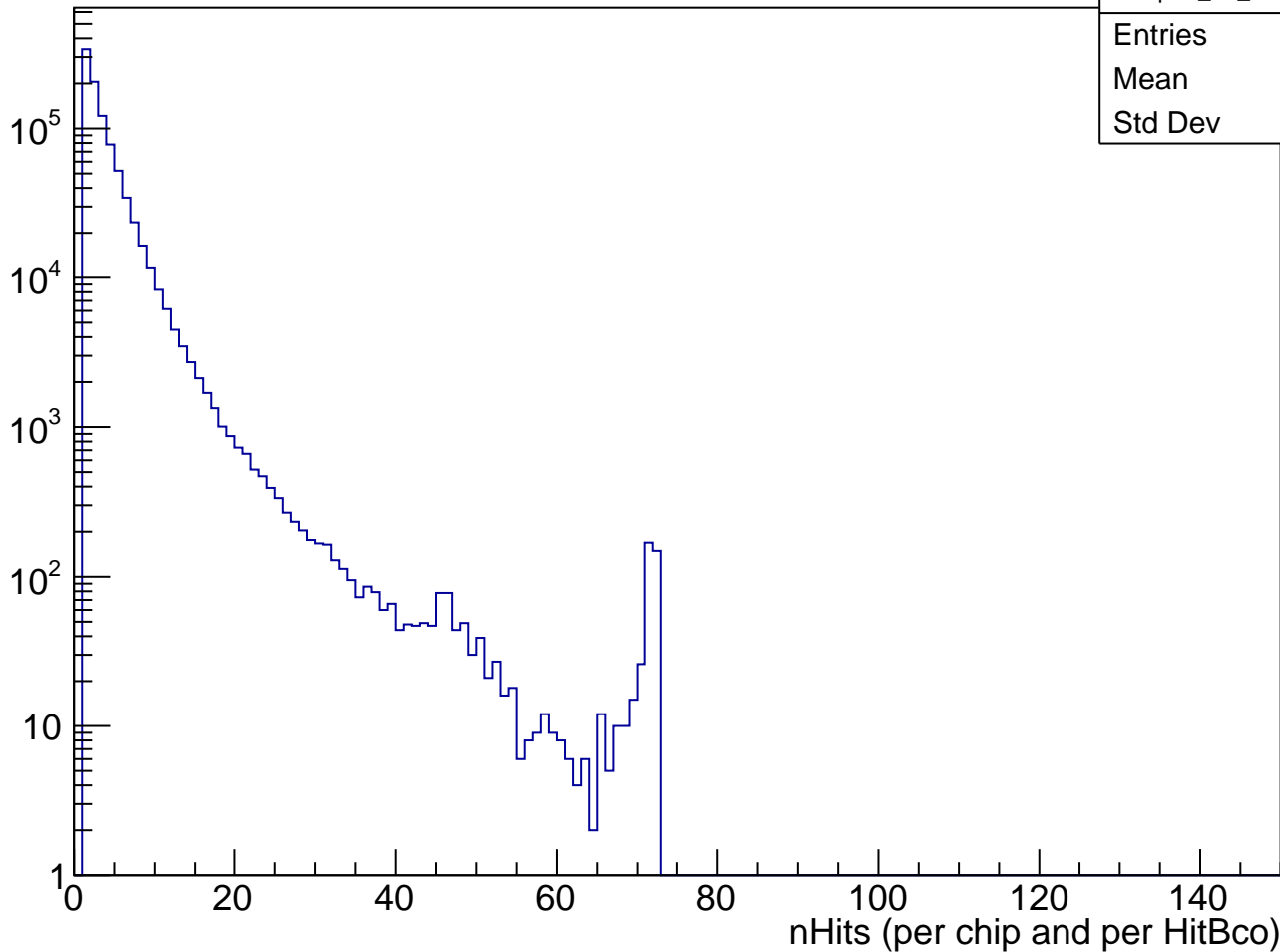
Entries



nChipHit_F0_Fch2_Chip13	
Entries	932091
Mean	3.095
Std Dev	3.64

nChipHit_F0_Fch2_Chip14

Entries

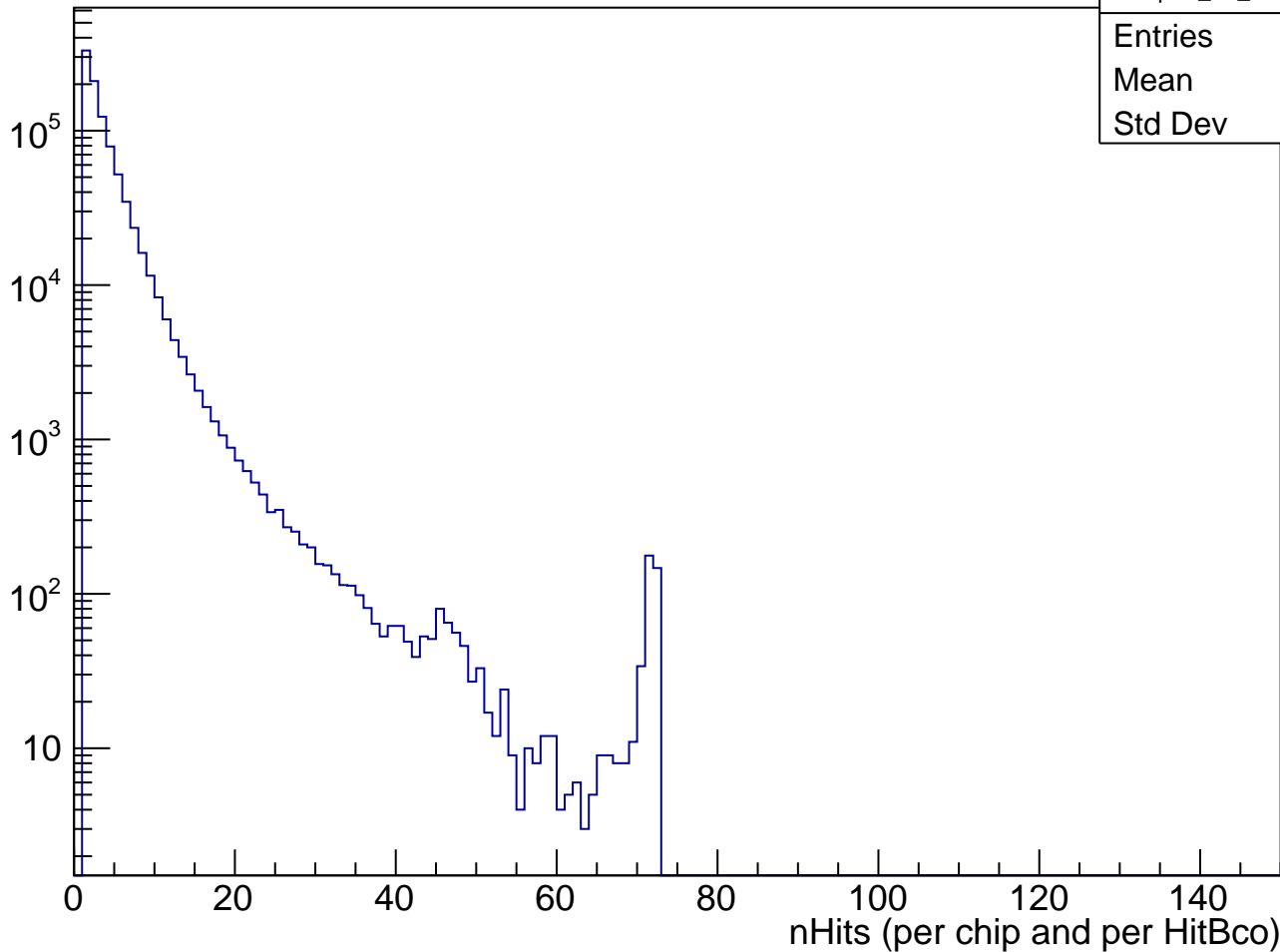


nChipHit_F0_Fch2_Chip14	
Entries	919624
Mean	3.136
Std Dev	3.66

nHits (per chip and per HitBco)

nChipHit_F0_Fch2_Chip15

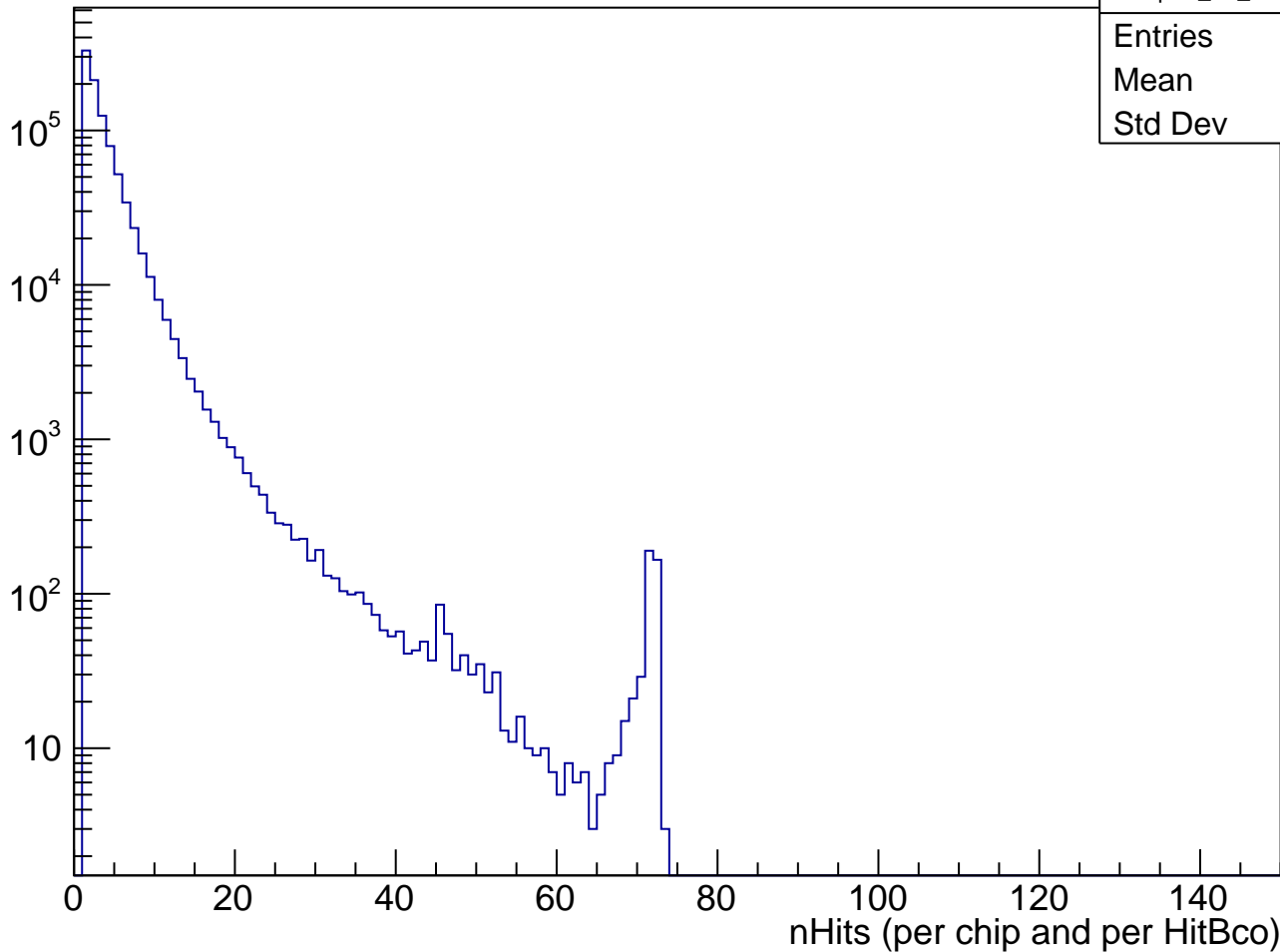
Entries



nChipHit_F0_Fch2_Chip15	
Entries	918141
Mean	3.144
Std Dev	3.65

nChipHit_F0_Fch2_Chip16

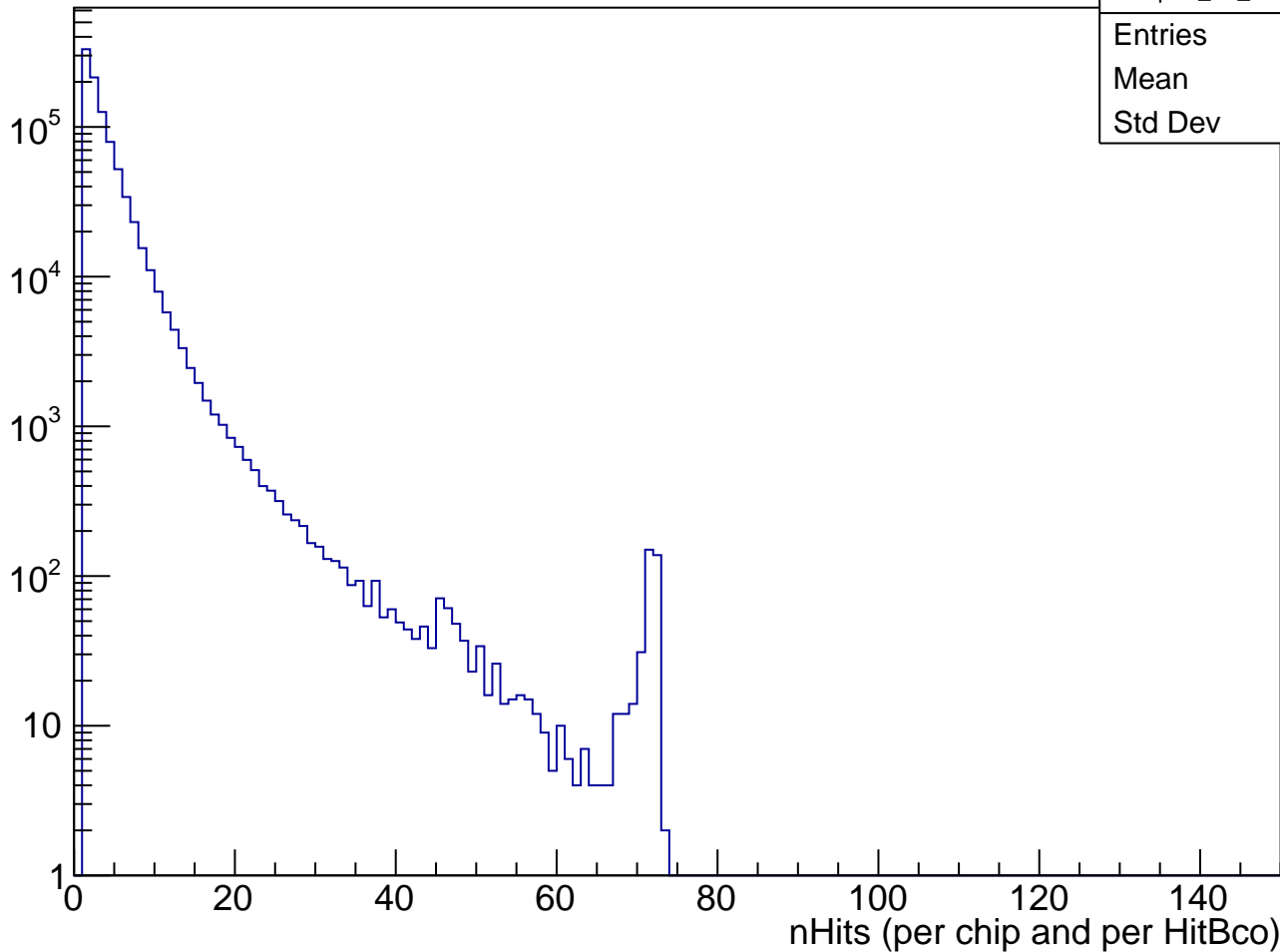
Entries



nChipHit_F0_Fch2_Chip16	
Entries	918701
Mean	3.132
Std Dev	3.65

nChipHit_F0_Fch2_Chip17

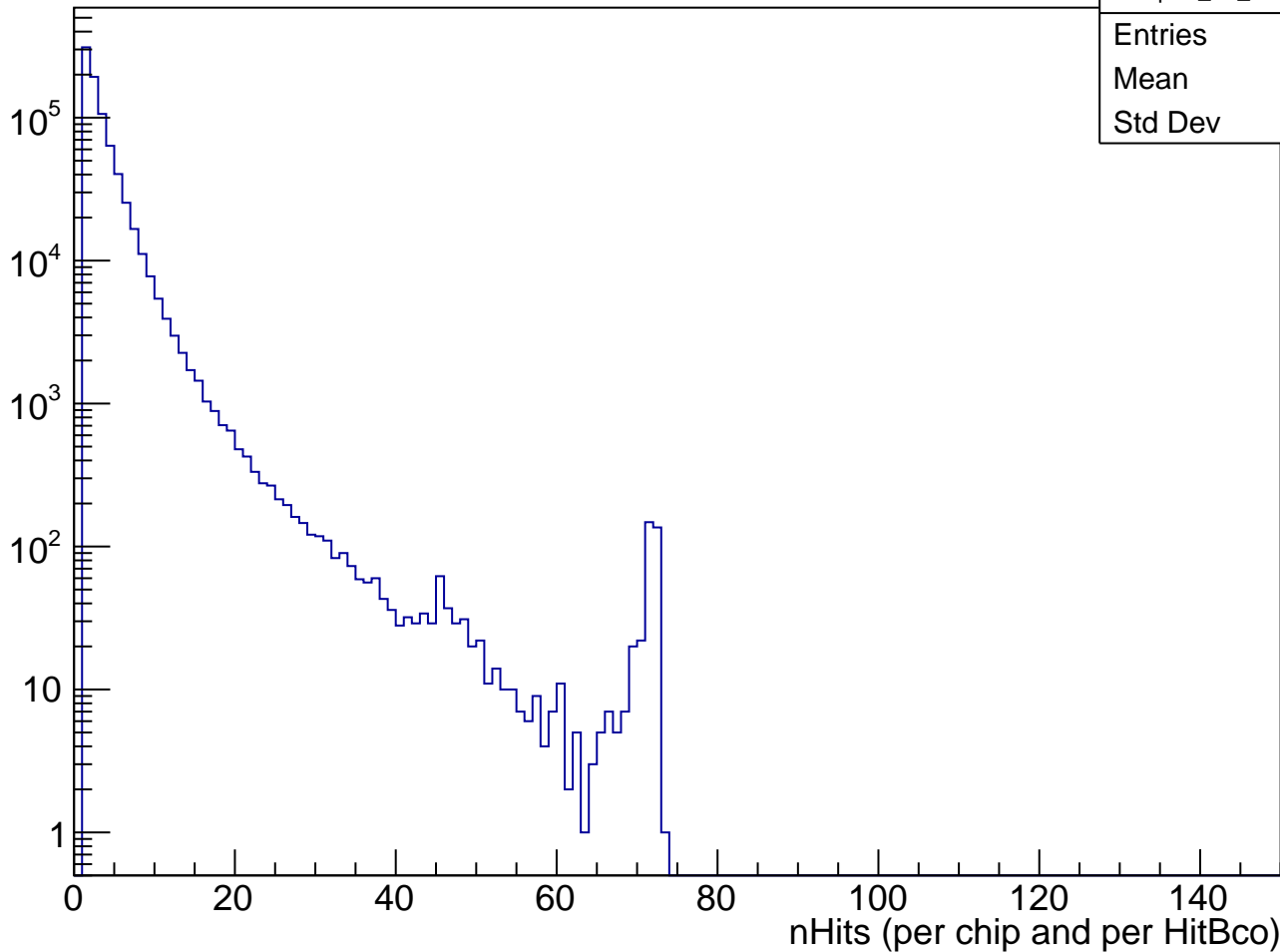
Entries



nChipHit_F0_Fch2_Chip17	
Entries	922578
Mean	3.105
Std Dev	3.565

nChipHit_F0_Fch2_Chip18

Entries

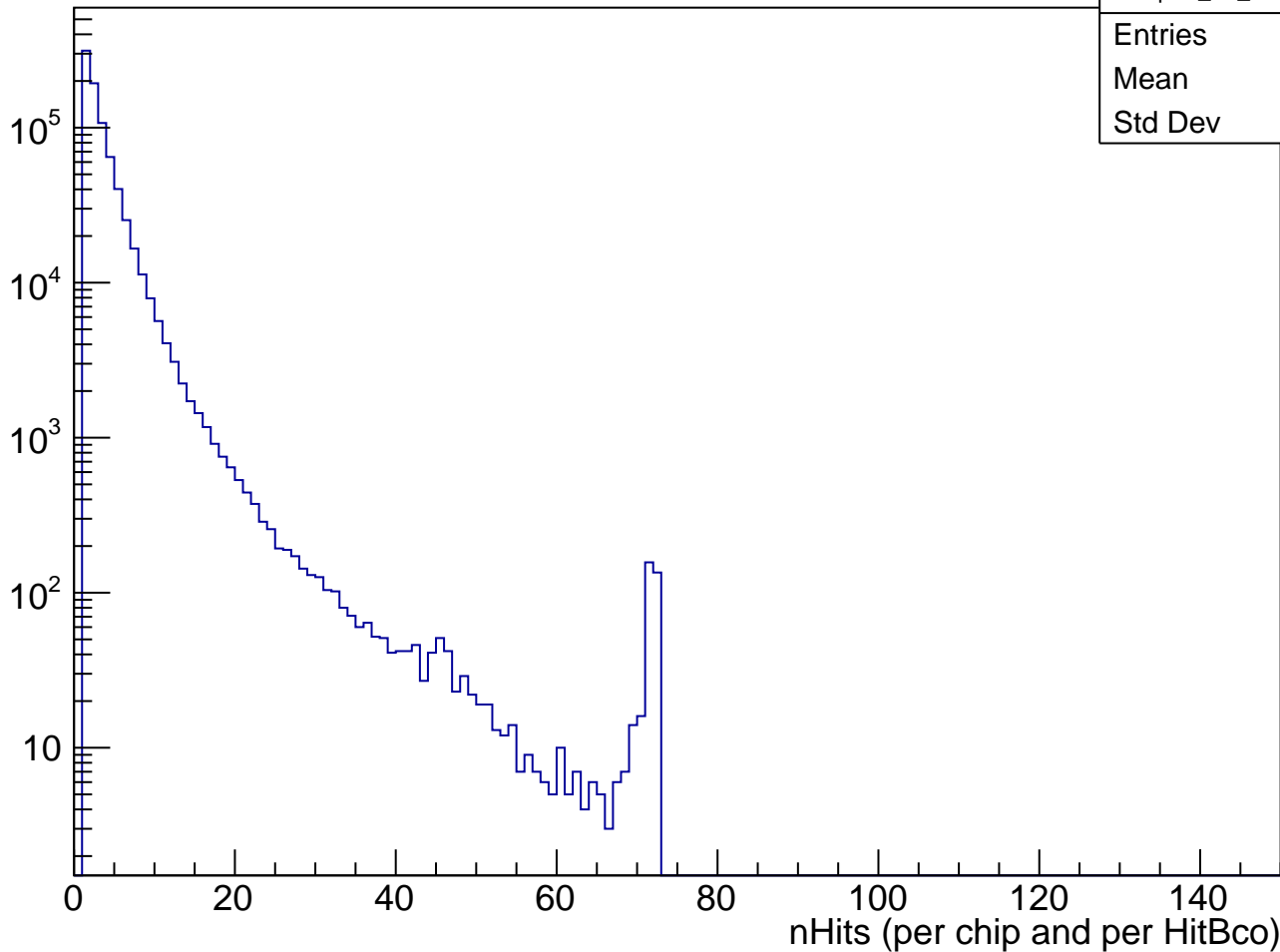


nChipHit_F0_Fch2_Chip18	
Entries	799669
Mean	2.87
Std Dev	3.368

nHits (per chip and per HitBco)

nChipHit_F0_Fch2_Chip19

Entries

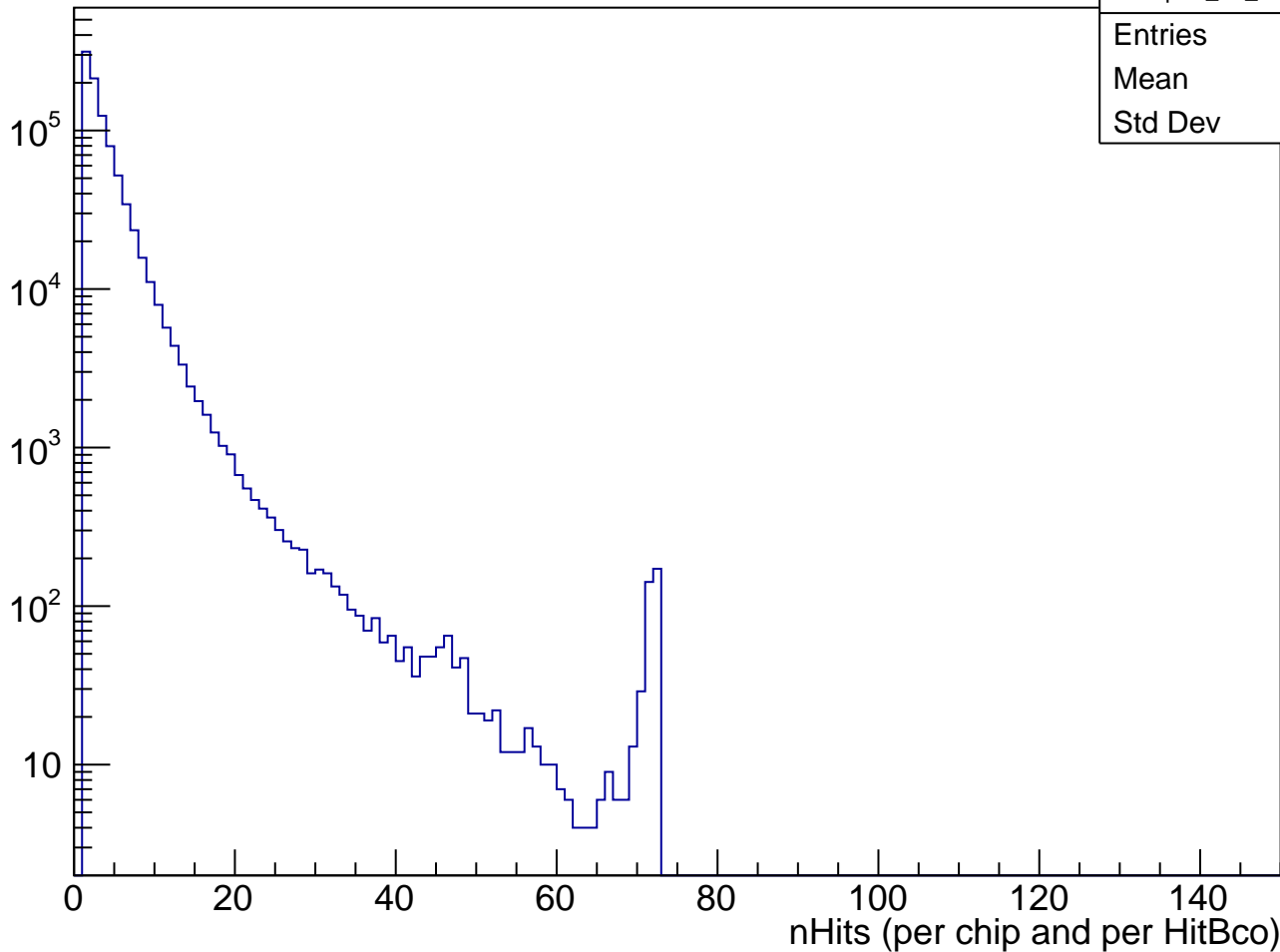


nChipHit_F0_Fch2_Chip19	
Entries	806312
Mean	2.879
Std Dev	3.395

nHits (per chip and per HitBco)

nChipHit_F0_Fch2_Chip2

Entries

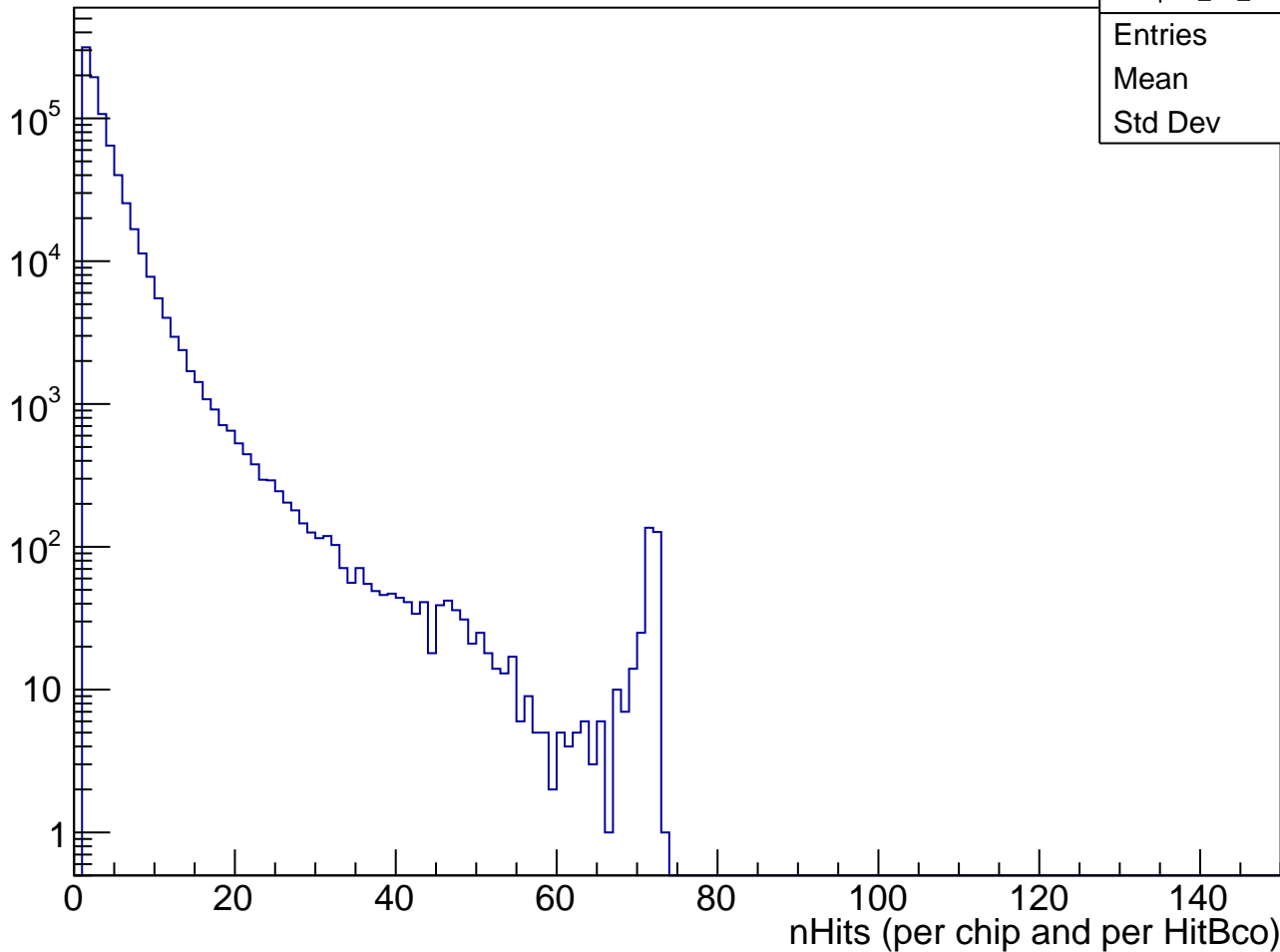


nChipHit_F0_Fch2_Chip2	
Entries	903749
Mean	3.151
Std Dev	3.606

nHits (per chip and per HitBco)

nChipHit_F0_Fch2_Chip20

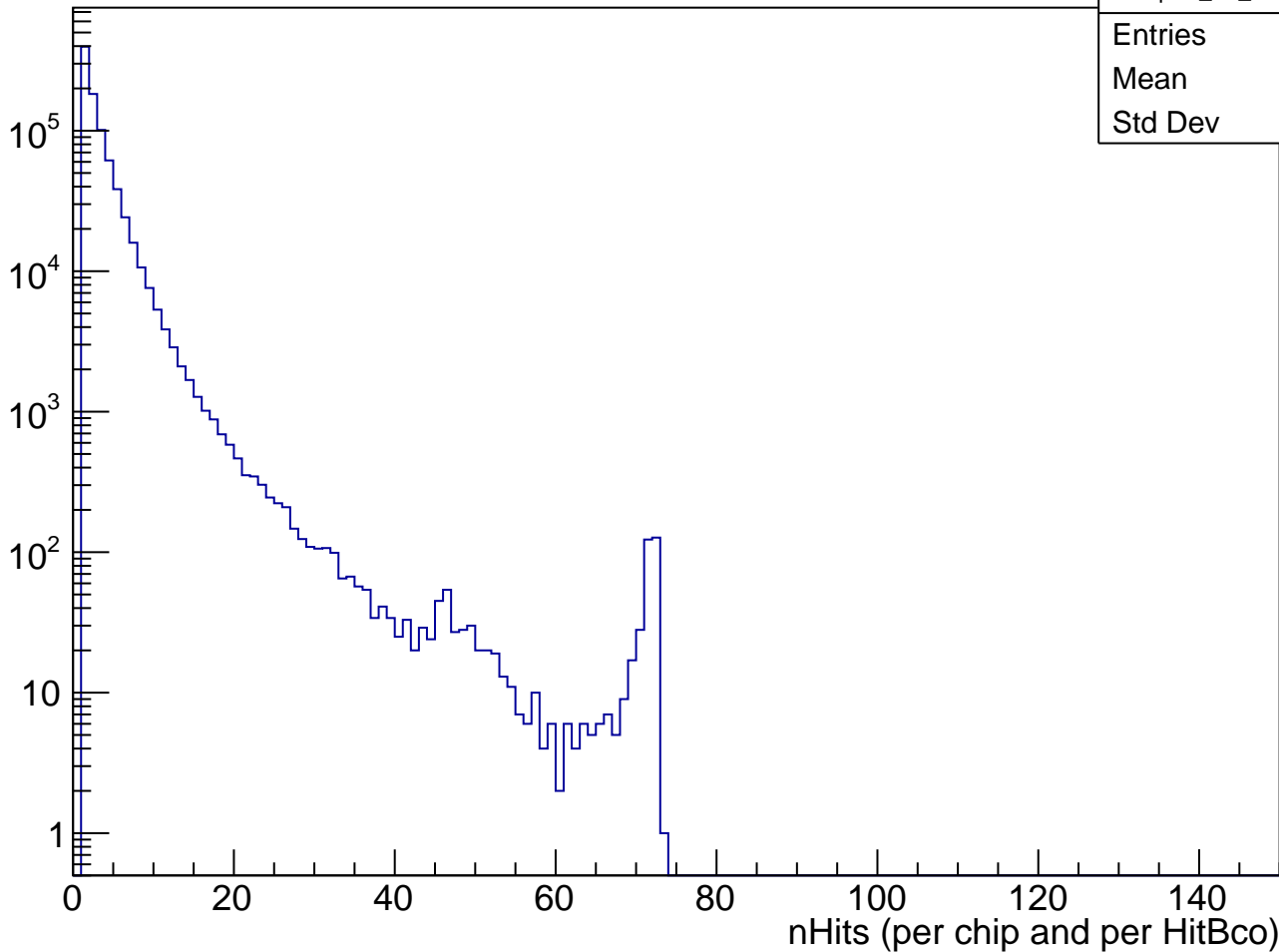
Entries



nChipHit_F0_Fch2_Chip20	
Entries	807488
Mean	2.871
Std Dev	3.369

nChipHit_F0_Fch2_Chip21

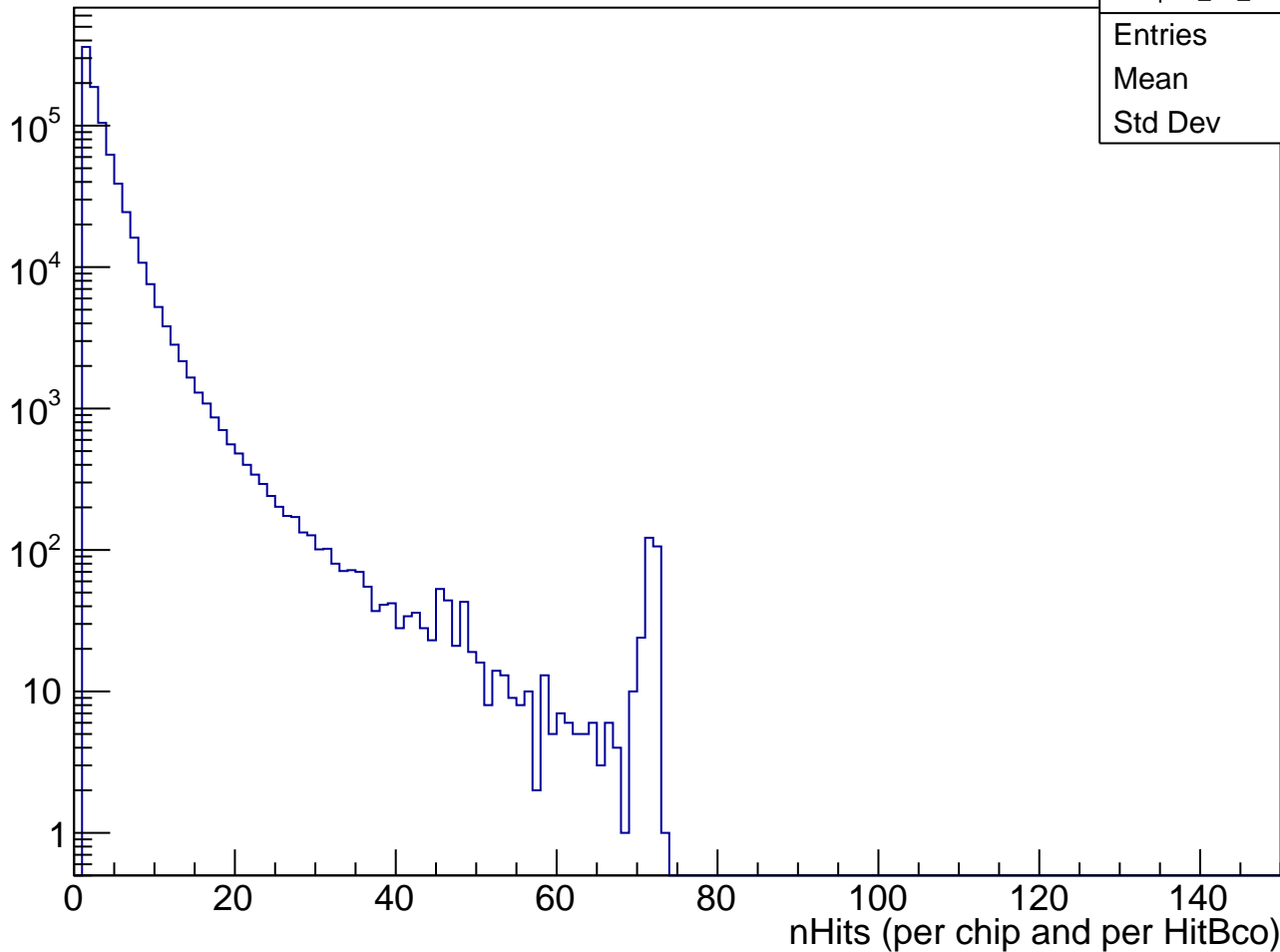
Entries



nChipHit_F0_Fch2_Chip21	
Entries	862915
Mean	2.658
Std Dev	3.224

nChipHit_F0_Fch2_Chip22

Entries

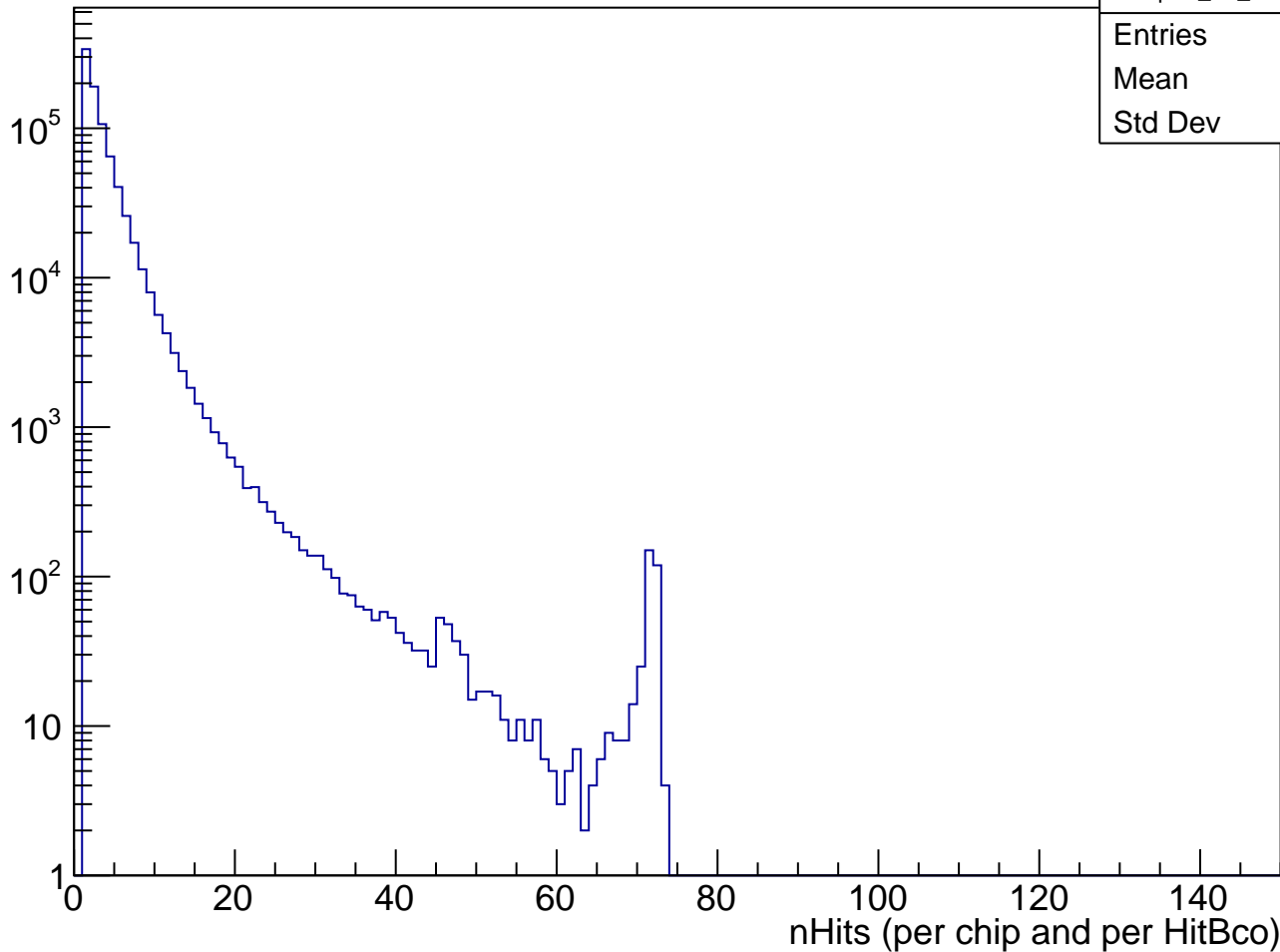


nChipHit_F0_Fch2_Chip22	
Entries	836774
Mean	2.73
Std Dev	3.225

nHits (per chip and per HitBco)

nChipHit_F0_Fch2_Chip23

Entries

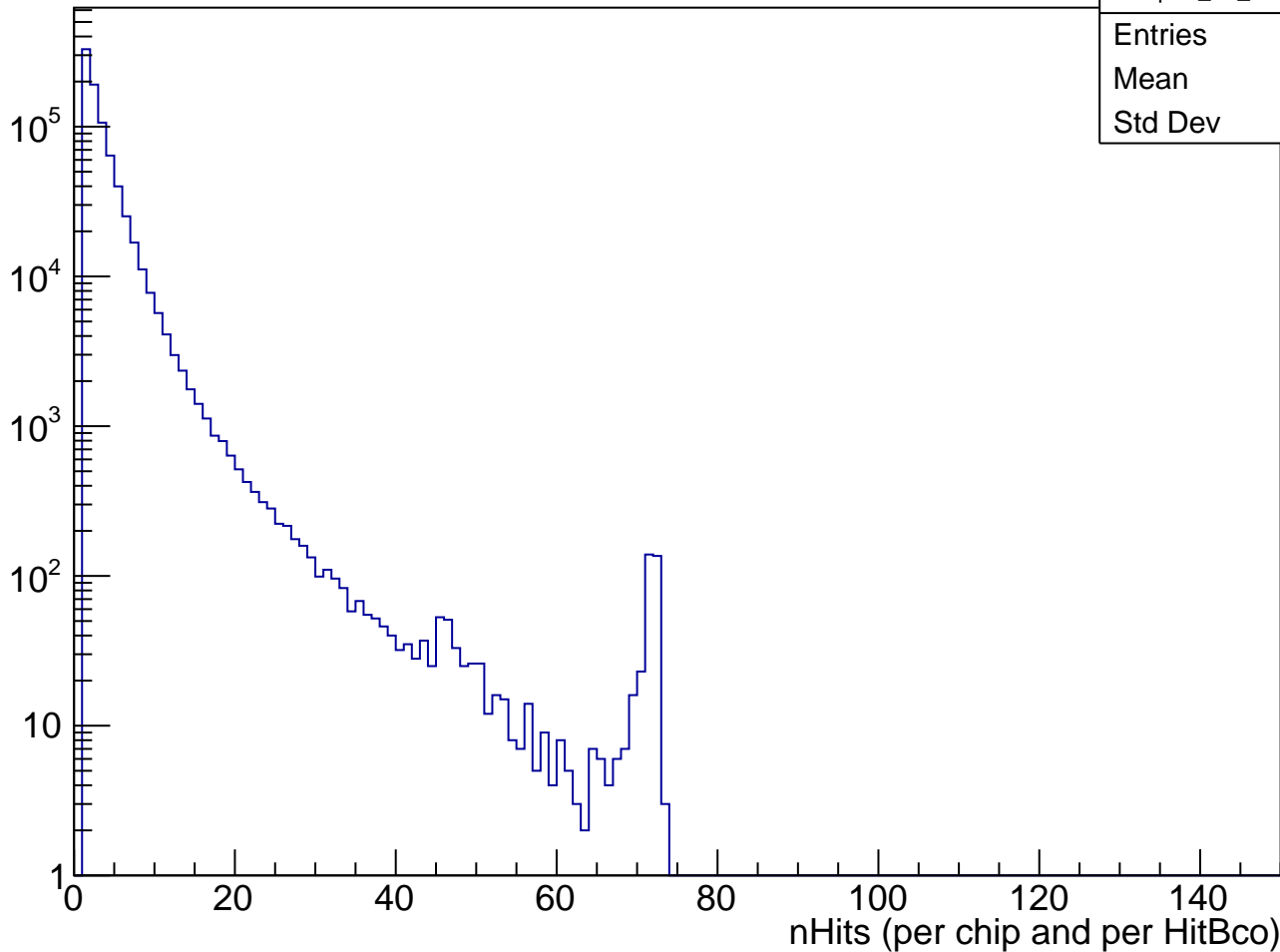


nChipHit_F0_Fch2_Chip23	
Entries	829488
Mean	2.84
Std Dev	3.377

nHits (per chip and per HitBco)

nChipHit_F0_Fch2_Chip24

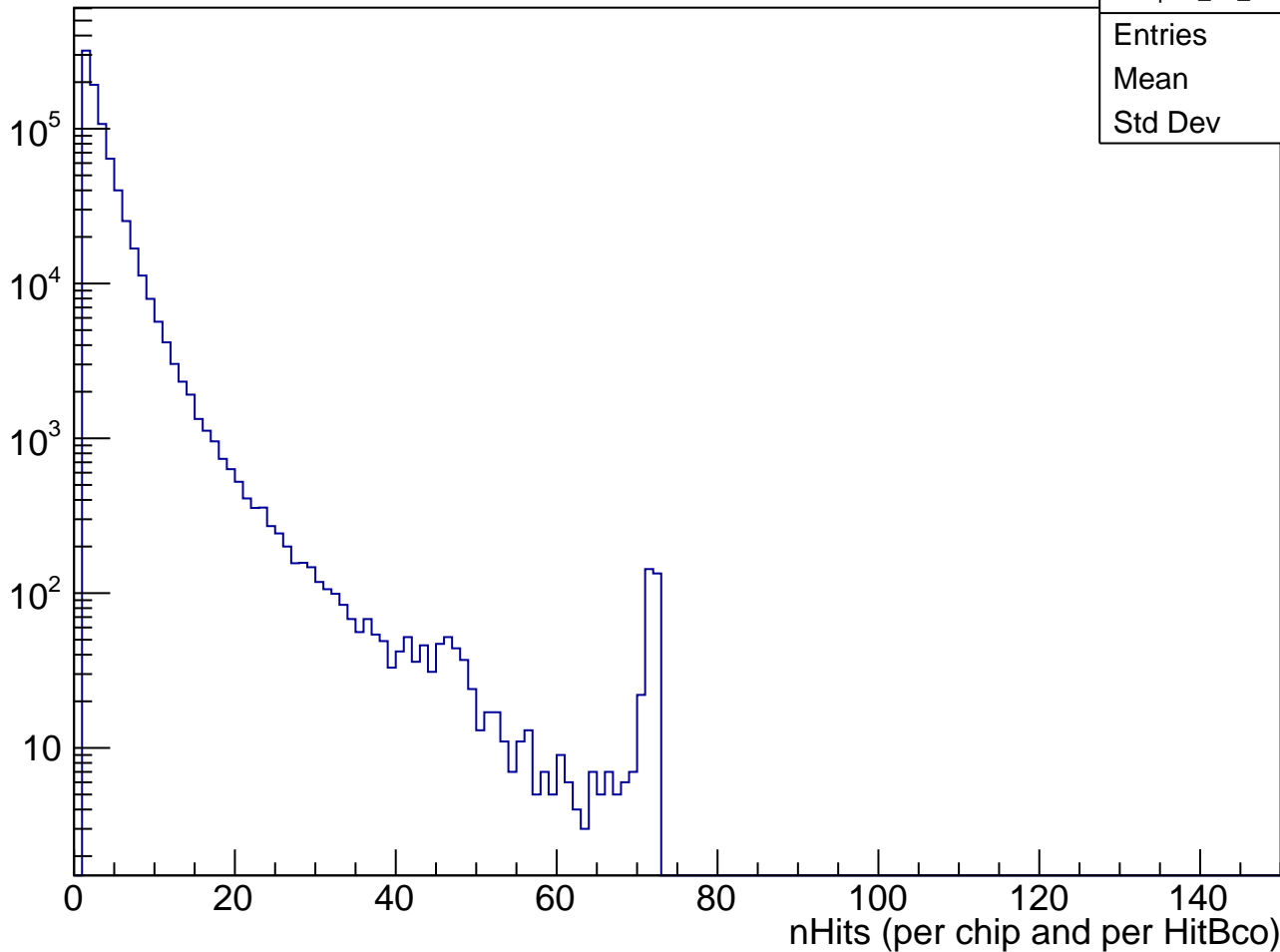
Entries



nChipHit_F0_Fch2_Chip24	
Entries	817244
Mean	2.843
Std Dev	3.372

nChipHit_F0_Fch2_Chip25

Entries

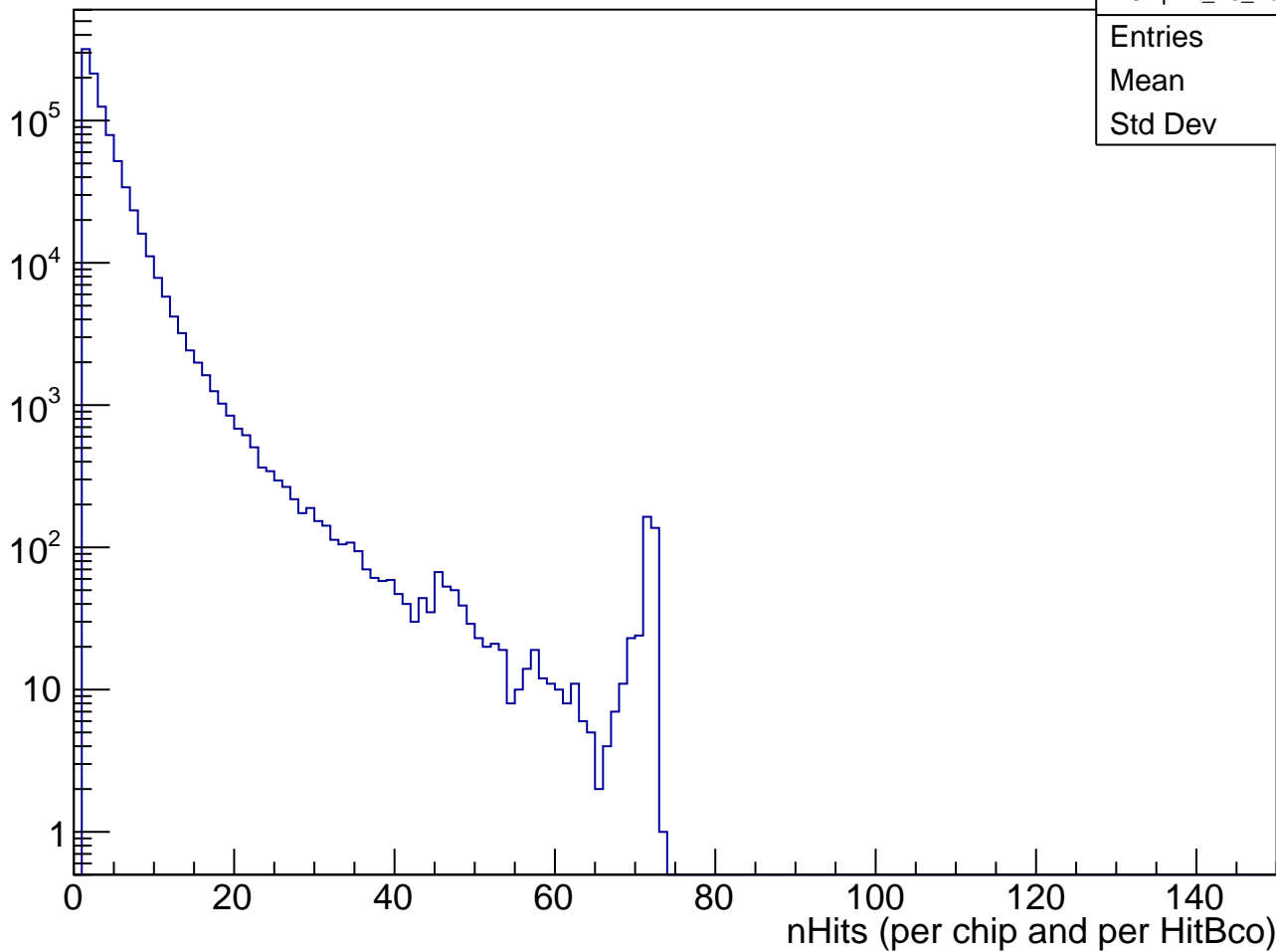


nChipHit_F0_Fch2_Chip25	
Entries	811026
Mean	2.871
Std Dev	3.399

nHits (per chip and per HitBco)

nChipHit_F0_Fch2_Chip3

Entries



nChipHit_F0_Fch2_Chip3

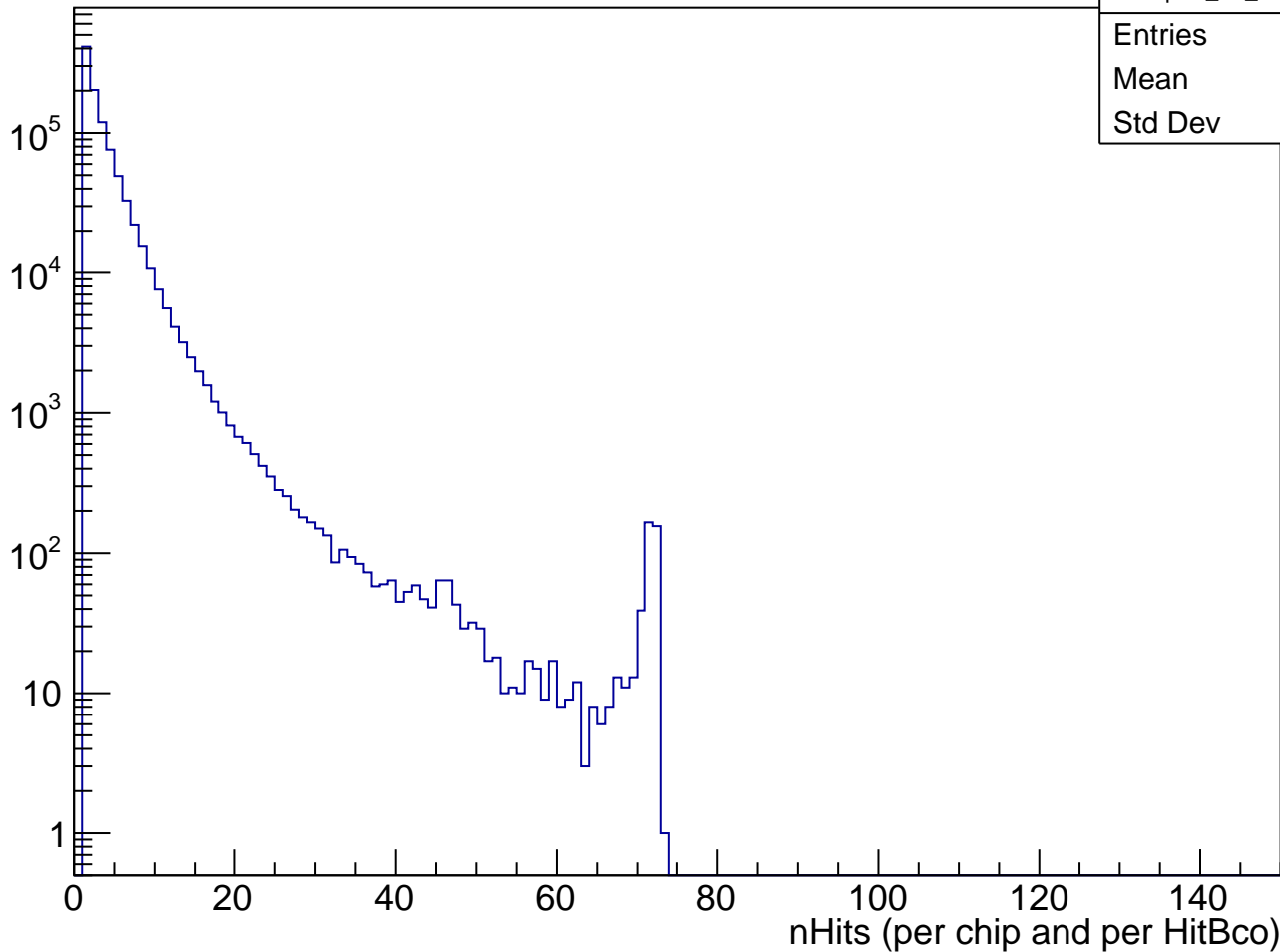
Entries 908514

Mean 3.133

Std Dev 3.58

nChipHit_F0_Fch2_Chip4

Entries



nChipHit_F0_Fch2_Chip4

Entries 975562

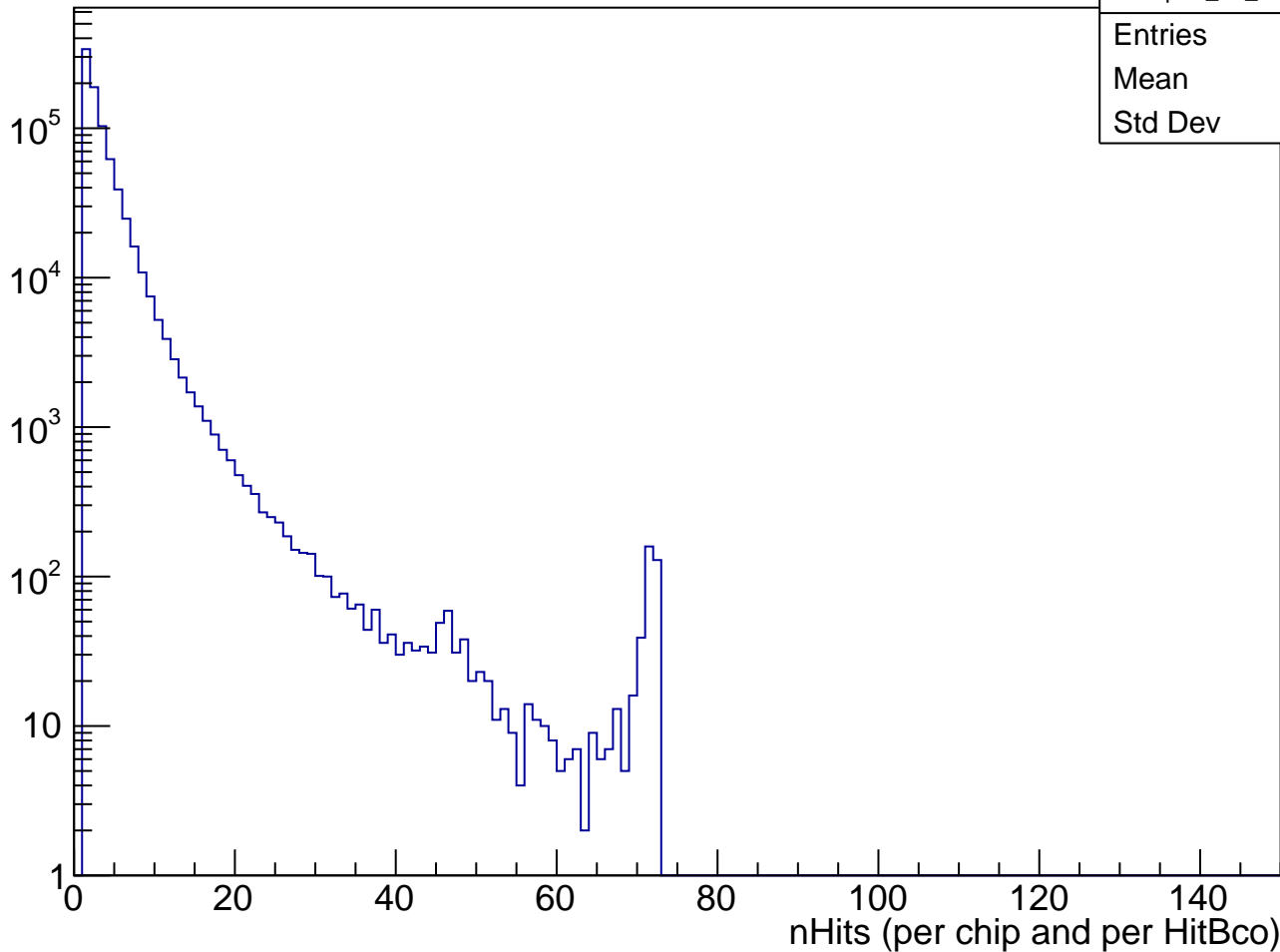
Mean 2.916

Std Dev 3.517

nHits (per chip and per HitBco)

nChipHit_F0_Fch2_Chip5

Entries



nChipHit_F0_Fch2_Chip5

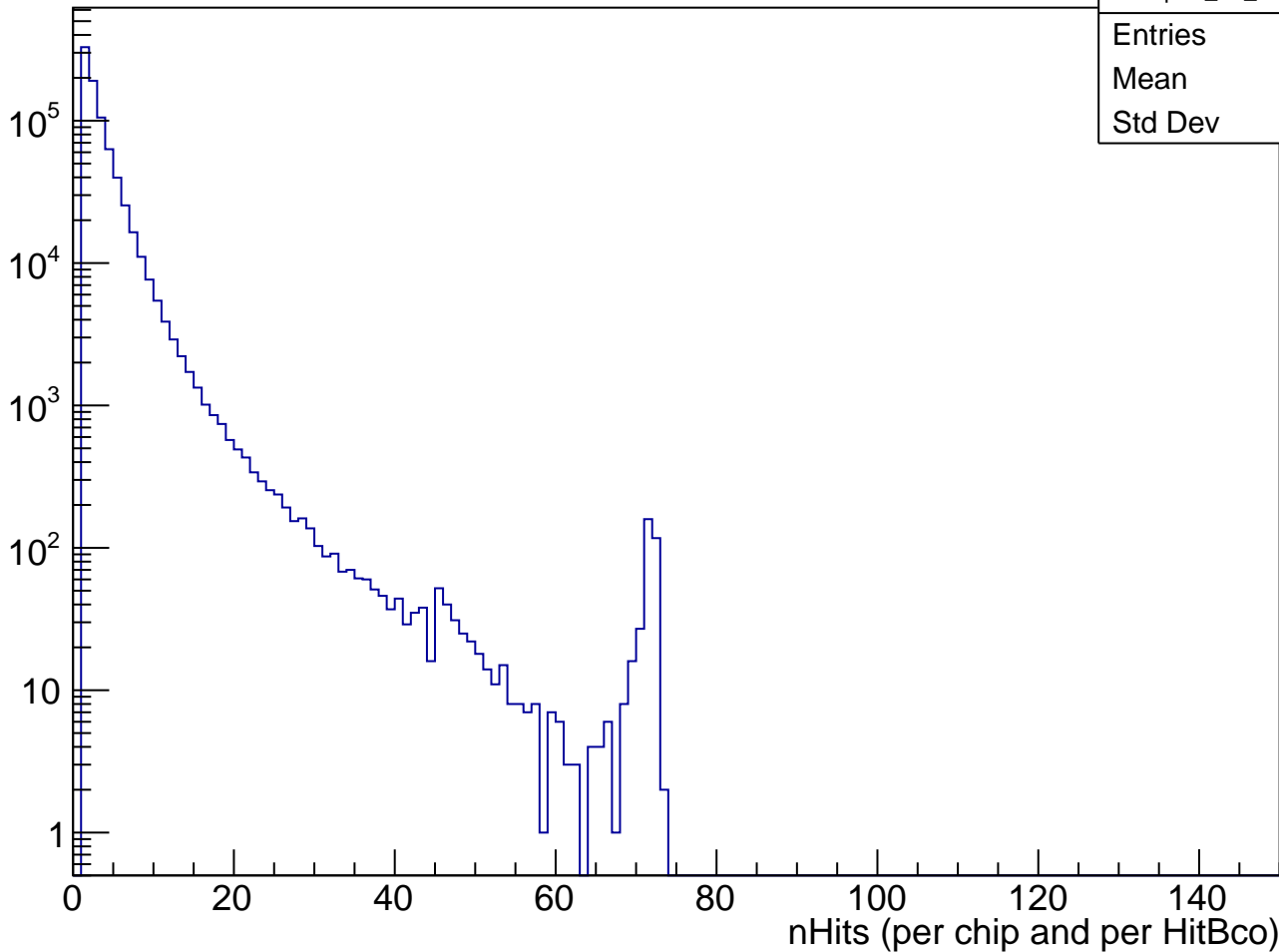
Entries 814786

Mean 2.793

Std Dev 3.37

nChipHit_F0_Fch2_Chip6

Entries



nChipHit_F0_Fch2_Chip6

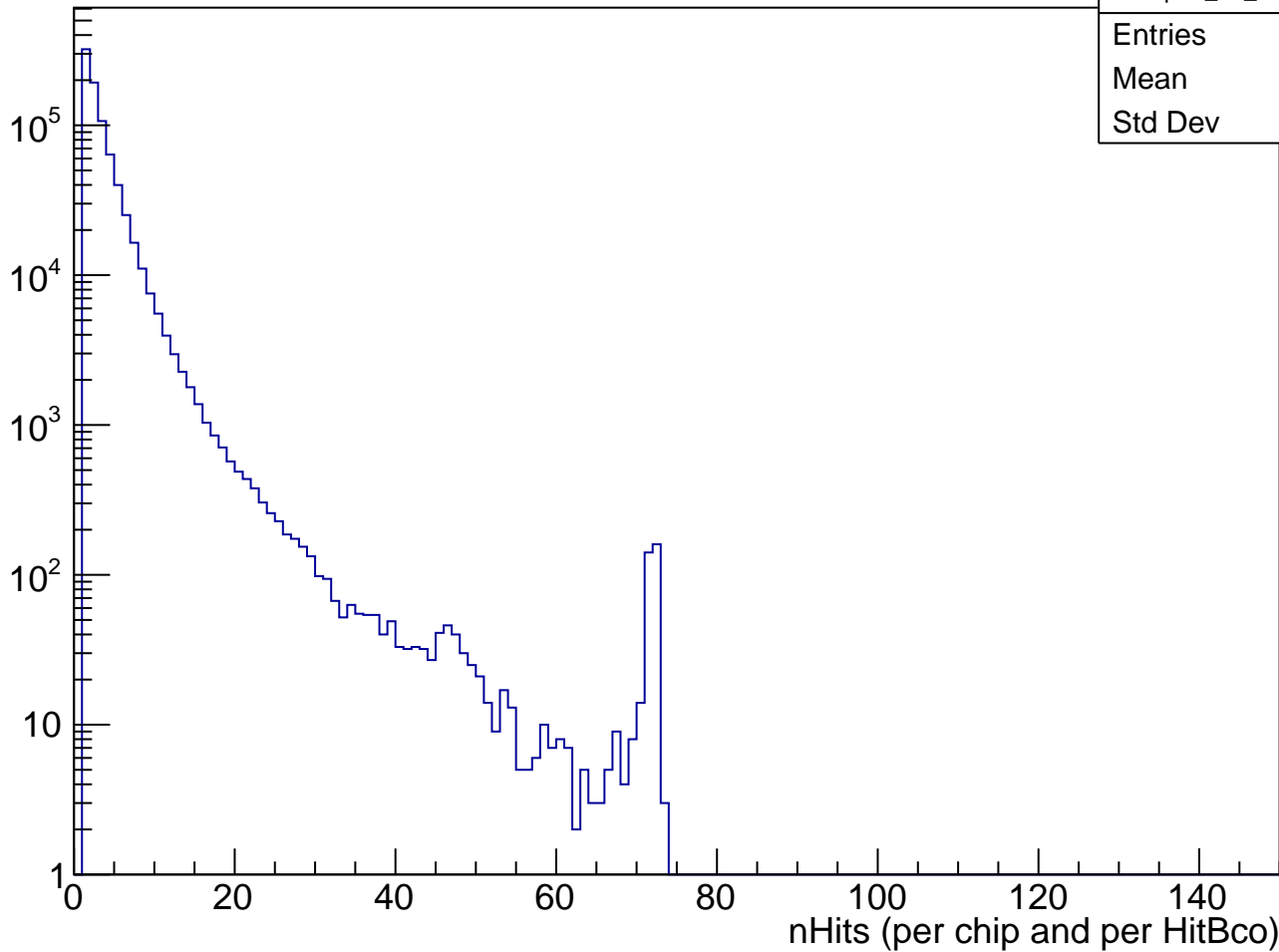
Entries 812468

Mean 2.821

Std Dev 3.324

nChipHit_F0_Fch2_Chip7

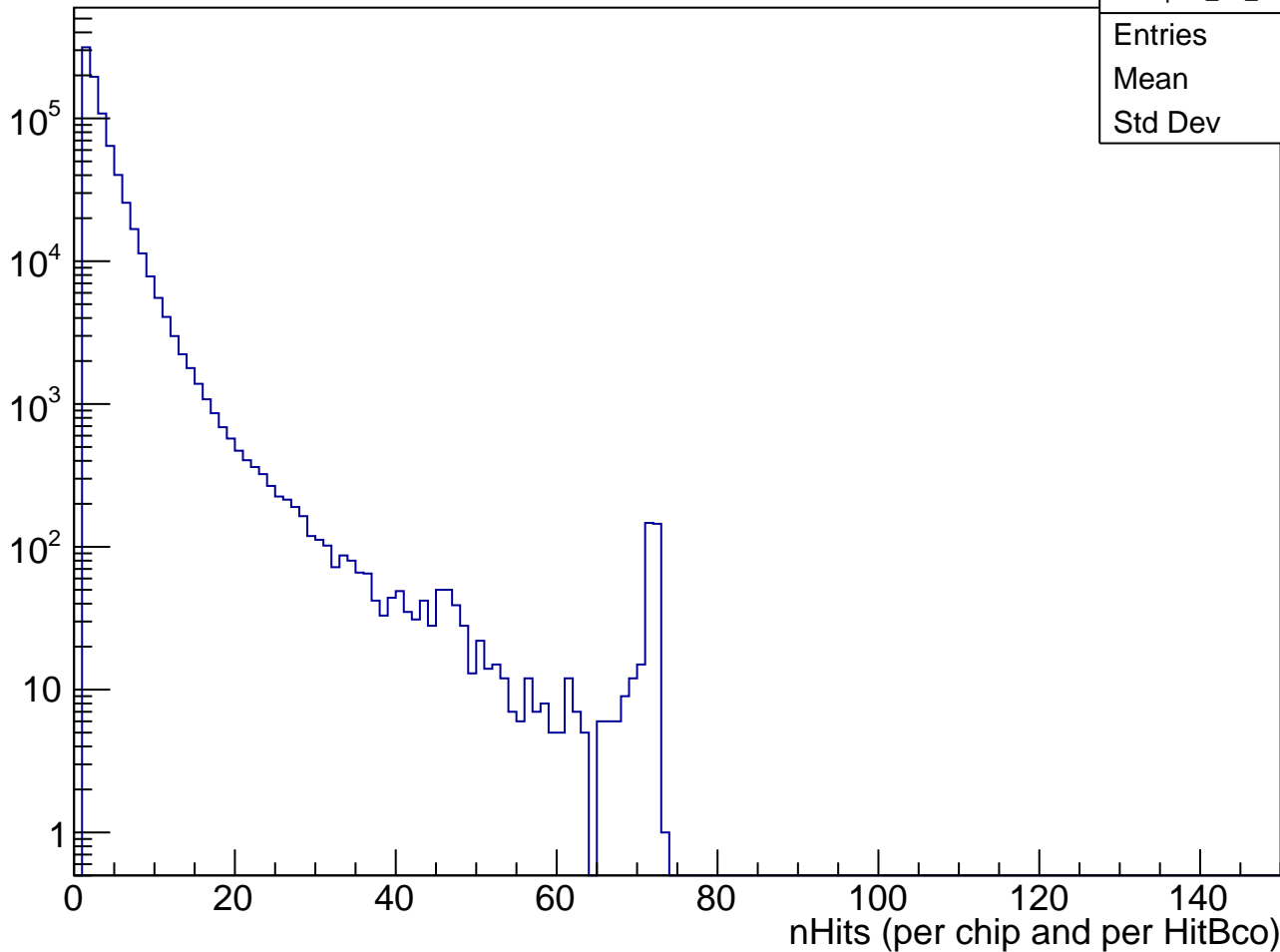
Entries



nChipHit_F0_Fch2_Chip7	
Entries	810498
Mean	2.839
Std Dev	3.347

nChipHit_F0_Fch2_Chip8

Entries



nChipHit_F0_Fch2_Chip8

Entries 809717

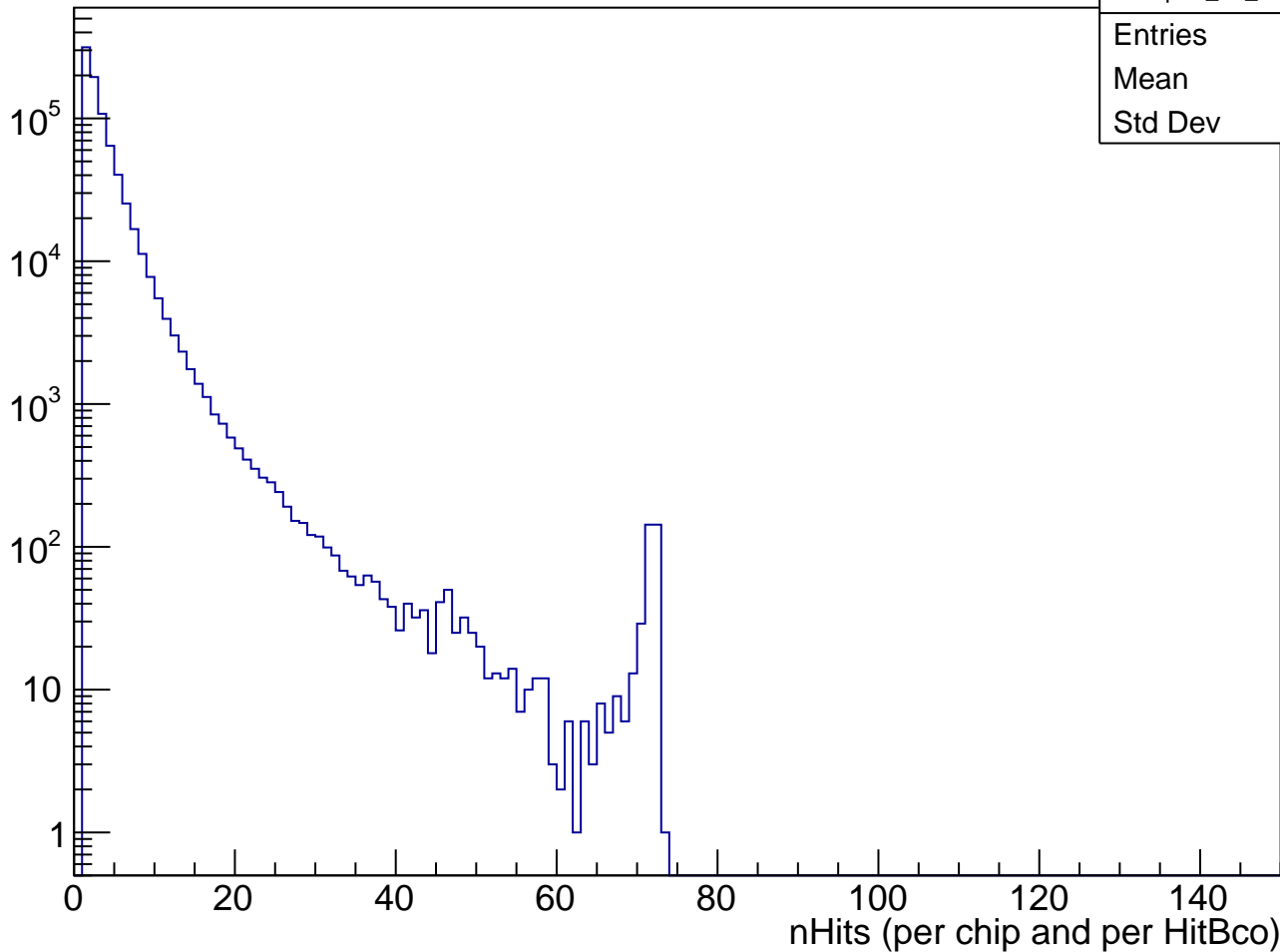
Mean 2.868

Std Dev 3.373

nHits (per chip and per HitBco)

nChipHit_F0_Fch2_Chip9

Entries



nChipHit_F0_Fch2_Chip9

Entries 808404

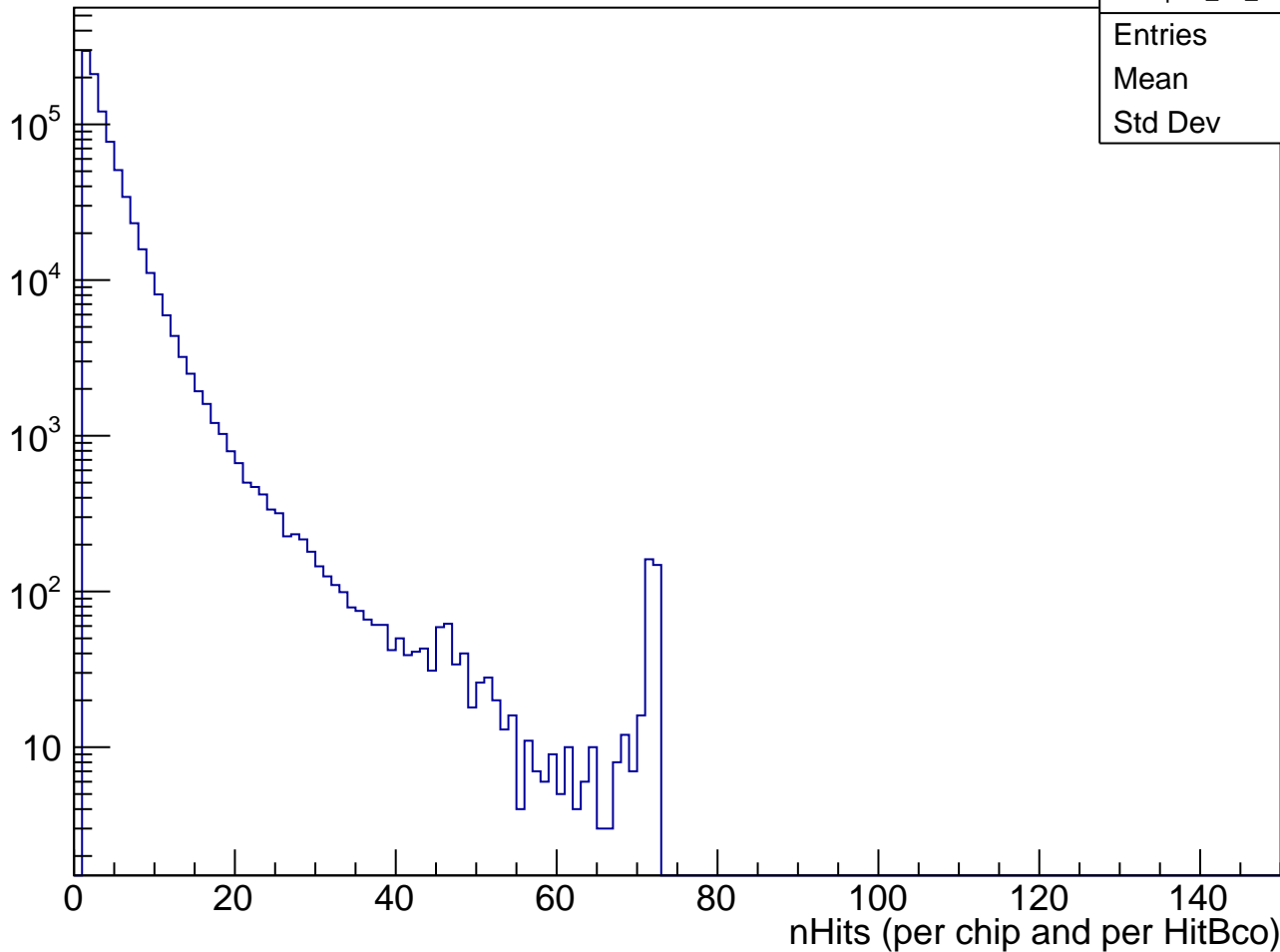
Mean 2.864

Std Dev 3.363

nHits (per chip and per HitBco)

nChipHit_F0_Fch3_Chip0

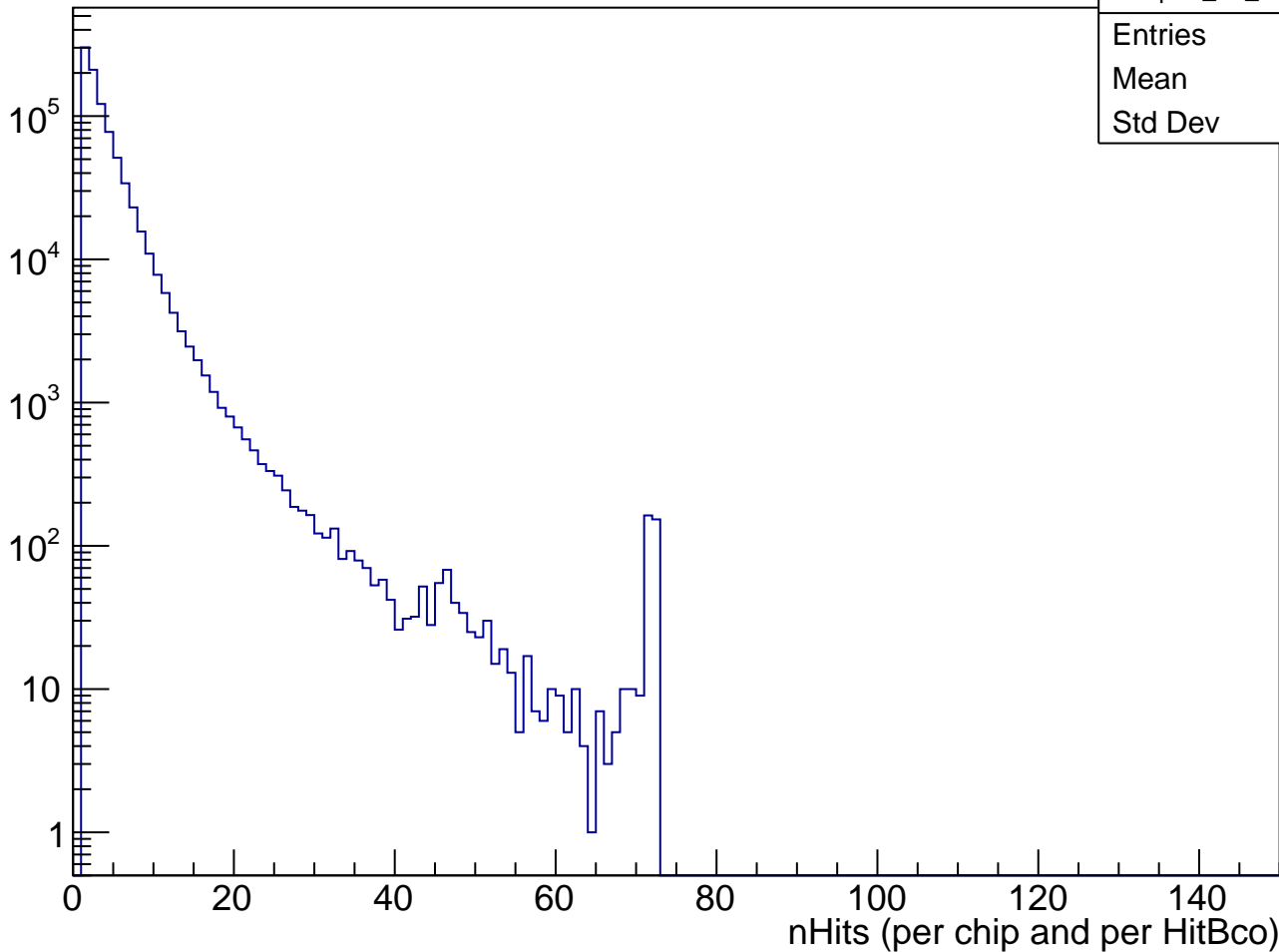
Entries



nChipHit_F0_Fch3_Chip0	
Entries	876320
Mean	3.18
Std Dev	3.584

nChipHit_F0_Fch3_Chip1

Entries

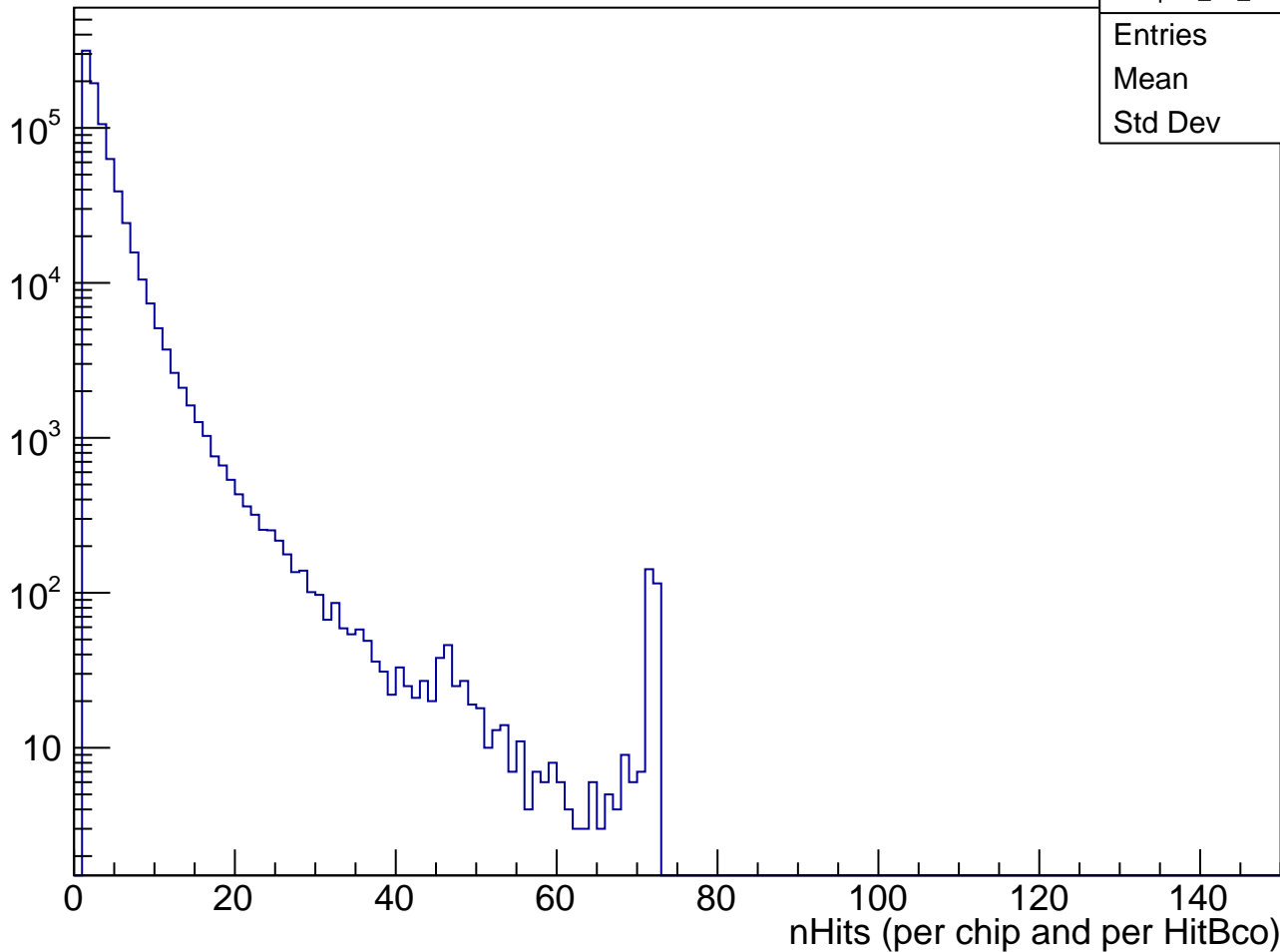


nChipHit_F0_Fch3_Chip1	
Entries	881148
Mean	3.153
Std Dev	3.548

nHits (per chip and per HitBco)

nChipHit_F0_Fch3_Chip10

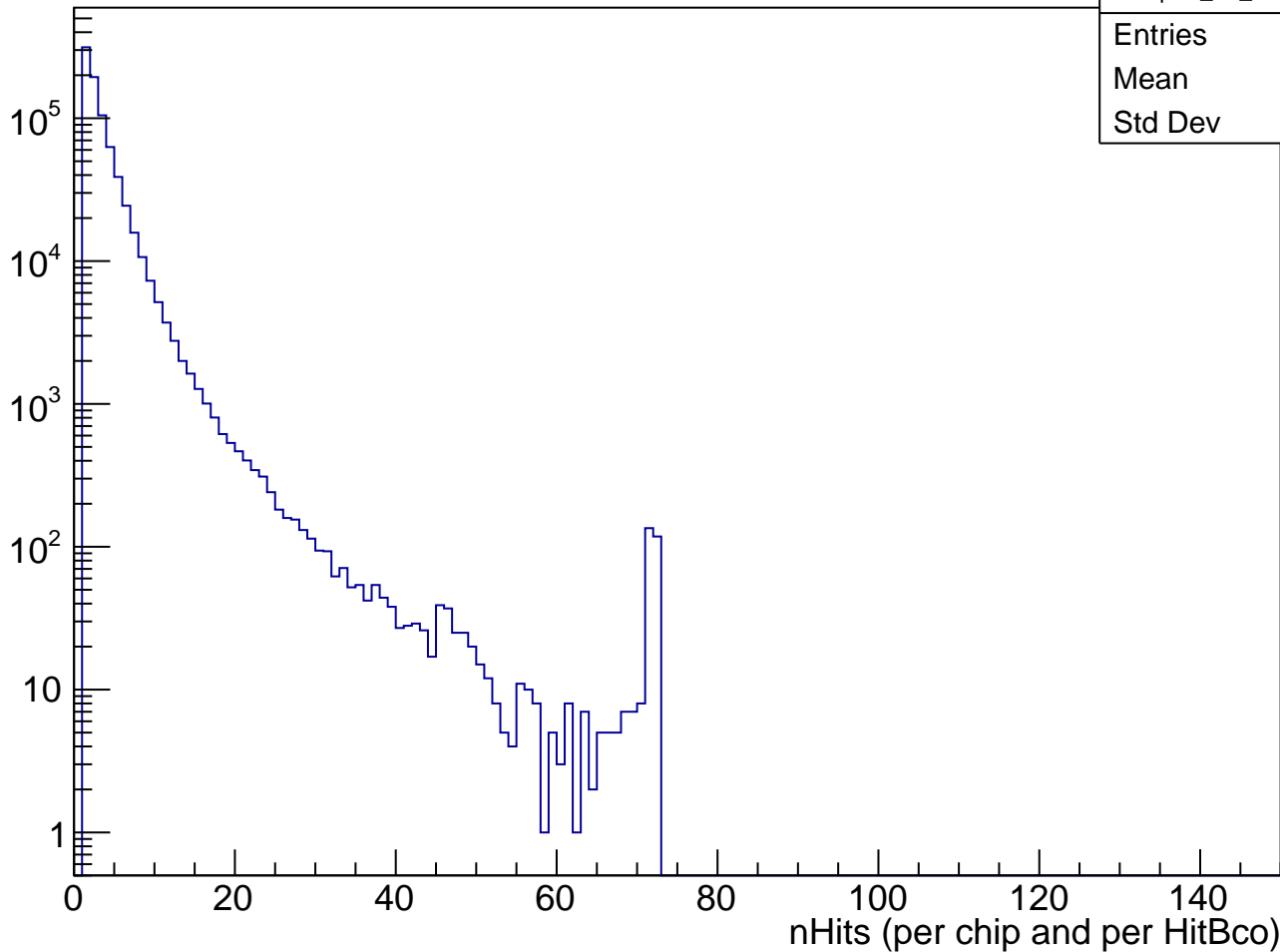
Entries



nChipHit_F0_Fch3_Chip10	
Entries	798125
Mean	2.796
Std Dev	3.21

nChipHit_F0_Fch3_Chip11

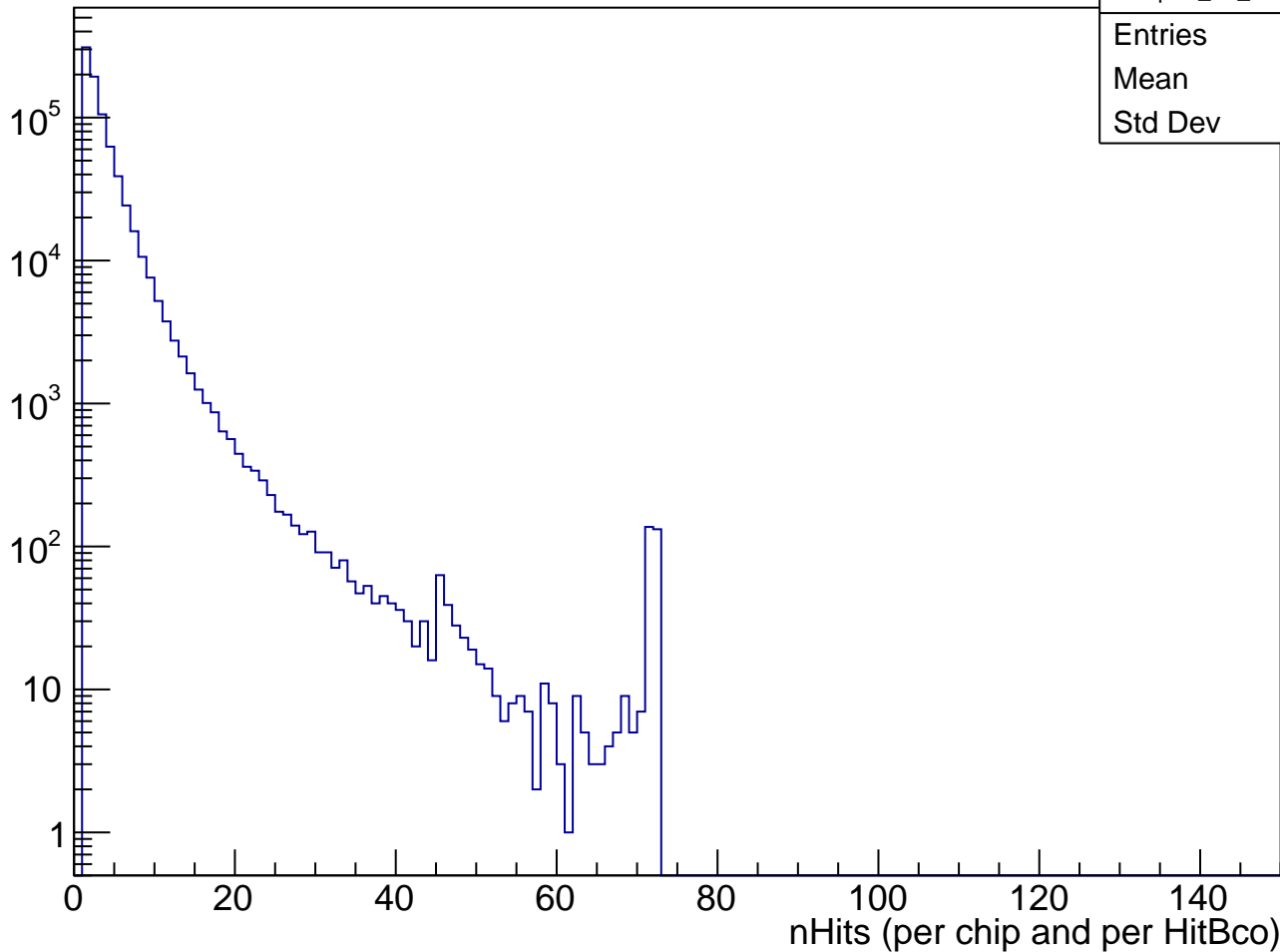
Entries



nChipHit_F0_Fch3_Chip11	
Entries	795713
Mean	2.803
Std Dev	3.217

nChipHit_F0_Fch3_Chip12

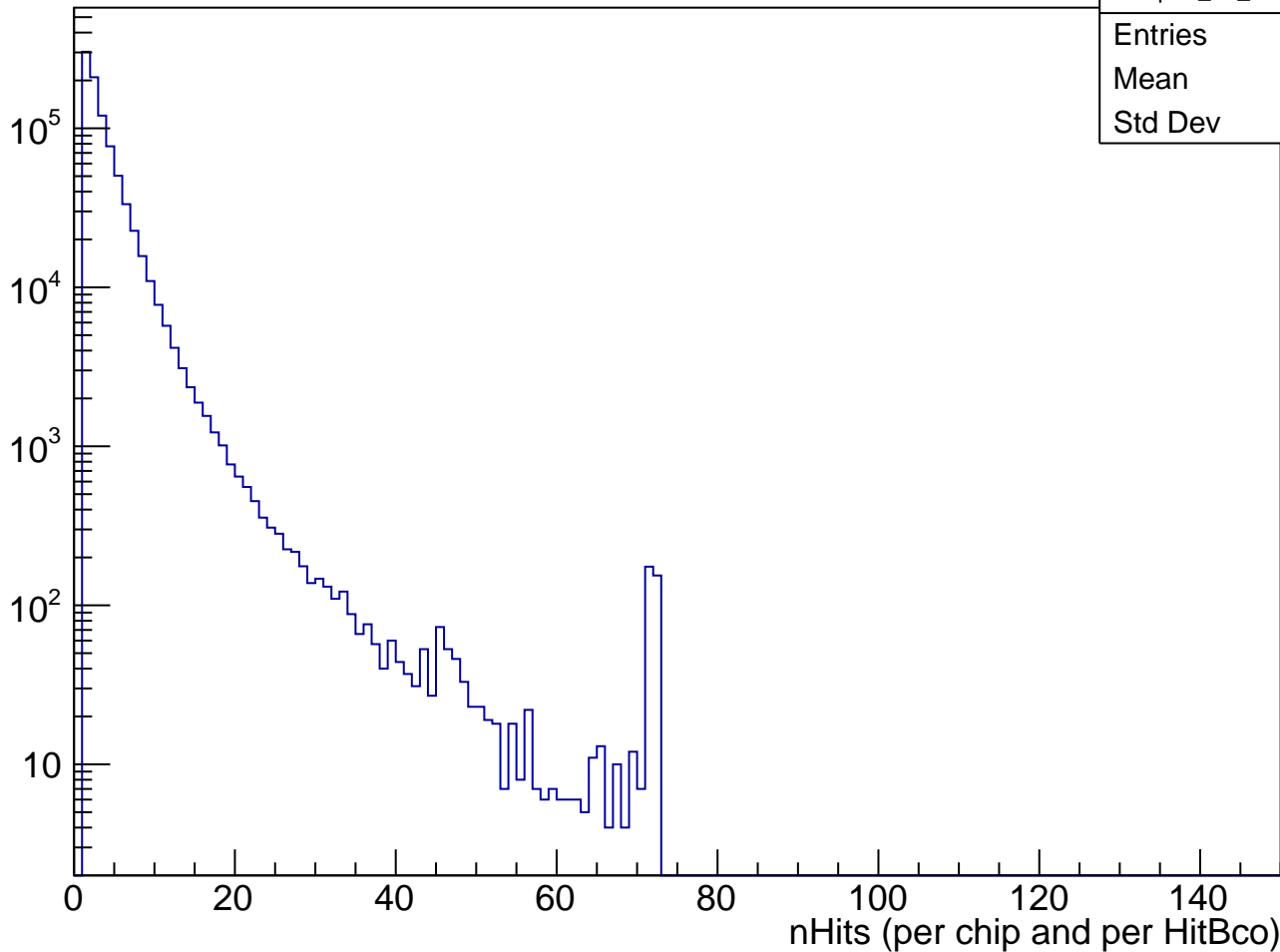
Entries



nChipHit_F0_Fch3_Chip12	
Entries	792218
Mean	2.821
Std Dev	3.254

nChipHit_F0_Fch3_Chip13

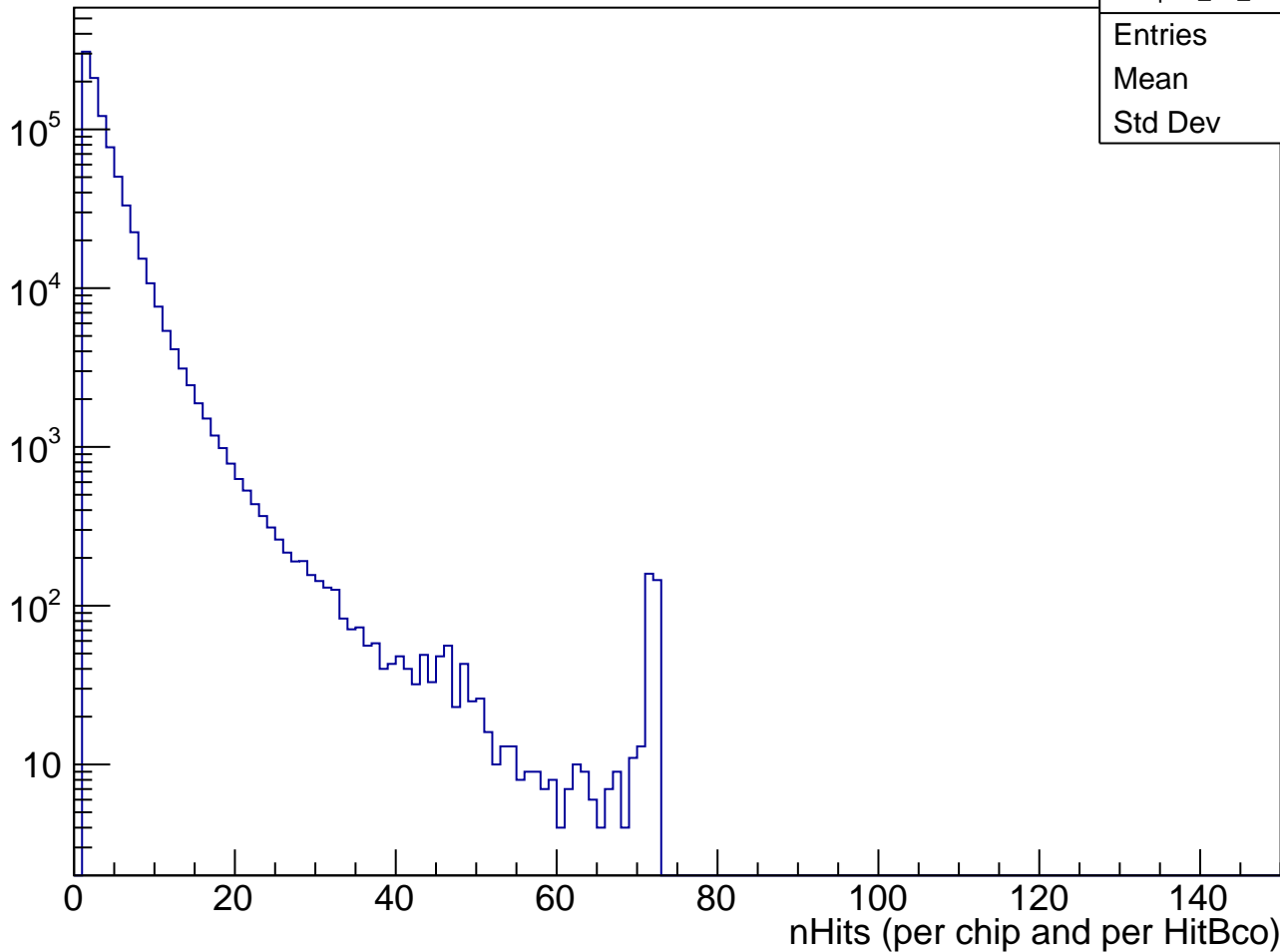
Entries



nChipHit_F0_Fch3_Chip13	
Entries	877299
Mean	3.144
Std Dev	3.569

nChipHit_F0_Fch3_Chip14

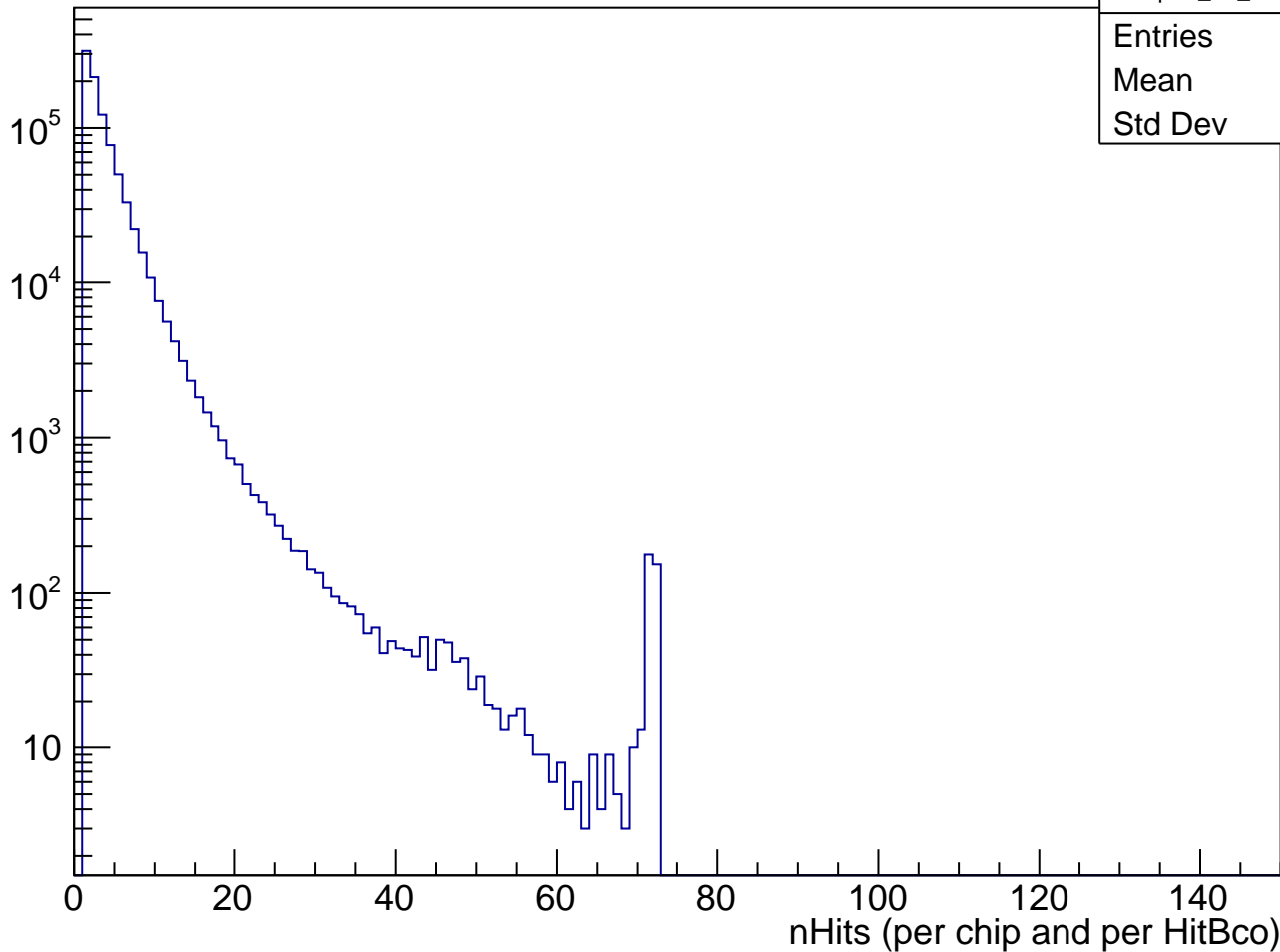
Entries



nChipHit_F0_Fch3_Chip14	
Entries	883785
Mean	3.111
Std Dev	3.504

nChipHit_F0_Fch3_Chip15

Entries

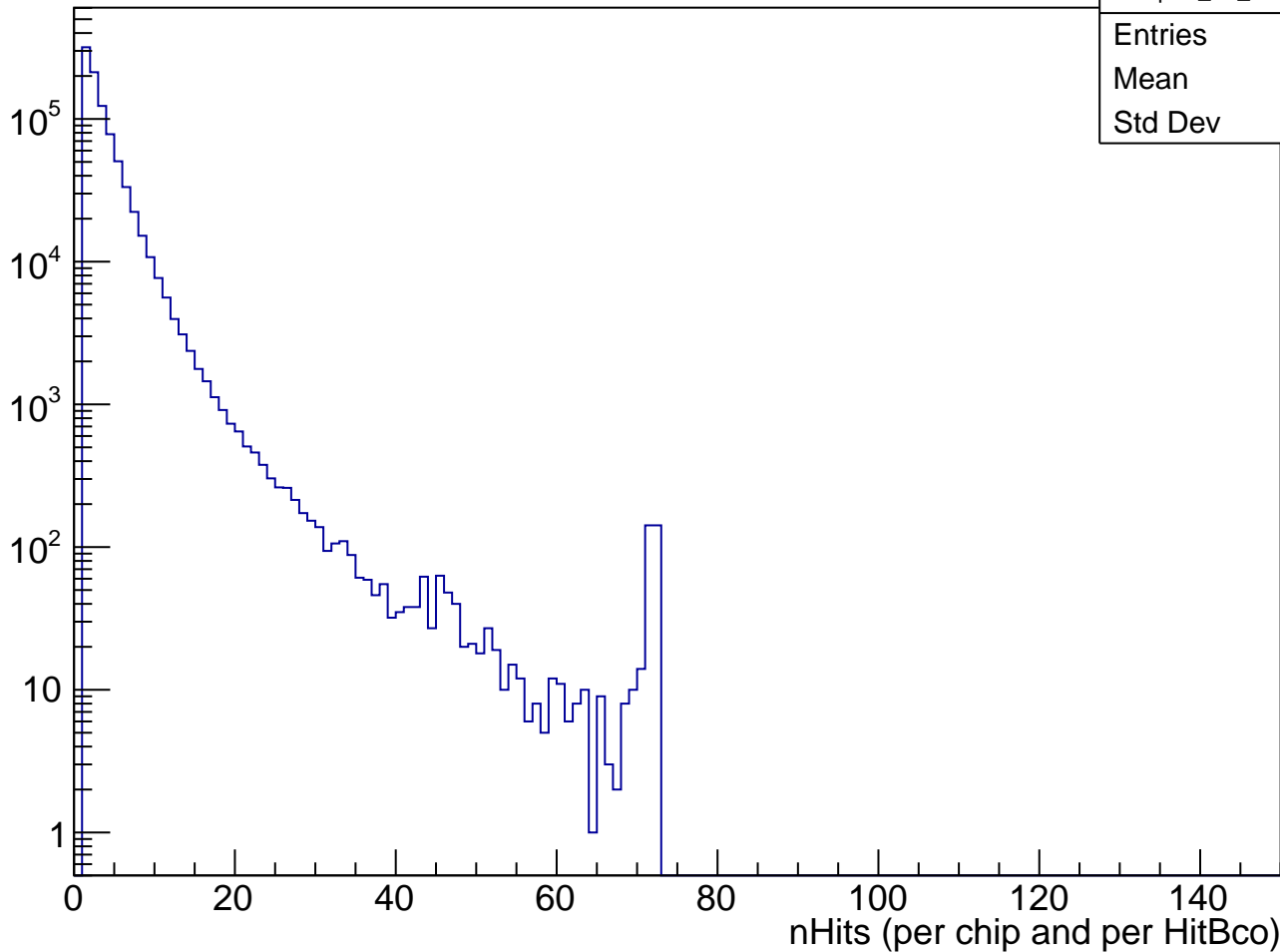


nChipHit_F0_Fch3_Chip15	
Entries	892201
Mean	3.097
Std Dev	3.511

nHits (per chip and per HitBco)

nChipHit_F0_Fch3_Chip16

Entries

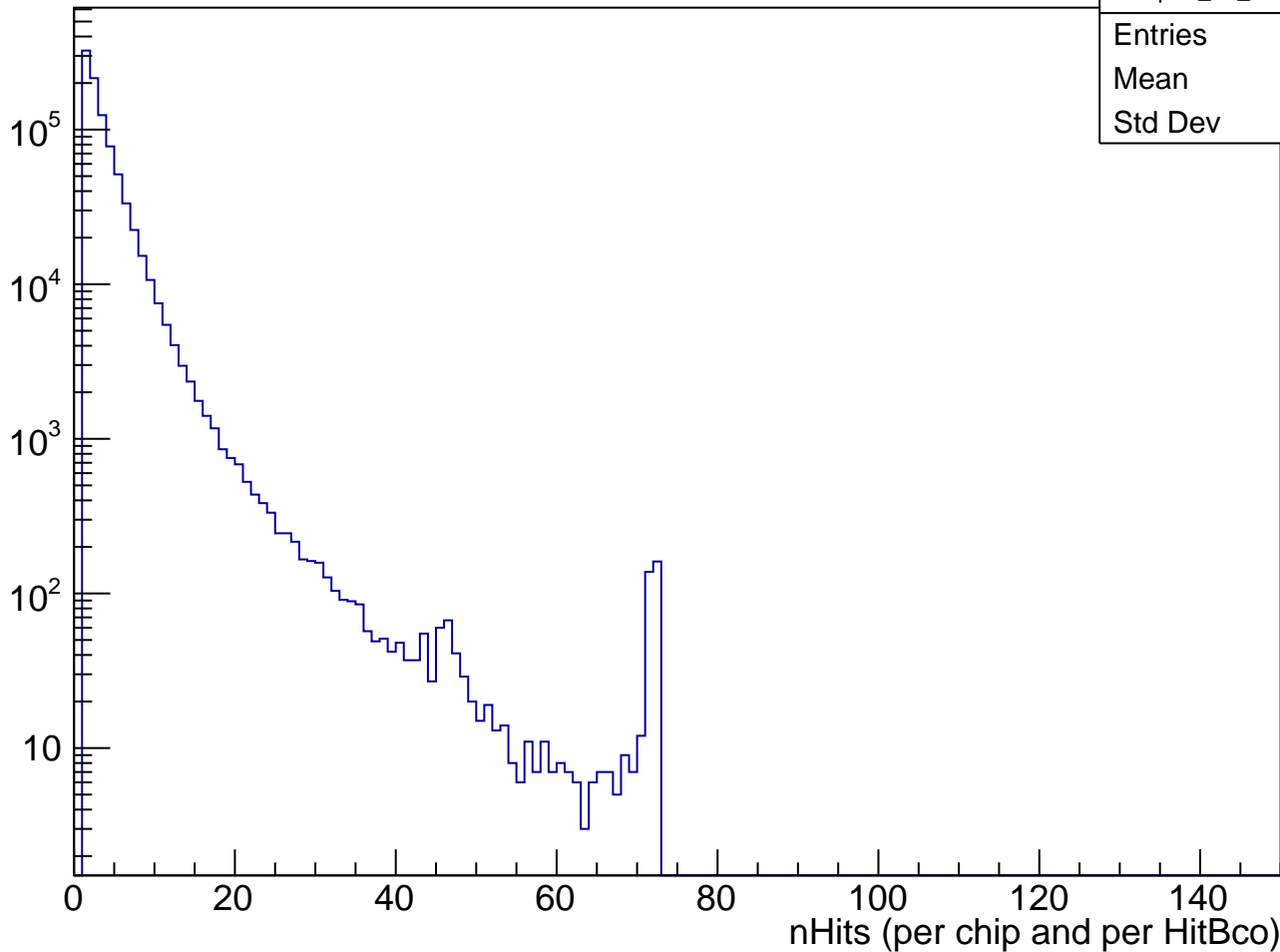


nChipHit_F0_Fch3_Chip16	
Entries	898107
Mean	3.08
Std Dev	3.458

nHits (per chip and per HitBco)

nChipHit_F0_Fch3_Chip17

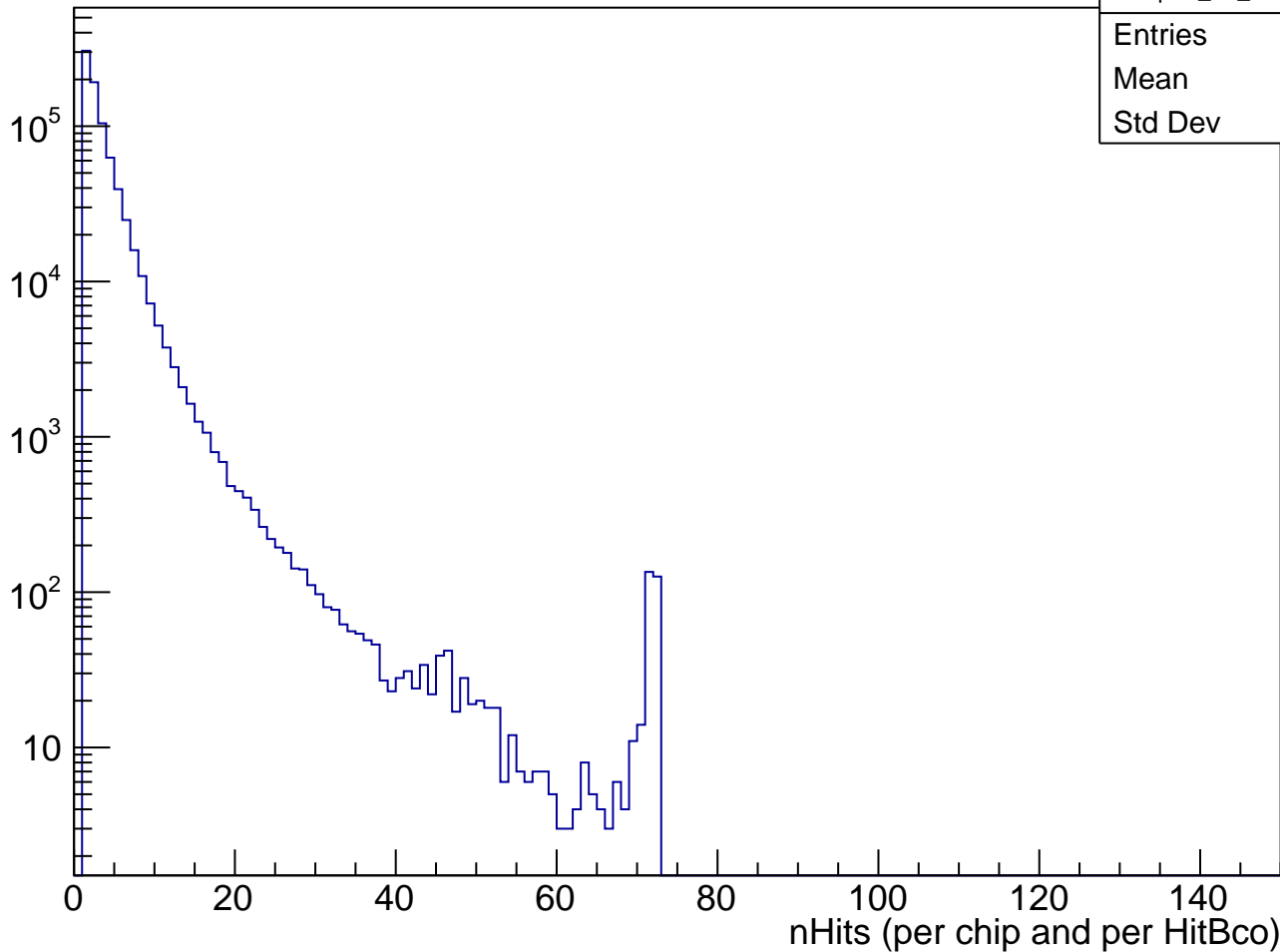
Entries



nChipHit_F0_Fch3_Chip17	
Entries	908135
Mean	3.064
Std Dev	3.459

nChipHit_F0_Fch3_Chip18

Entries

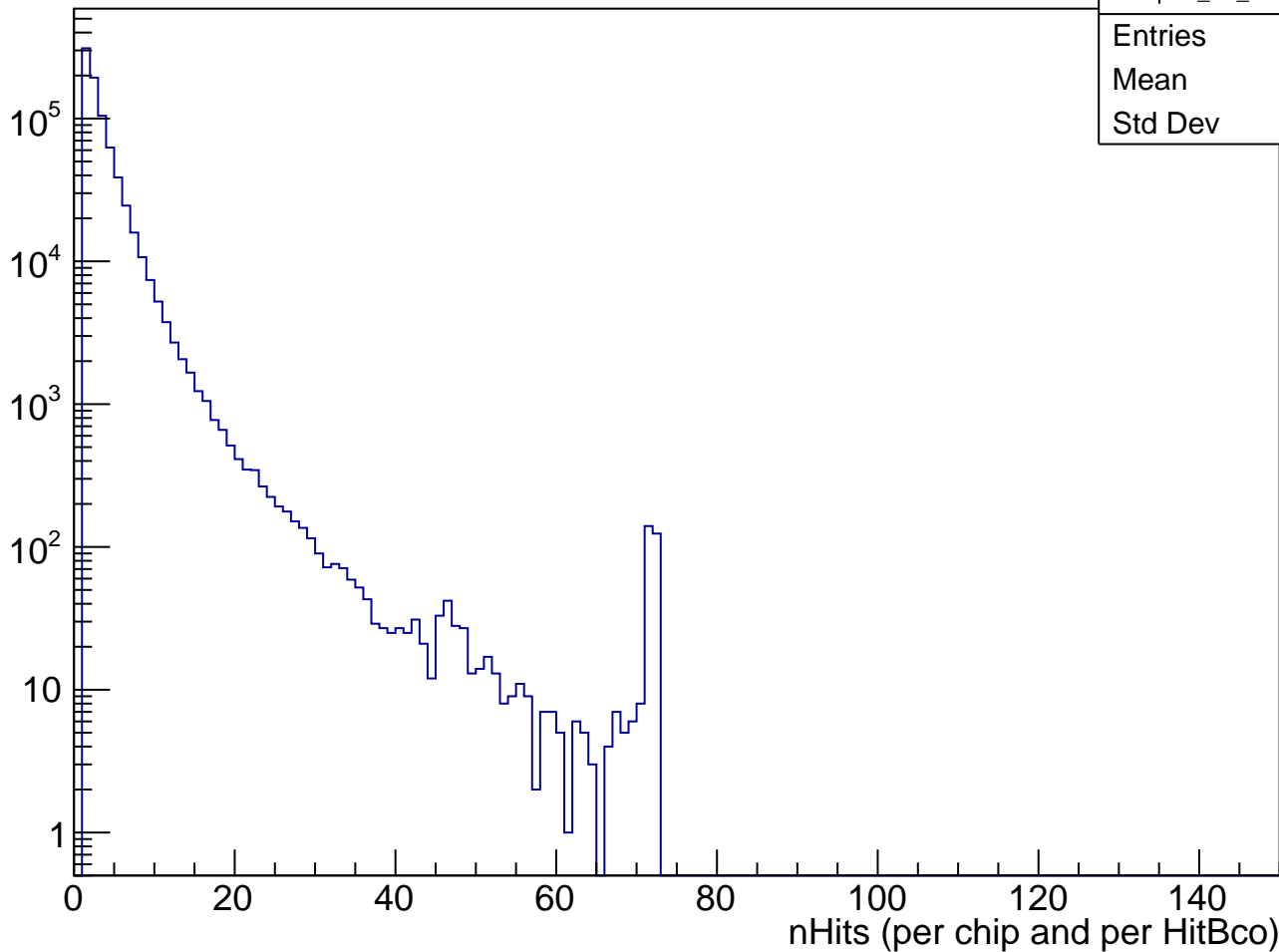


nChipHit_F0_Fch3_Chip18	
Entries	786626
Mean	2.832
Std Dev	3.256

nHits (per chip and per HitBco)

nChipHit_F0_Fch3_Chip19

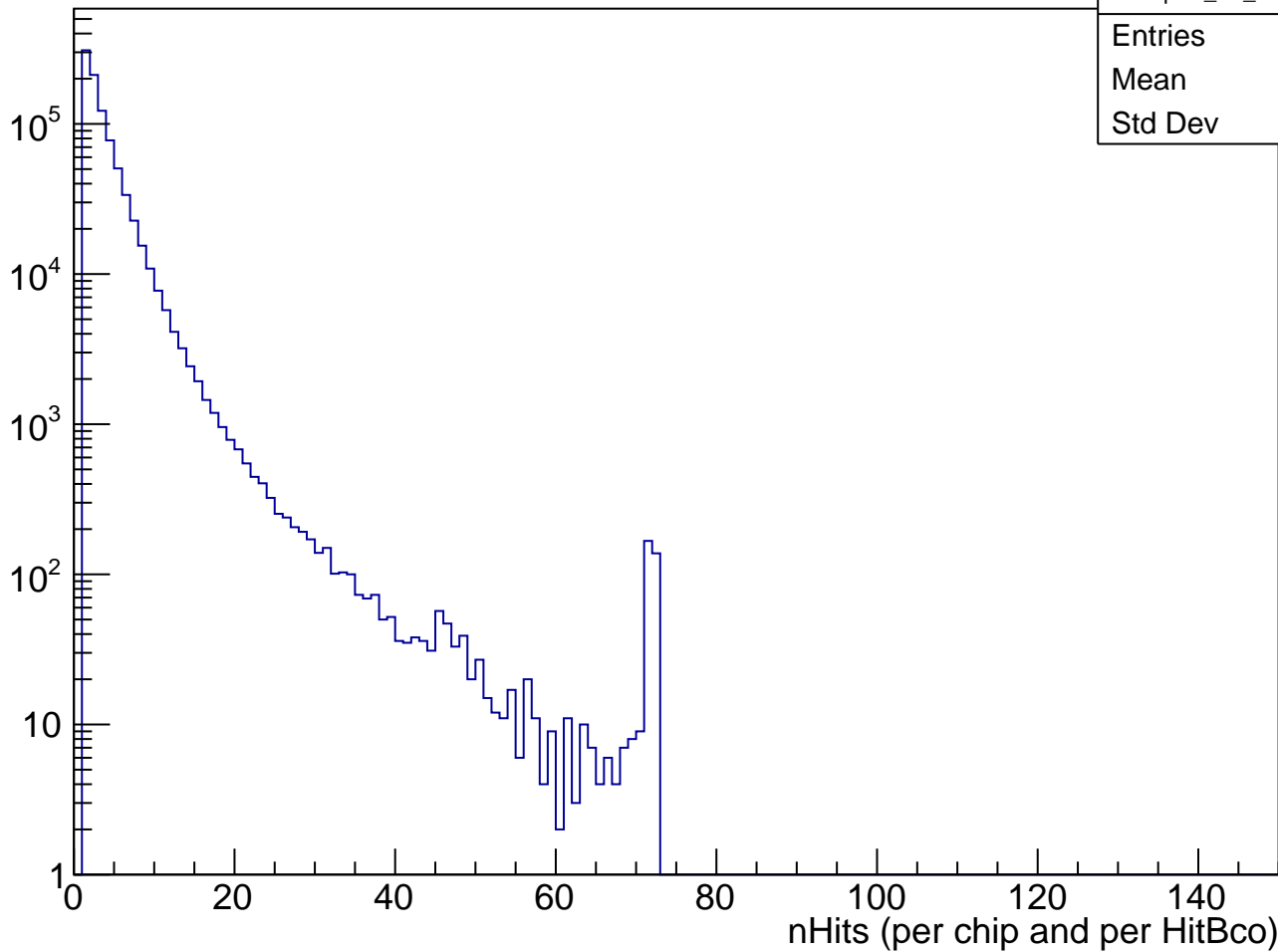
Entries



nChipHit_F0_Fch3_Chip19	
Entries	791385
Mean	2.81
Std Dev	3.214

nChipHit_F0_Fch3_Chip2

Entries



nChipHit_F0_Fch3_Chip2

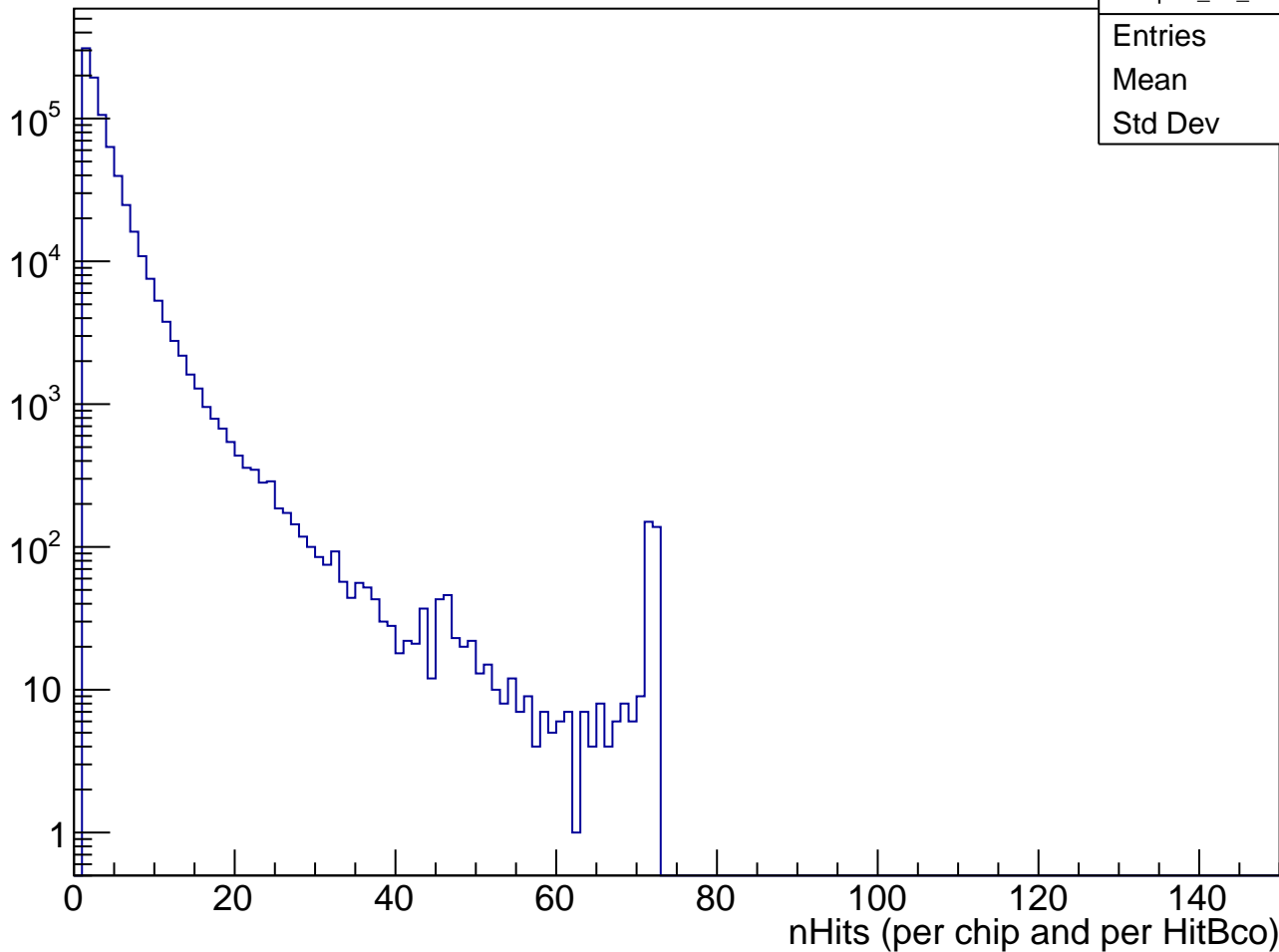
Entries 889204

Mean 3.123

Std Dev 3.519

nChipHit_F0_Fch3_Chip20

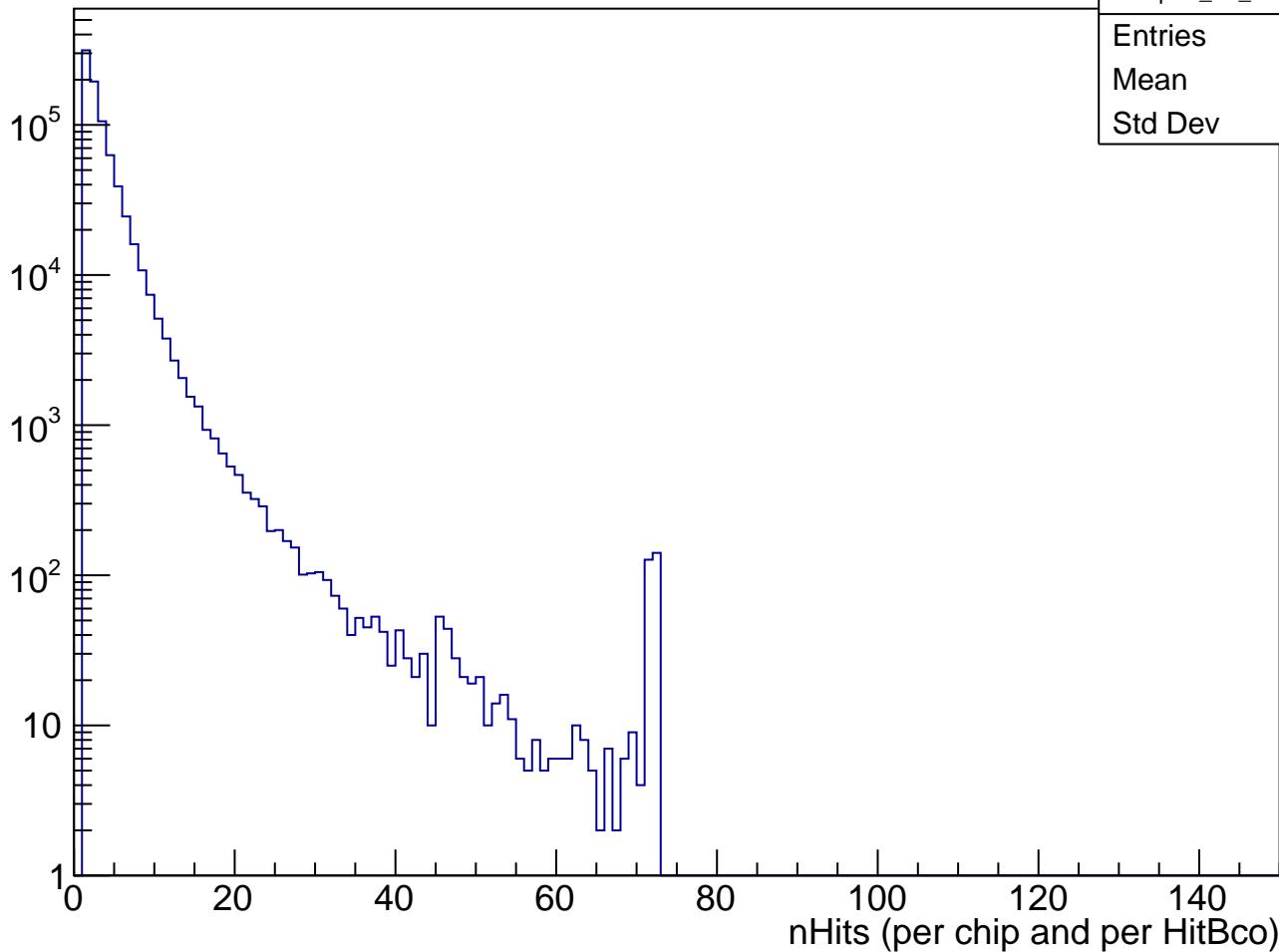
Entries



nChipHit_F0_Fch3_Chip20	
Entries	795546
Mean	2.826
Std Dev	3.255

nChipHit_F0_Fch3_Chip21

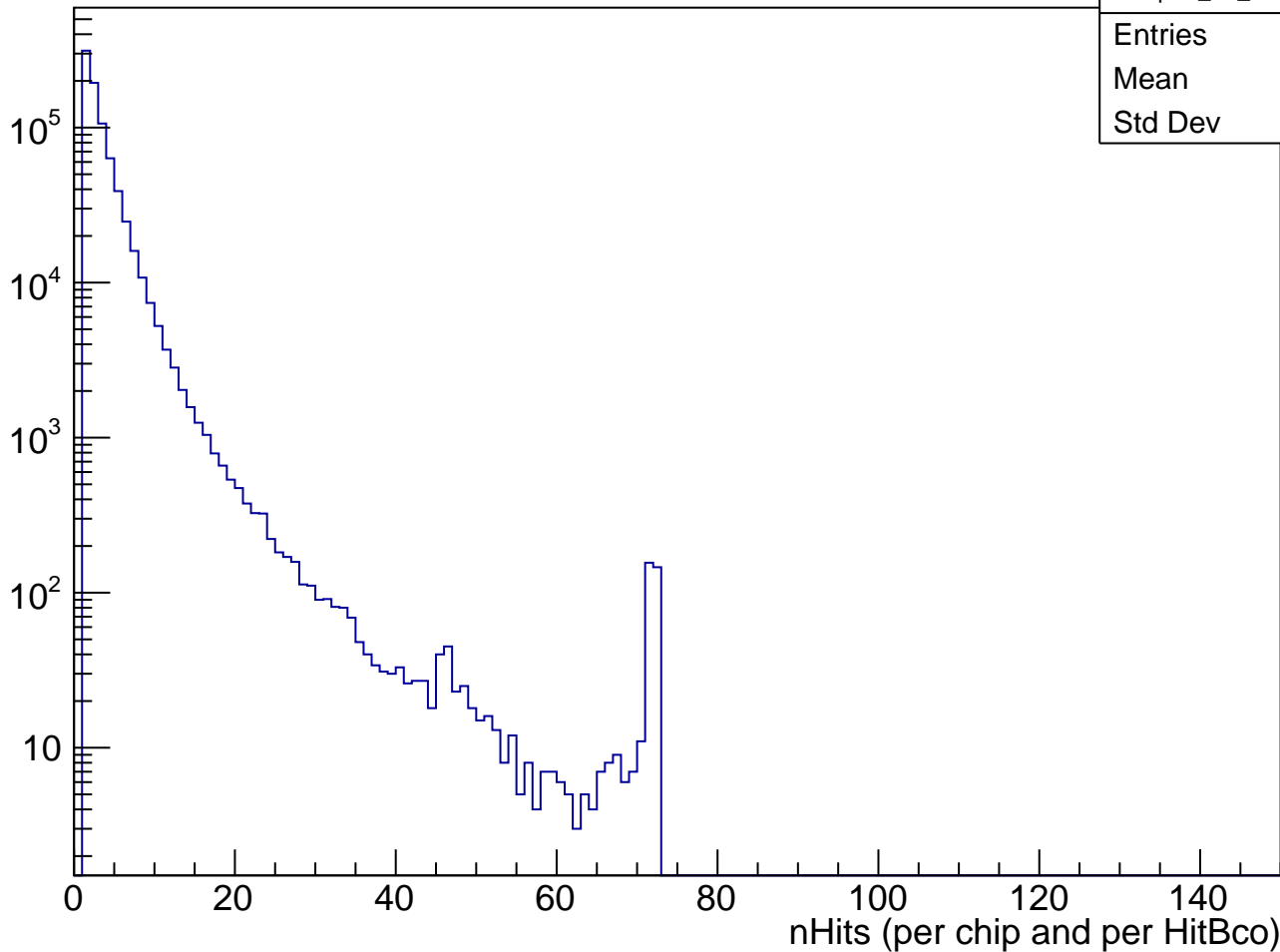
Entries



nChipHit_F0_Fch3_Chip21	
Entries	797834
Mean	2.806
Std Dev	3.234

nChipHit_F0_Fch3_Chip22

Entries

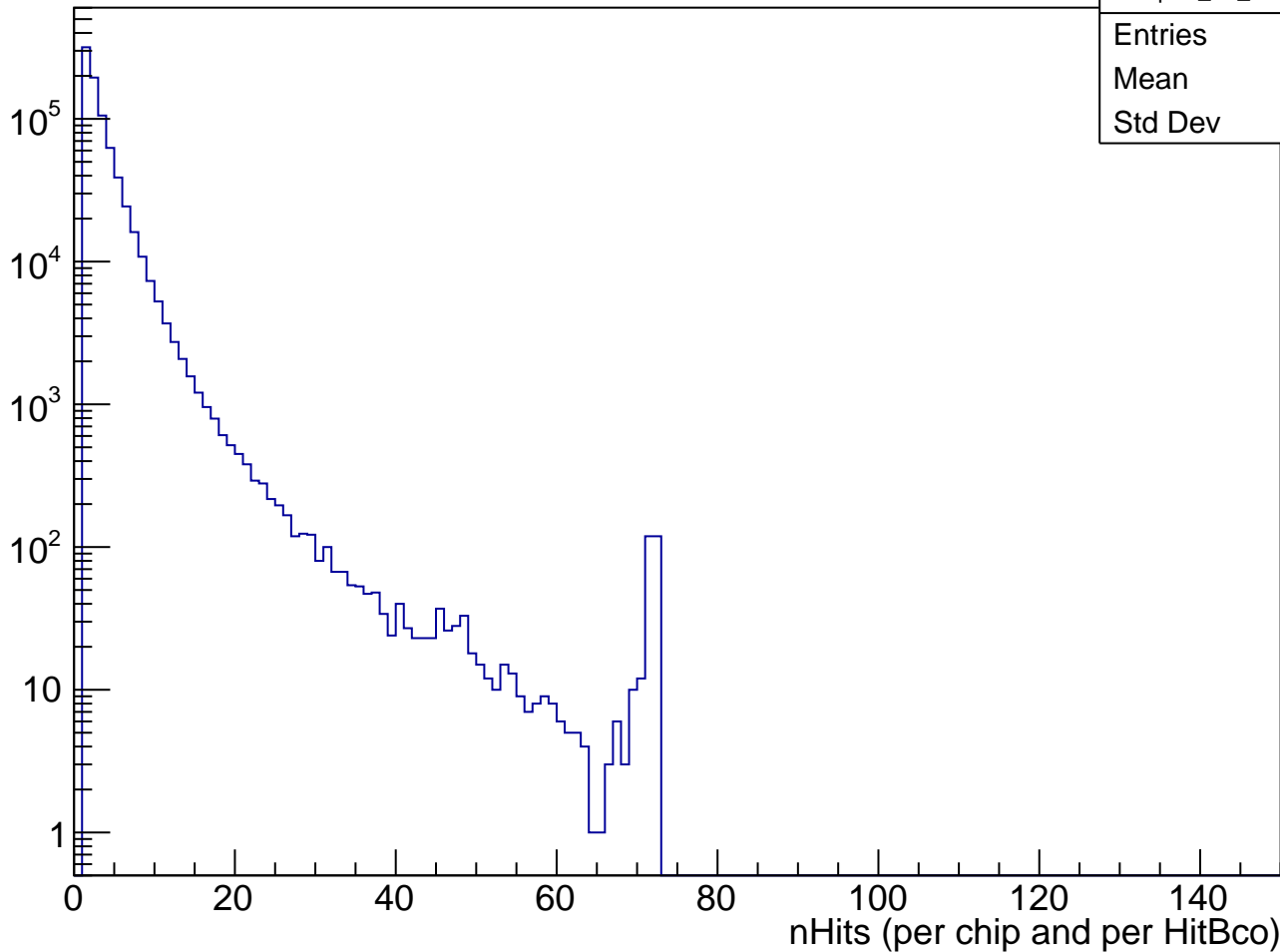


nChipHit_F0_Fch3_Chip22	
Entries	798534
Mean	2.816
Std Dev	3.275

nHits (per chip and per HitBco)

nChipHit_F0_Fch3_Chip23

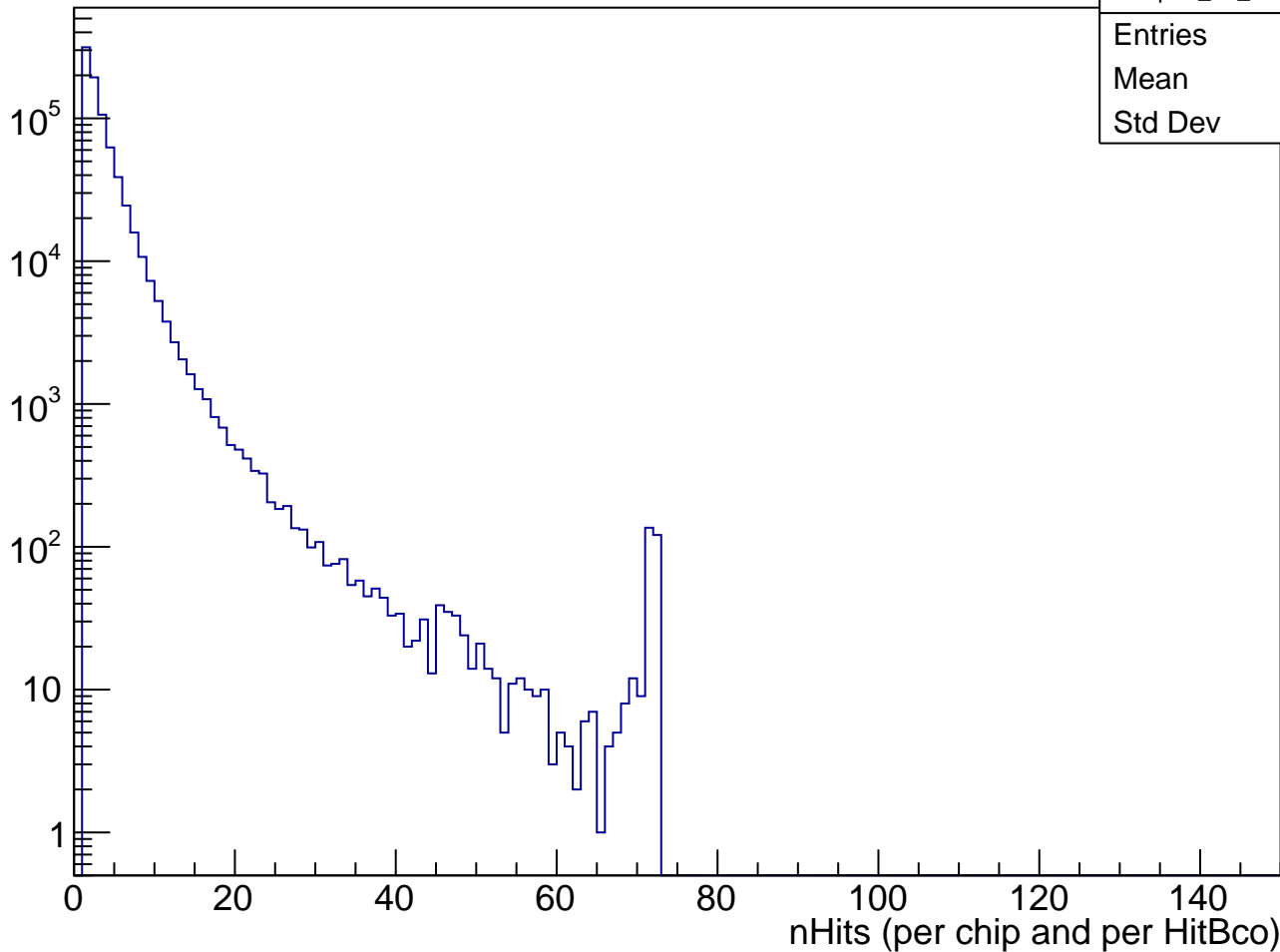
Entries



nChipHit_F0_Fch3_Chip23	
Entries	800812
Mean	2.79
Std Dev	3.188

nChipHit_F0_Fch3_Chip24

Entries

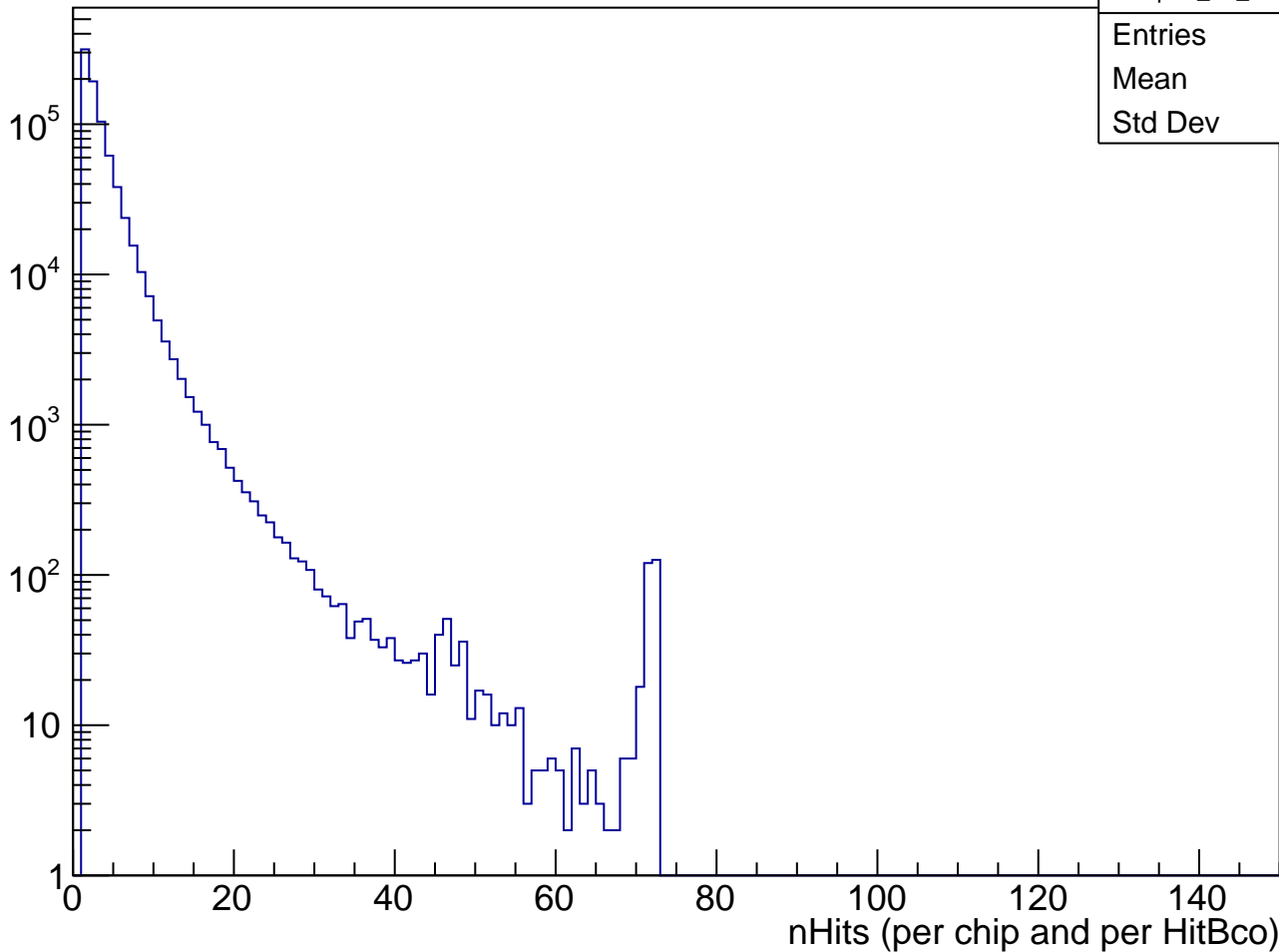


nChipHit_F0_Fch3_Chip24	
Entries	797338
Mean	2.81
Std Dev	3.243

nHits (per chip and per HitBco)

nChipHit_F0_Fch3_Chip25

Entries

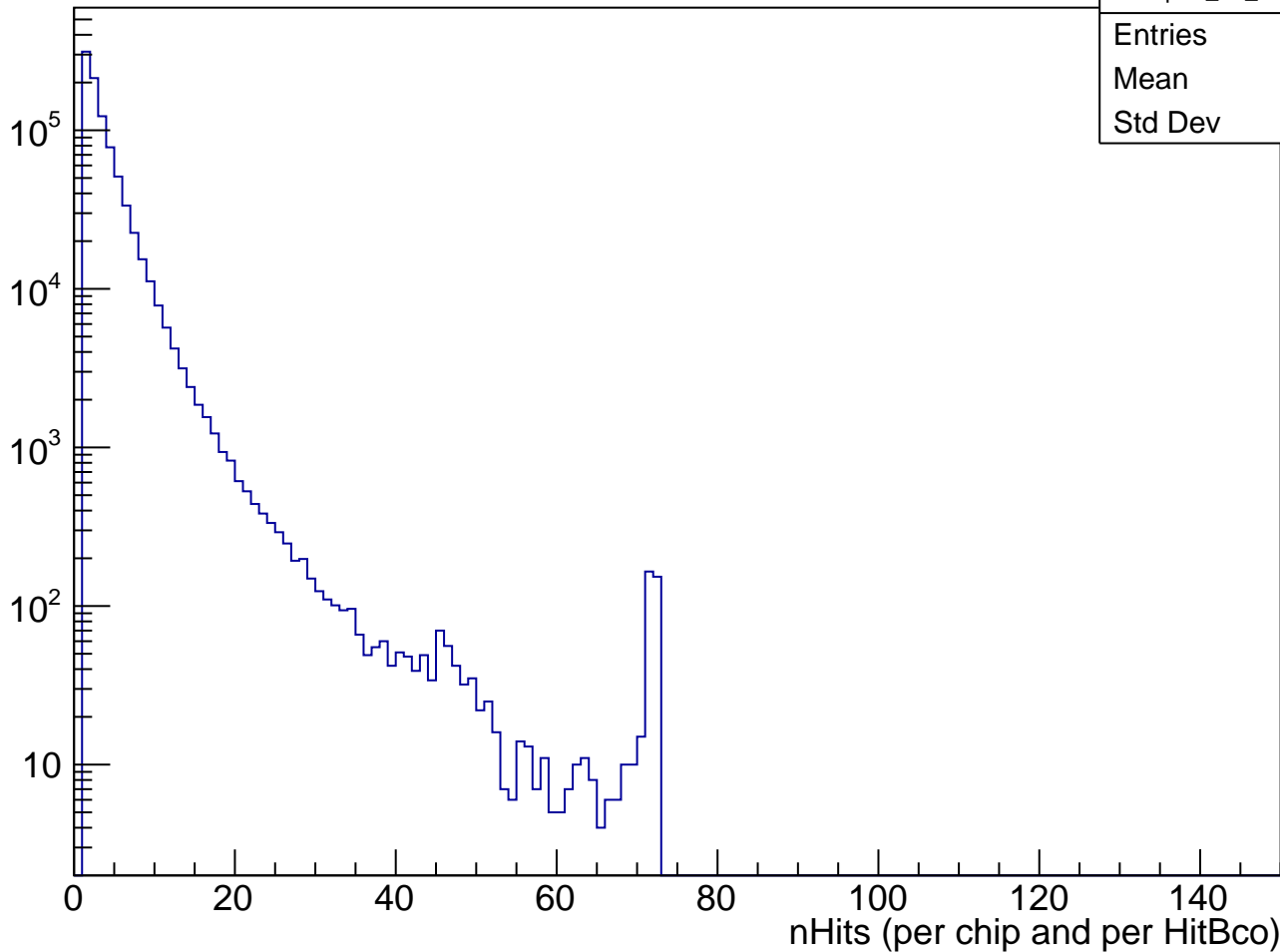


nChipHit_F0_Fch3_Chip25	
Entries	791096
Mean	2.779
Std Dev	3.19

nHits (per chip and per HitBco)

nChipHit_F0_Fch3_Chip3

Entries



nChipHit_F0_Fch3_Chip3

Entries 895694

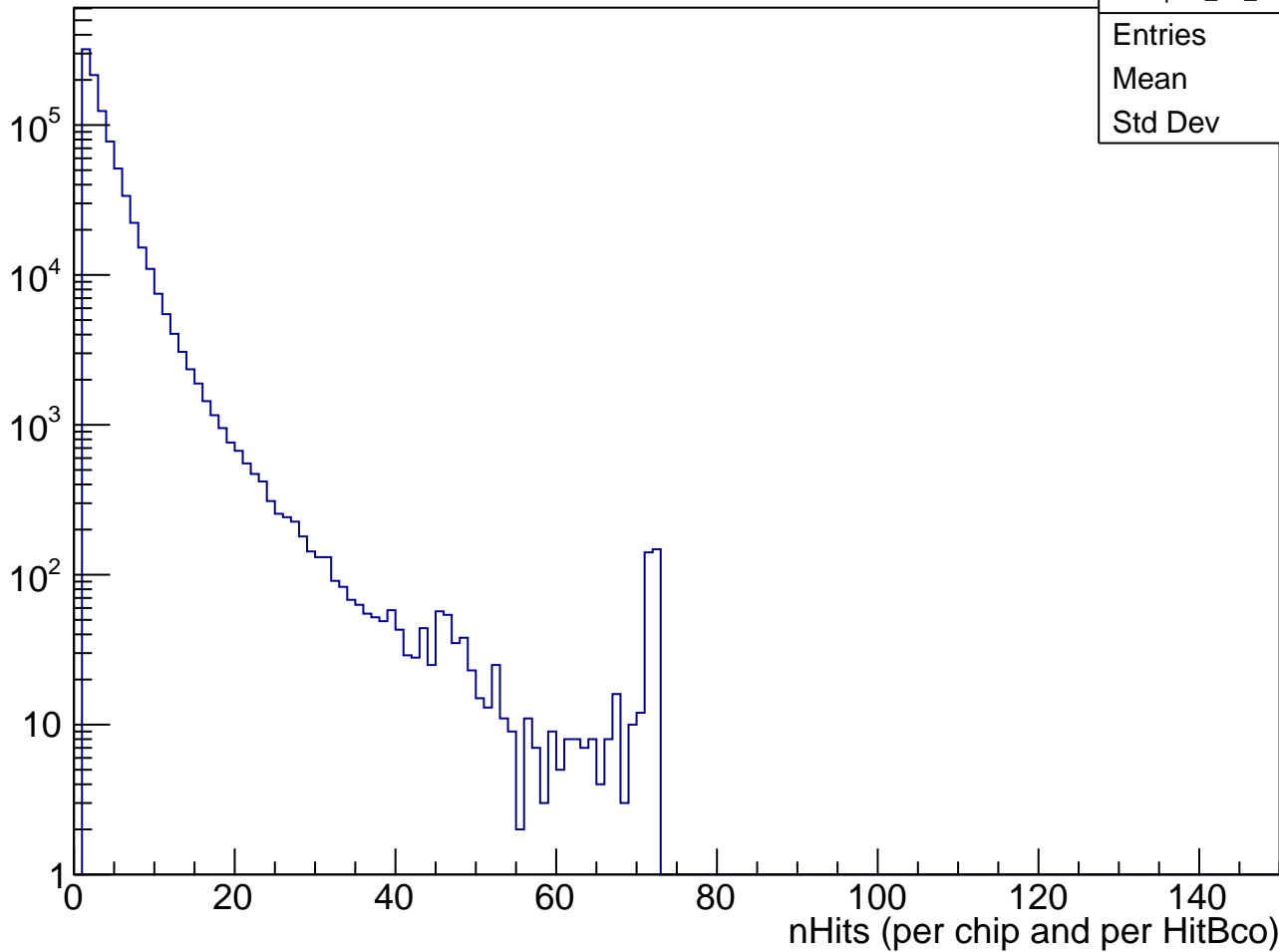
Mean 3.116

Std Dev 3.535

nHits (per chip and per HitBco)

nChipHit_F0_Fch3_Chip4

Entries



nChipHit_F0_Fch3_Chip4

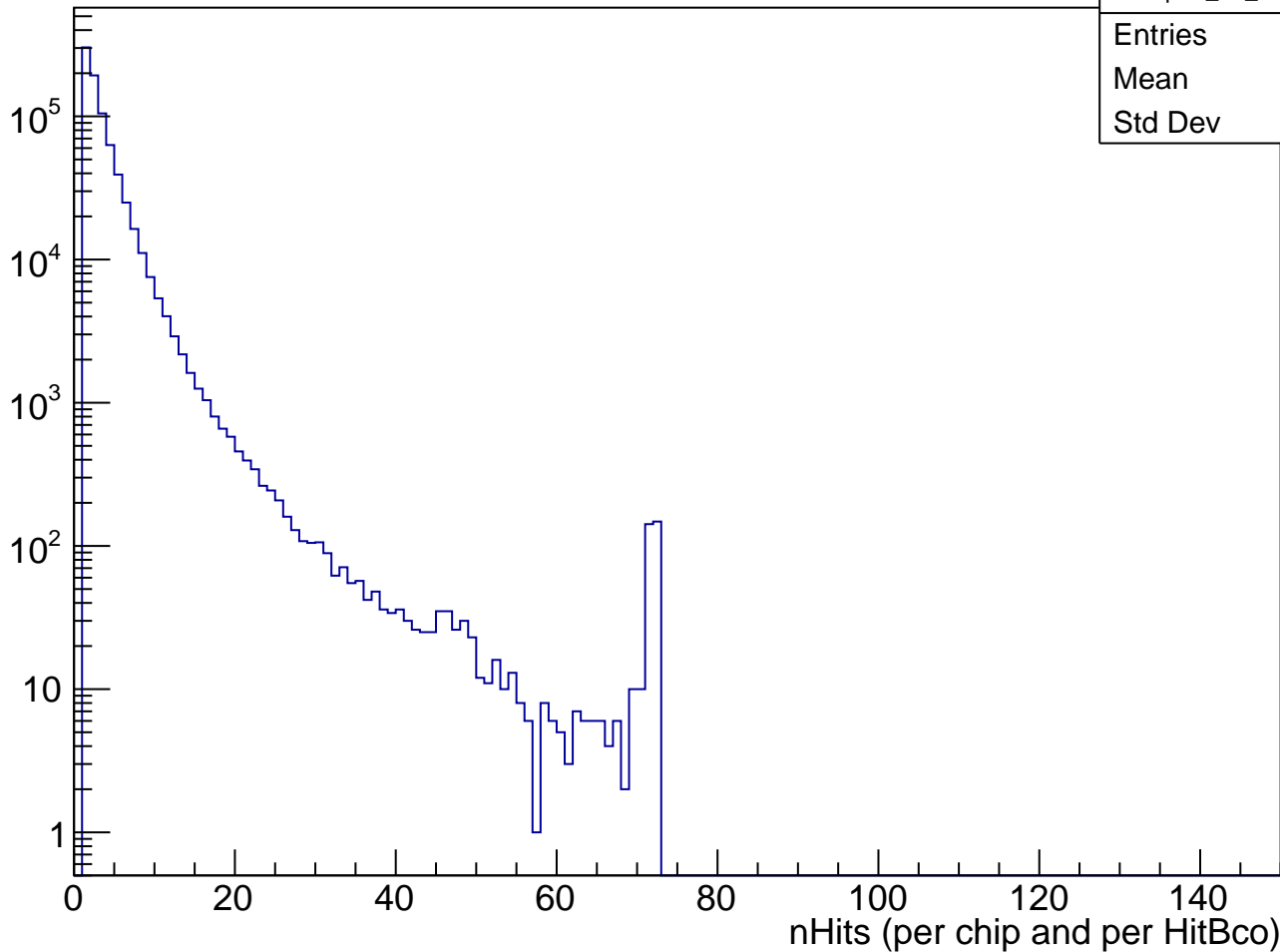
Entries 904616

Mean 3.077

Std Dev 3.454

nChipHit_F0_Fch3_Chip5

Entries



nChipHit_F0_Fch3_Chip5

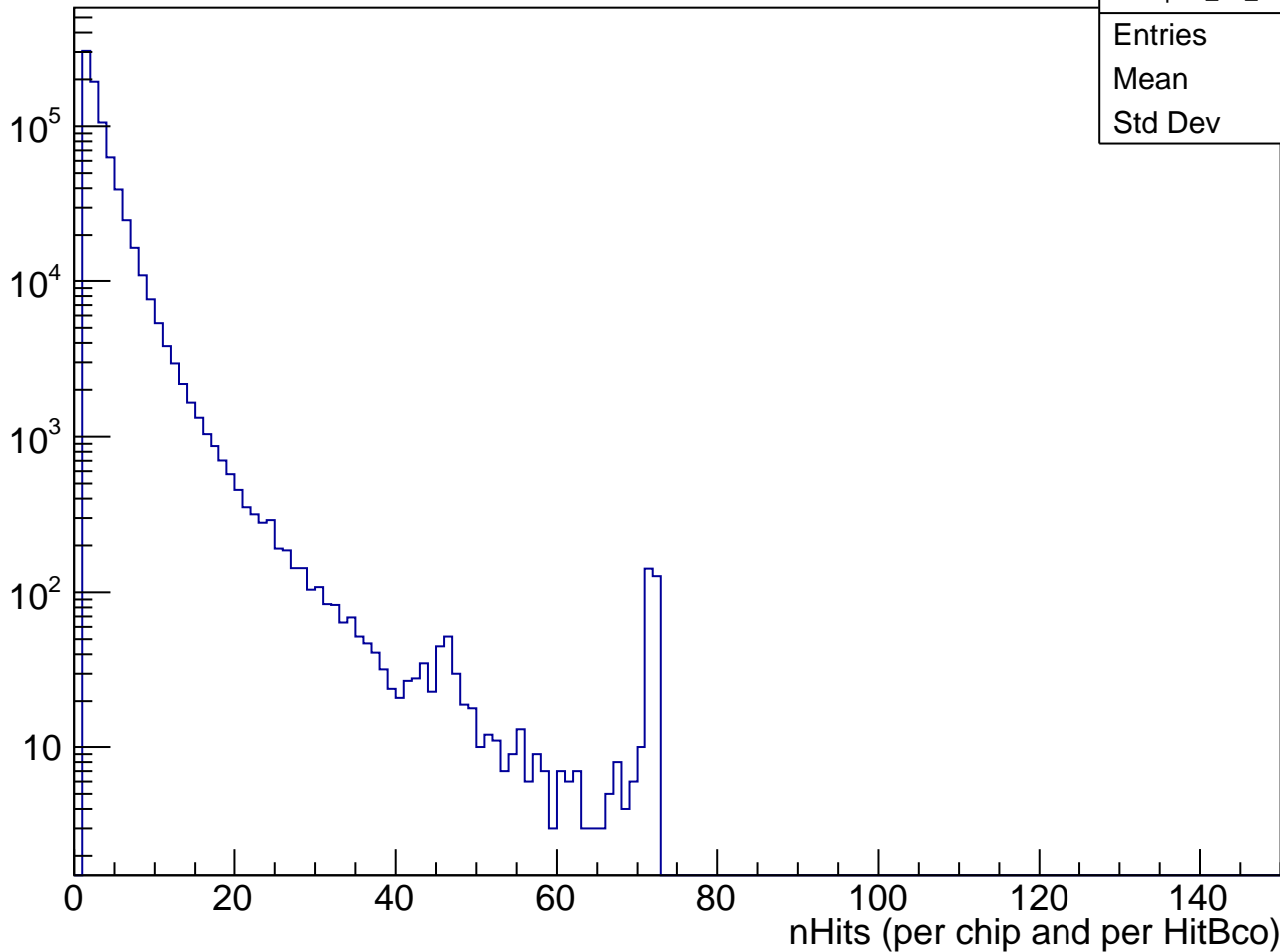
Entries 787312

Mean 2.854

Std Dev 3.292

nChipHit_F0_Fch3_Chip6

Entries



nChipHit_F0_Fch3_Chip6

Entries 790503

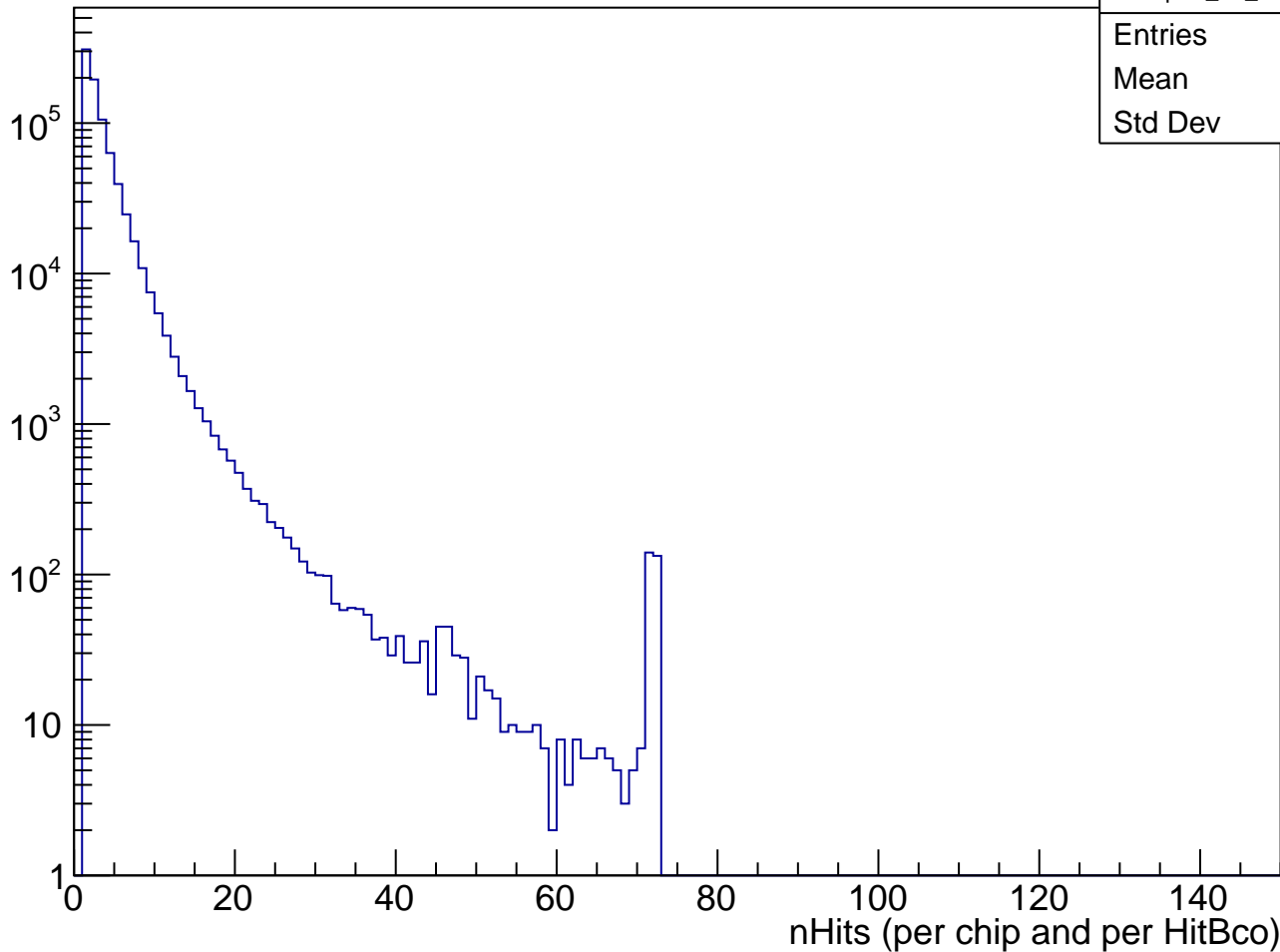
Mean 2.85

Std Dev 3.277

nHits (per chip and per HitBco)

nChipHit_F0_Fch3_Chip7

Entries

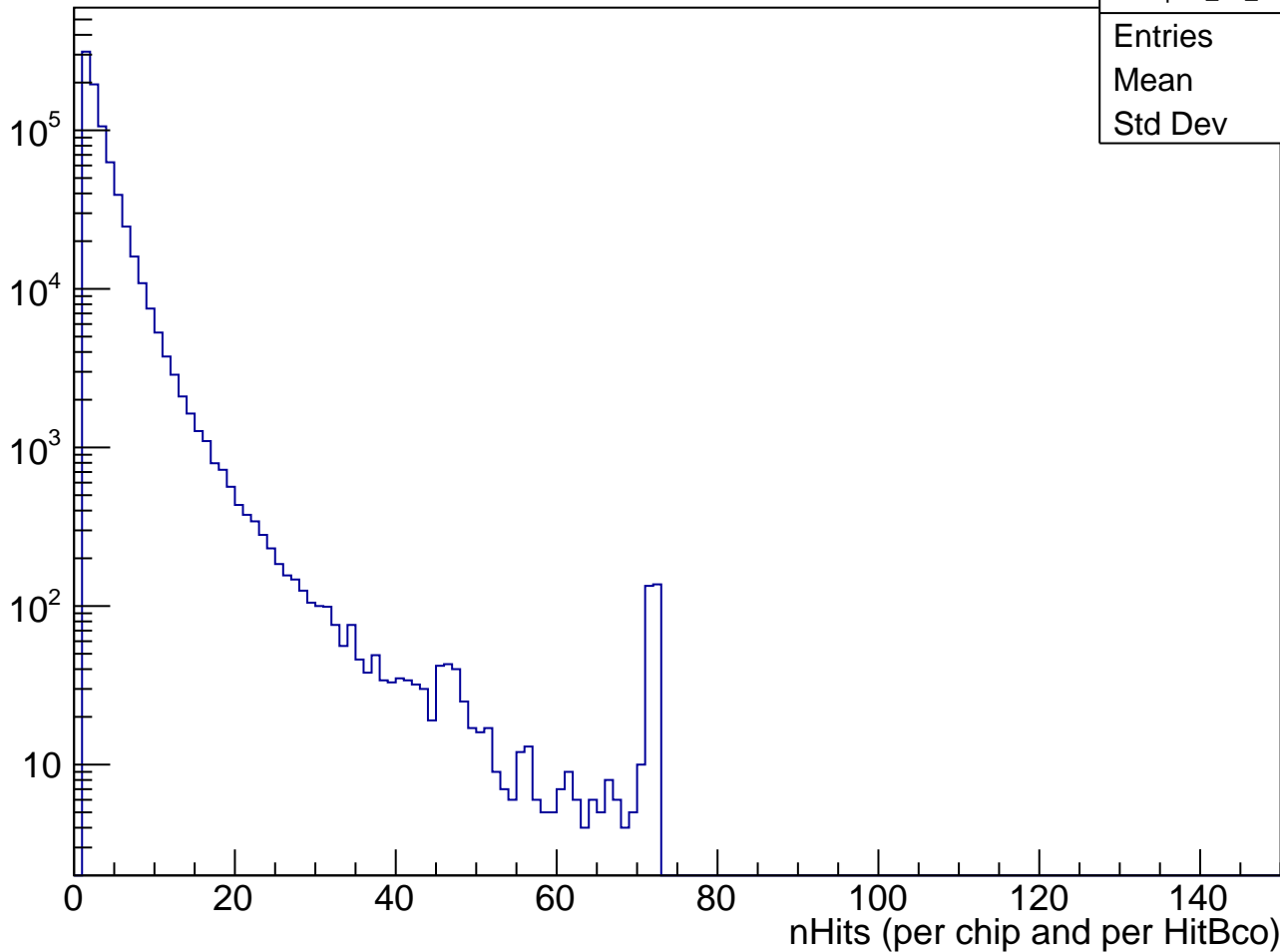


nChipHit_F0_Fch3_Chip7	
Entries	795336
Mean	2.836
Std Dev	3.272

nHits (per chip and per HitBco)

nChipHit_F0_Fch3_Chip8

Entries



nChipHit_F0_Fch3_Chip8

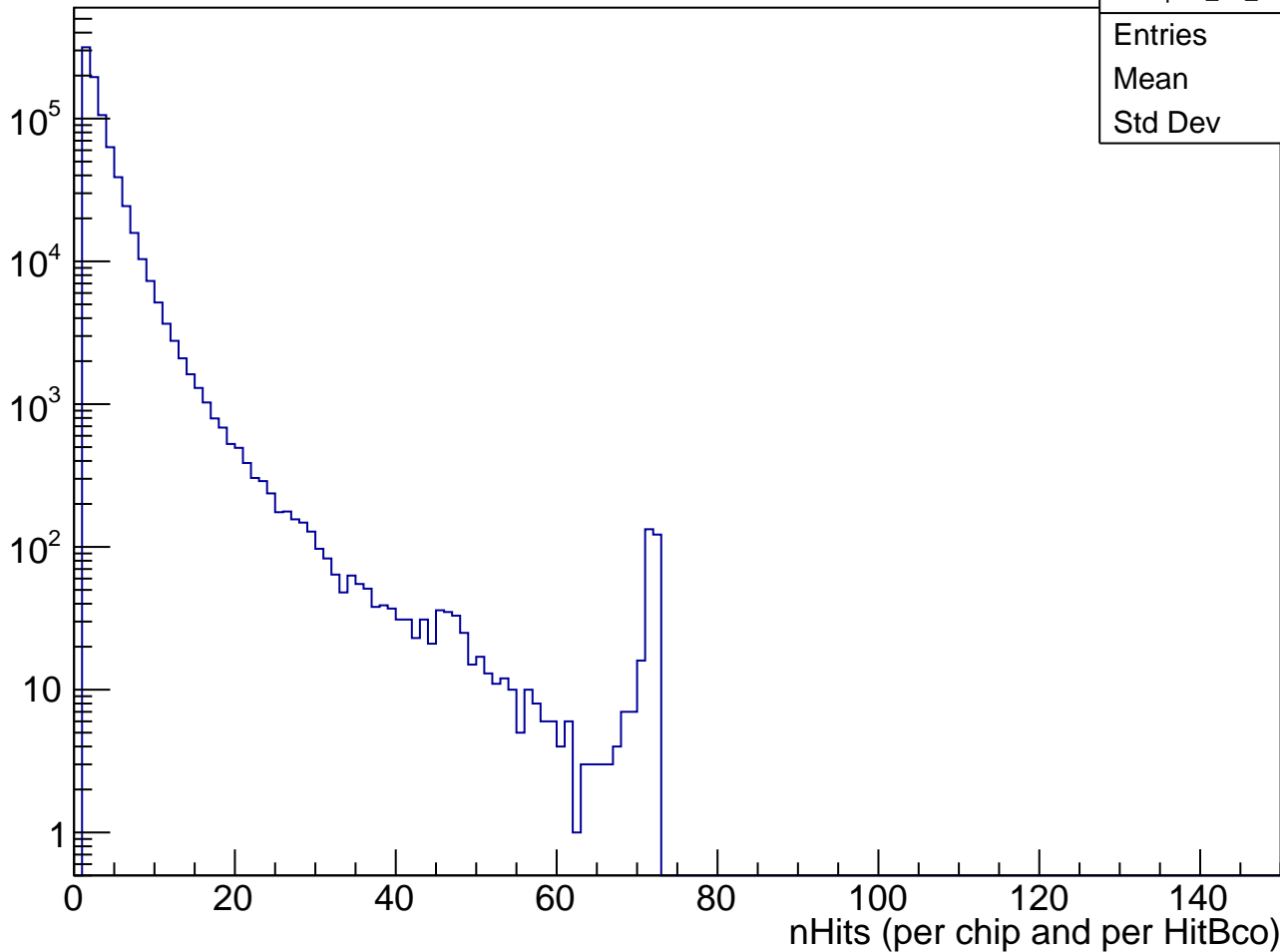
Entries 799051

Mean 2.821

Std Dev 3.266

nChipHit_F0_Fch3_Chip9

Entries



nChipHit_F0_Fch3_Chip9

Entries 800015

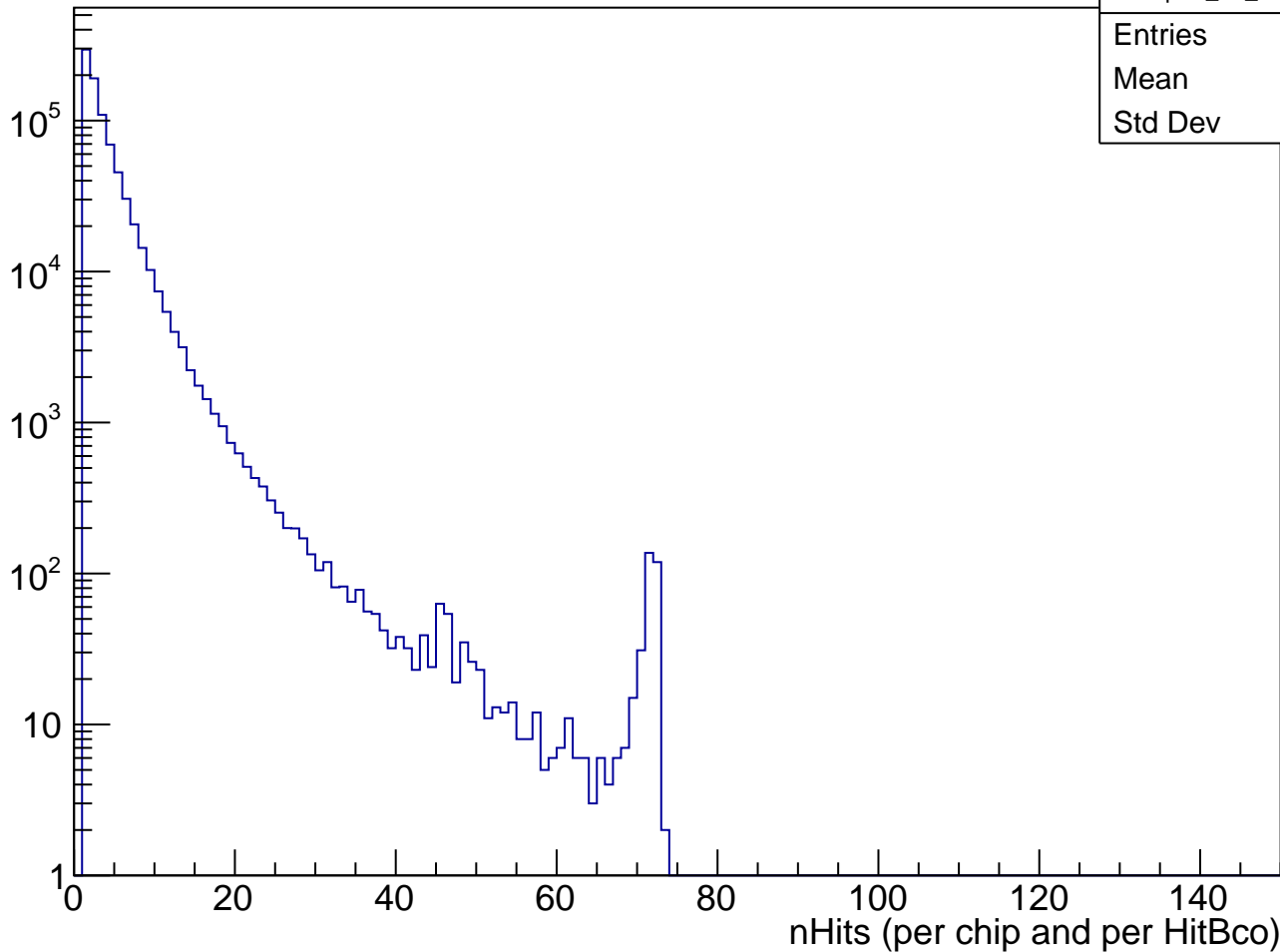
Mean 2.799

Std Dev 3.223

nHits (per chip and per HitBco)

nChipHit_F0_Fch4_Chip0

Entries

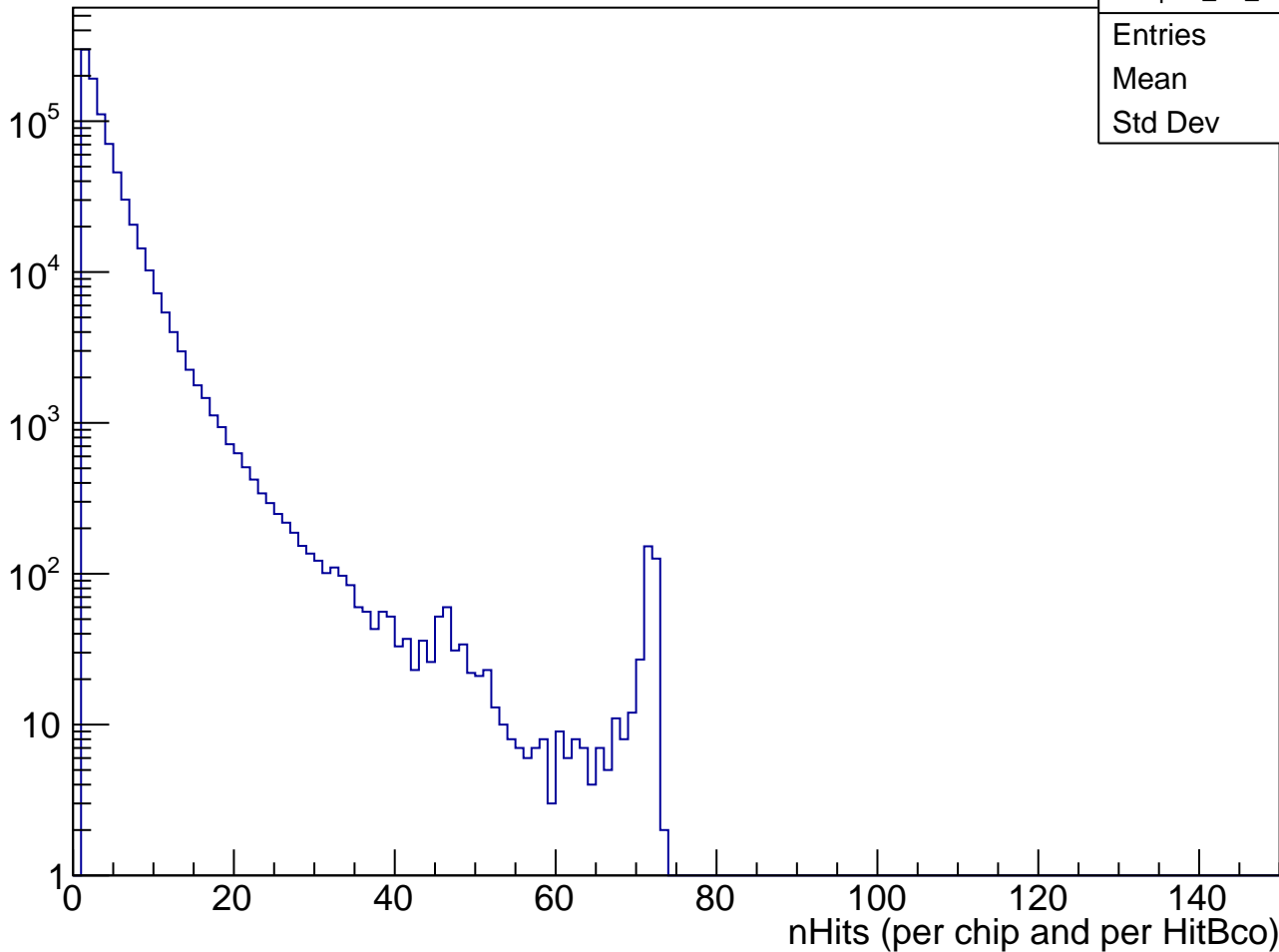


nChipHit_F0_Fch4_Chip0	
Entries	818332
Mean	3.103
Std Dev	3.529

nHits (per chip and per HitBco)

nChipHit_F0_Fch4_Chip1

Entries

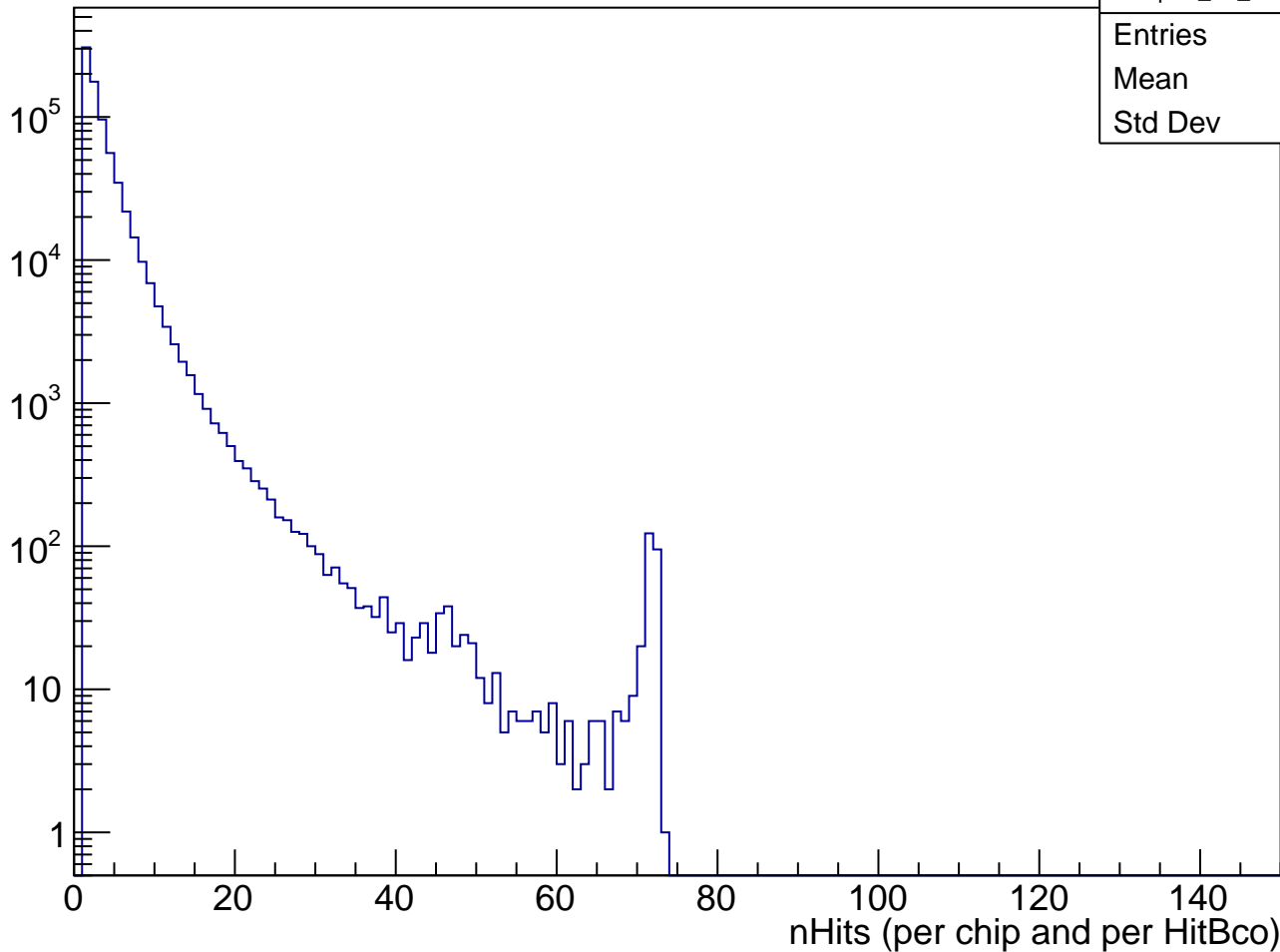


nChipHit_F0_Fch4_Chip1	
Entries	825560
Mean	3.095
Std Dev	3.534

nHits (per chip and per HitBco)

nChipHit_F0_Fch4_Chip10

Entries

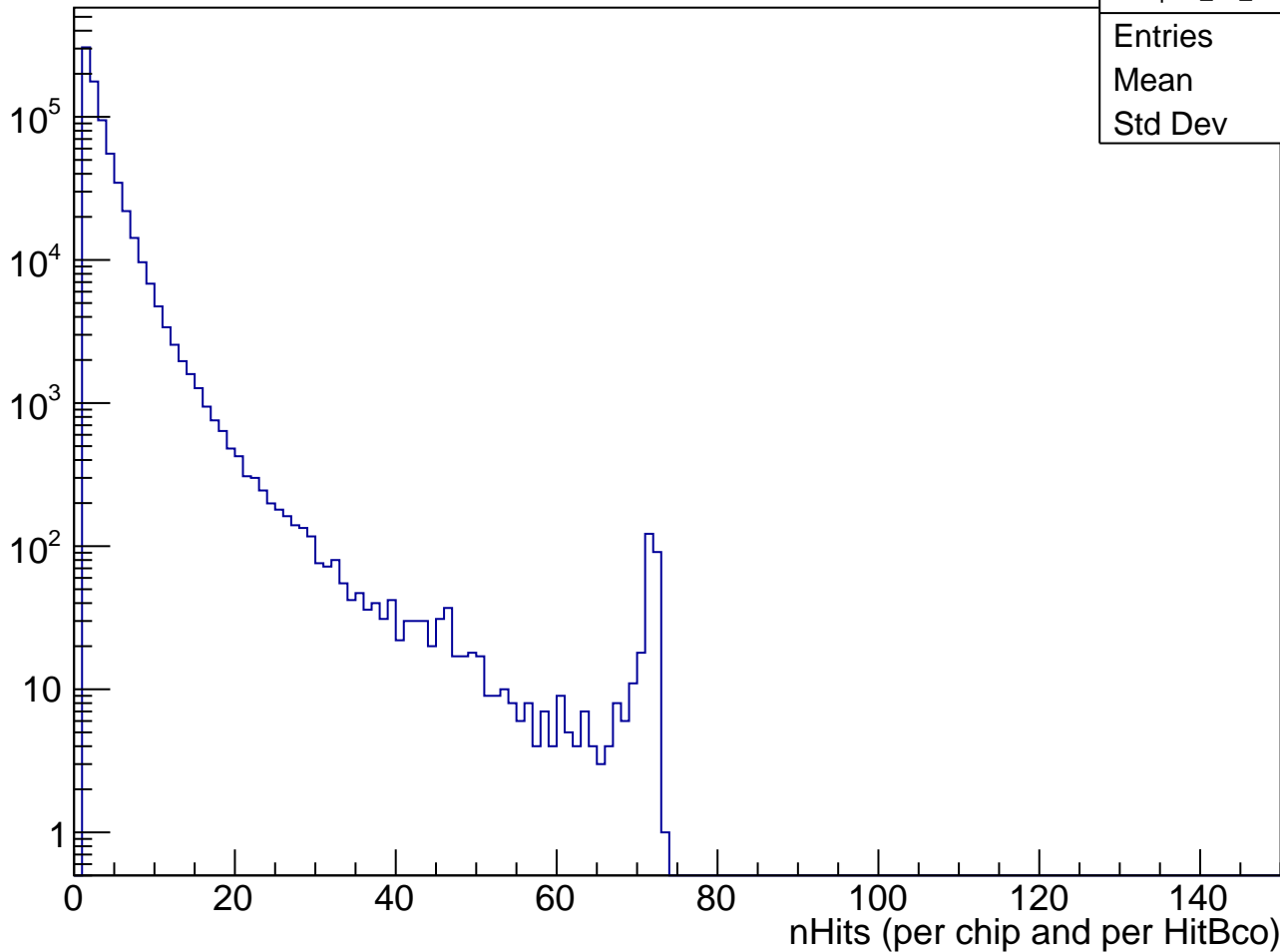


nChipHit_F0_Fch4_Chip10	
Entries	743906
Mean	2.752
Std Dev	3.189

nHits (per chip and per HitBco)

nChipHit_F0_Fch4_Chip11

Entries

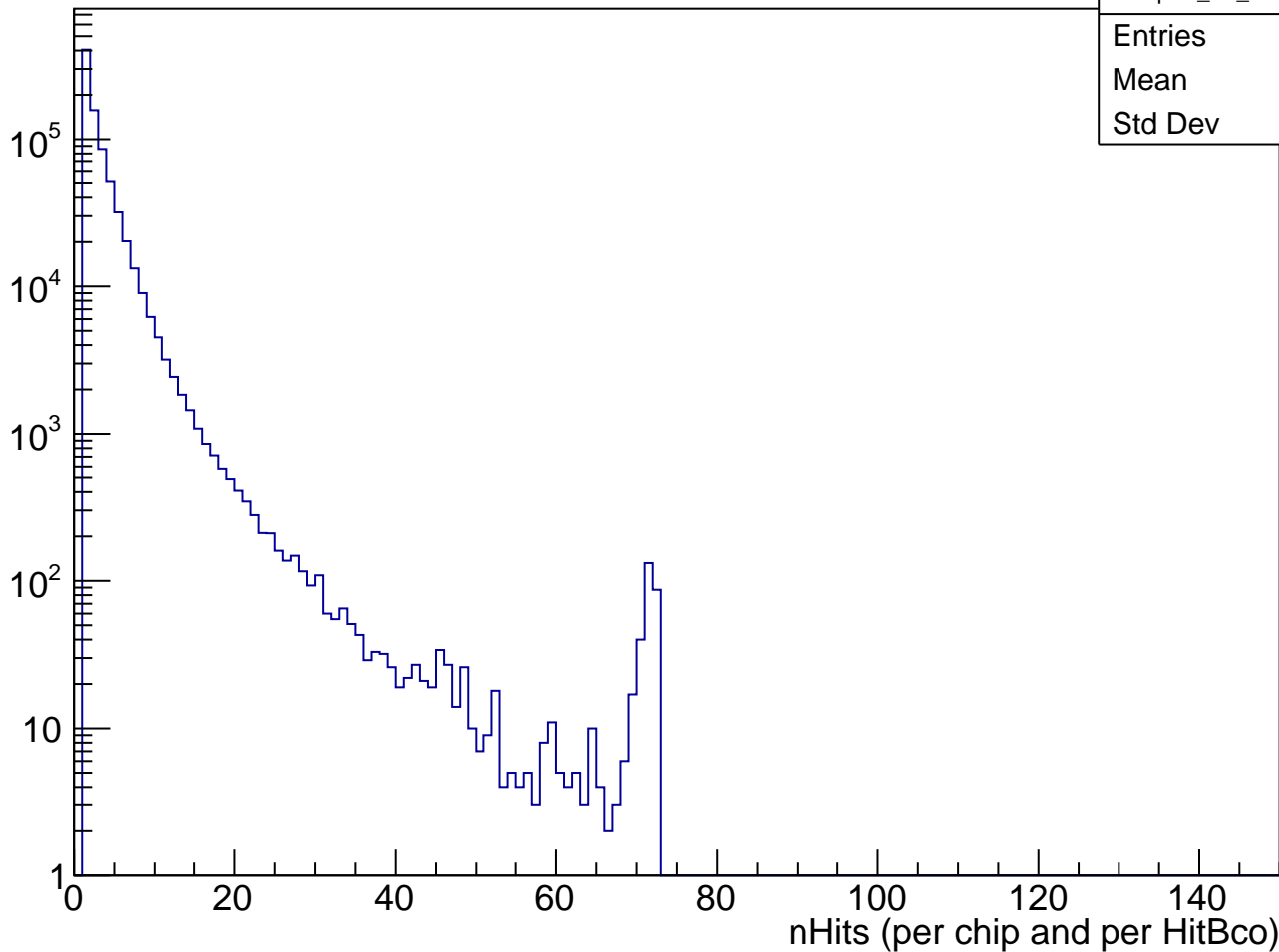


nChipHit_F0_Fch4_Chip11	
Entries	741551
Mean	2.756
Std Dev	3.212

nHits (per chip and per HitBco)

nChipHit_F0_Fch4_Chip12

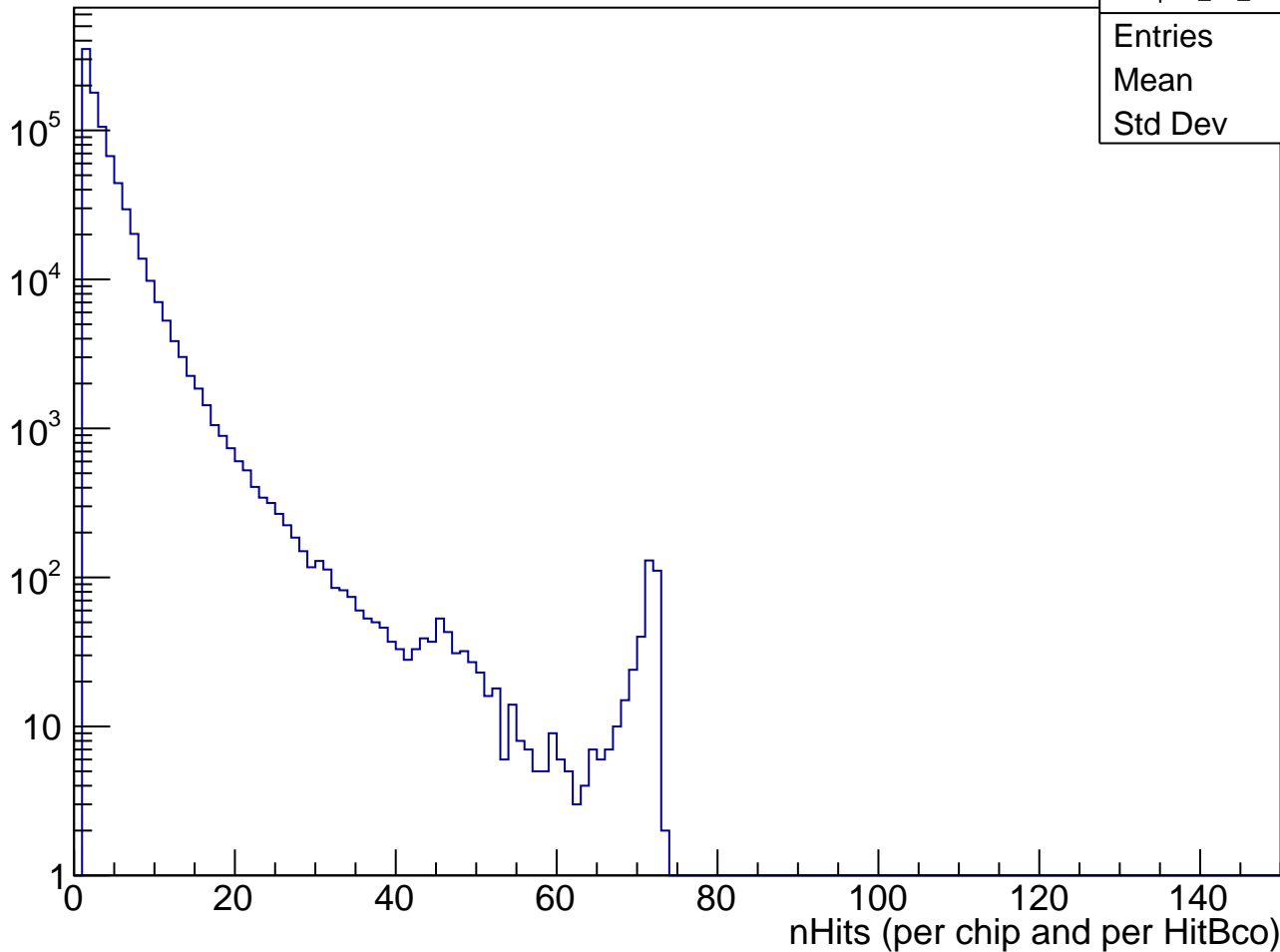
Entries



nChipHit_F0_Fch4_Chip12	
Entries	800813
Mean	2.499
Std Dev	3.086

nChipHit_F0_Fch4_Chip13

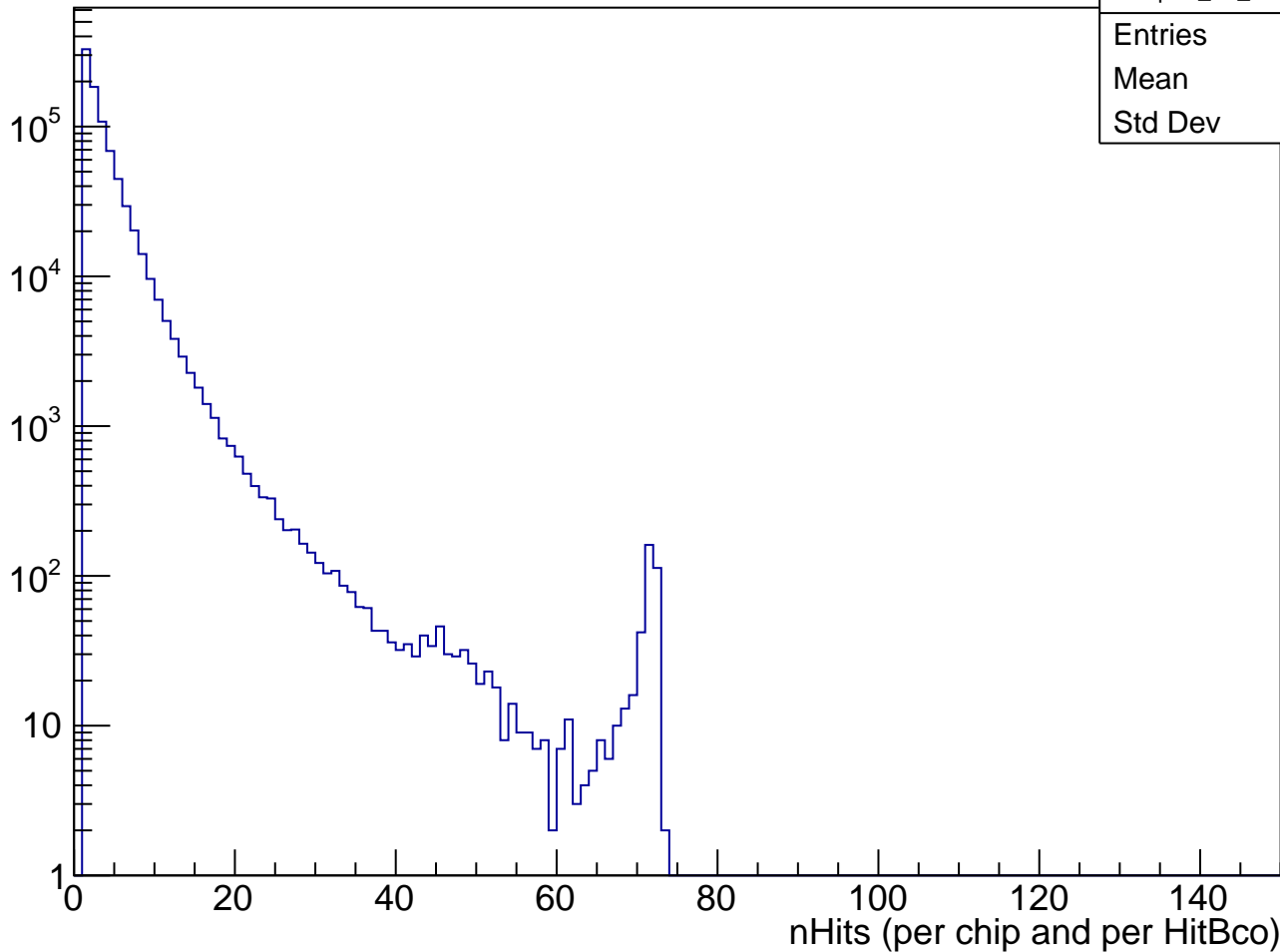
Entries



nChipHit_F0_Fch4_Chip13	
Entries	853221
Mean	2.956
Std Dev	3.478

nChipHit_F0_Fch4_Chip14

Entries

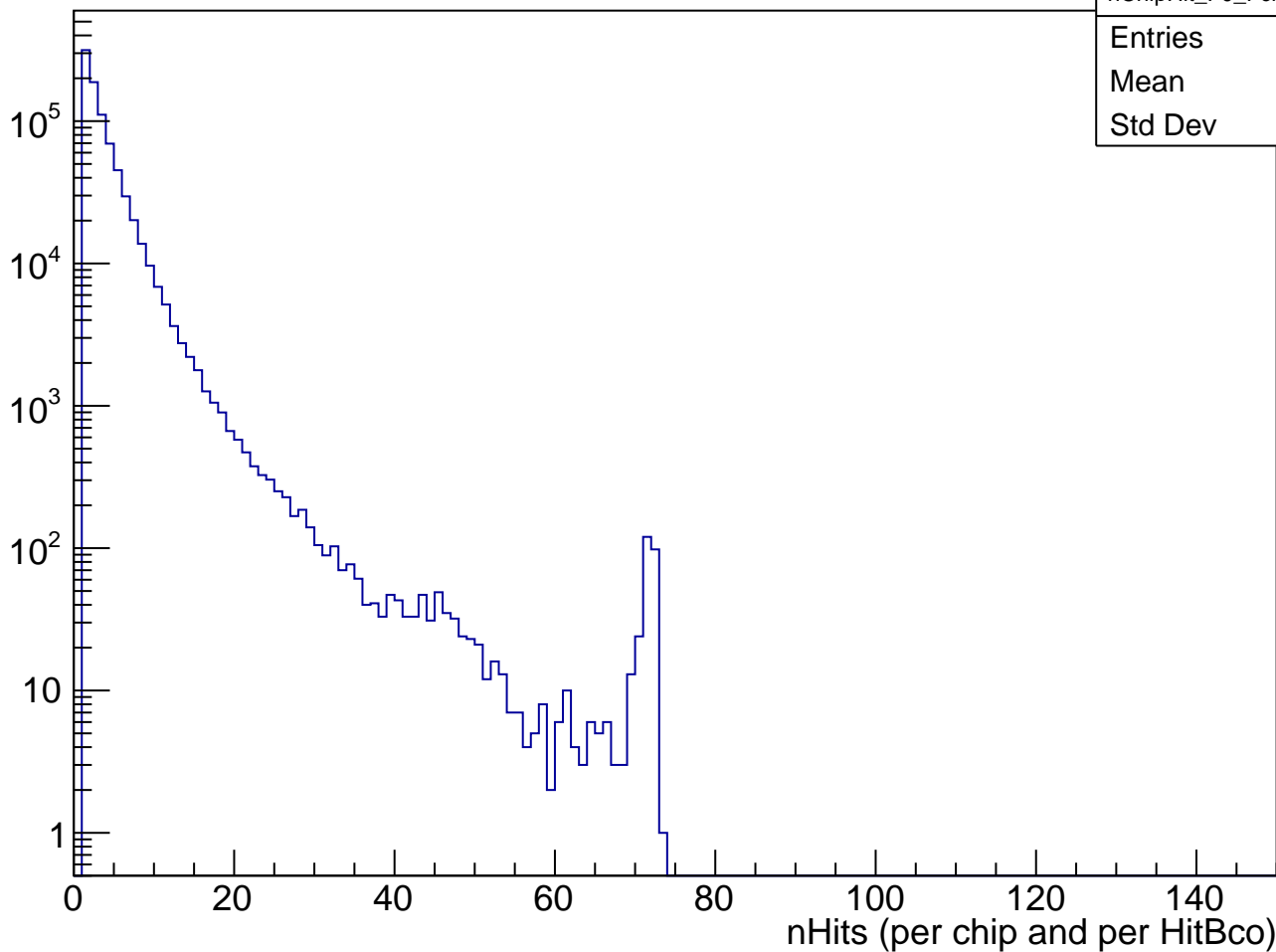


nChipHit_F0_Fch4_Chip14	
Entries	838686
Mean	3.006
Std Dev	3.509

nHits (per chip and per HitBco)

nChipHit_F0_Fch4_Chip15

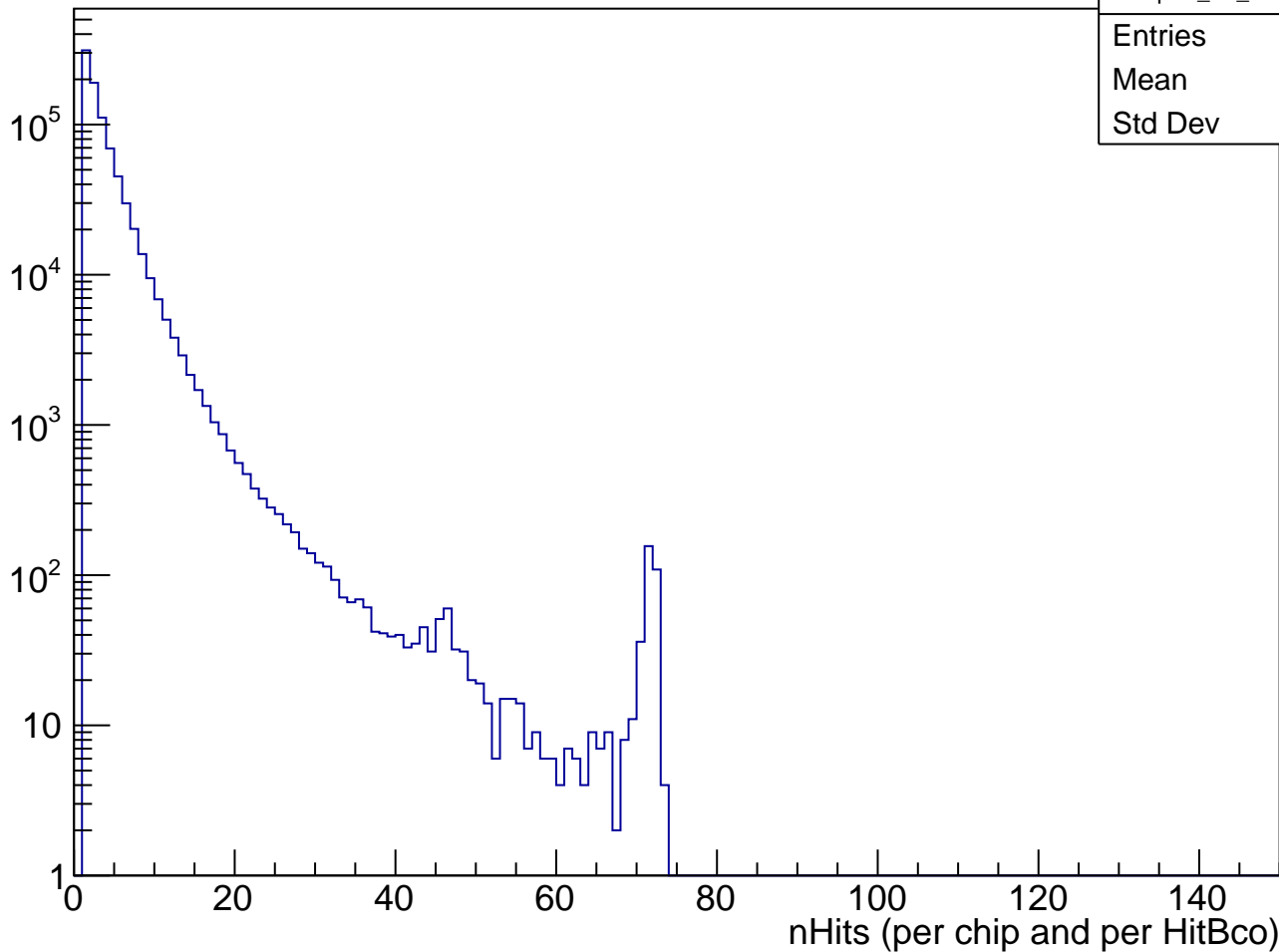
Entries



nChipHit_F0_Fch4_Chip15	
Entries	832618
Mean	3.01
Std Dev	3.398

nChipHit_F0_Fch4_Chip16

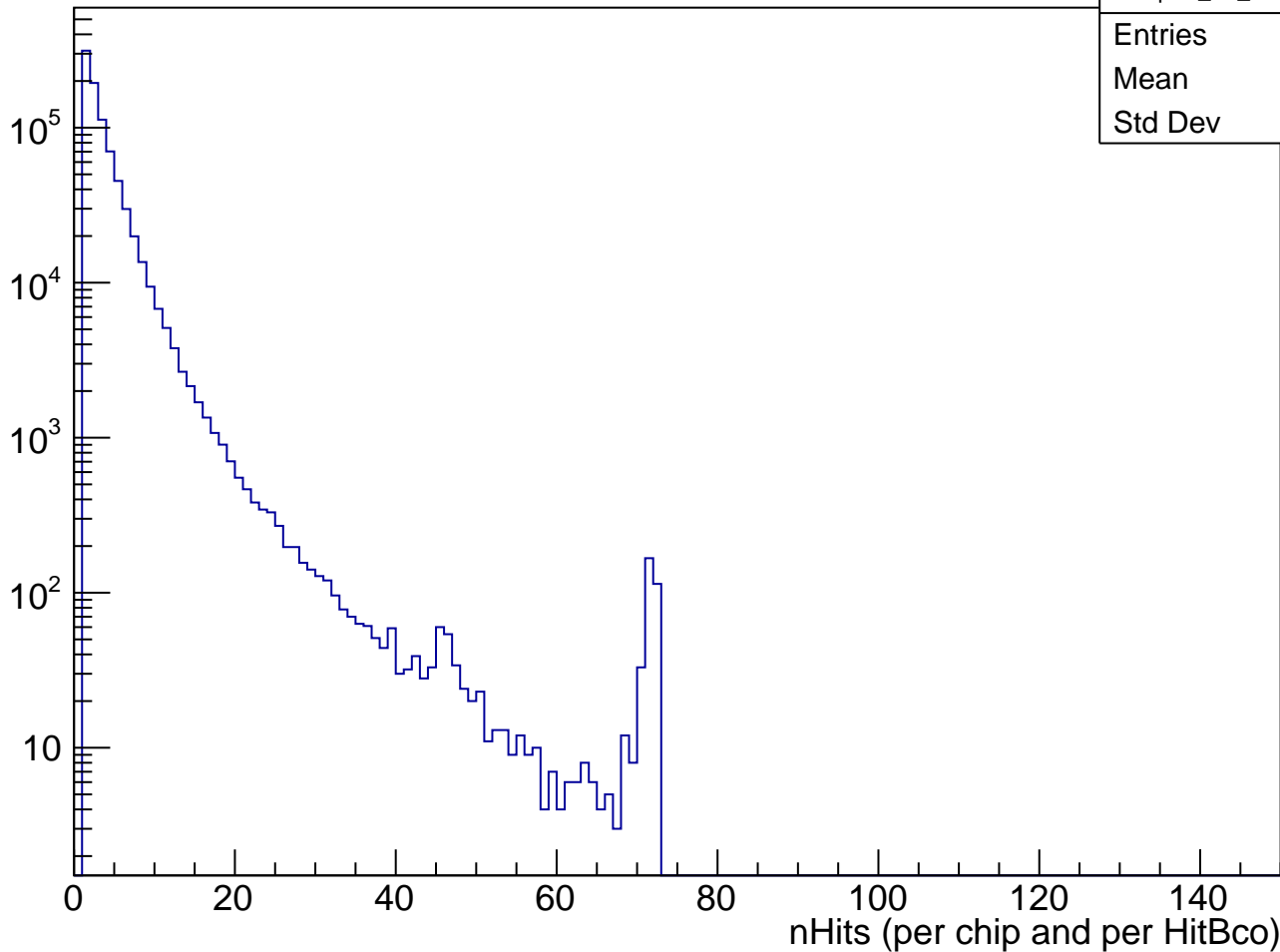
Entries



nChipHit_F0_Fch4_Chip16	
Entries	831736
Mean	3.026
Std Dev	3.476

nChipHit_F0_Fch4_Chip17

Entries

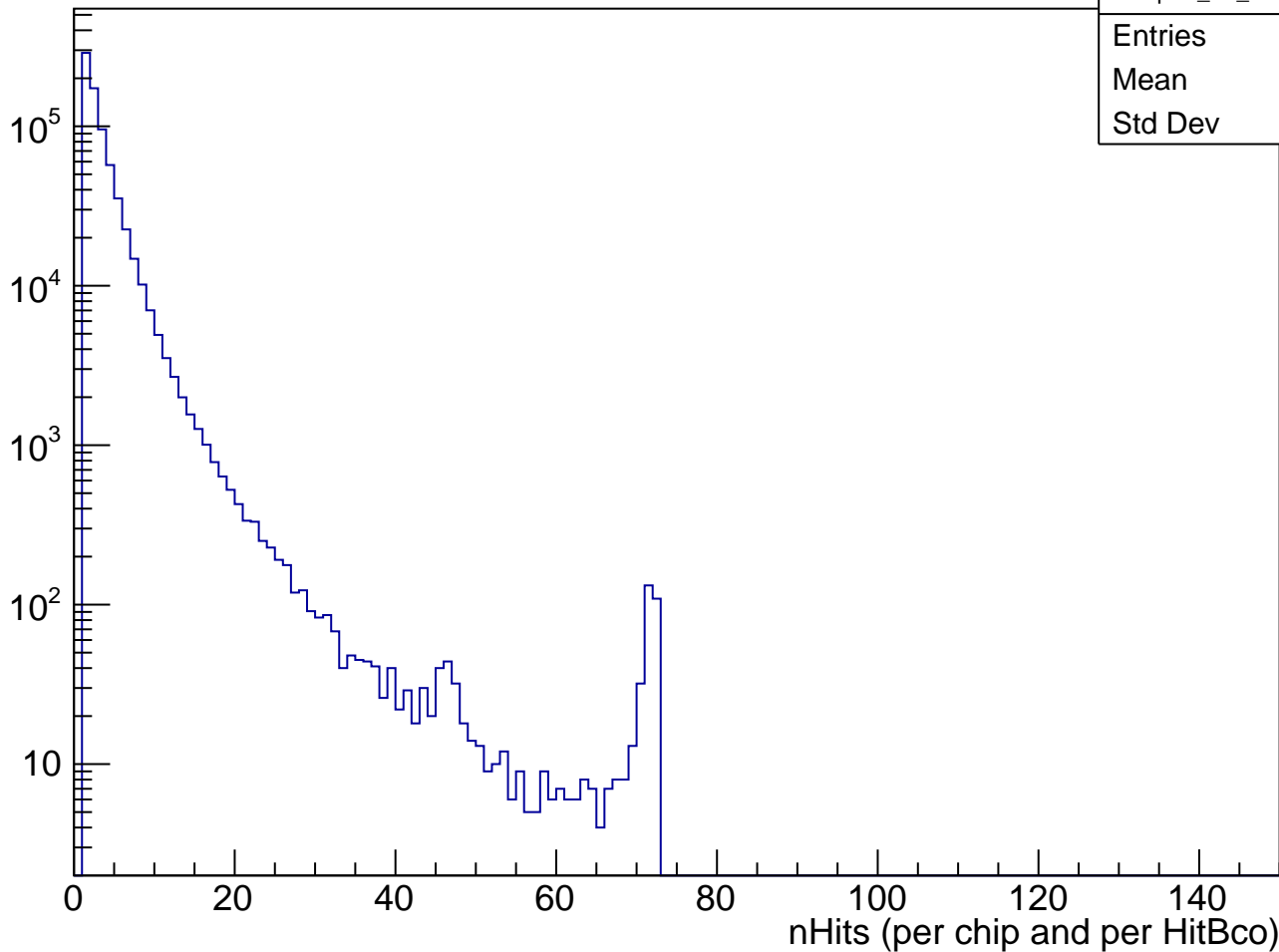


nChipHit_F0_Fch4_Chip17	
Entries	839984
Mean	3.016
Std Dev	3.471

nHits (per chip and per HitBco)

nChipHit_F0_Fch4_Chip18

Entries

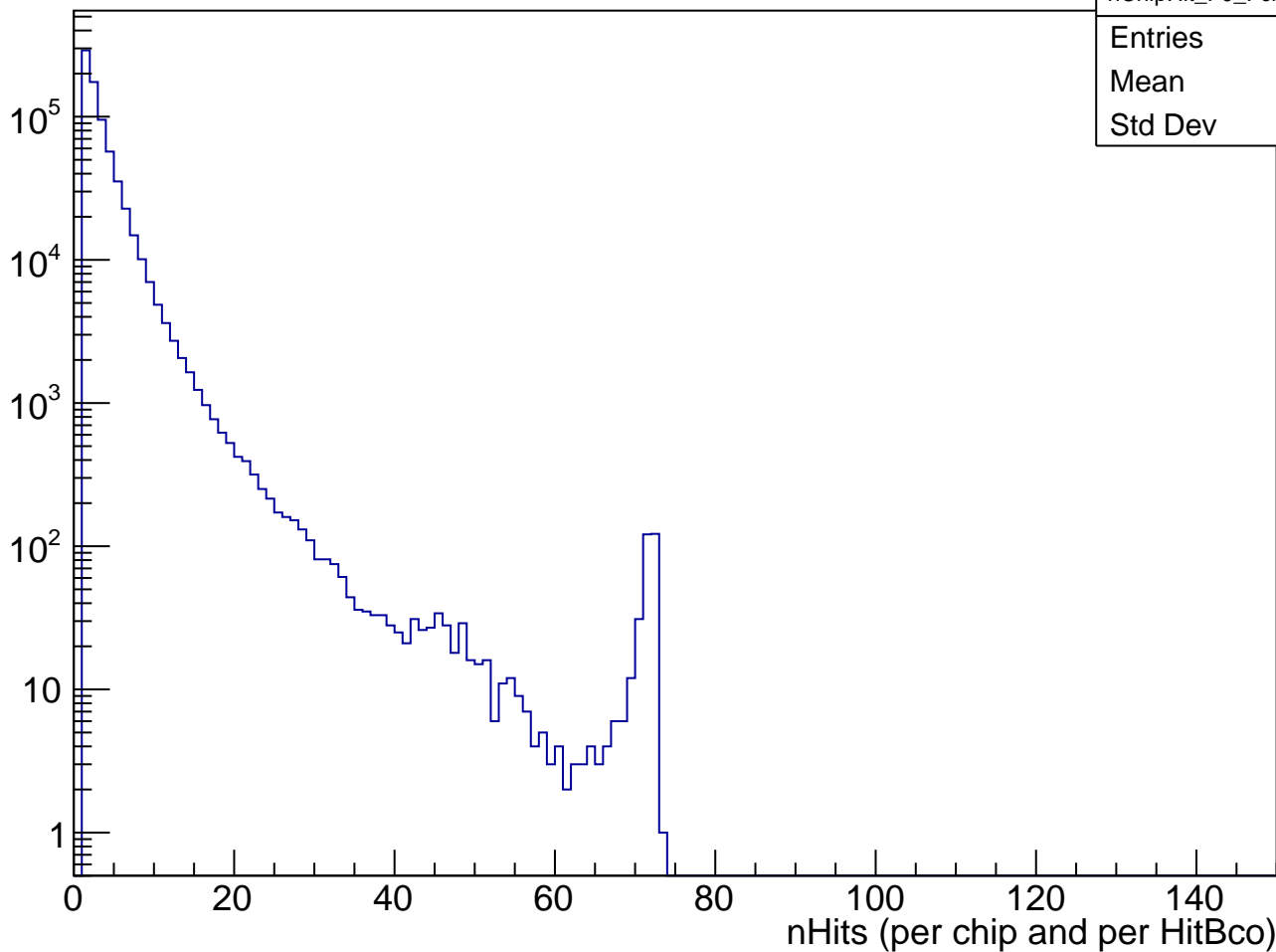


nChipHit_F0_Fch4_Chip18	
Entries	726790
Mean	2.833
Std Dev	3.314

nHits (per chip and per HitBco)

nChipHit_F0_Fch4_Chip19

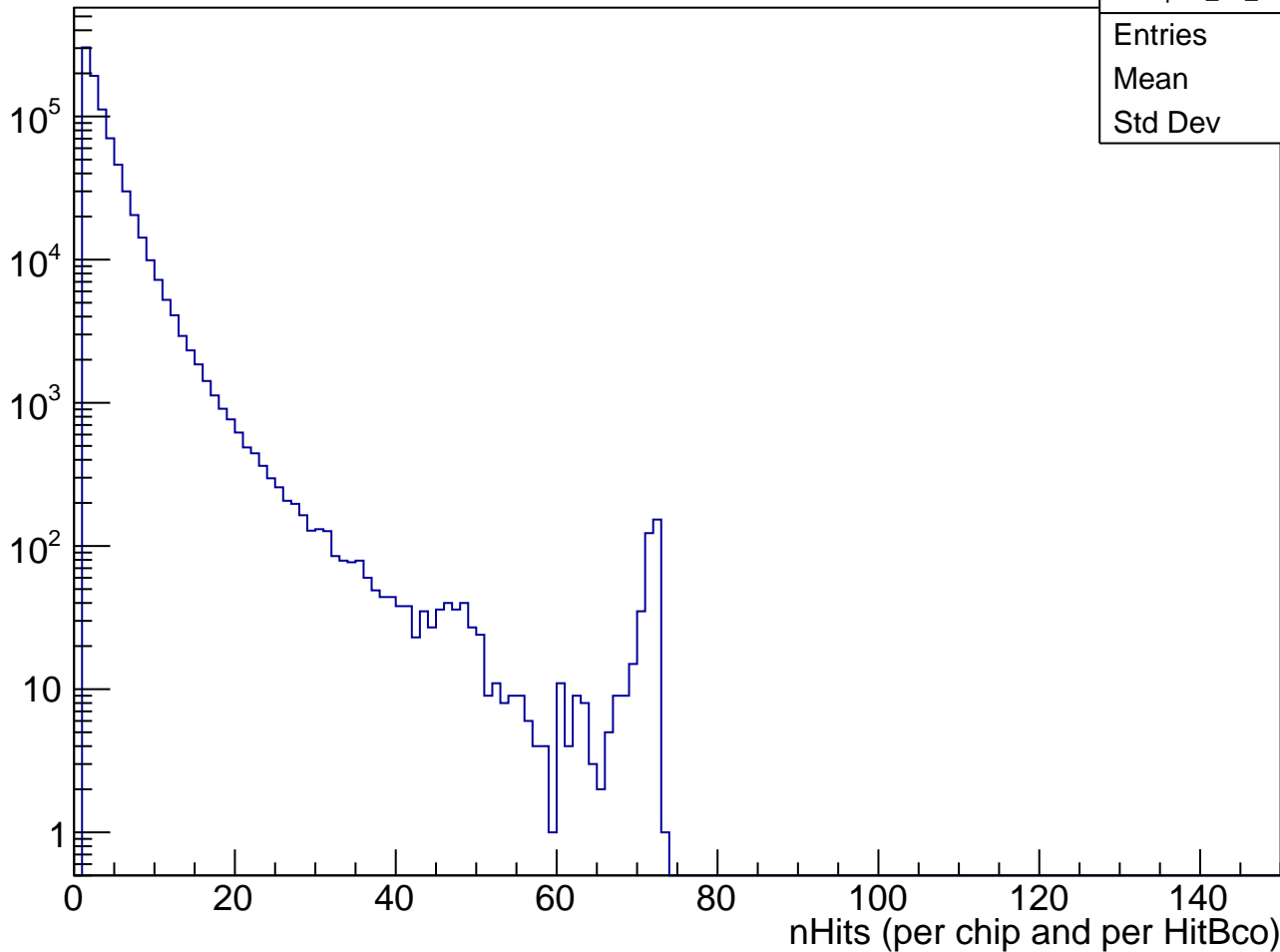
Entries



nChipHit_F0_Fch4_Chip19	
Entries	730060
Mean	2.826
Std Dev	3.289

nChipHit_F0_Fch4_Chip2

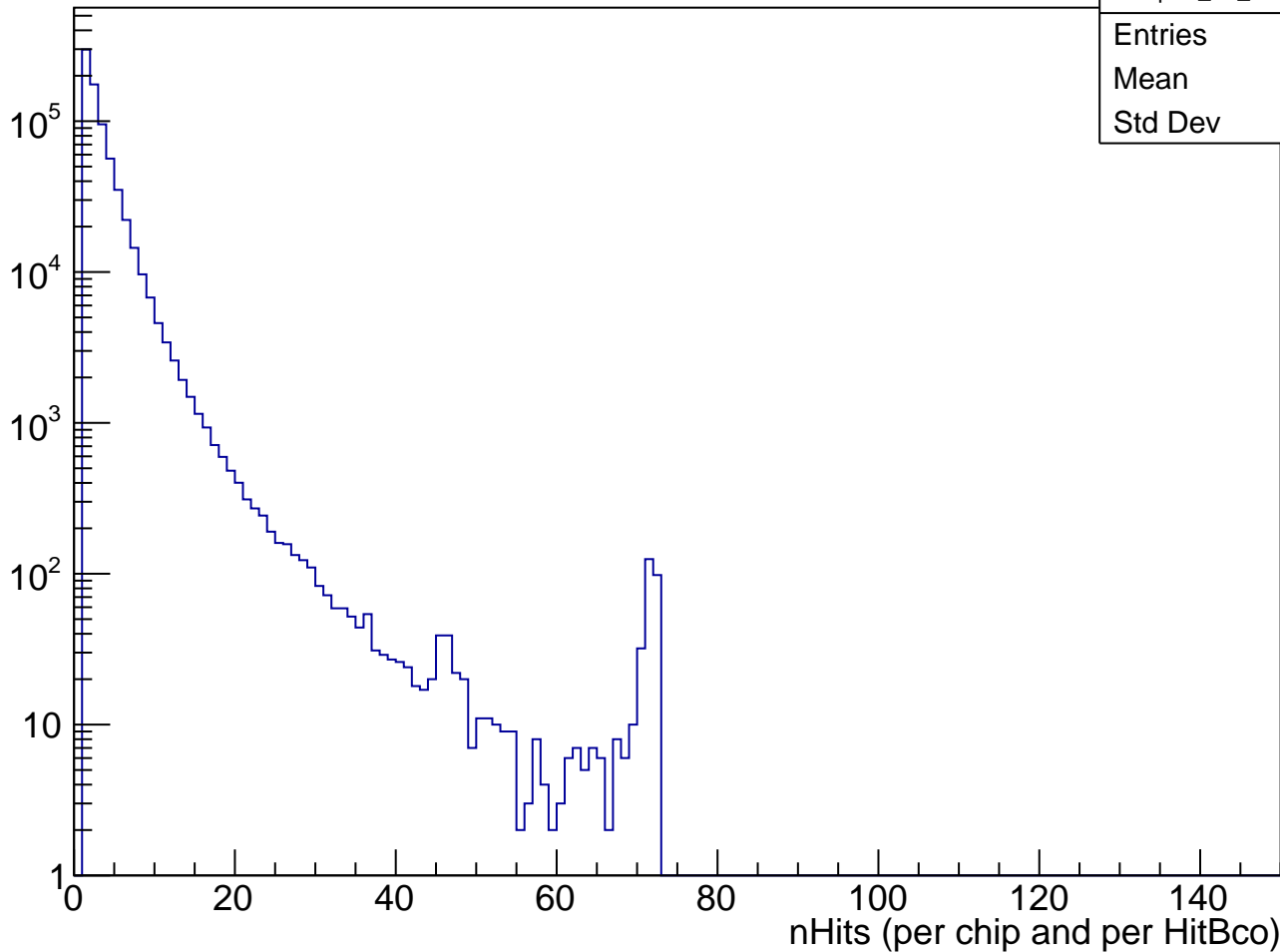
Entries



nChipHit_F0_Fch4_Chip2	
Entries	831572
Mean	3.076
Std Dev	3.515

nChipHit_F0_Fch4_Chip20

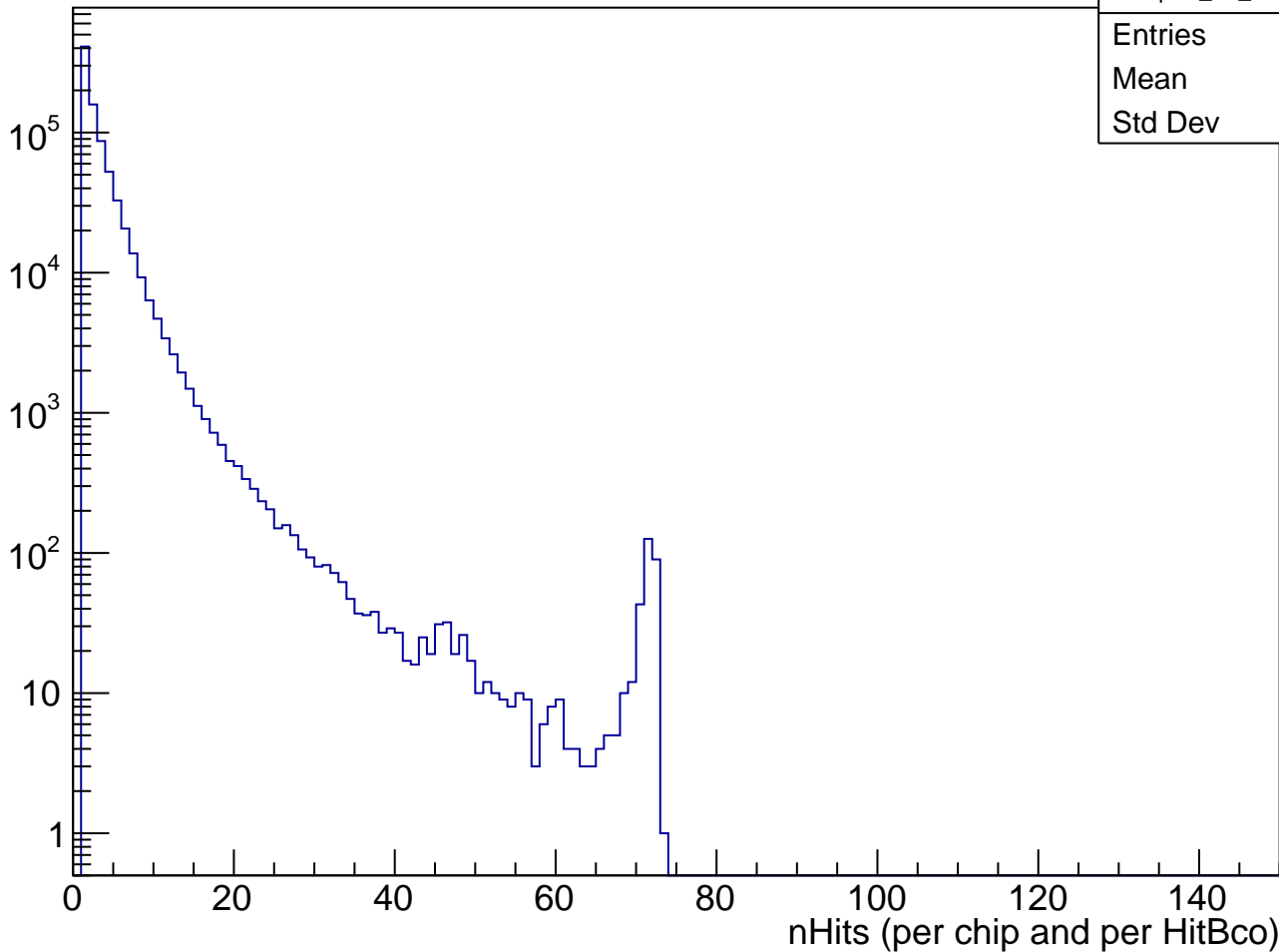
Entries



nChipHit_F0_Fch4_Chip20	
Entries	734627
Mean	2.77
Std Dev	3.204

nChipHit_F0_Fch4_Chip21

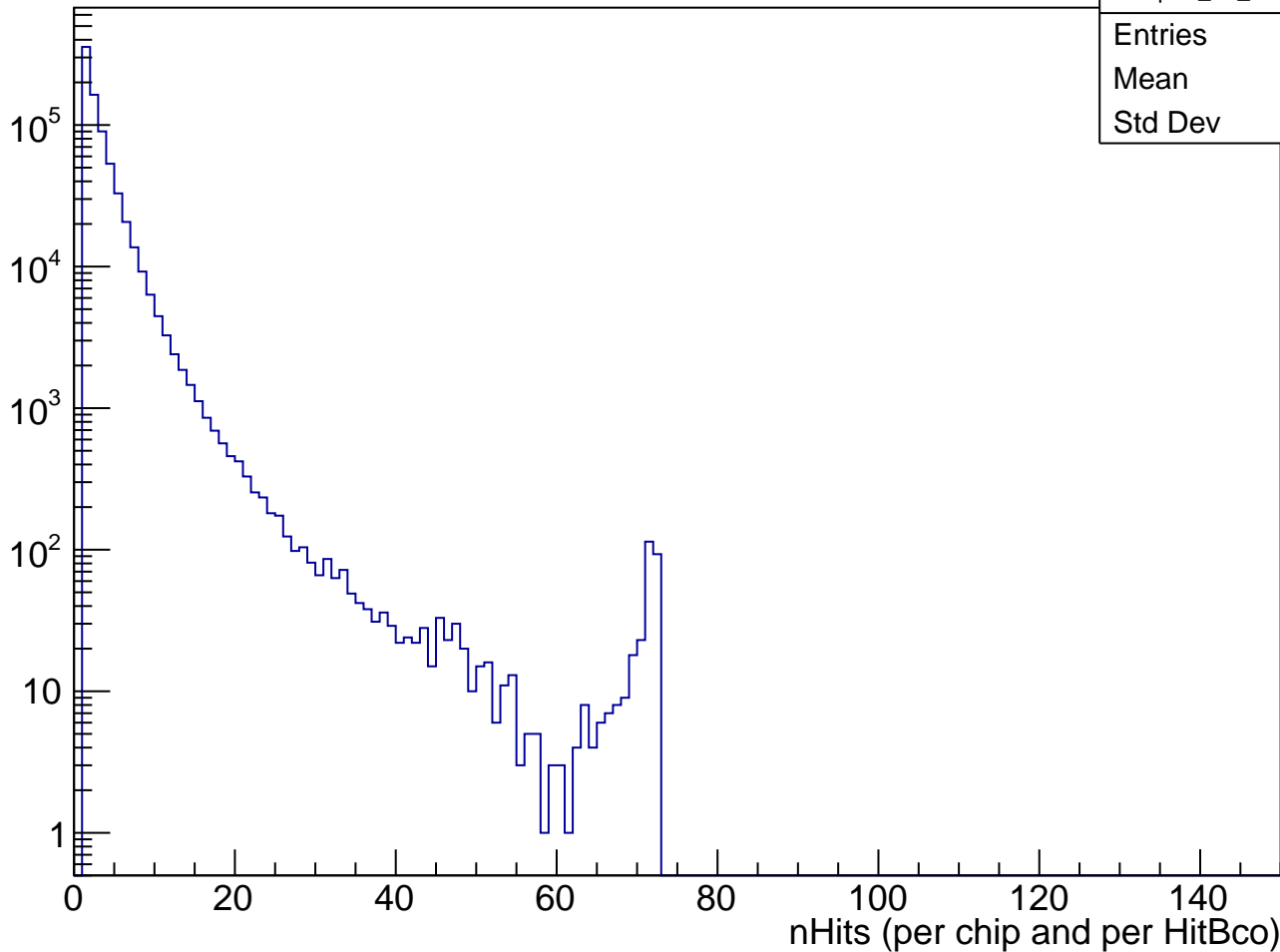
Entries



nChipHit_F0_Fch4_Chip21	
Entries	812886
Mean	2.512
Std Dev	3.099

nChipHit_F0_Fch4_Chip22

Entries

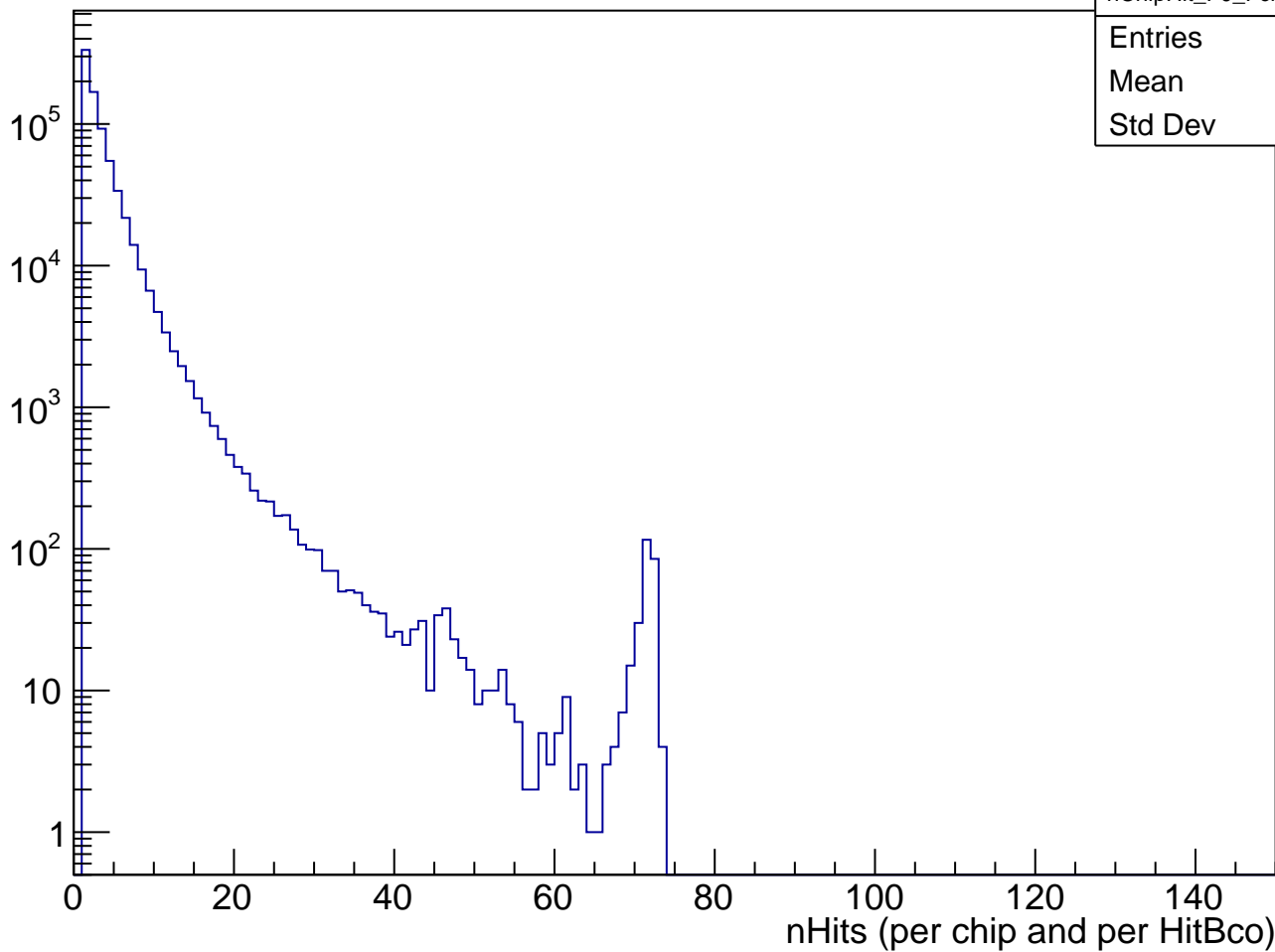


nChipHit_F0_Fch4_Chip22	
Entries	766092
Mean	2.604
Std Dev	3.111

nHits (per chip and per HitBco)

nChipHit_F0_Fch4_Chip23

Entries



nChipHit_F0_Fch4_Chip23

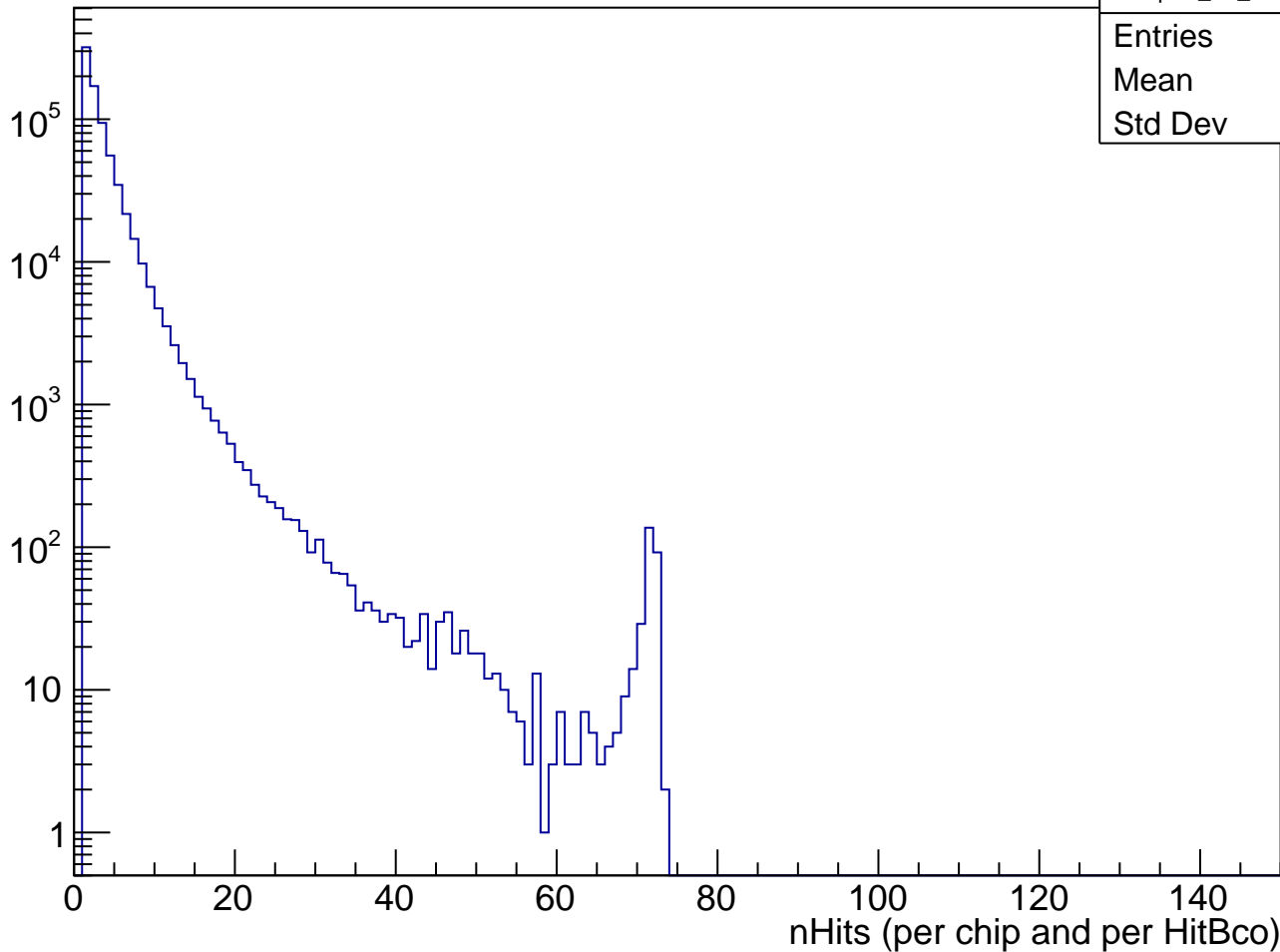
Entries 756114

Mean 2.679

Std Dev 3.15

nChipHit_F0_Fch4_Chip24

Entries

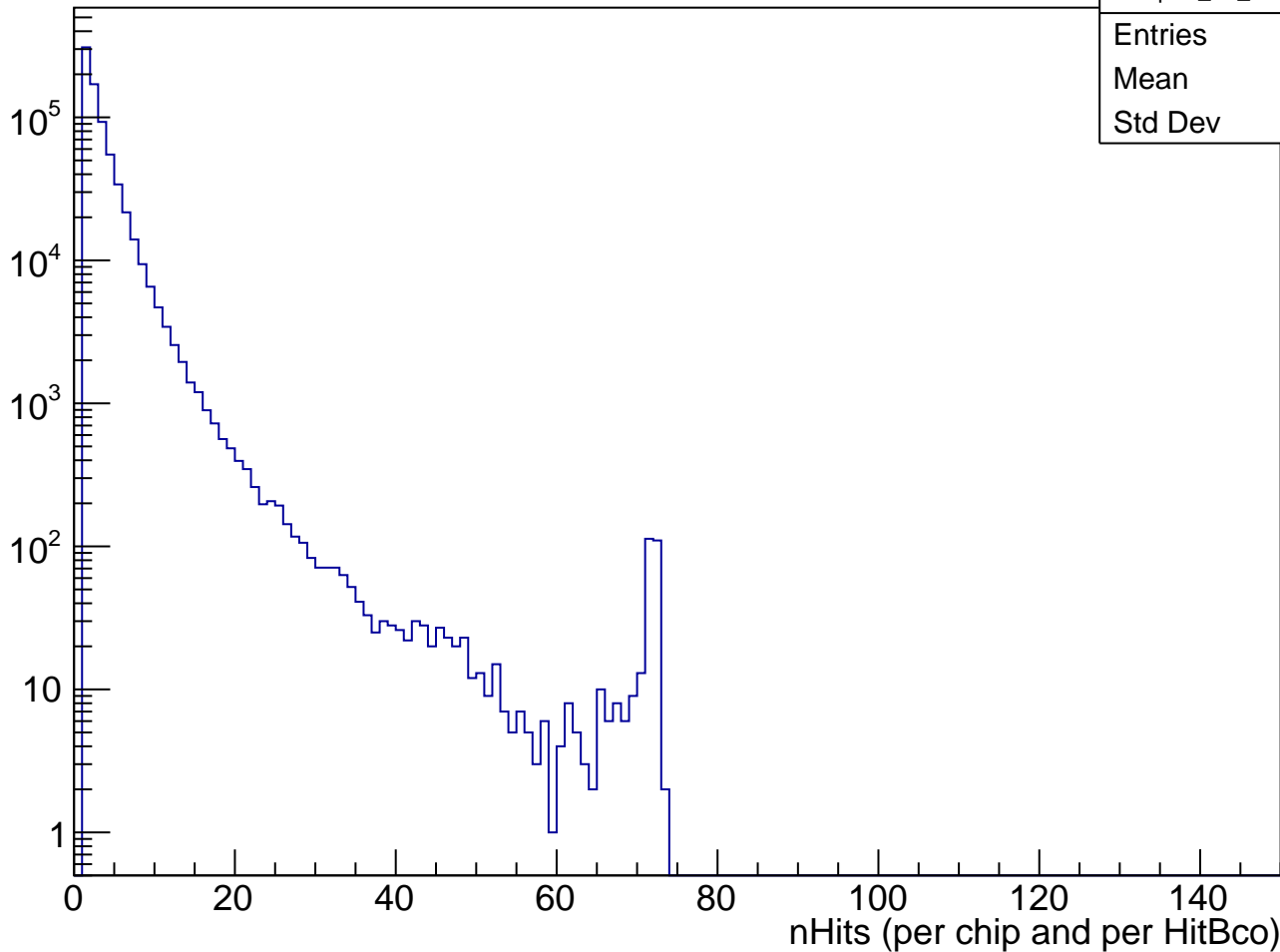


nChipHit_F0_Fch4_Chip24	
Entries	749311
Mean	2.732
Std Dev	3.23

nHits (per chip and per HitBco)

nChipHit_F0_Fch4_Chip25

Entries

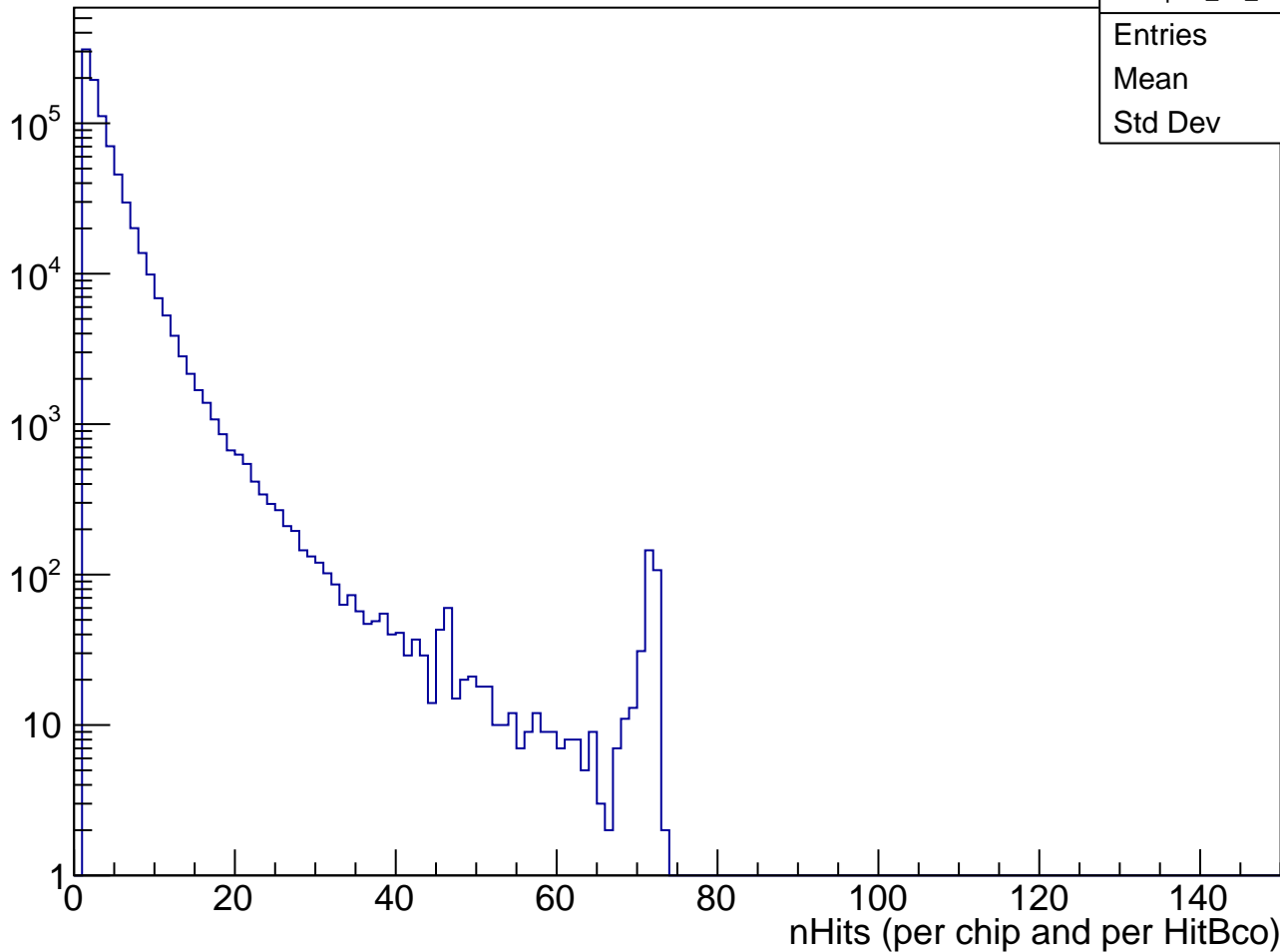


nChipHit_F0_Fch4_Chip25	
Entries	733549
Mean	2.73
Std Dev	3.175

nHits (per chip and per HitBco)

nChipHit_F0_Fch4_Chip3

Entries



nChipHit_F0_Fch4_Chip3

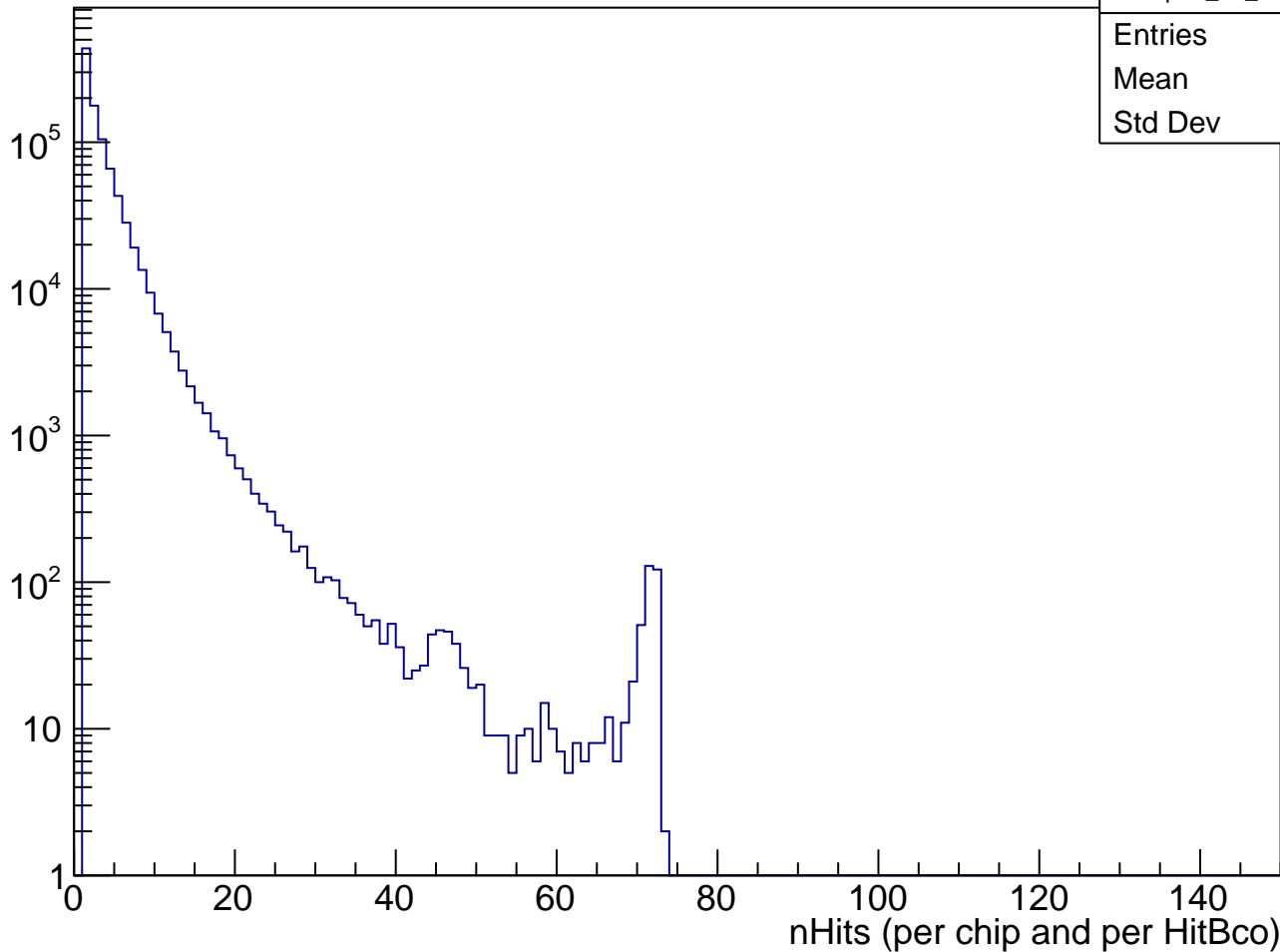
Entries 836375

Mean 3.032

Std Dev 3.448

nChipHit_F0_Fch4_Chip4

Entries



nChipHit_F0_Fch4_Chip4

Entries 929437

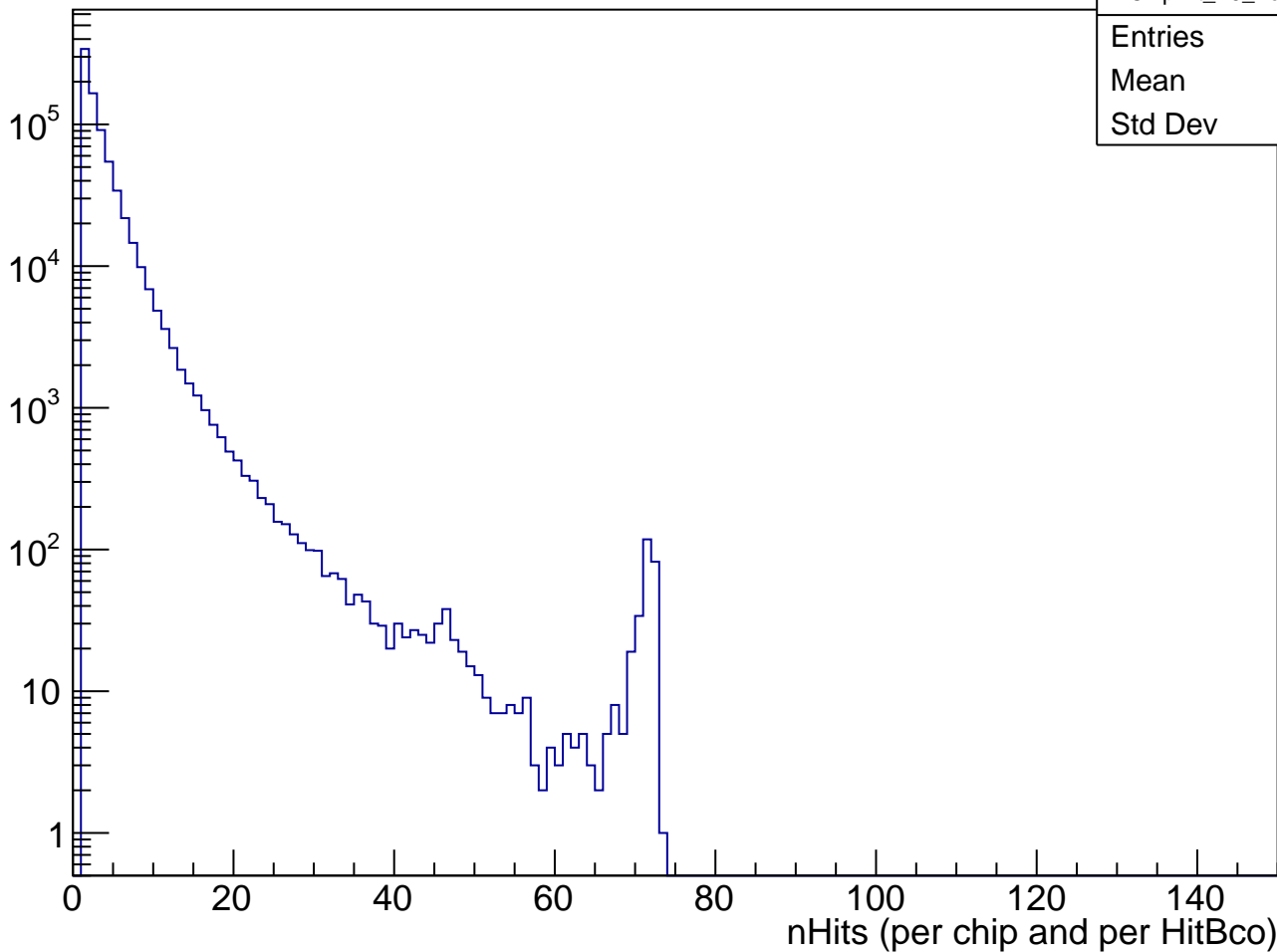
Mean 2.751

Std Dev 3.361

nHits (per chip and per HitBco)

nChipHit_F0_Fch4_Chip5

Entries



nChipHit_F0_Fch4_Chip5

Entries 761225

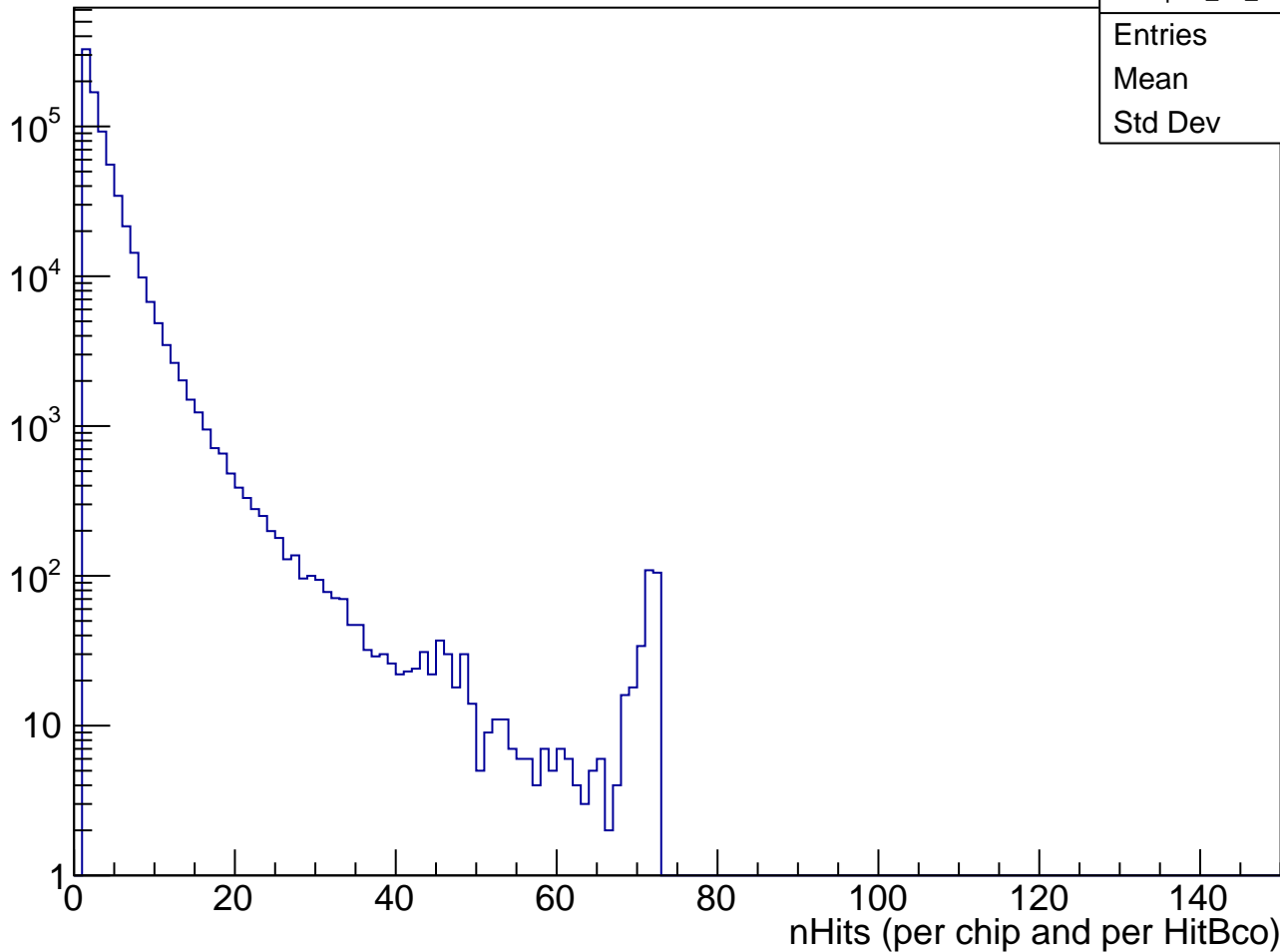
Mean 2.682

Std Dev 3.172

nHits (per chip and per HitBco)

nChipHit_F0_Fch4_Chip6

Entries



nChipHit_F0_Fch4_Chip6

Entries 753373

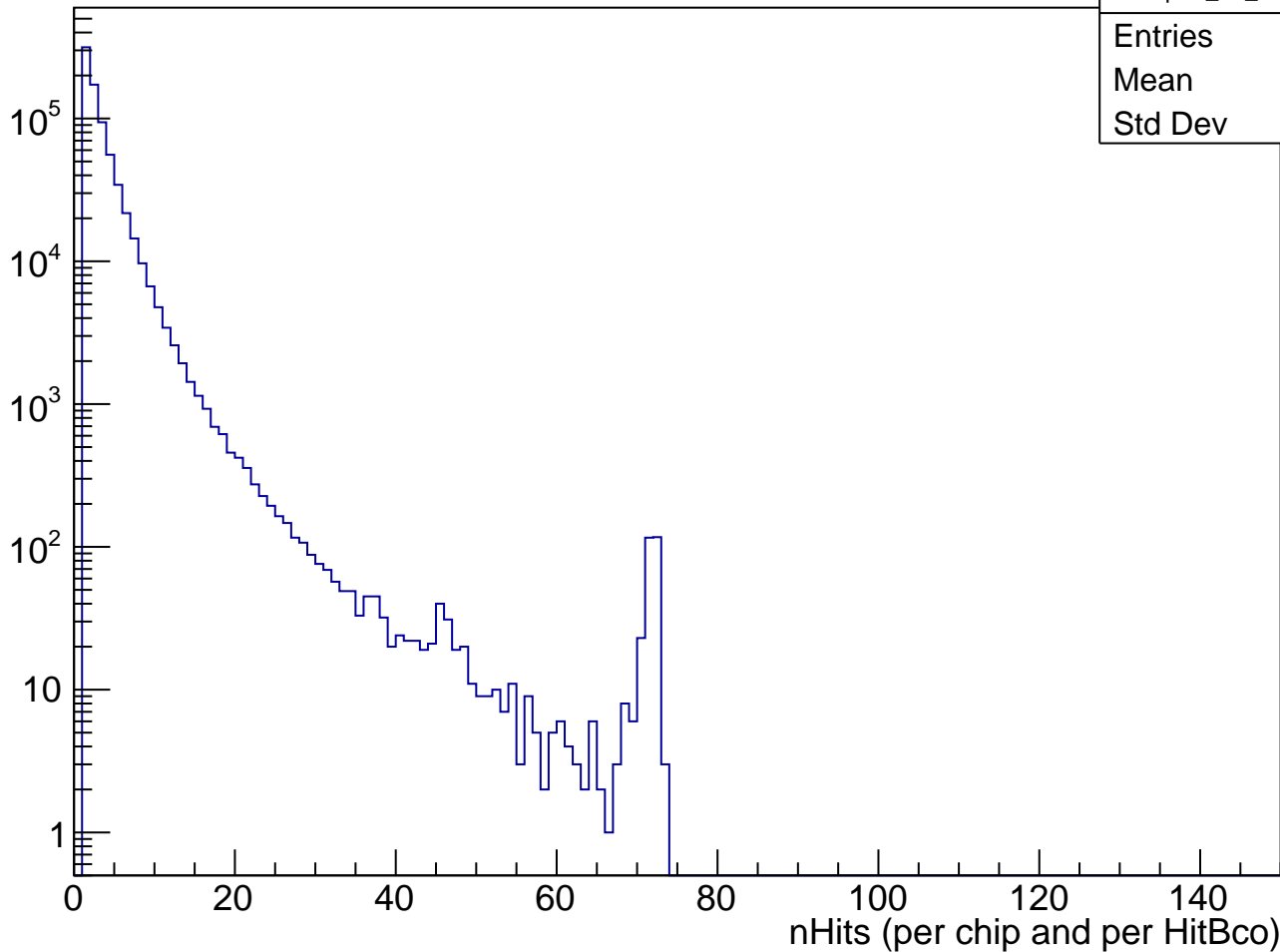
Mean 2.71

Std Dev 3.204

nHits (per chip and per HitBco)

nChipHit_F0_Fch4_Chip7

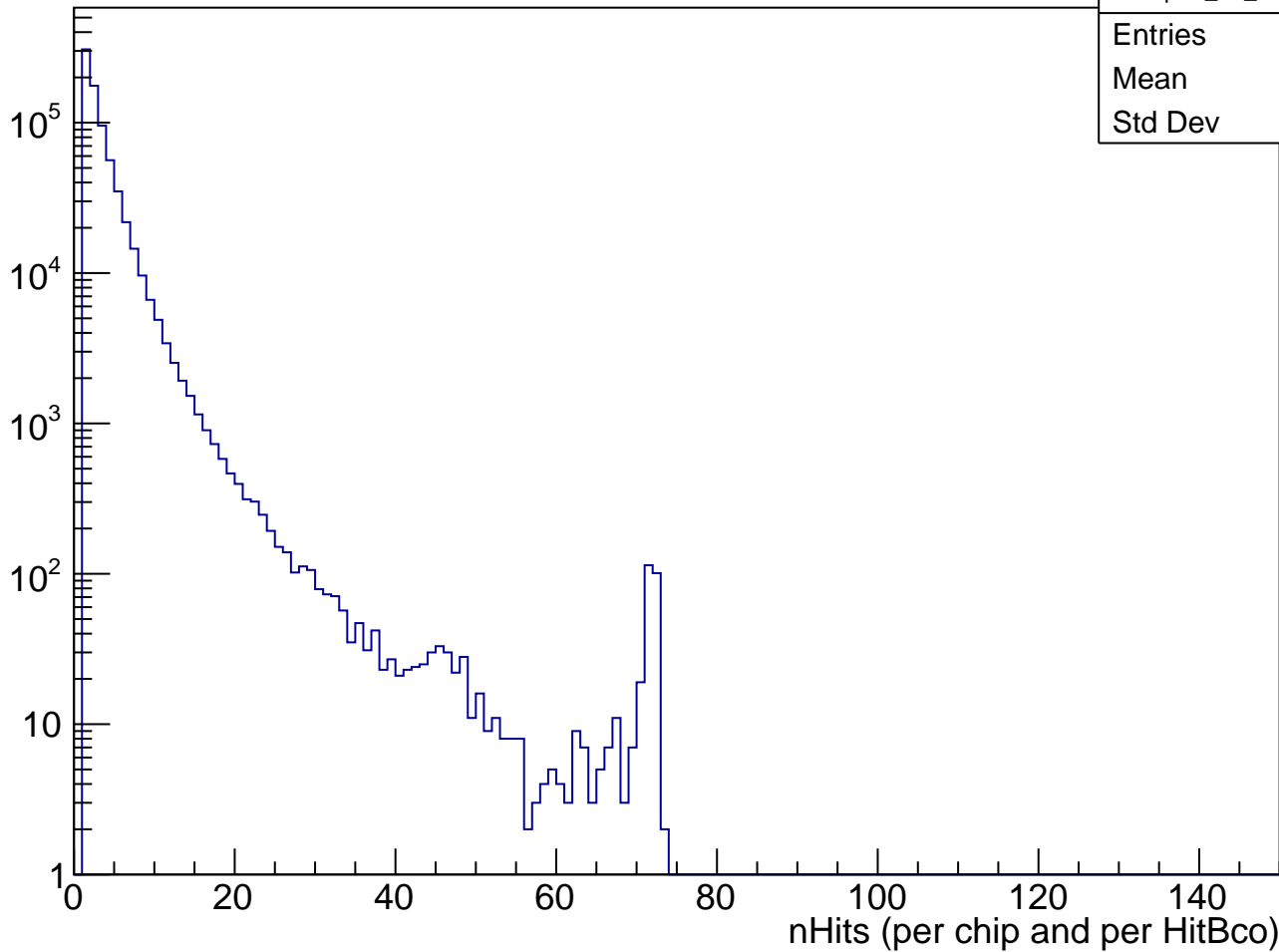
Entries



nChipHit_F0_Fch4_Chip7	
Entries	745671
Mean	2.723
Std Dev	3.165

nChipHit_F0_Fch4_Chip8

Entries



nChipHit_F0_Fch4_Chip8

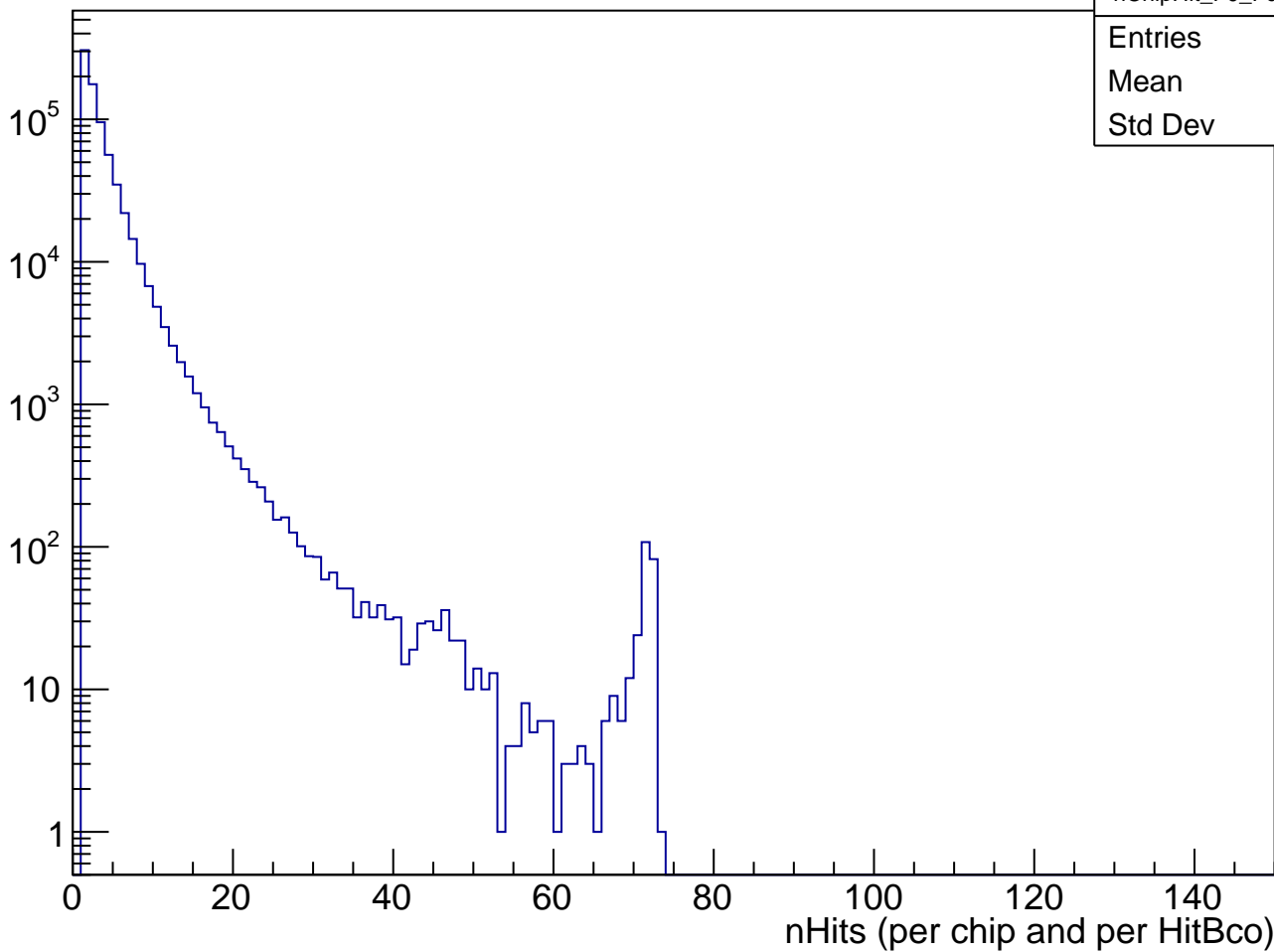
Entries 744014

Mean 2.743

Std Dev 3.167

nChipHit_F0_Fch4_Chip9

Entries



nChipHit_F0_Fch4_Chip9

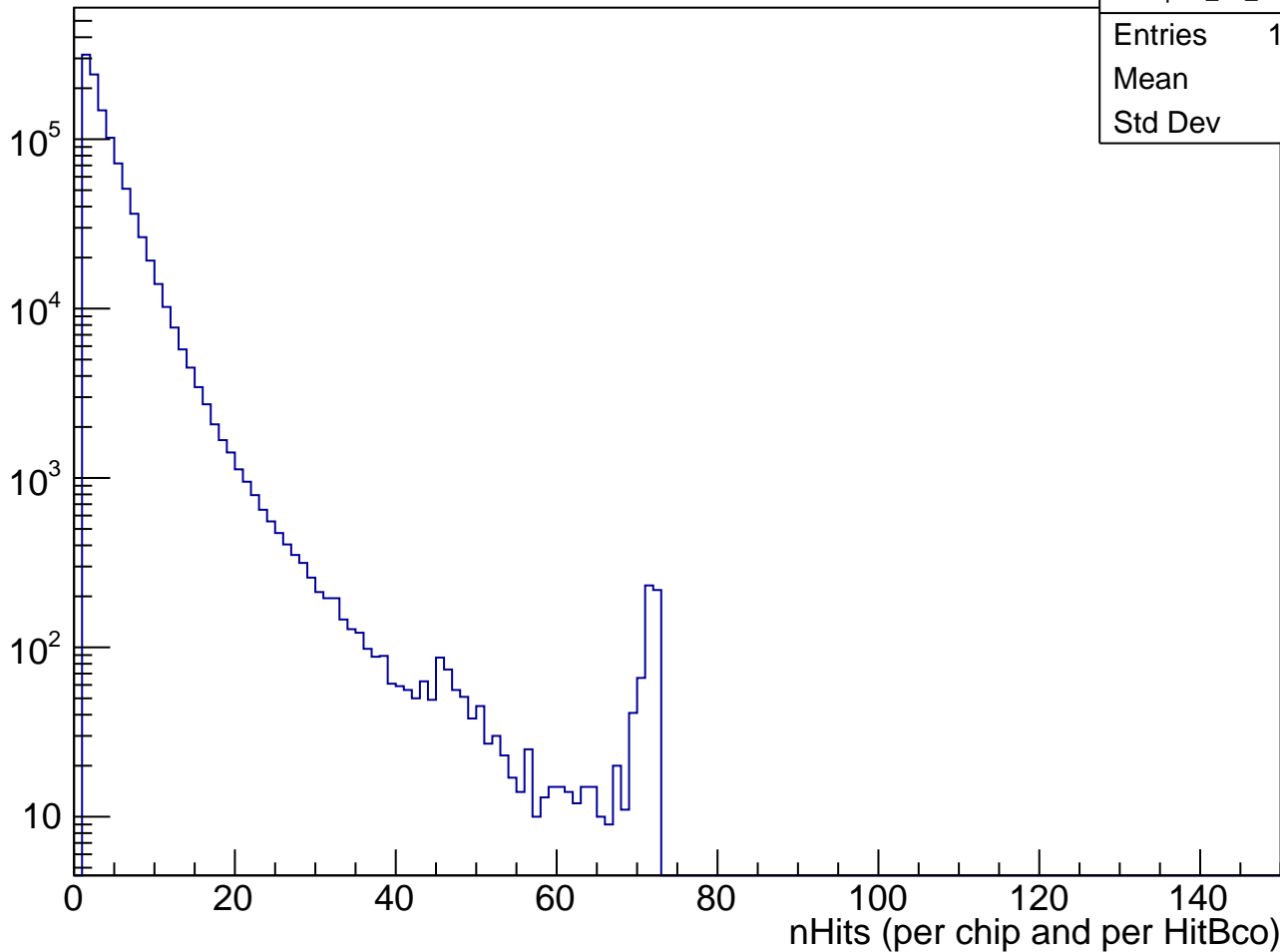
Entries 743425

Mean 2.754

Std Dev 3.158

nChipHit_F0_Fch5_Chip0

Entries

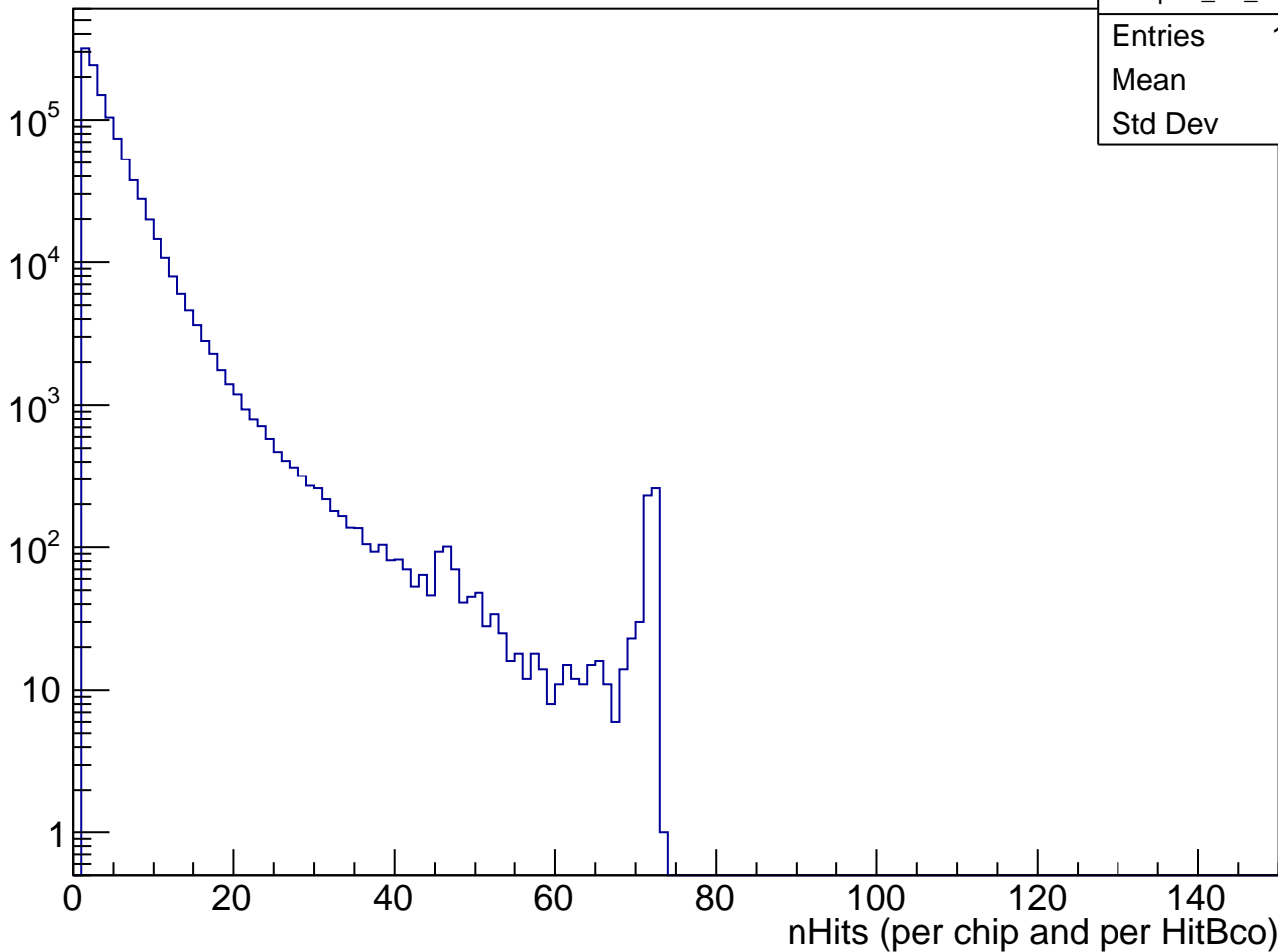


nChipHit_F0_Fch5_Chip0	
Entries	1073927
Mean	3.622
Std Dev	4.018

nHits (per chip and per HitBco)

nChipHit_F0_Fch5_Chip1

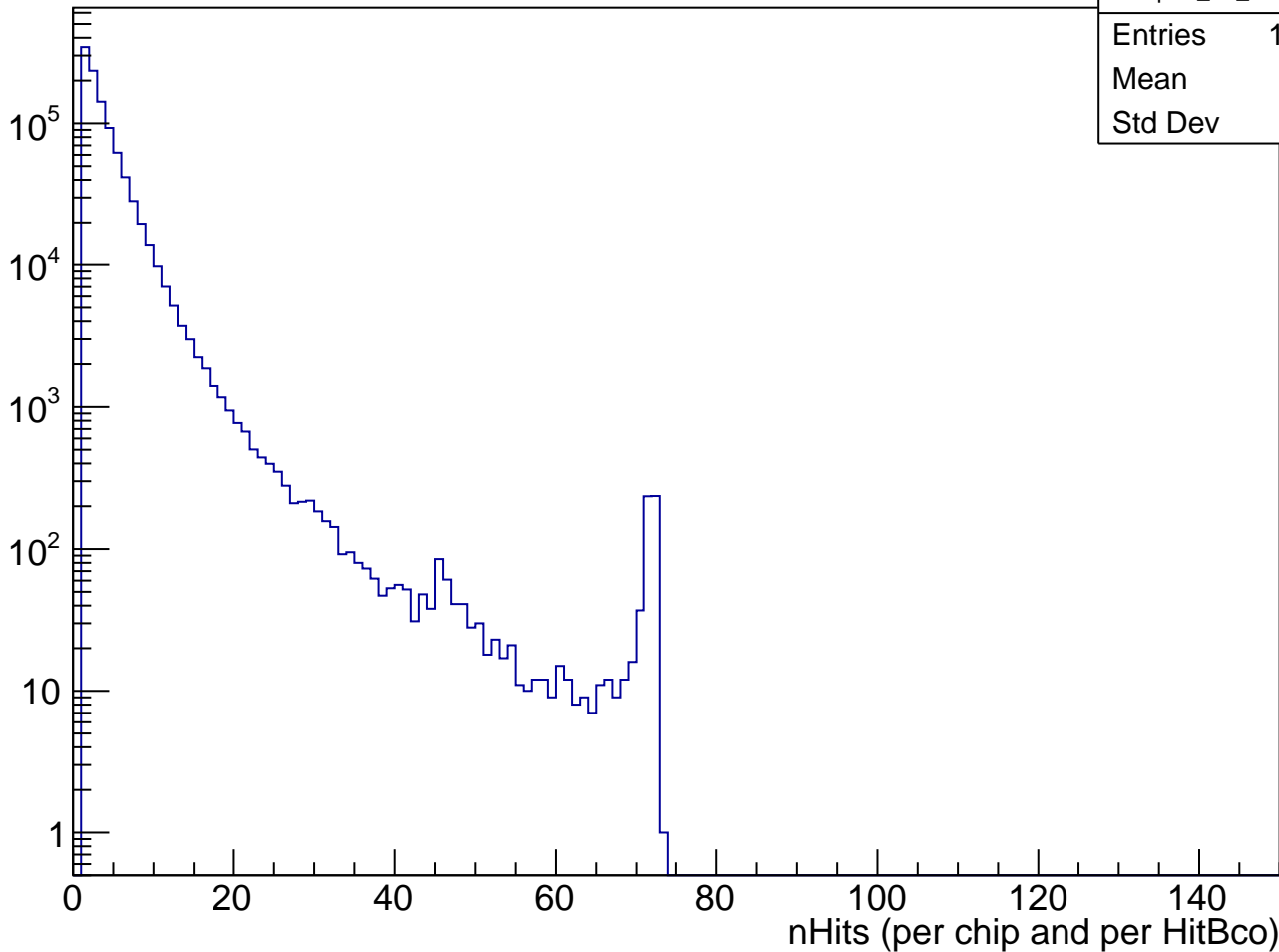
Entries



nChipHit_F0_Fch5_Chip1	
Entries	1089511
Mean	3.662
Std Dev	4.047

nChipHit_F0_Fch5_Chip10

Entries

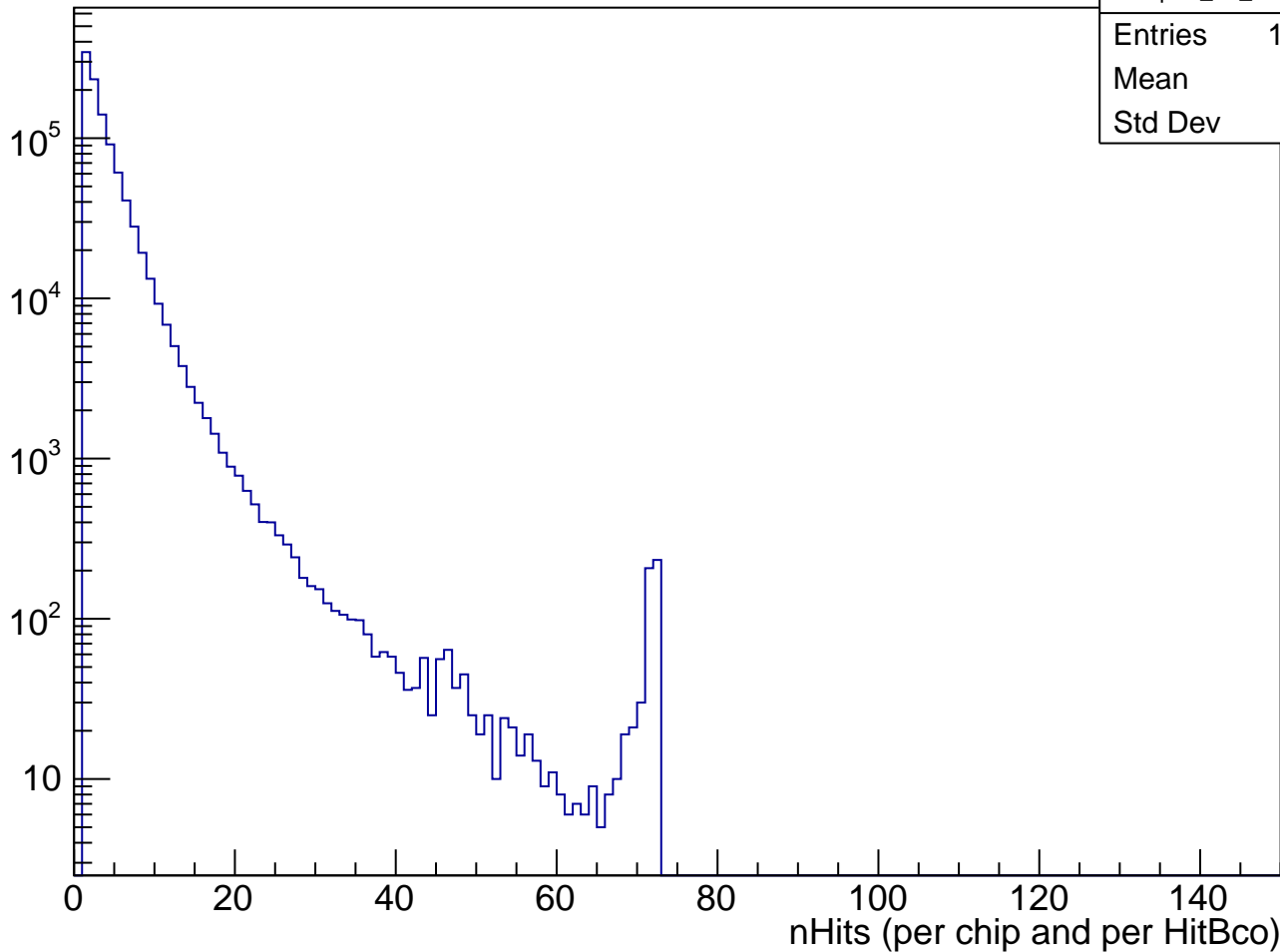


nChipHit_F0_Fch5_Chip10	
Entries	1021513
Mean	3.234
Std Dev	3.682

nHits (per chip and per HitBco)

nChipHit_F0_Fch5_Chip11

Entries

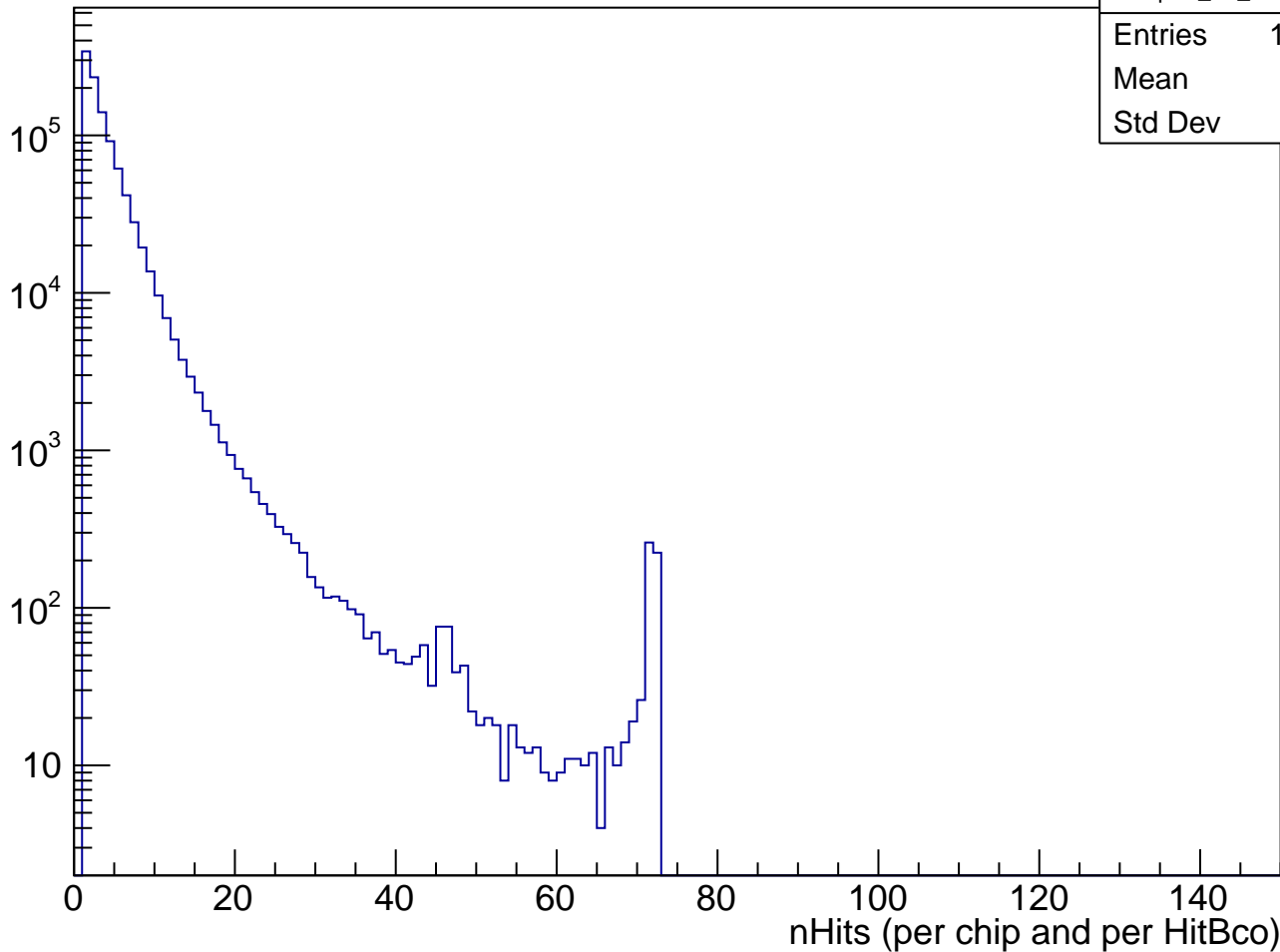


nChipHit_F0_Fch5_Chip11	
Entries	1013404
Mean	3.206
Std Dev	3.631

nHits (per chip and per HitBco)

nChipHit_F0_Fch5_Chip12

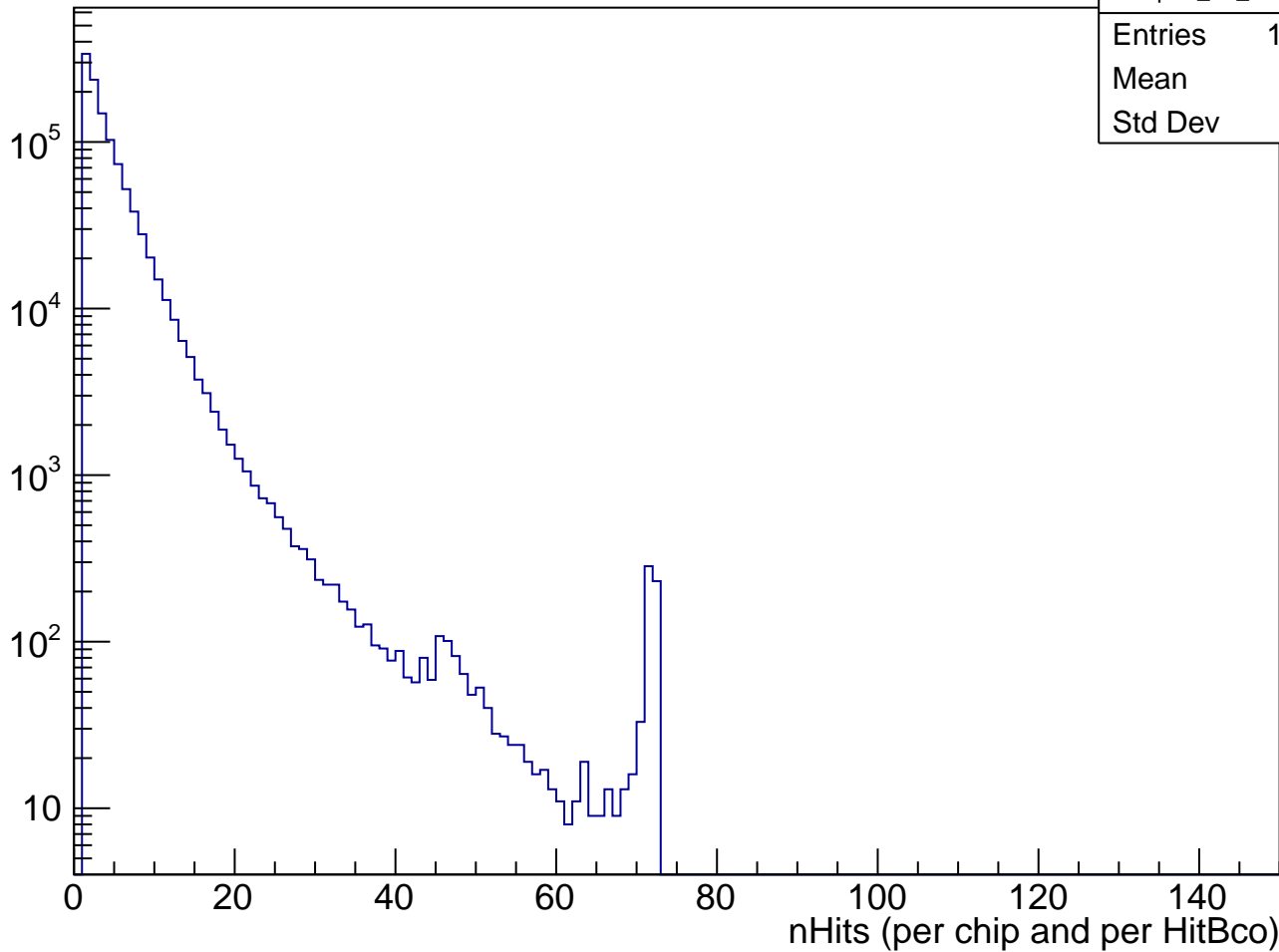
Entries



nChipHit_F0_Fch5_Chip12	
Entries	1013910
Mean	3.232
Std Dev	3.678

nChipHit_F0_Fch5_Chip13

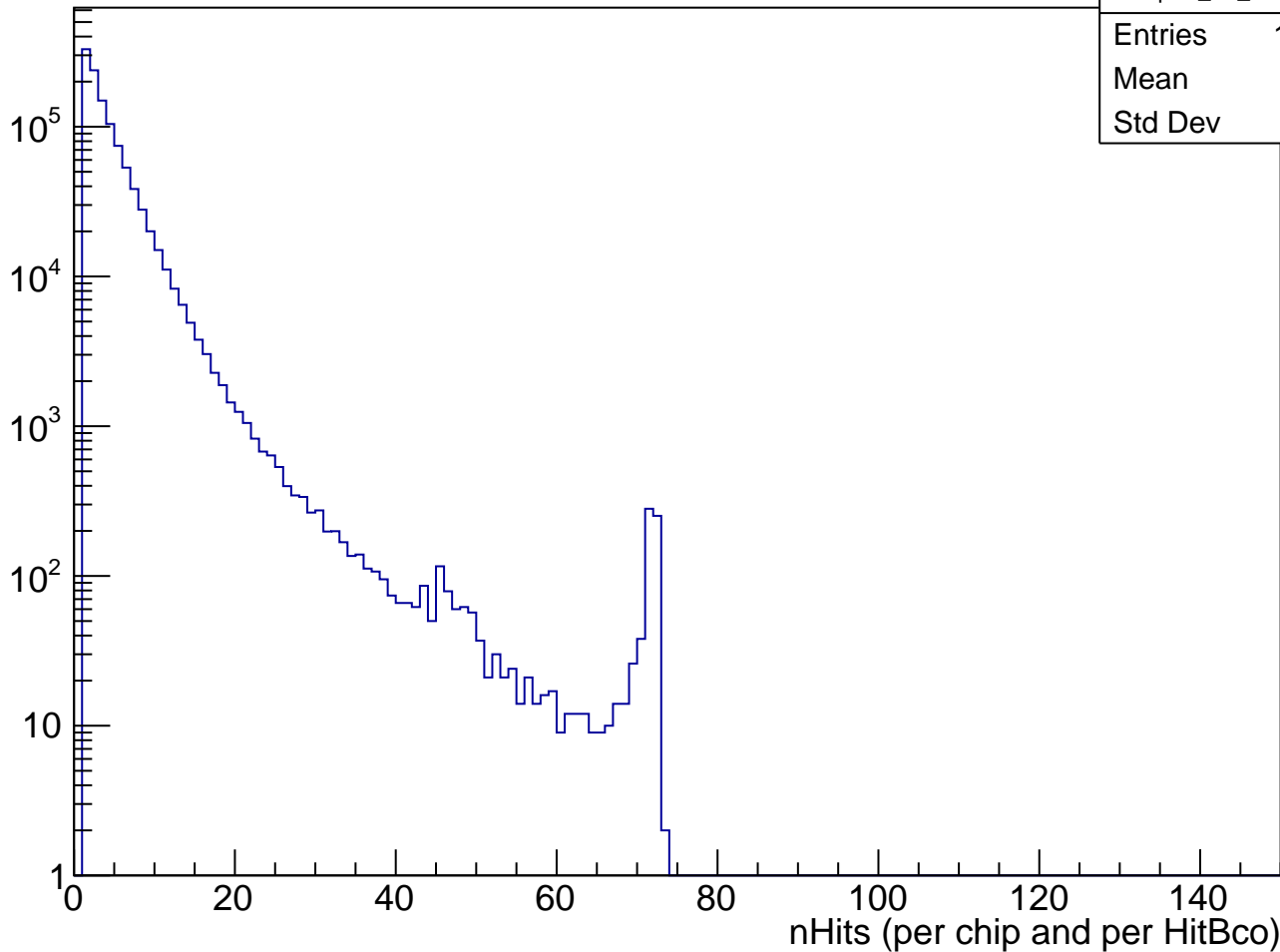
Entries



nChipHit_F0_Fch5_Chip13	
Entries	1106877
Mean	3.67
Std Dev	4.134

nChipHit_F0_Fch5_Chip14

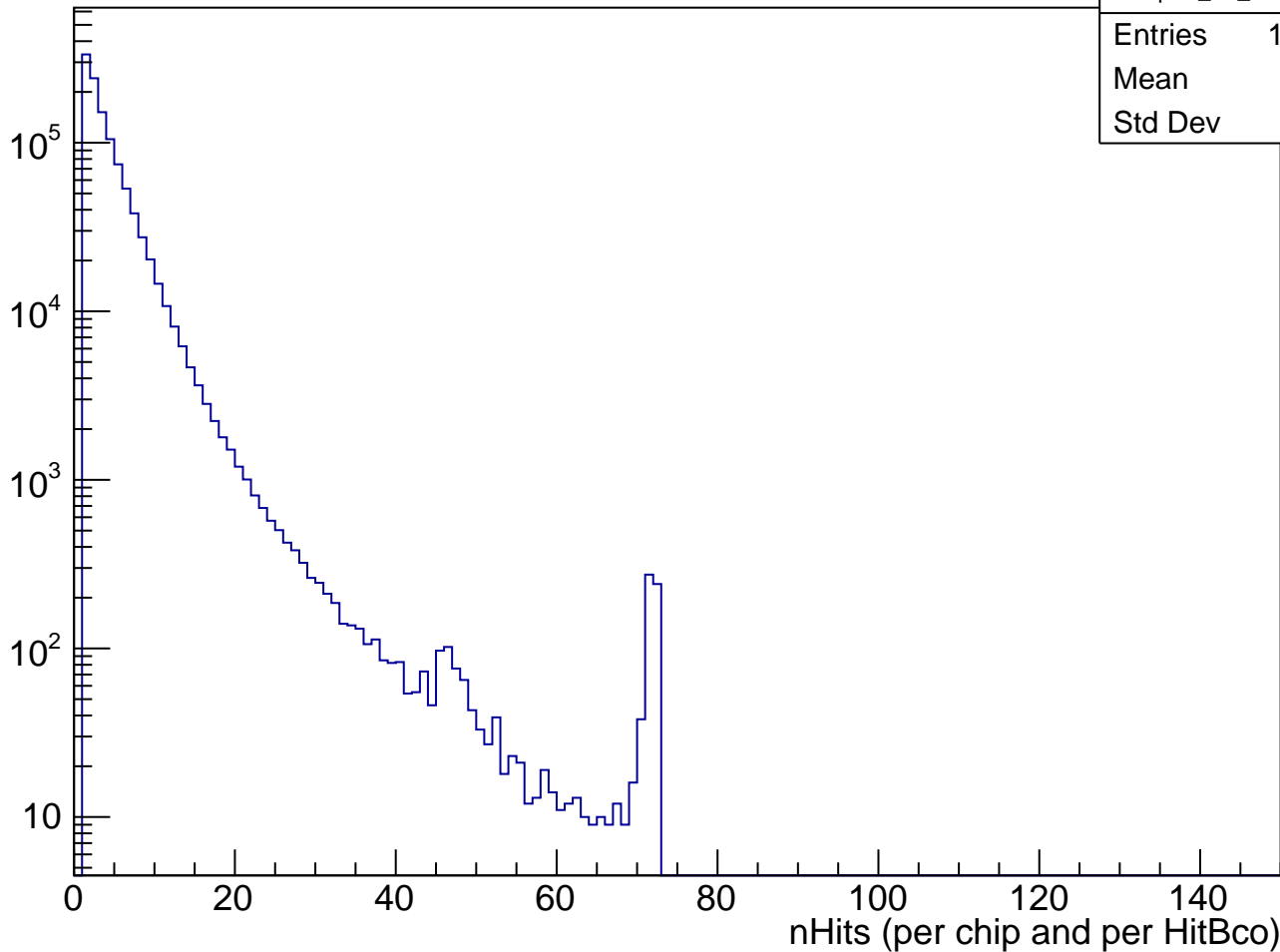
Entries



nChipHit_F0_Fch5_Chip14	
Entries	1102971
Mean	3.676
Std Dev	4.104

nChipHit_F0_Fch5_Chip15

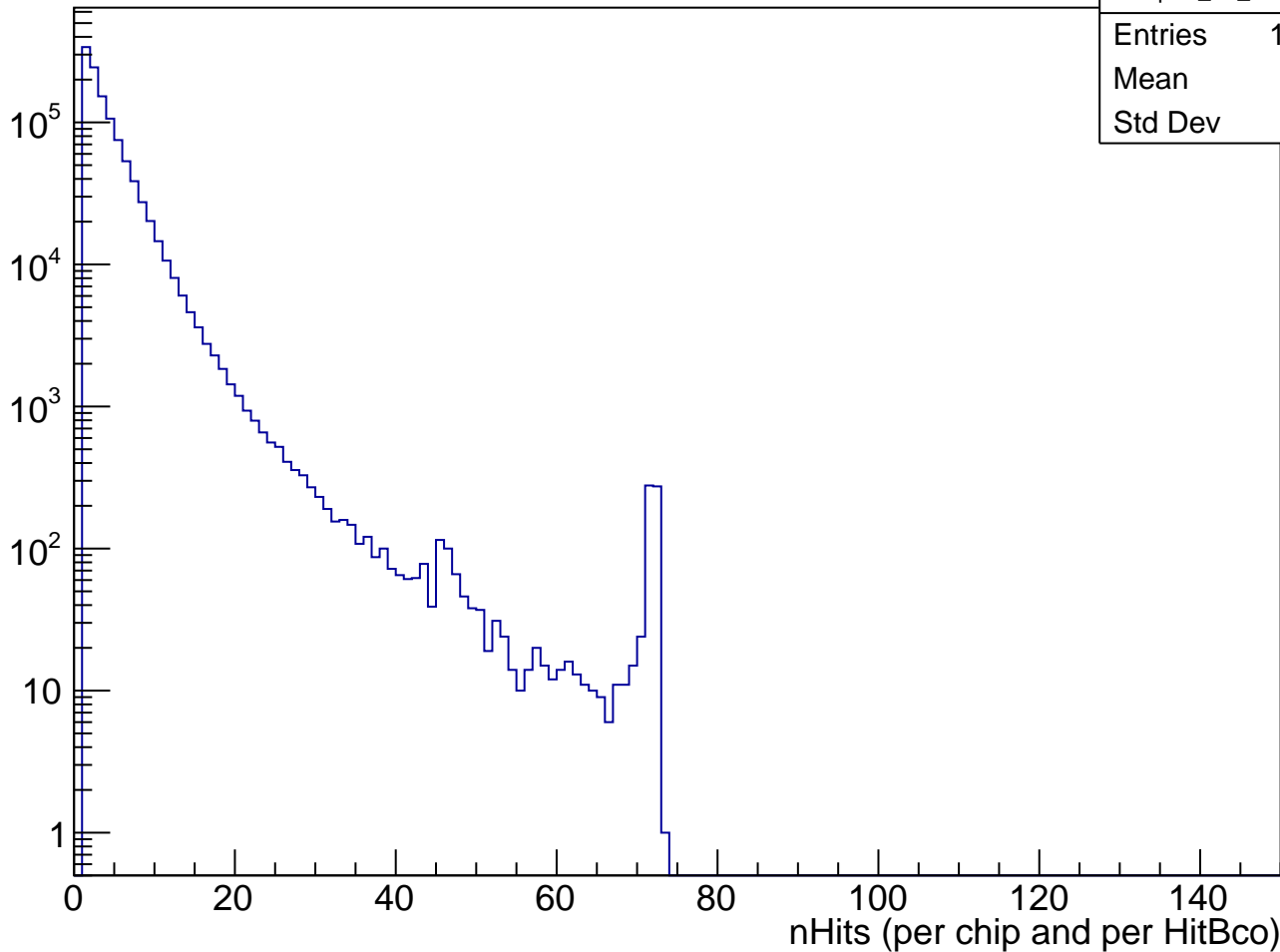
Entries



nChipHit_F0_Fch5_Chip15	
Entries	1110747
Mean	3.635
Std Dev	4.045

nChipHit_F0_Fch5_Chip16

Entries

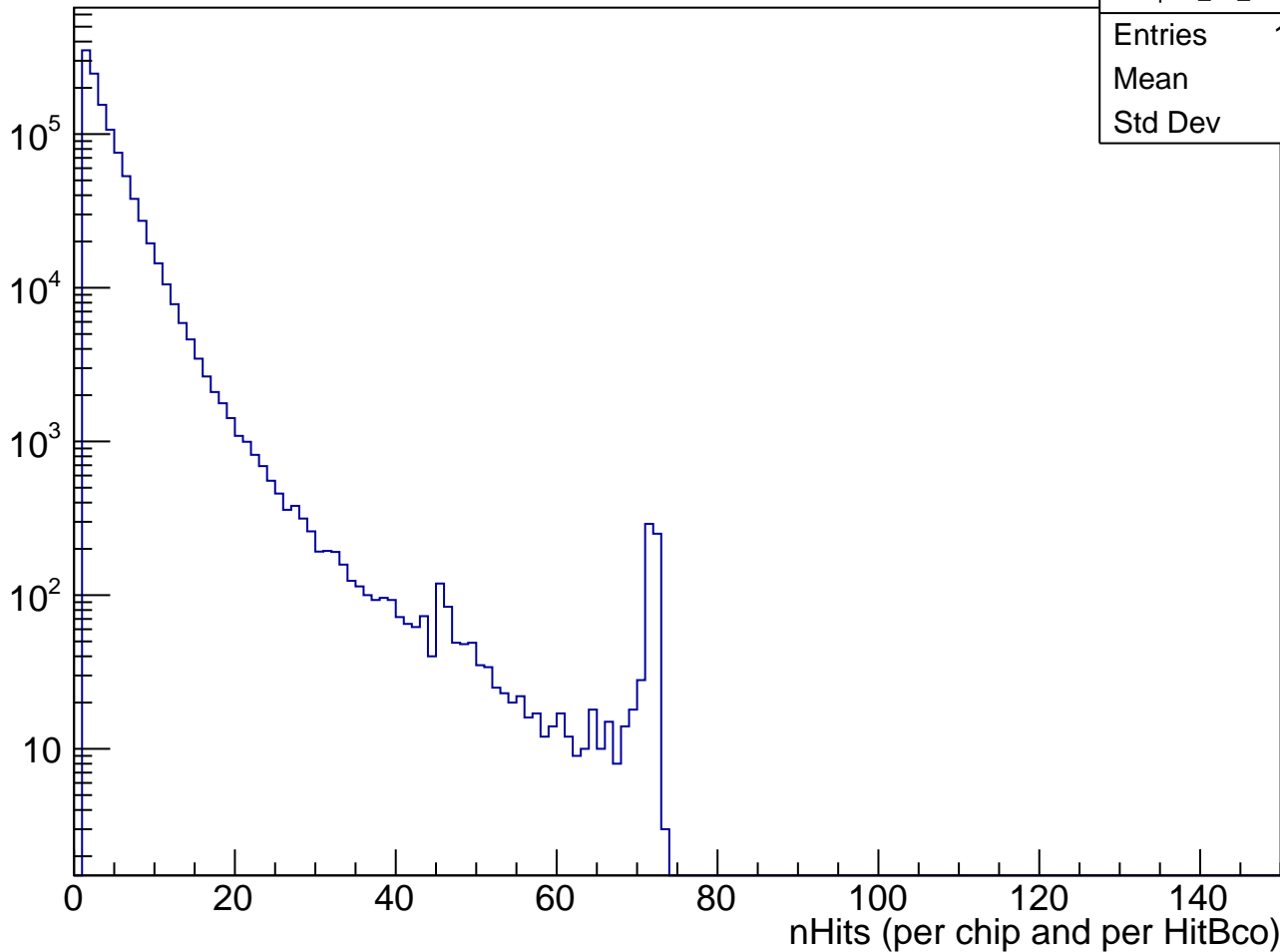


nChipHit_F0_Fch5_Chip16	
Entries	1121153
Mean	3.613
Std Dev	4.019

nHits (per chip and per HitBco)

nChipHit_F0_Fch5_Chip17

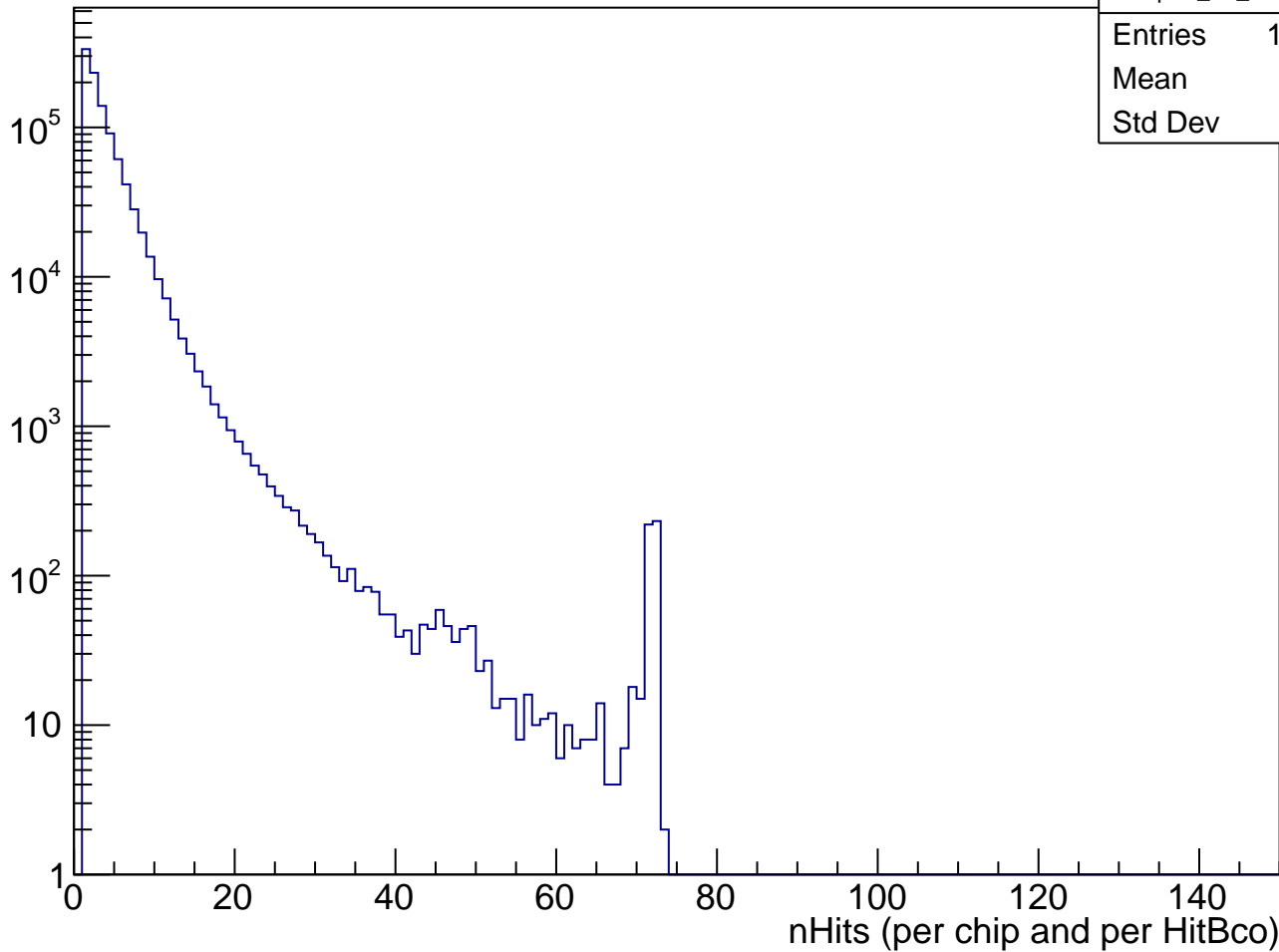
Entries



nChipHit_F0_Fch5_Chip17	
Entries	1137301
Mean	3.562
Std Dev	3.985

nChipHit_F0_Fch5_Chip18

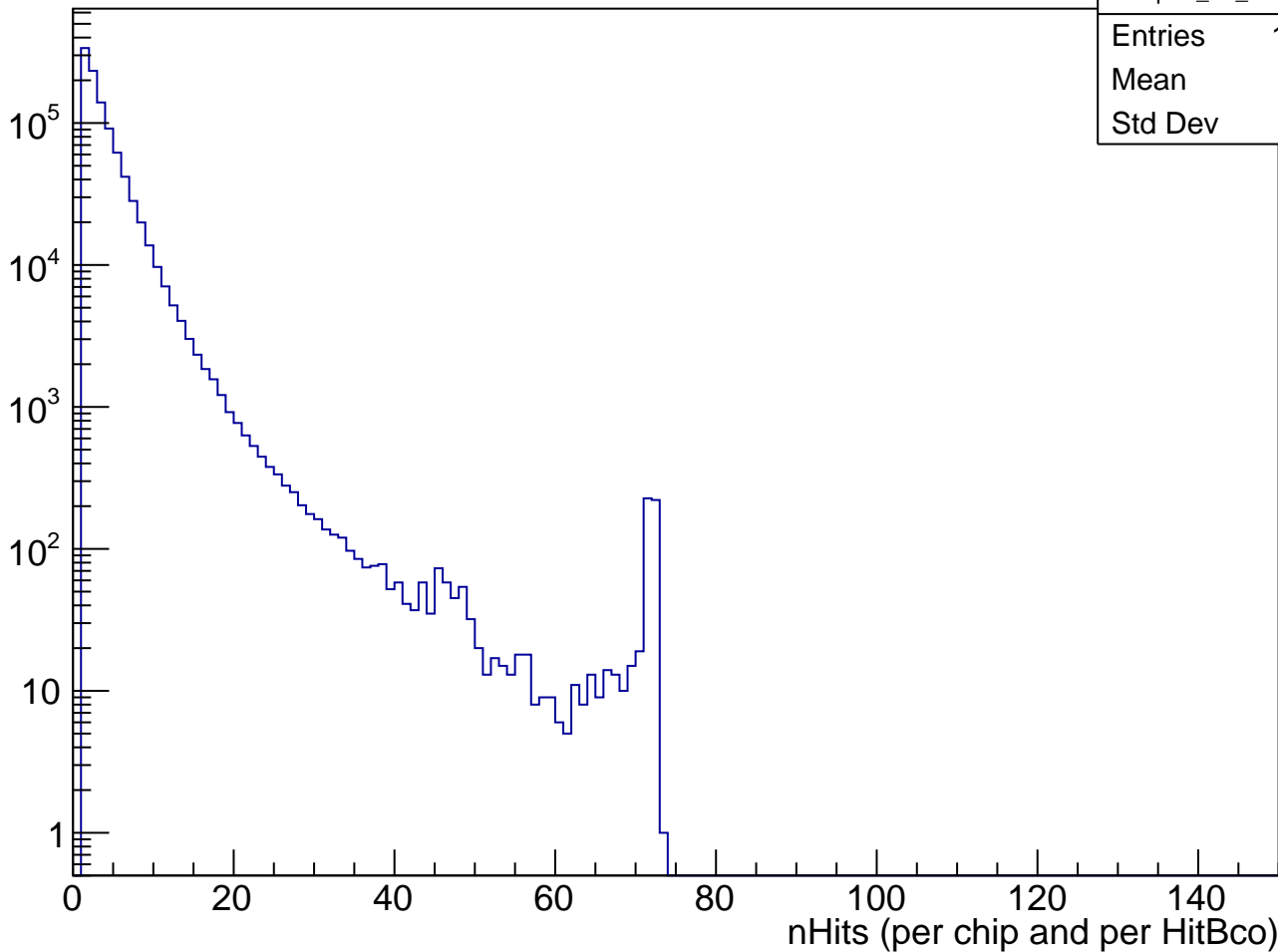
Entries



nChipHit_F0_Fch5_Chip18	
Entries	1003885
Mean	3.256
Std Dev	3.668

nChipHit_F0_Fch5_Chip19

Entries

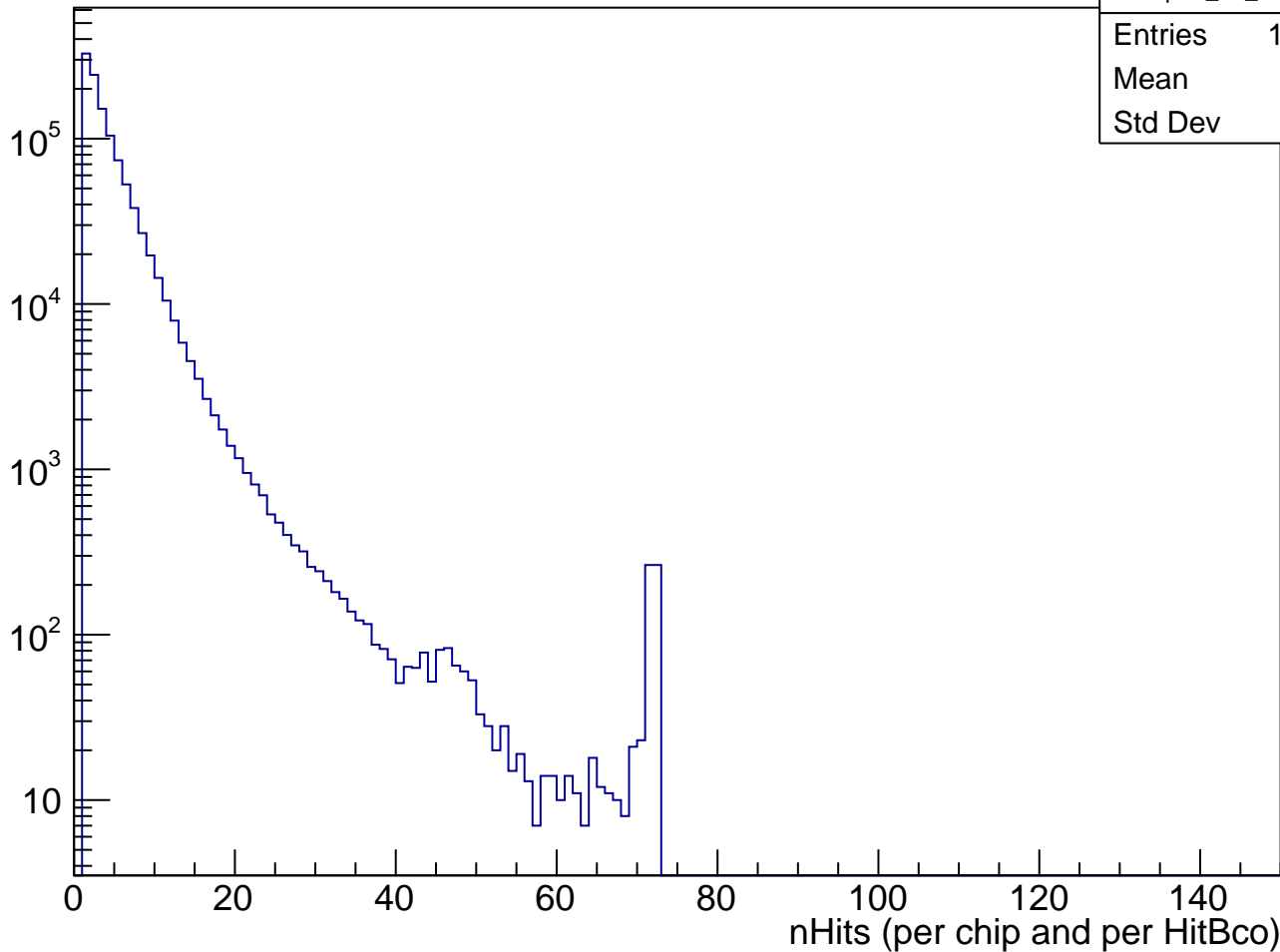


nChipHit_F0_Fch5_Chip19	
Entries	1010401
Mean	3.254
Std Dev	3.679

nHits (per chip and per HitBco)

nChipHit_F0_Fch5_Chip2

Entries

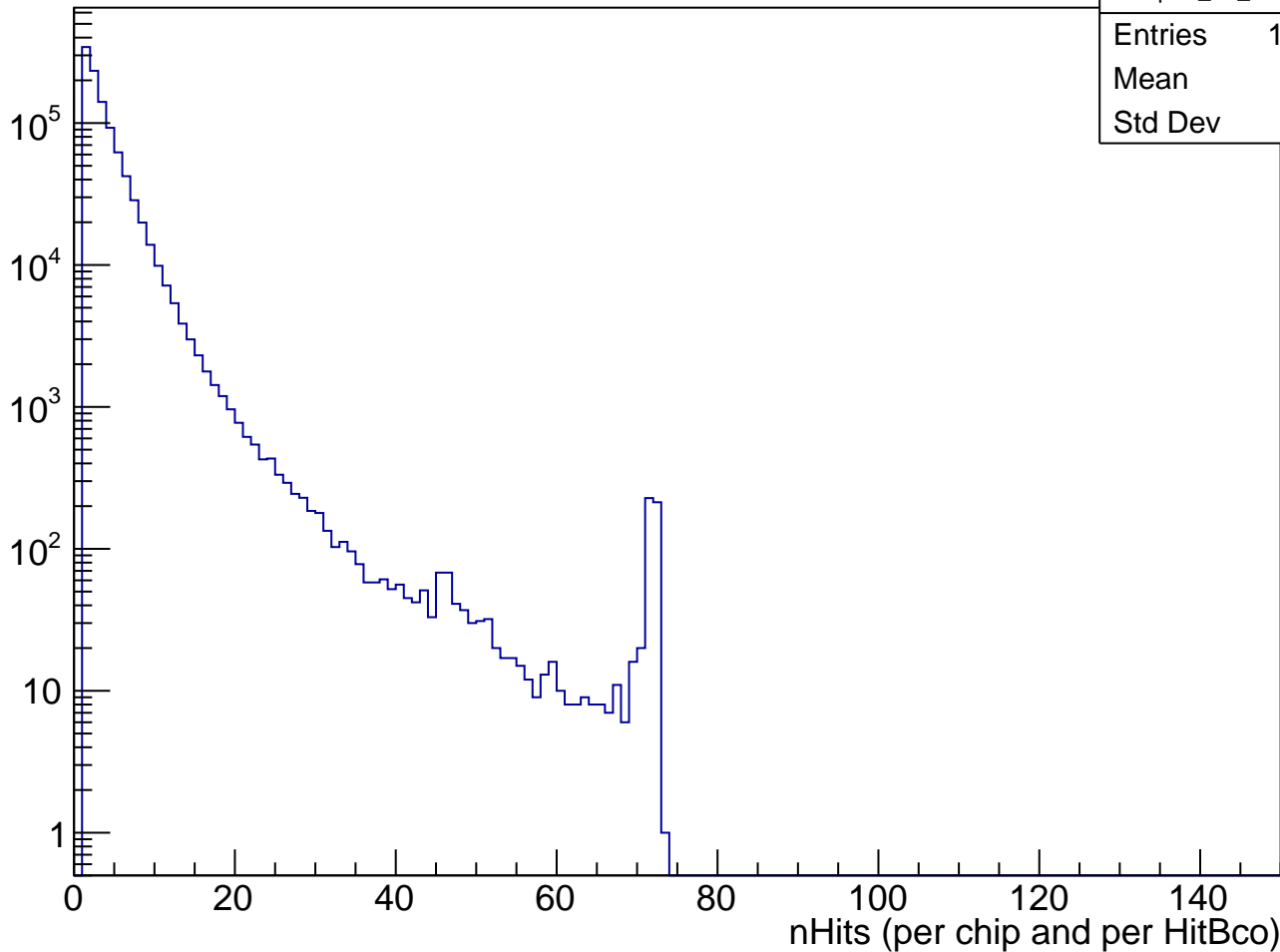


nChipHit_F0_Fch5_Chip2	
Entries	1100949
Mean	3.62
Std Dev	4.011

nHits (per chip and per HitBco)

nChipHit_F0_Fch5_Chip20

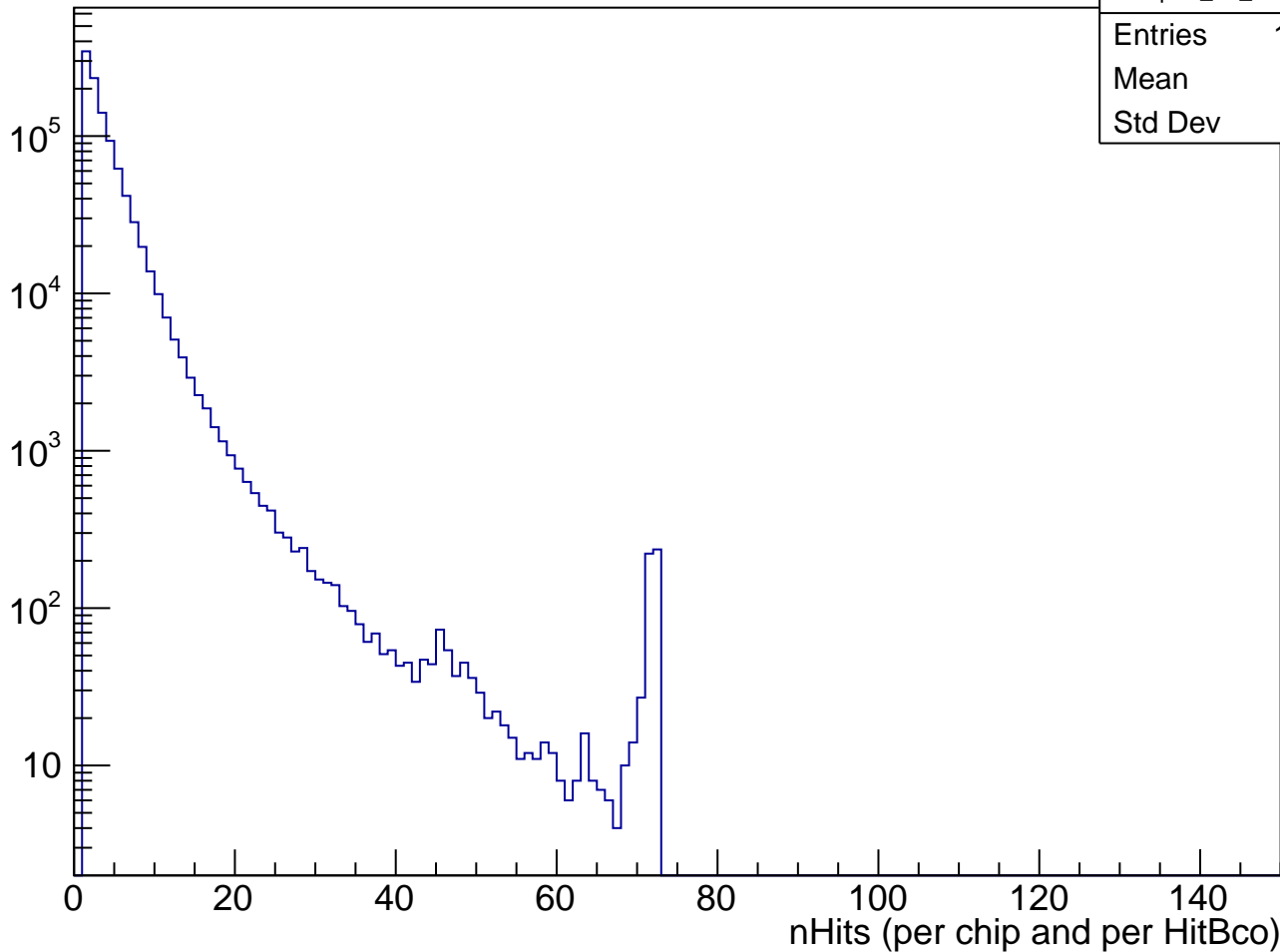
Entries



nChipHit_F0_Fch5_Chip20	
Entries	1020232
Mean	3.243
Std Dev	3.655

nChipHit_F0_Fch5_Chip21

Entries

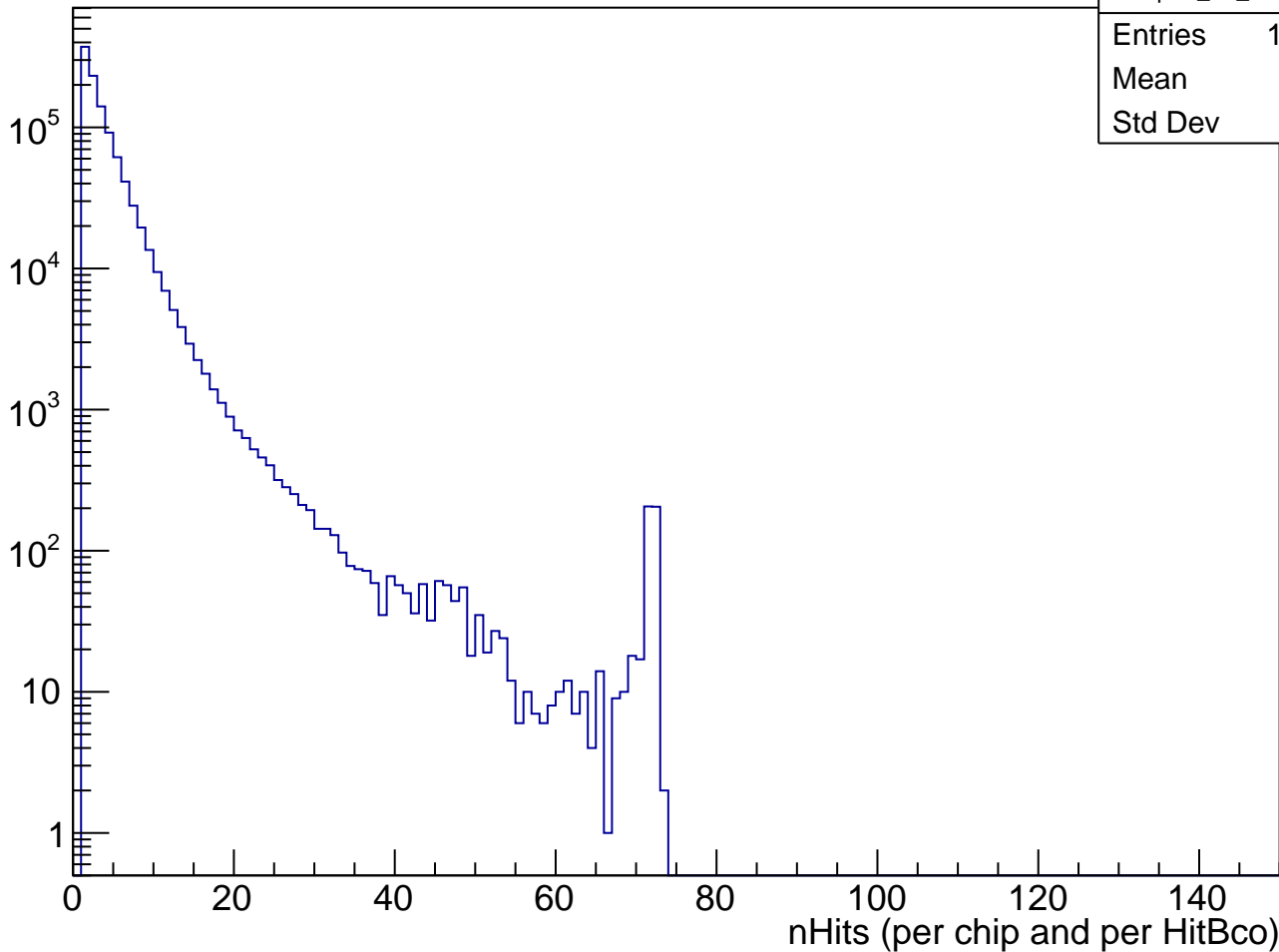


nChipHit_F0_Fch5_Chip21	
Entries	1021451
Mean	3.23
Std Dev	3.65

nHits (per chip and per HitBco)

nChipHit_F0_Fch5_Chip22

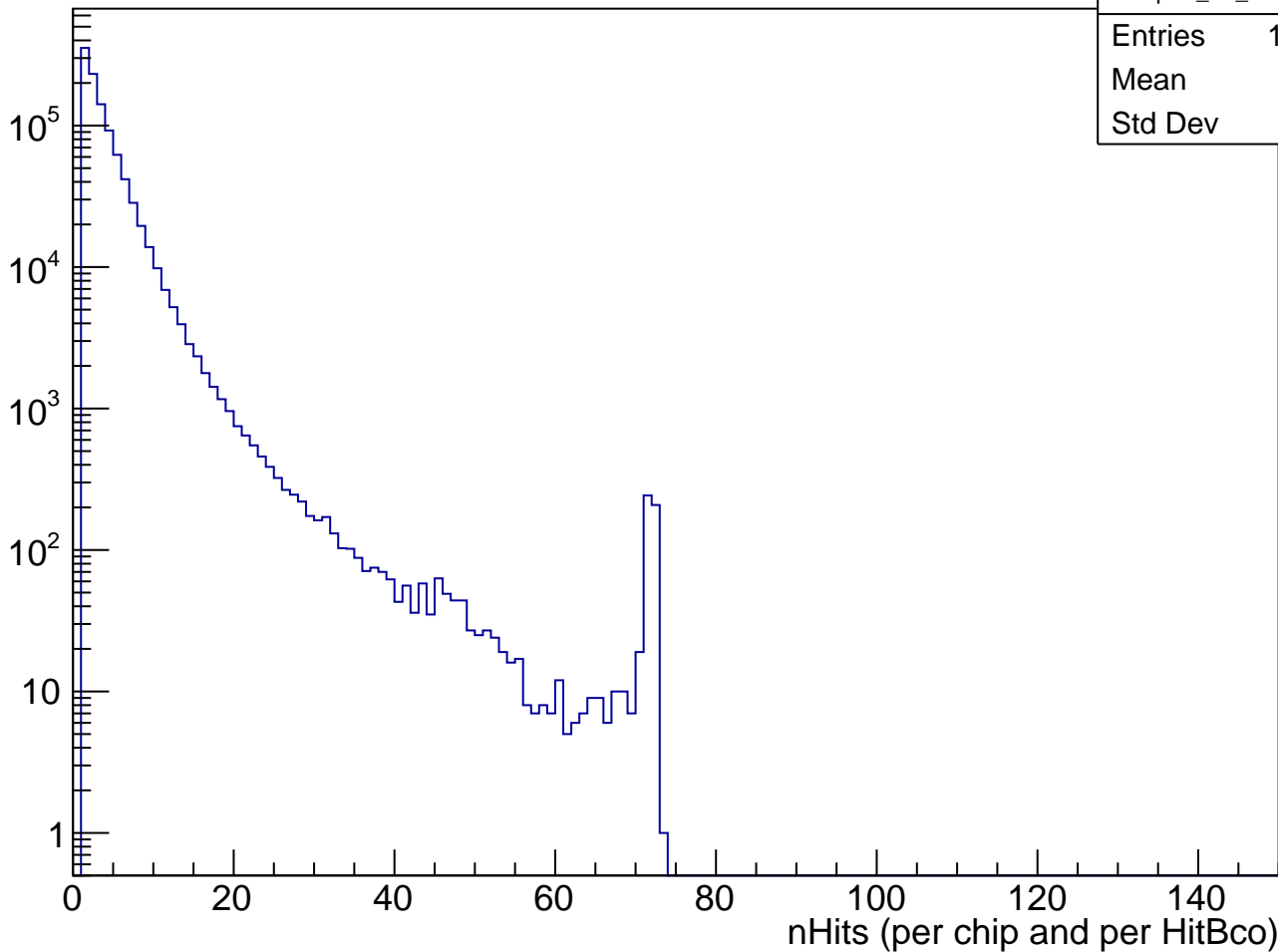
Entries



nChipHit_F0_Fch5_Chip22	
Entries	1042096
Mean	3.155
Std Dev	3.577

nChipHit_F0_Fch5_Chip23

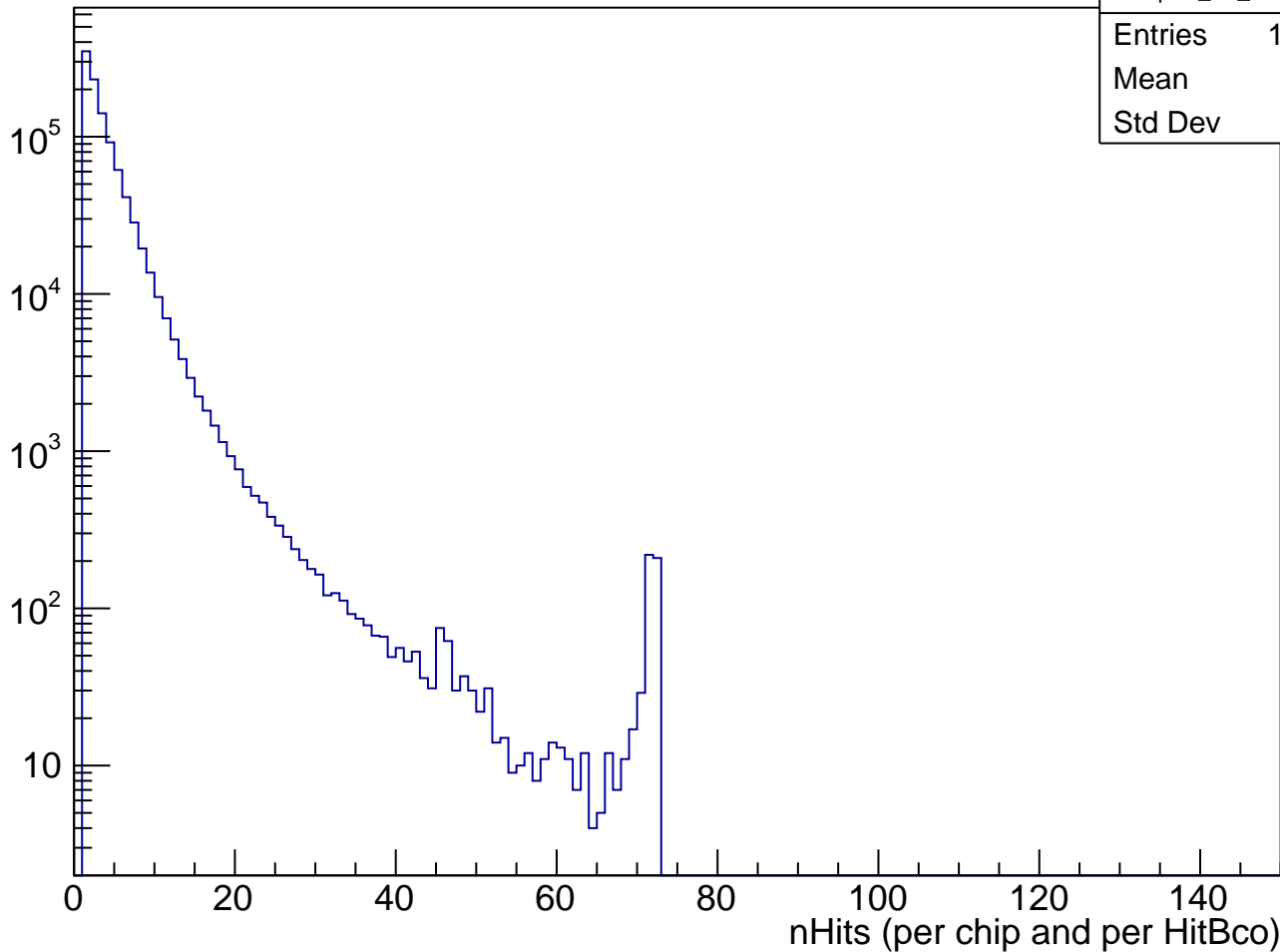
Entries



nChipHit_F0_Fch5_Chip23	
Entries	1027890
Mean	3.213
Std Dev	3.638

nChipHit_F0_Fch5_Chip24

Entries

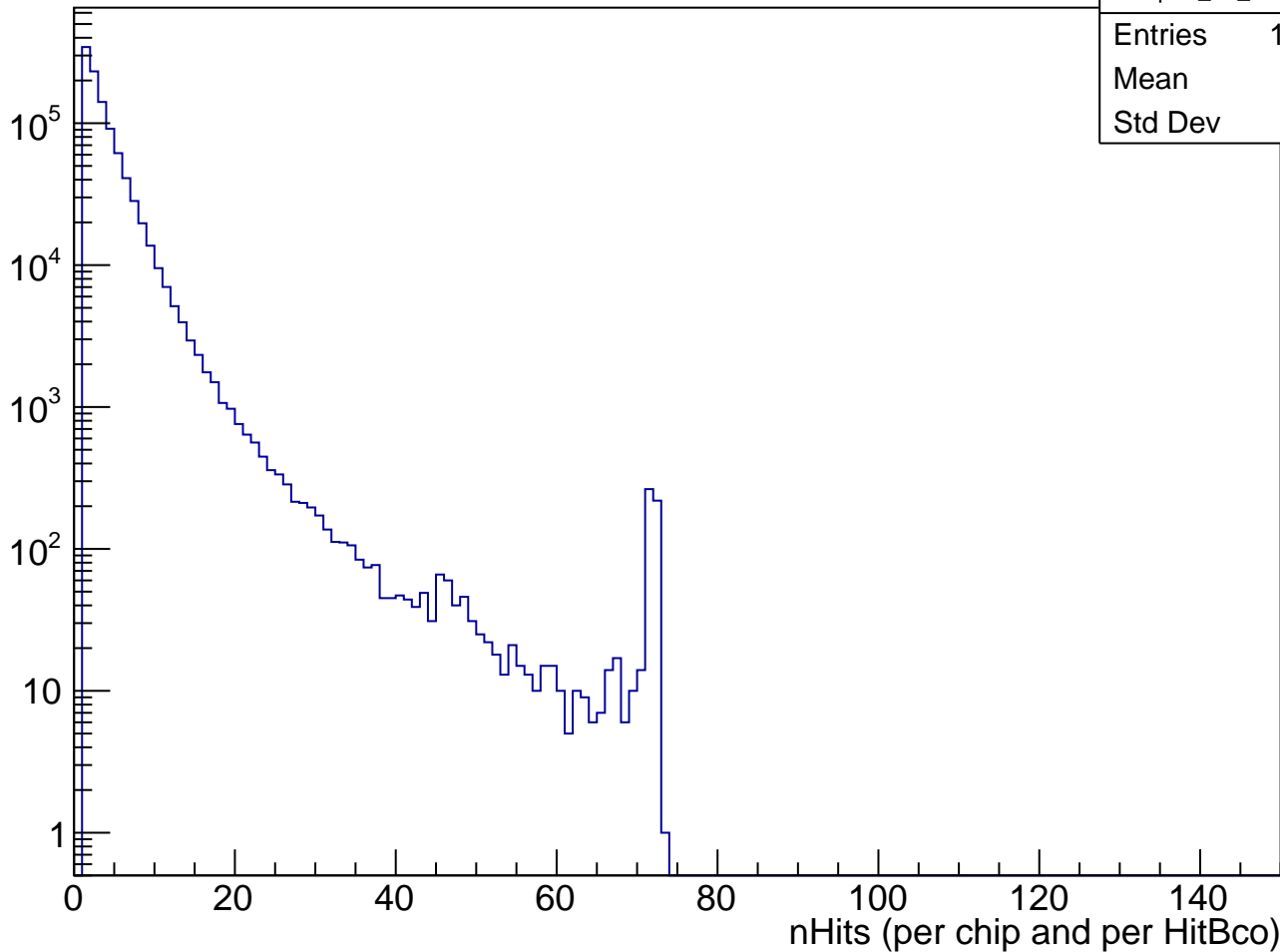


nChipHit_F0_Fch5_Chip24	
Entries	1020567
Mean	3.212
Std Dev	3.628

nHits (per chip and per HitBco)

nChipHit_F0_Fch5_Chip25

Entries

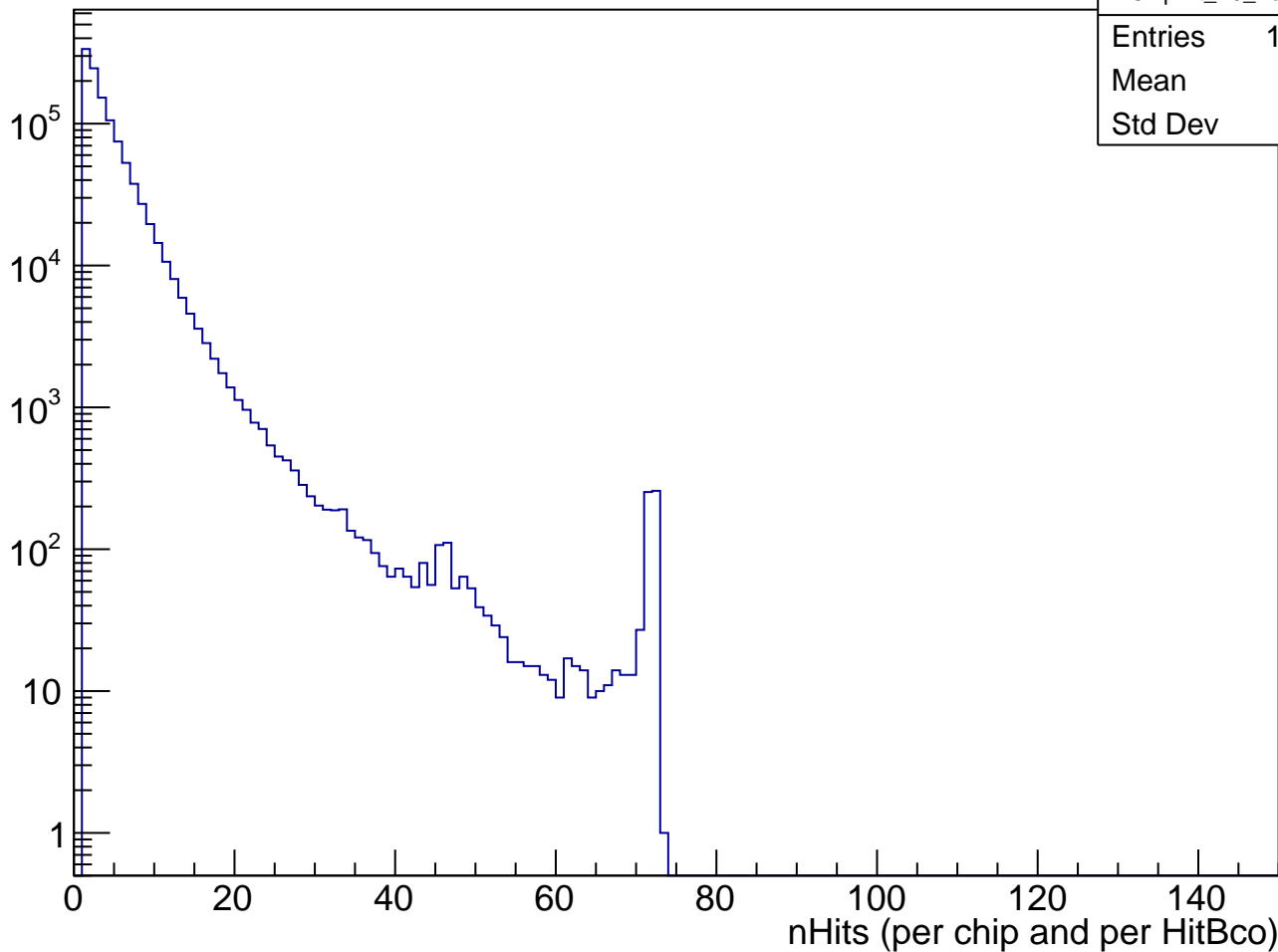


nChipHit_F0_Fch5_Chip25	
Entries	1015303
Mean	3.229
Std Dev	3.67

nHits (per chip and per HitBco)

nChipHit_F0_Fch5_Chip3

Entries



nChipHit_F0_Fch5_Chip3

Entries 1116214

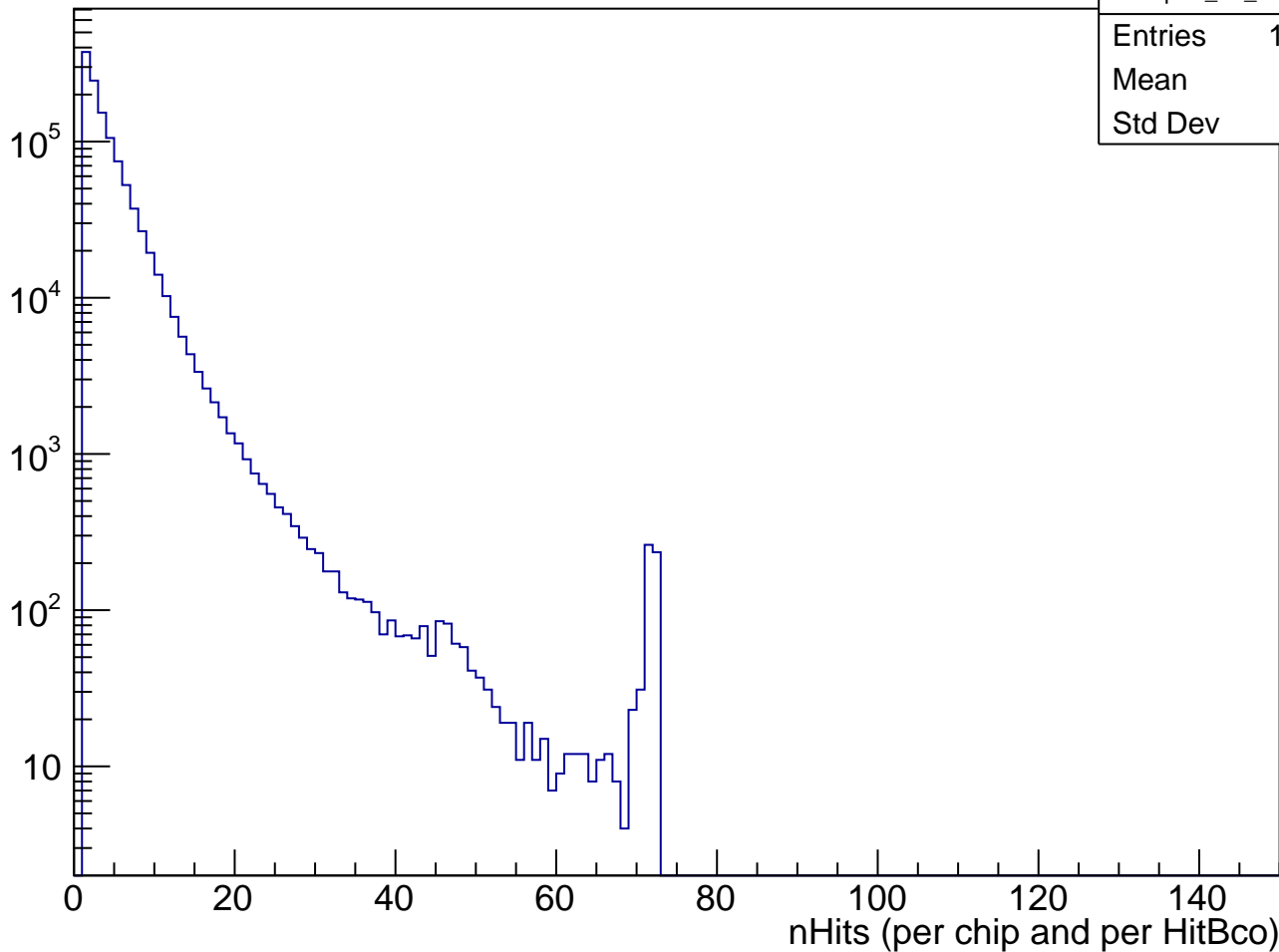
Mean 3.602

Std Dev 4.005

nHits (per chip and per HitBco)

nChipHit_F0_Fch5_Chip4

Entries



nChipHit_F0_Fch5_Chip4

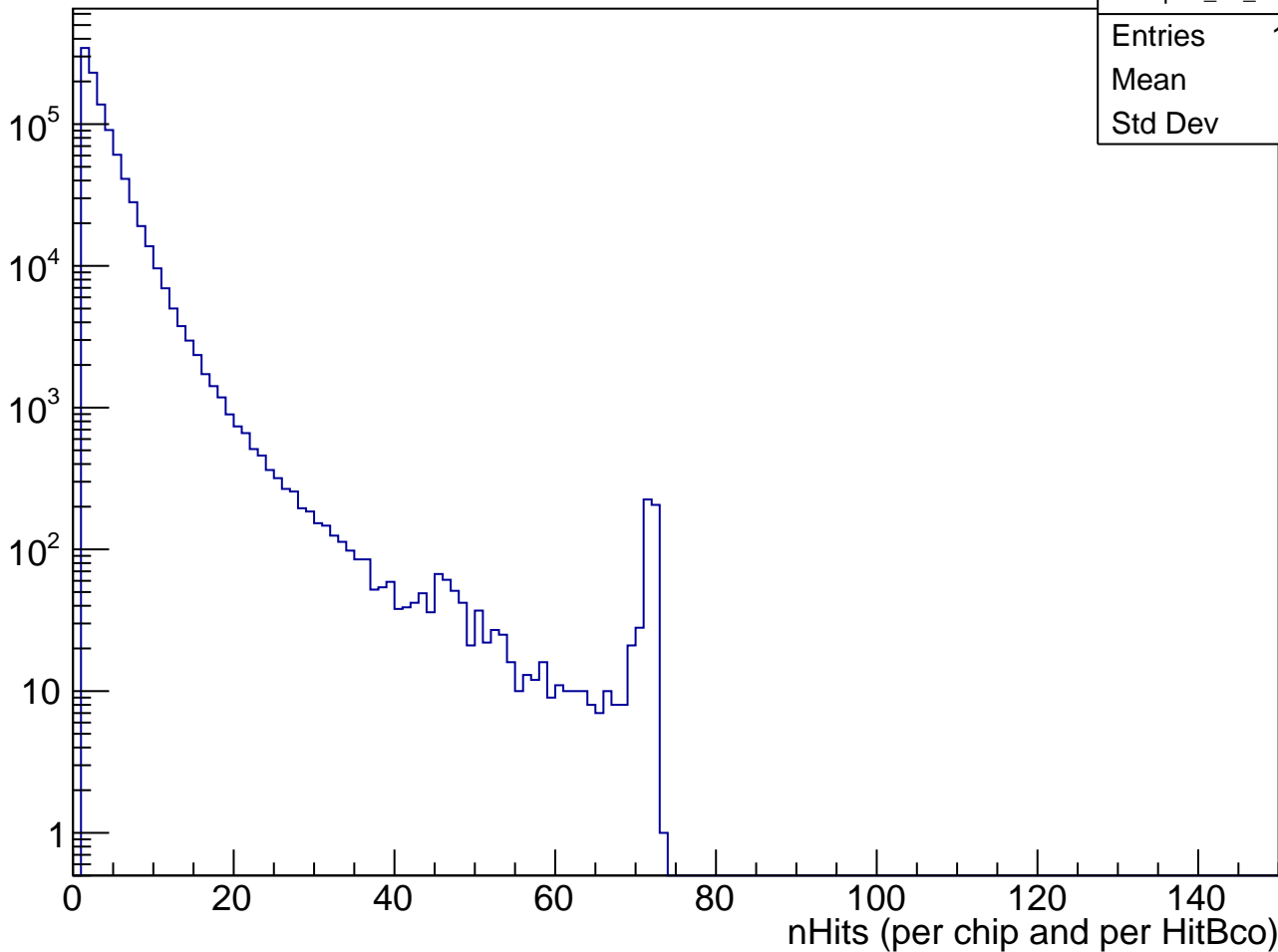
Entries 1151173

Mean 3.485

Std Dev 3.909

nChipHit_F0_Fch5_Chip5

Entries



nChipHit_F0_Fch5_Chip5

Entries 1008451

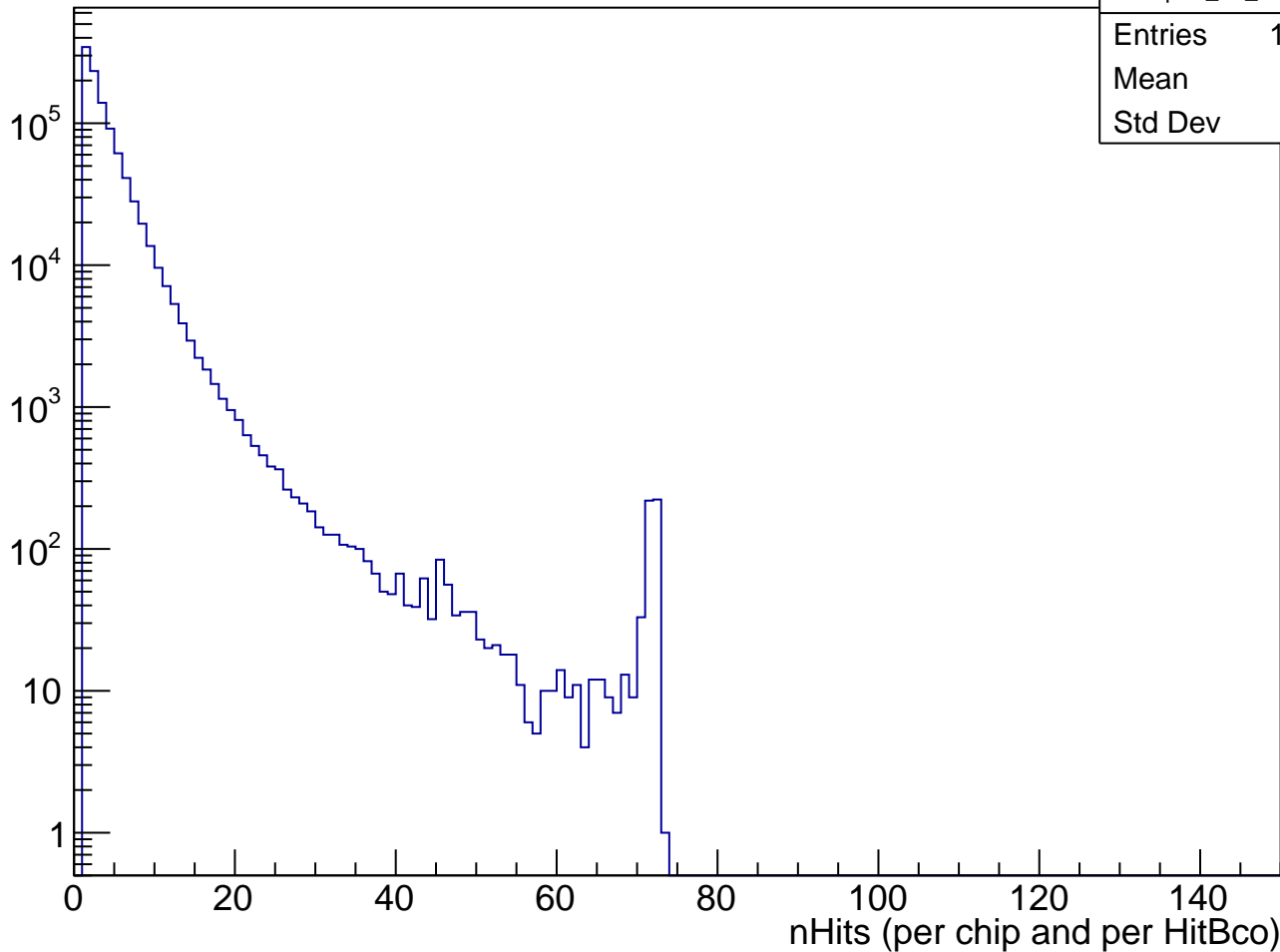
Mean 3.221

Std Dev 3.658

nHits (per chip and per HitBco)

nChipHit_F0_Fch5_Chip6

Entries



nChipHit_F0_Fch5_Chip6

Entries 1015420

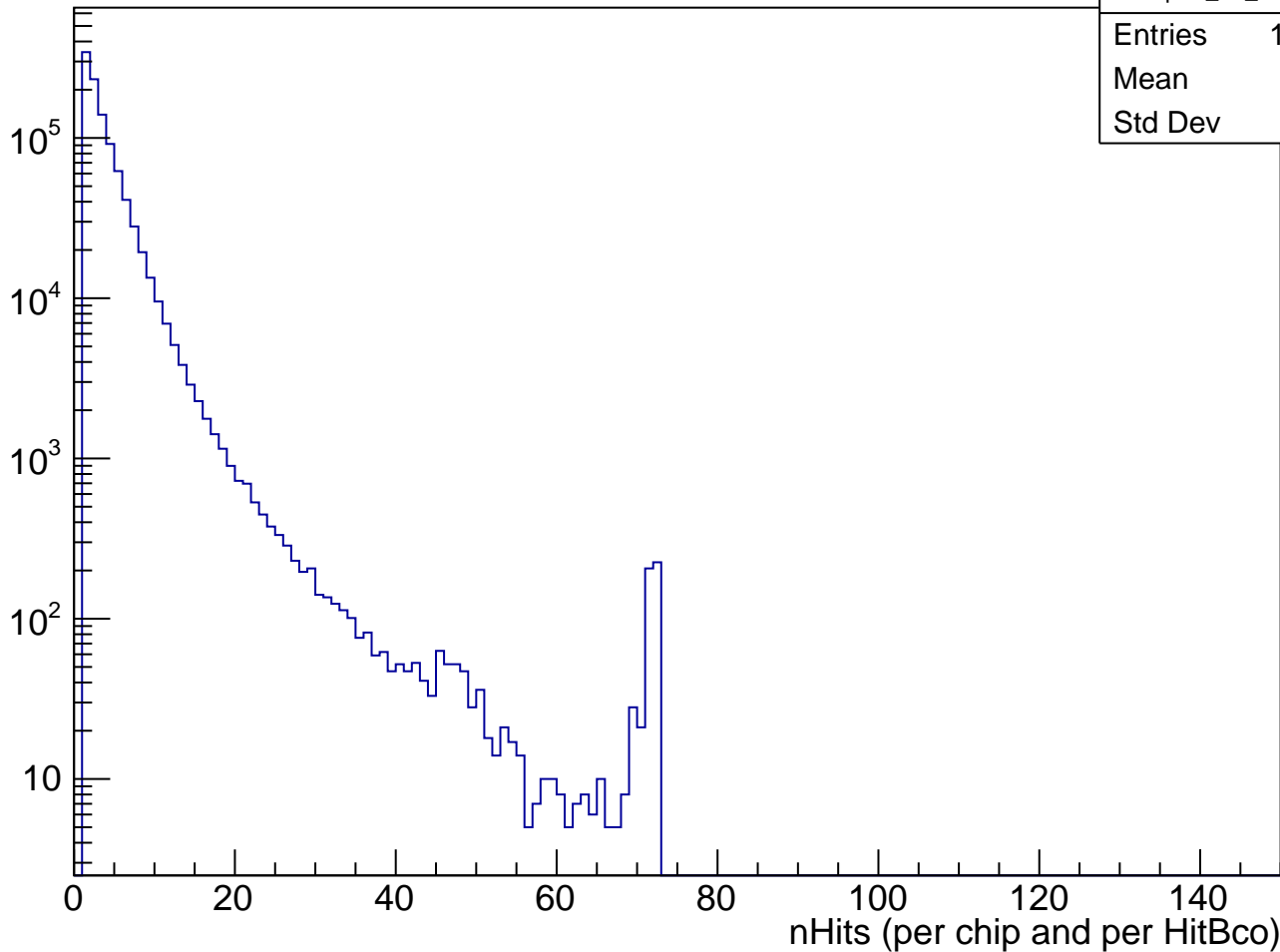
Mean 3.228

Std Dev 3.659

nHits (per chip and per HitBco)

nChipHit_F0_Fch5_Chip7

Entries

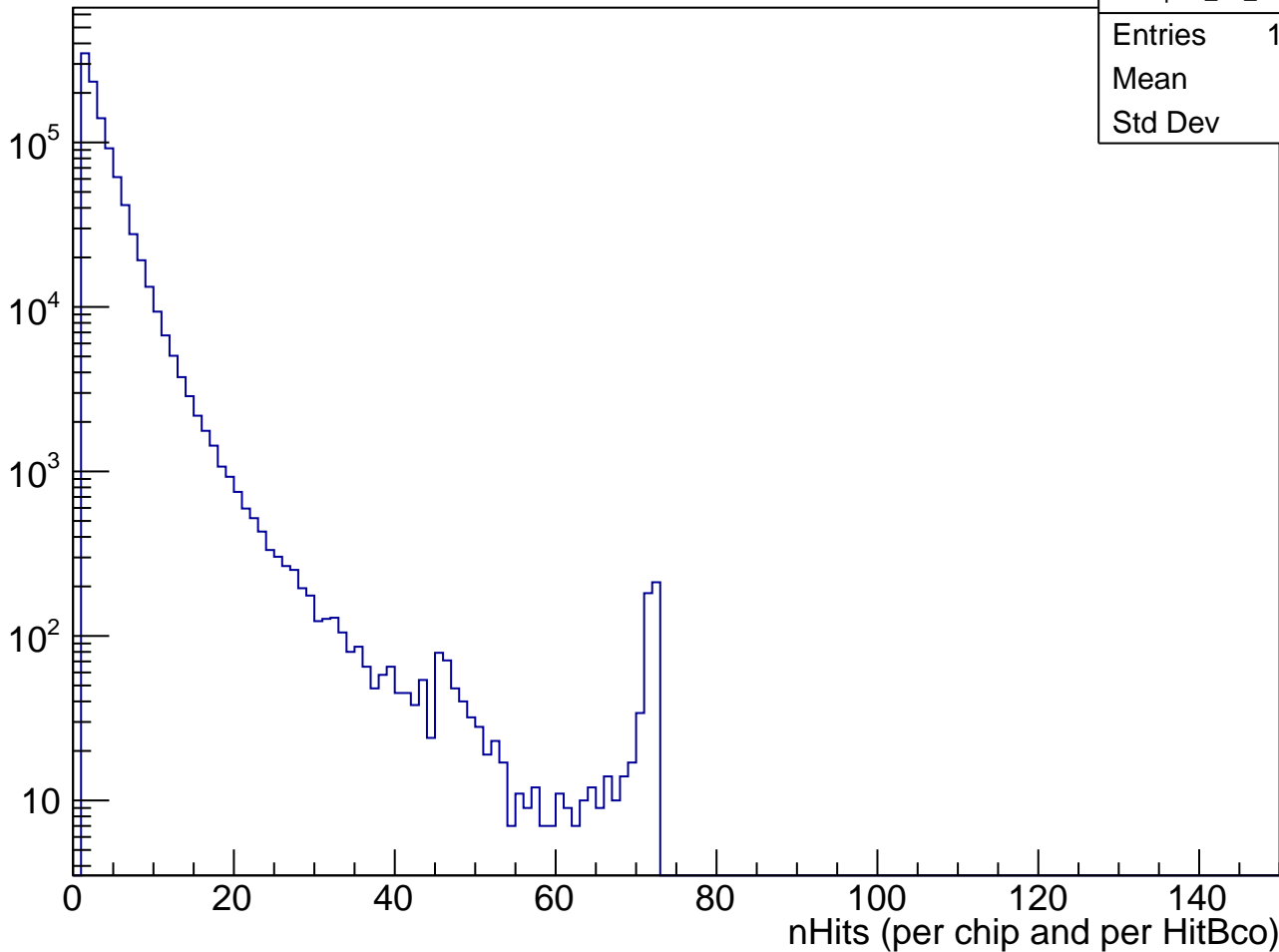


nChipHit_F0_Fch5_Chip7	
Entries	1013313
Mean	3.221
Std Dev	3.631

nHits (per chip and per HitBco)

nChipHit_F0_Fch5_Chip8

Entries



nChipHit_F0_Fch5_Chip8

Entries 1019124

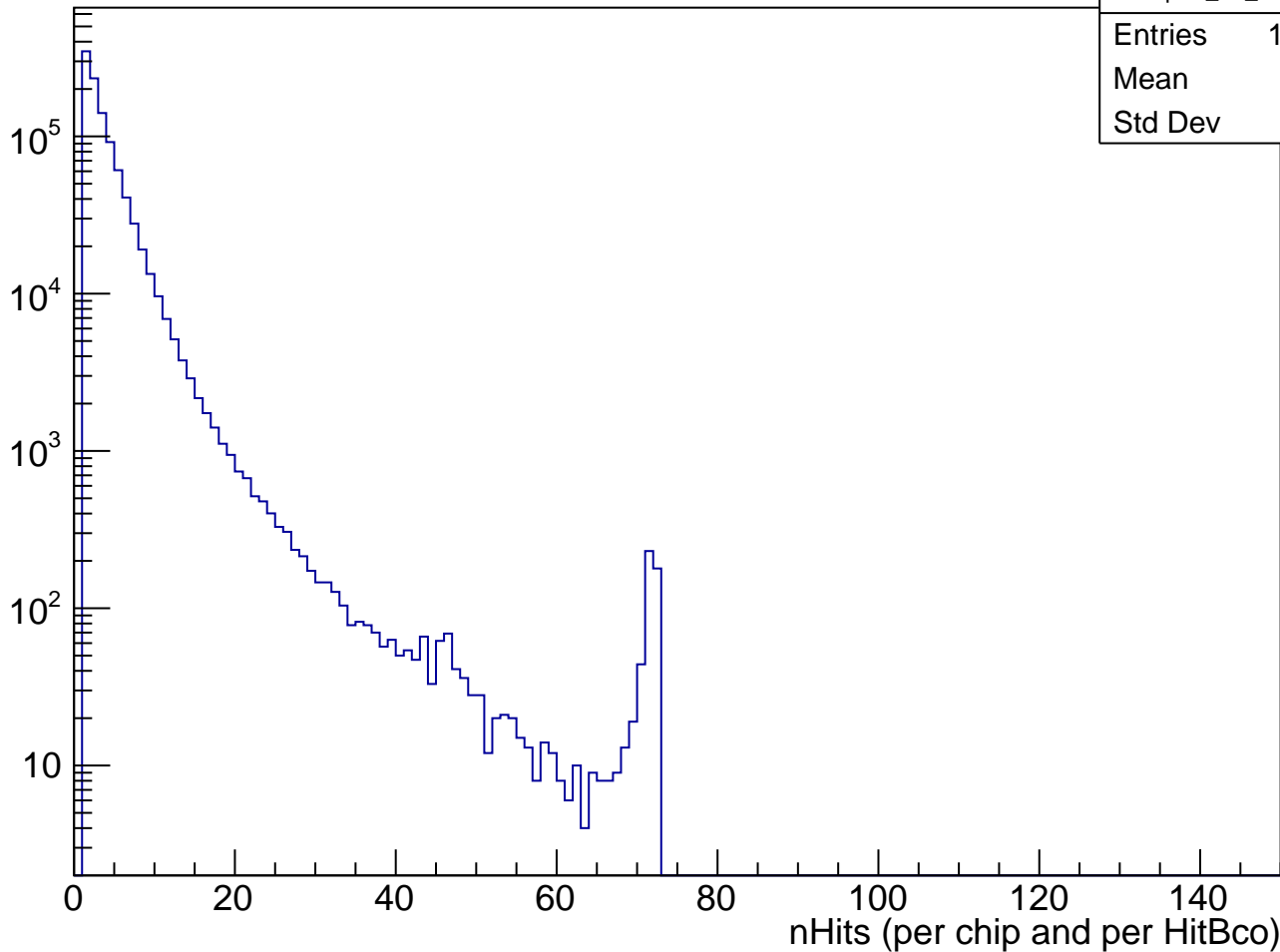
Mean 3.193

Std Dev 3.591

nHits (per chip and per HitBco)

nChipHit_F0_Fch5_Chip9

Entries



nChipHit_F0_Fch5_Chip9

Entries 1018072

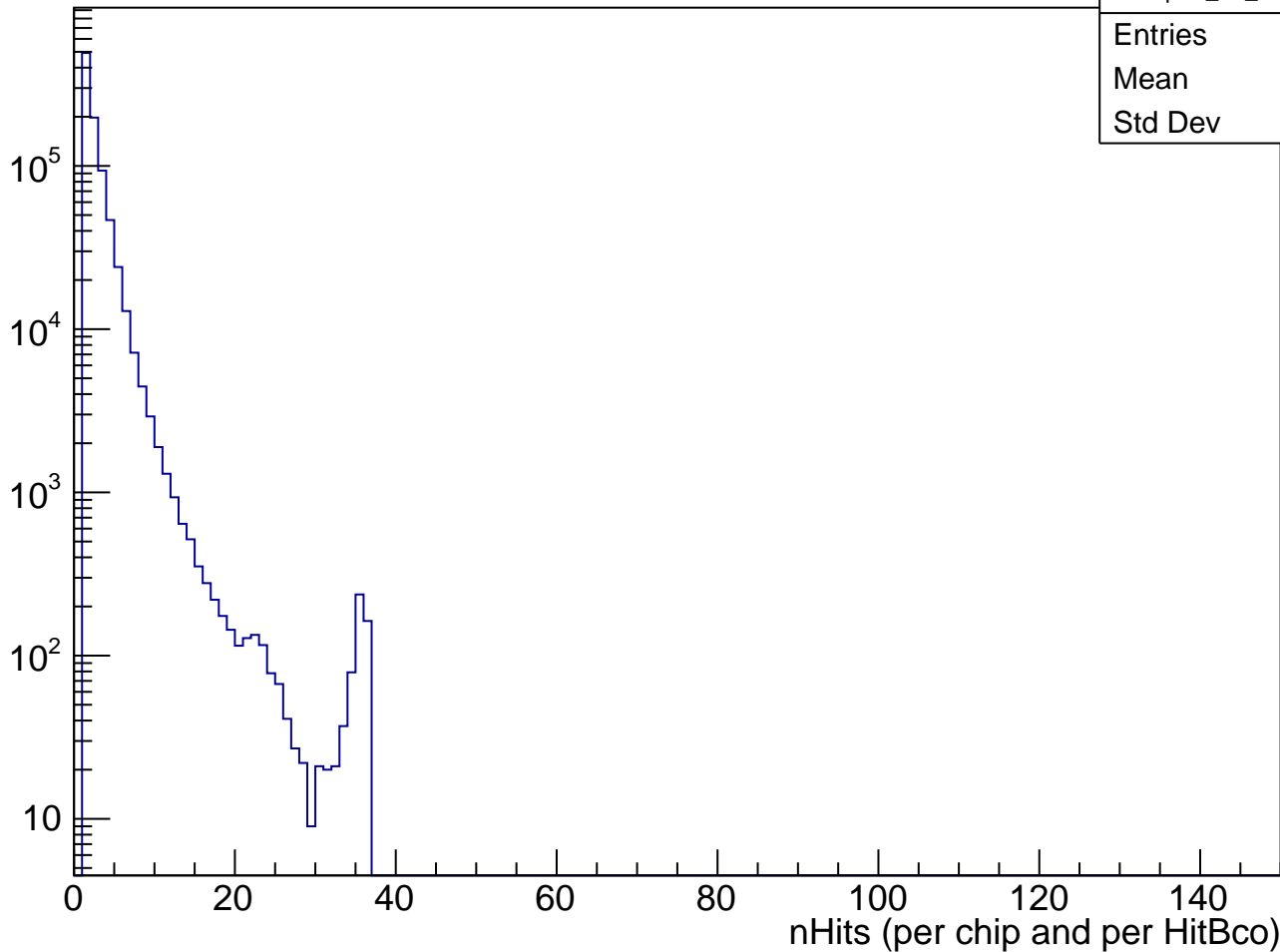
Mean 3.206

Std Dev 3.633

nHits (per chip and per HitBco)

nChipHit_F0_Fch6_Chip0

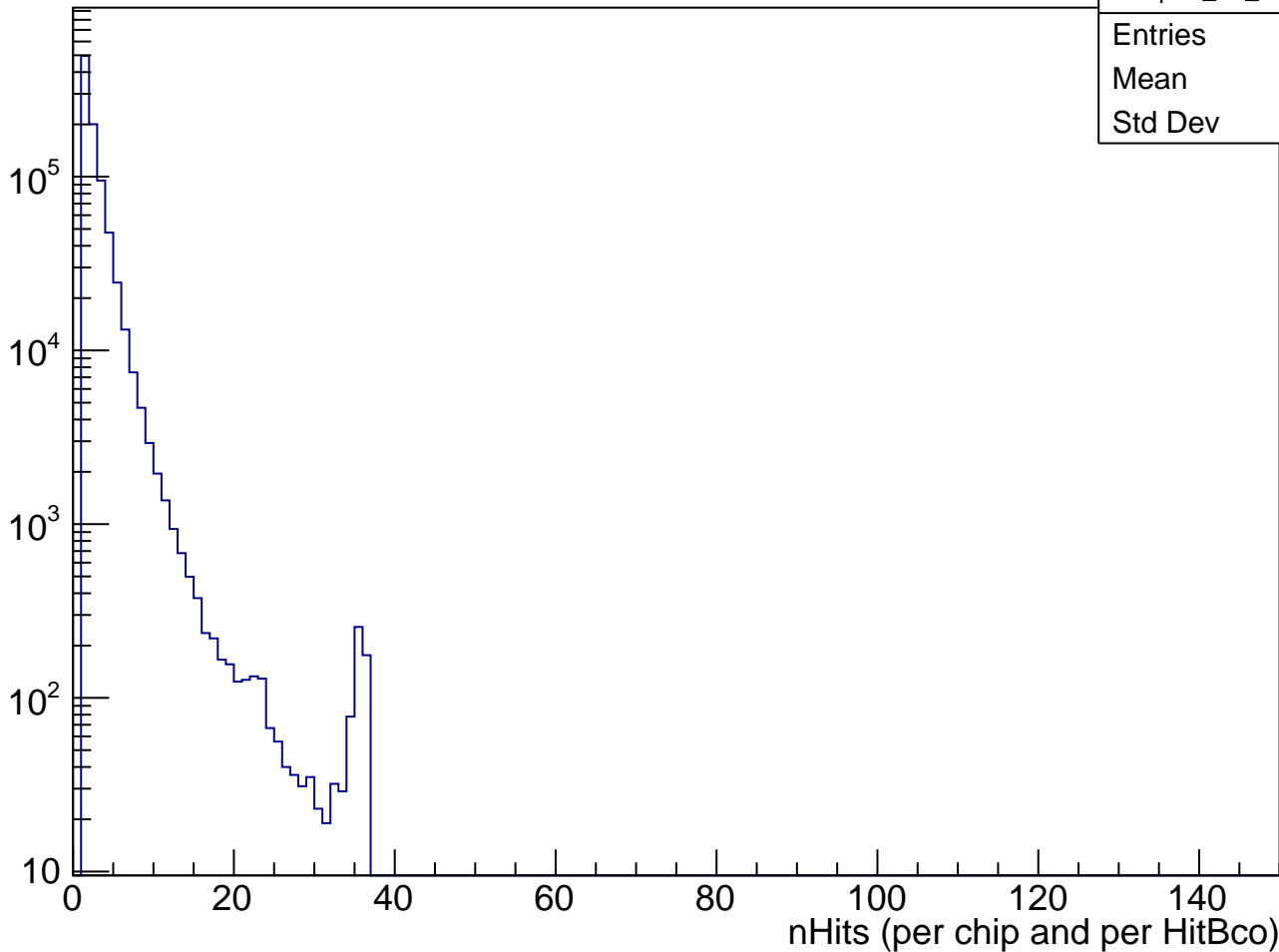
Entries



nChipHit_F0_Fch6_Chip0	
Entries	889433
Mean	2.002
Std Dev	1.937

nChipHit_F0_Fch6_Chip1

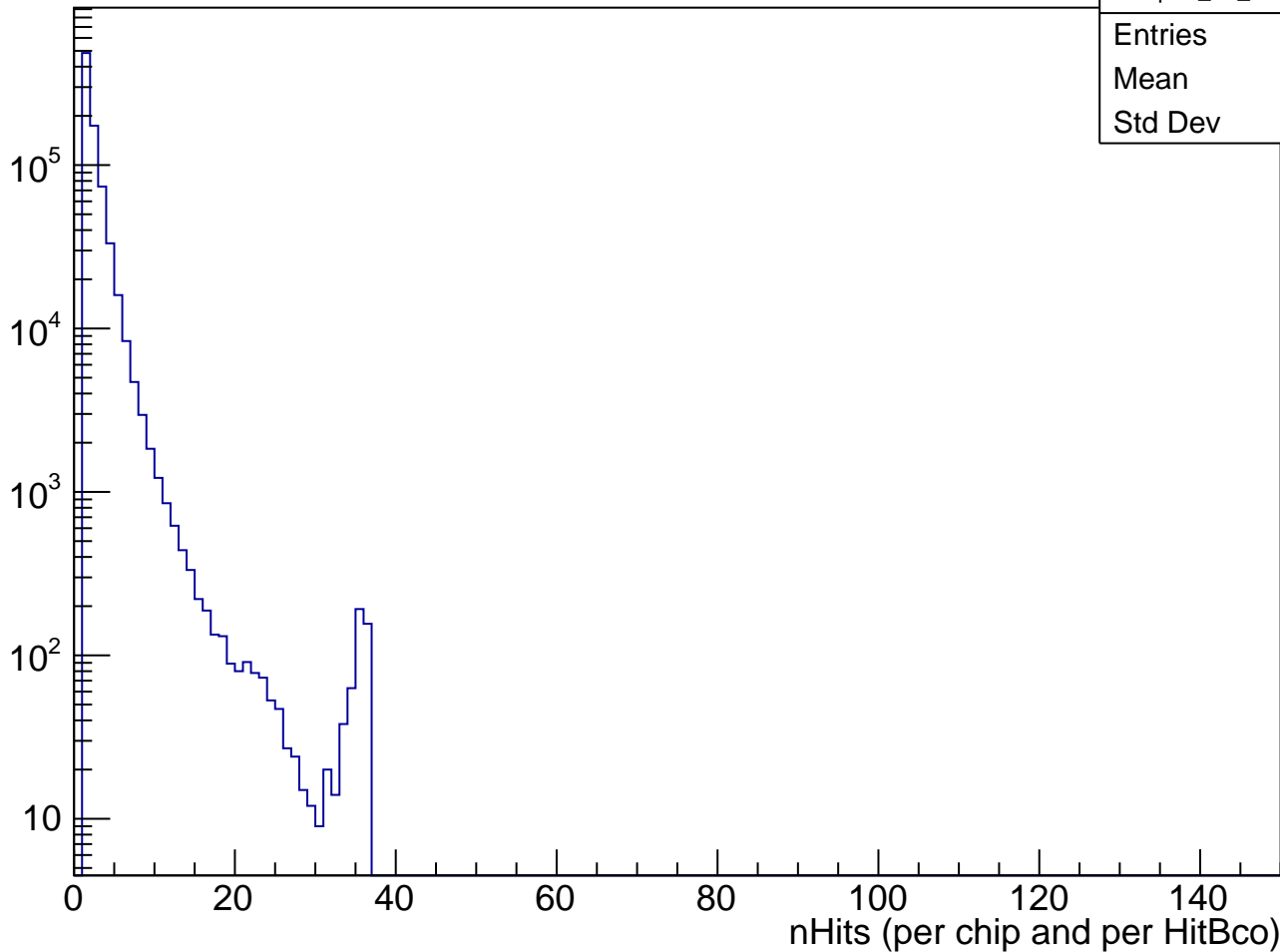
Entries



nChipHit_F0_Fch6_Chip1	
Entries	899828
Mean	2.012
Std Dev	1.957

nChipHit_F0_Fch6_Chip10

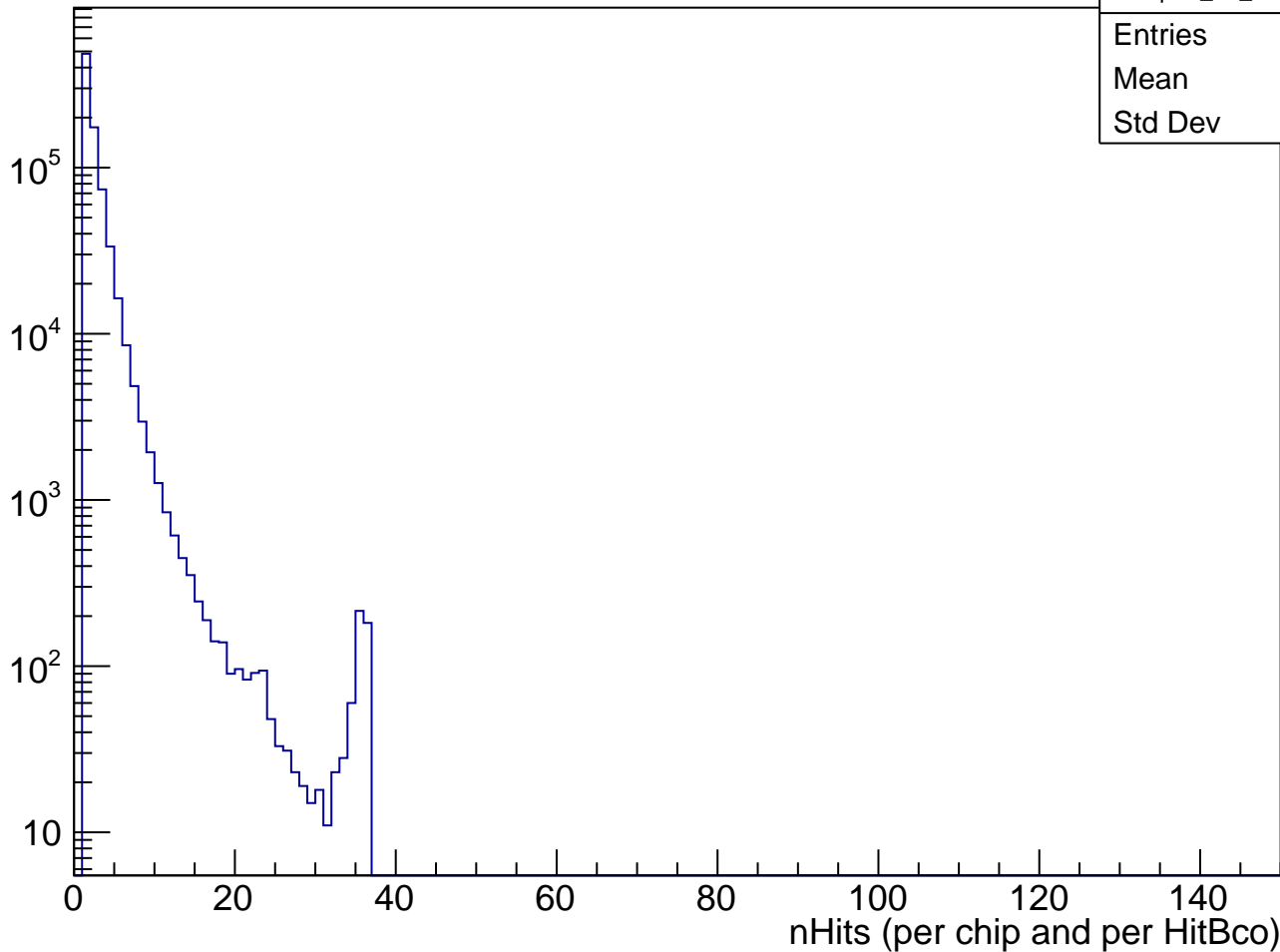
Entries



nChipHit_F0_Fch6_Chip10	
Entries	805469
Mean	1.828
Std Dev	1.756

nChipHit_F0_Fch6_Chip11

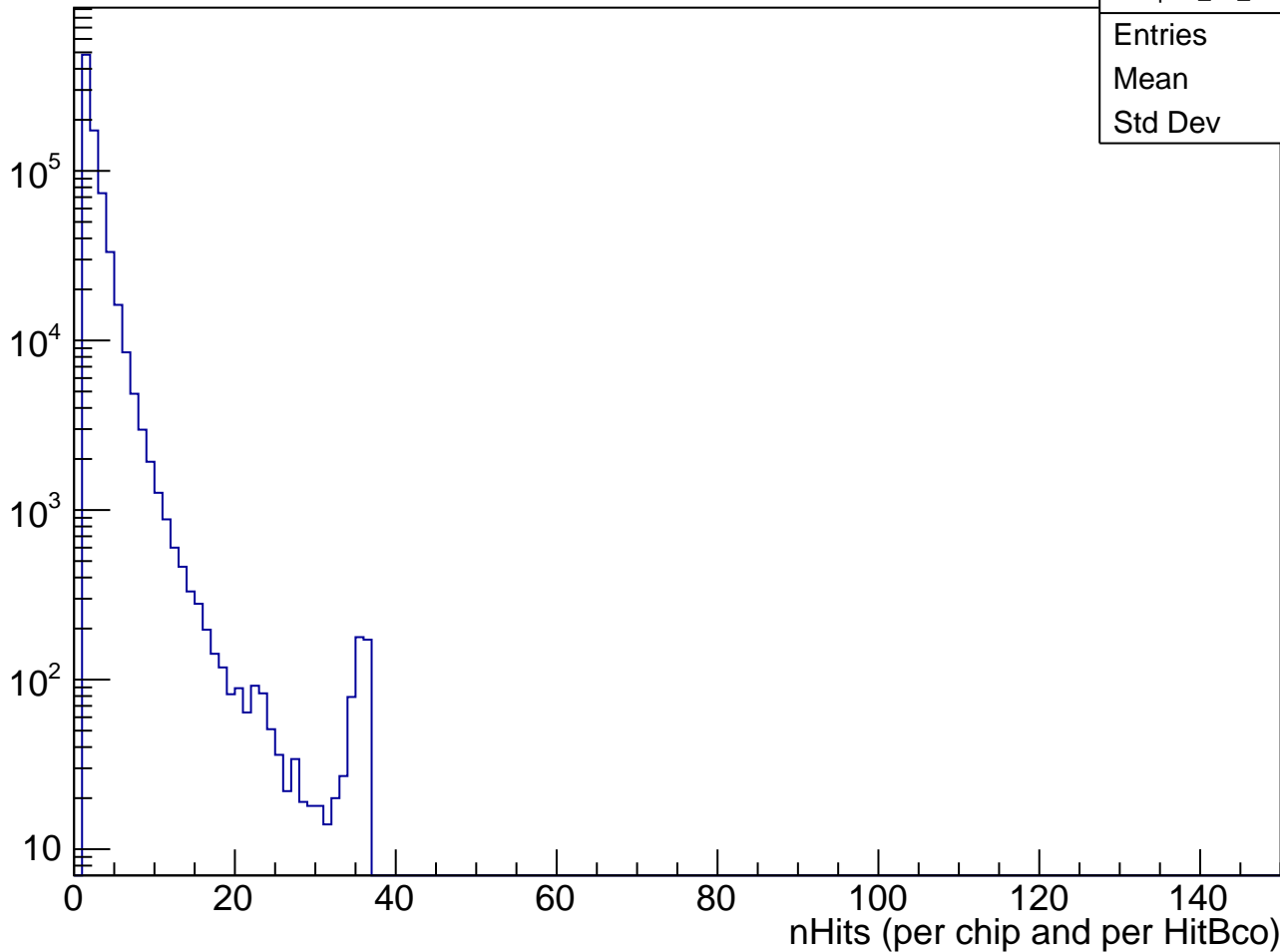
Entries



nChipHit_F0_Fch6_Chip11	
Entries	806340
Mean	1.837
Std Dev	1.787

nChipHit_F0_Fch6_Chip12

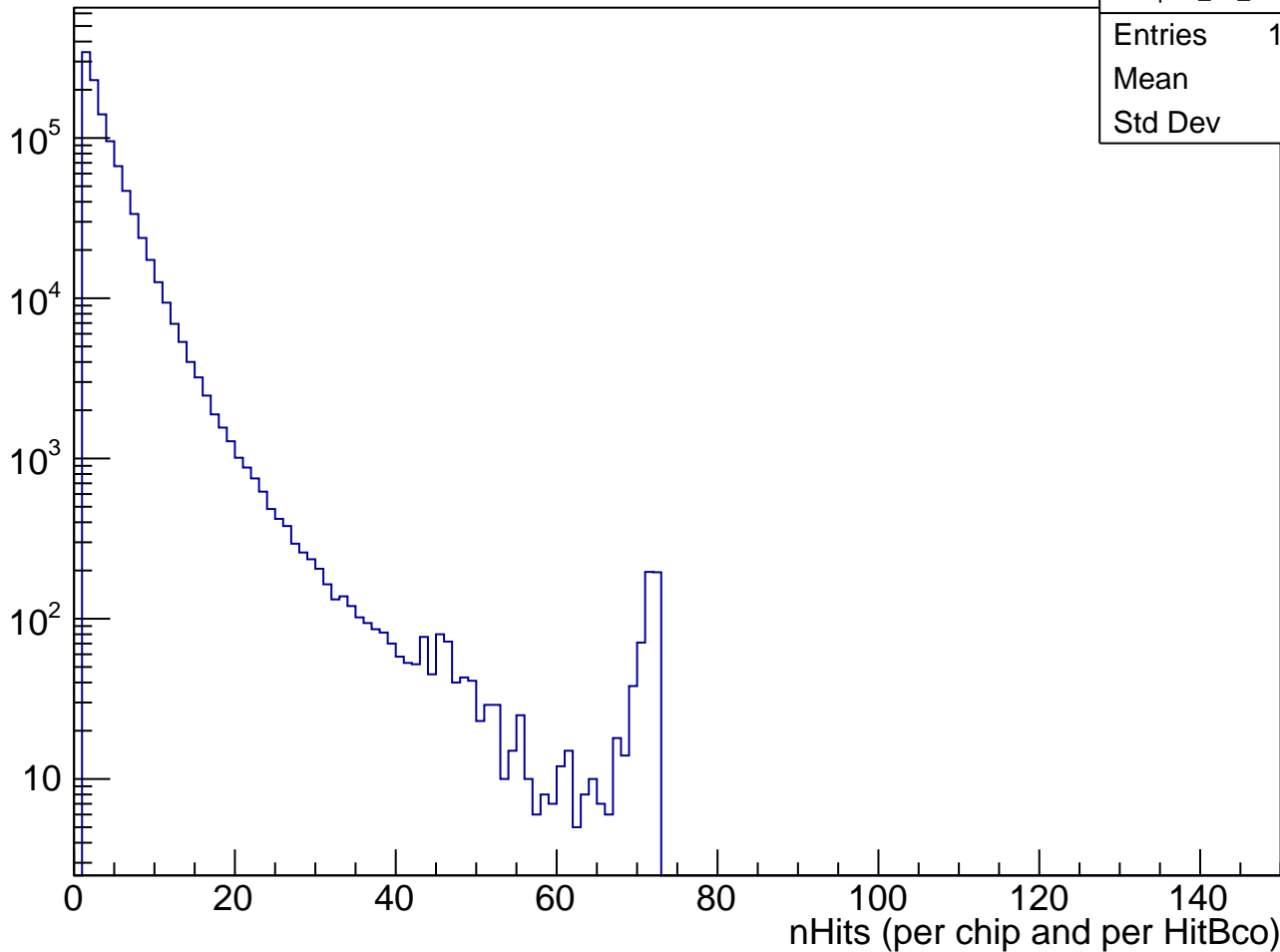
Entries



nChipHit_F0_Fch6_Chip12	
Entries	804417
Mean	1.834
Std Dev	1.775

nChipHit_F0_Fch6_Chip13

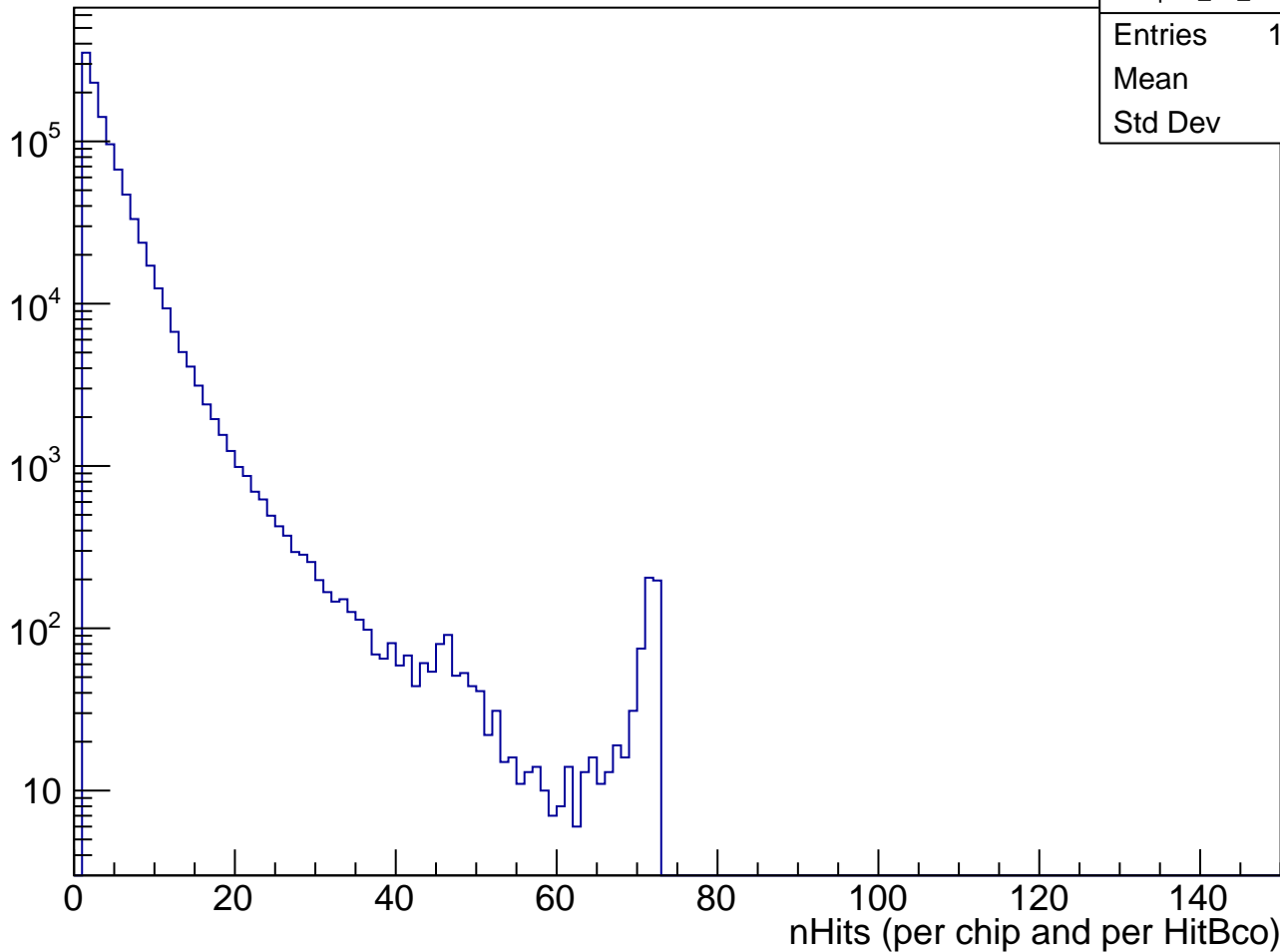
Entries



nChipHit_F0_Fch6_Chip13	
Entries	1053954
Mean	3.464
Std Dev	3.898

nChipHit_F0_Fch6_Chip14

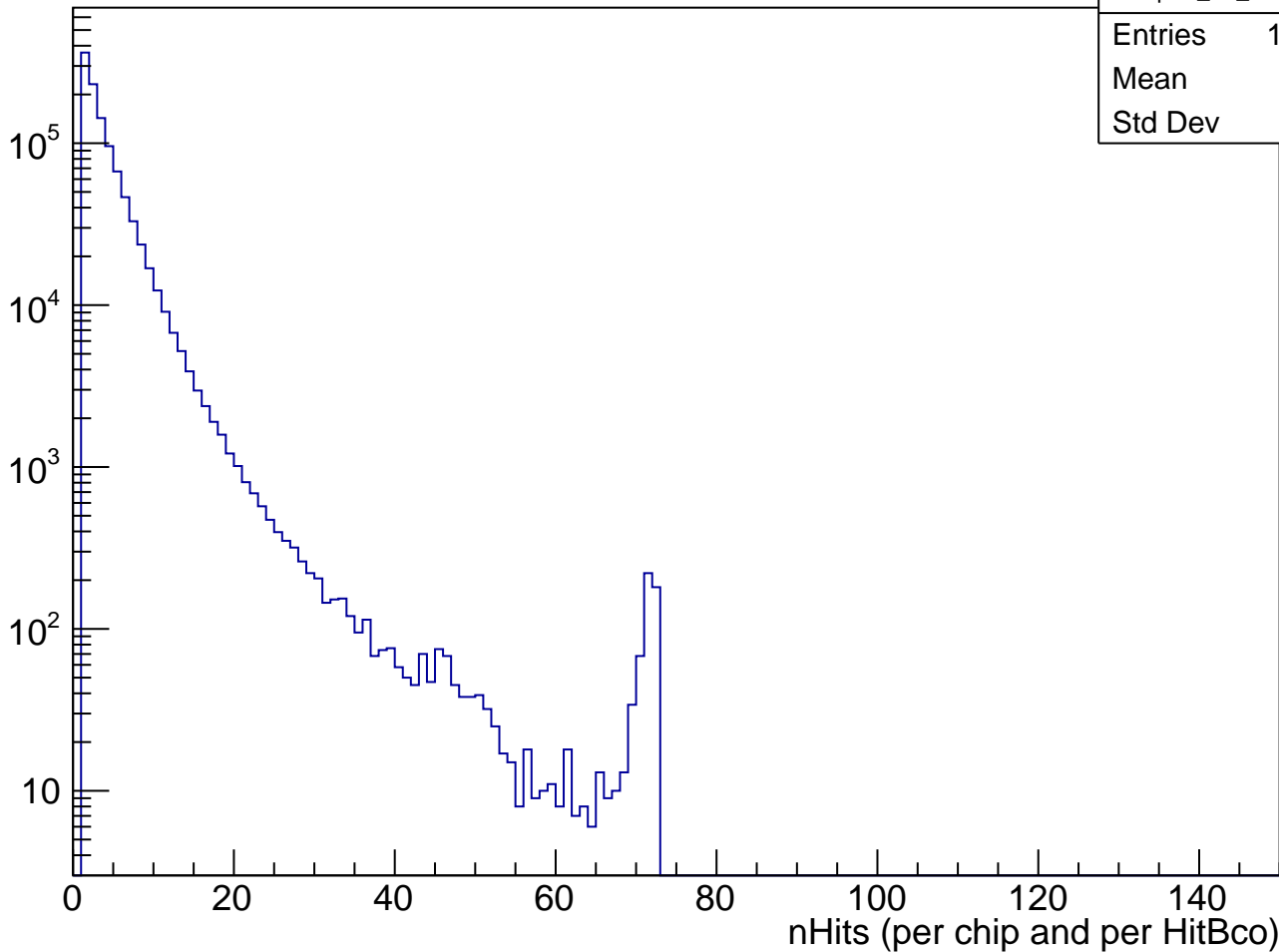
Entries



nChipHit_F0_Fch6_Chip14	
Entries	1063159
Mean	3.441
Std Dev	3.91

nChipHit_F0_Fch6_Chip15

Entries

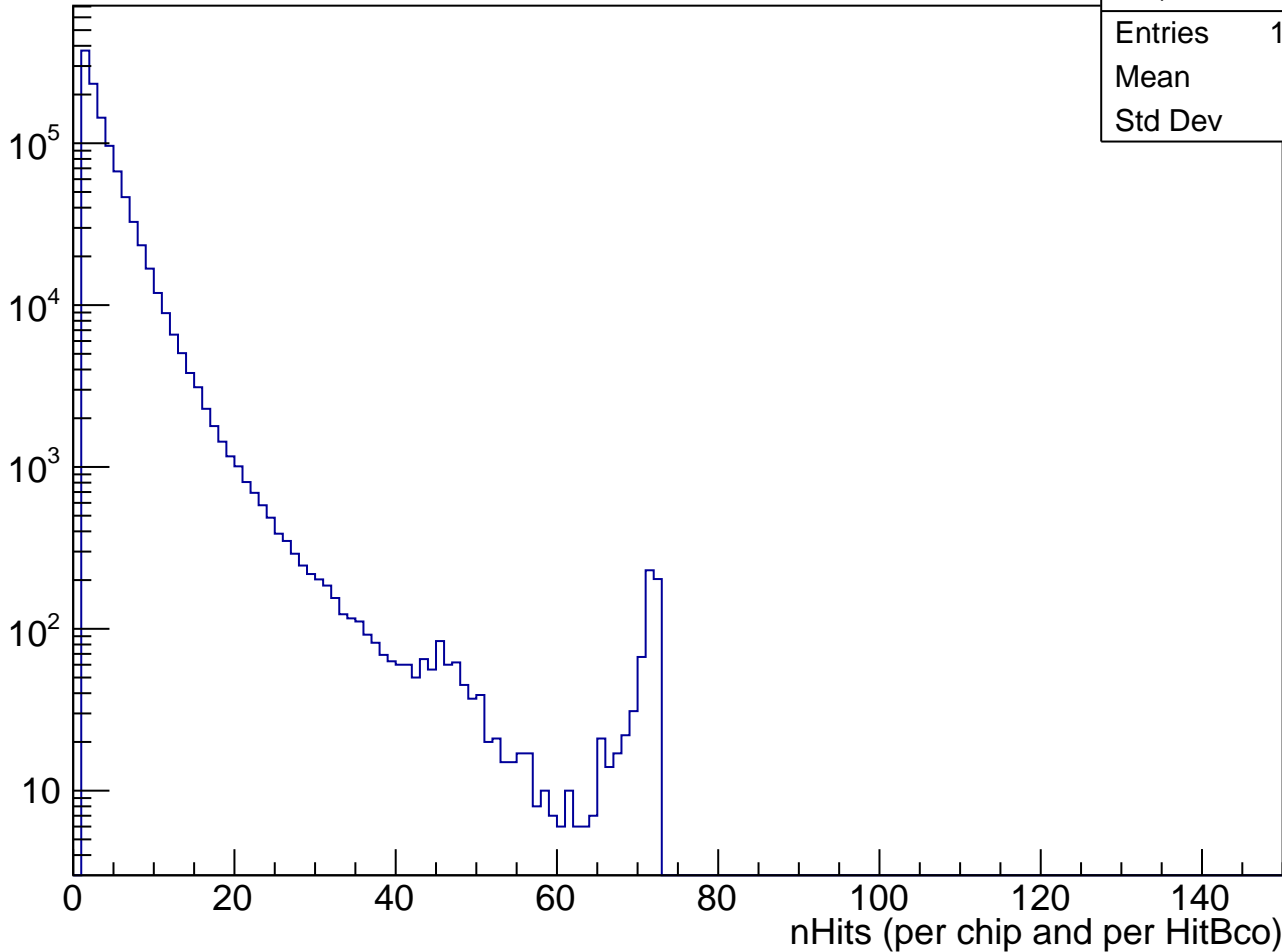


nChipHit_F0_Fch6_Chip15	
Entries	1075028
Mean	3.395
Std Dev	3.848

nHits (per chip and per HitBco)

nChipHit_F0_Fch6_Chip16

Entries

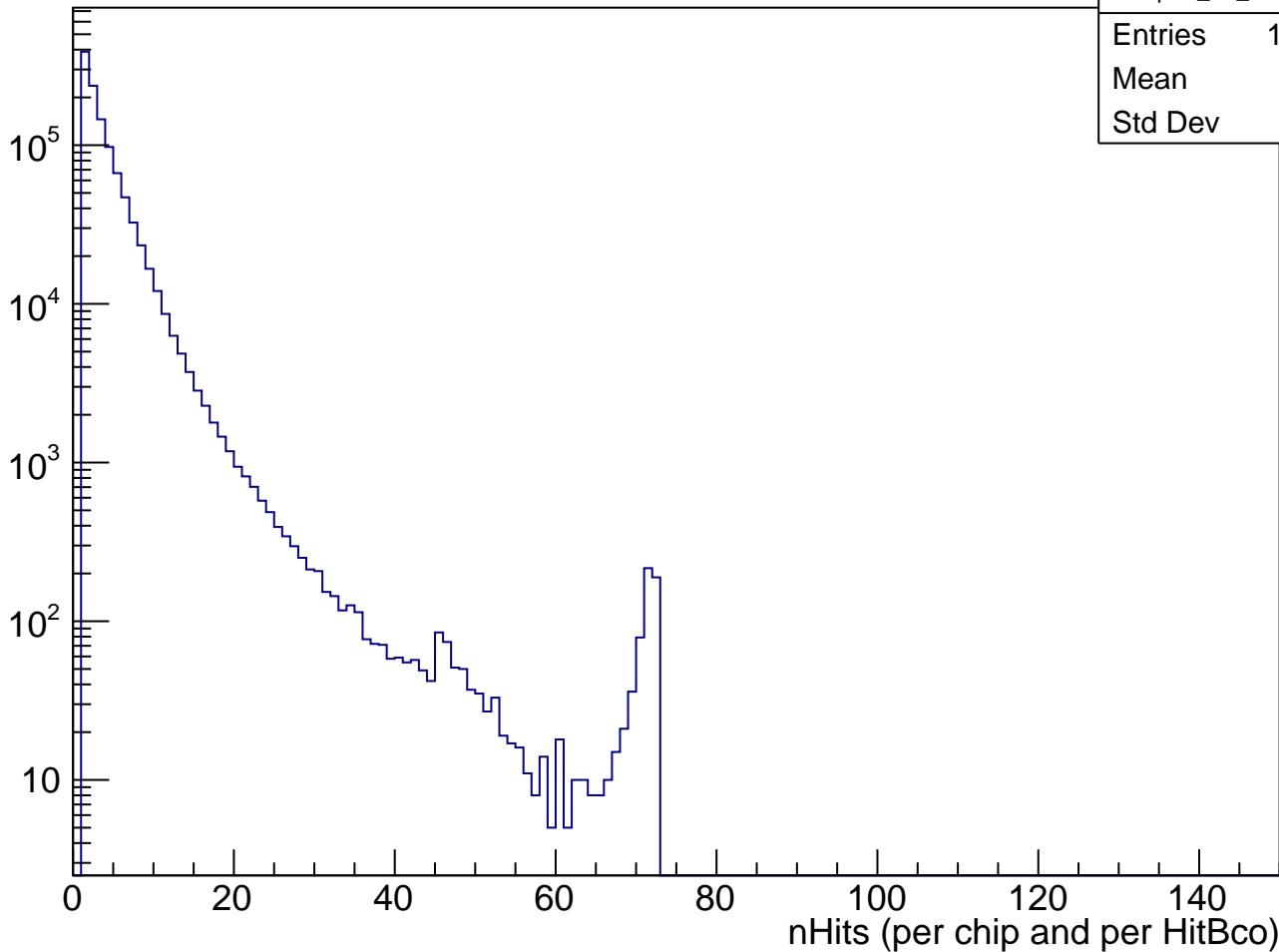


nChipHit_F0_Fch6_Chip16	
Entries	1087142
Mean	3.359
Std Dev	3.843

nHits (per chip and per HitBco)

nChipHit_F0_Fch6_Chip17

Entries

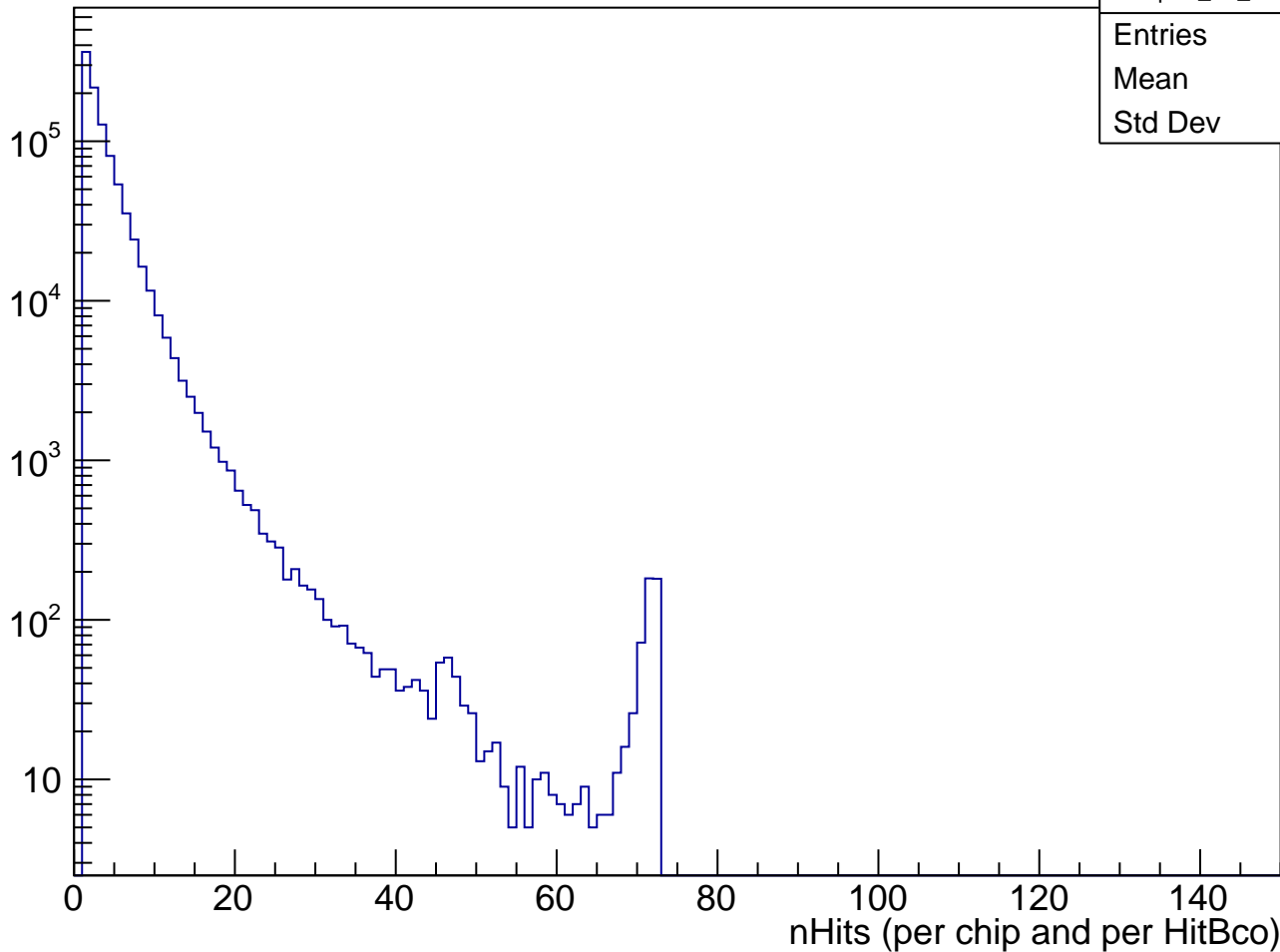


nChipHit_F0_Fch6_Chip17	
Entries	1106966
Mean	3.311
Std Dev	3.791

nHits (per chip and per HitBco)

nChipHit_F0_Fch6_Chip18

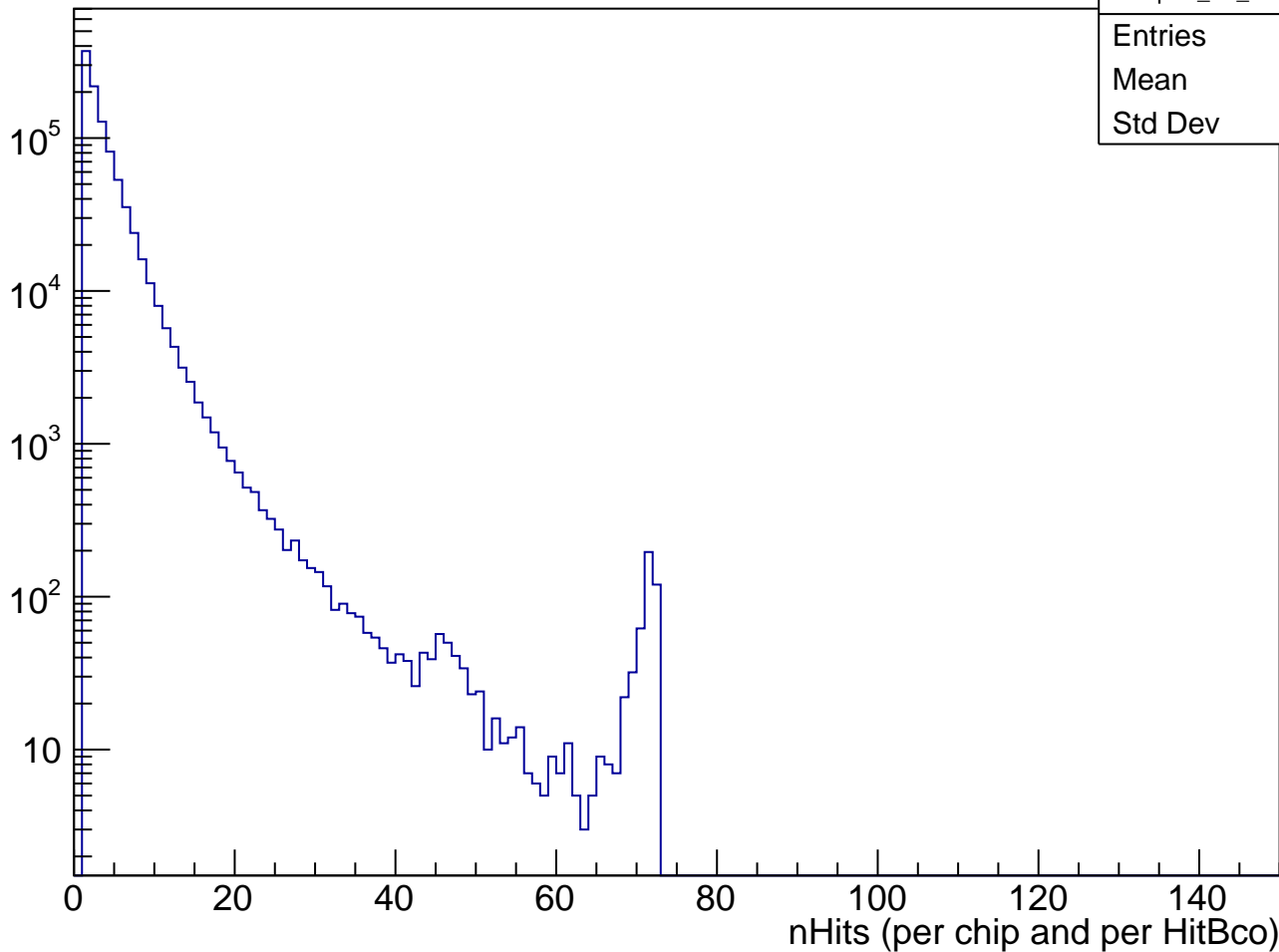
Entries



nChipHit_F0_Fch6_Chip18	
Entries	965093
Mean	3.039
Std Dev	3.511

nChipHit_F0_Fch6_Chip19

Entries

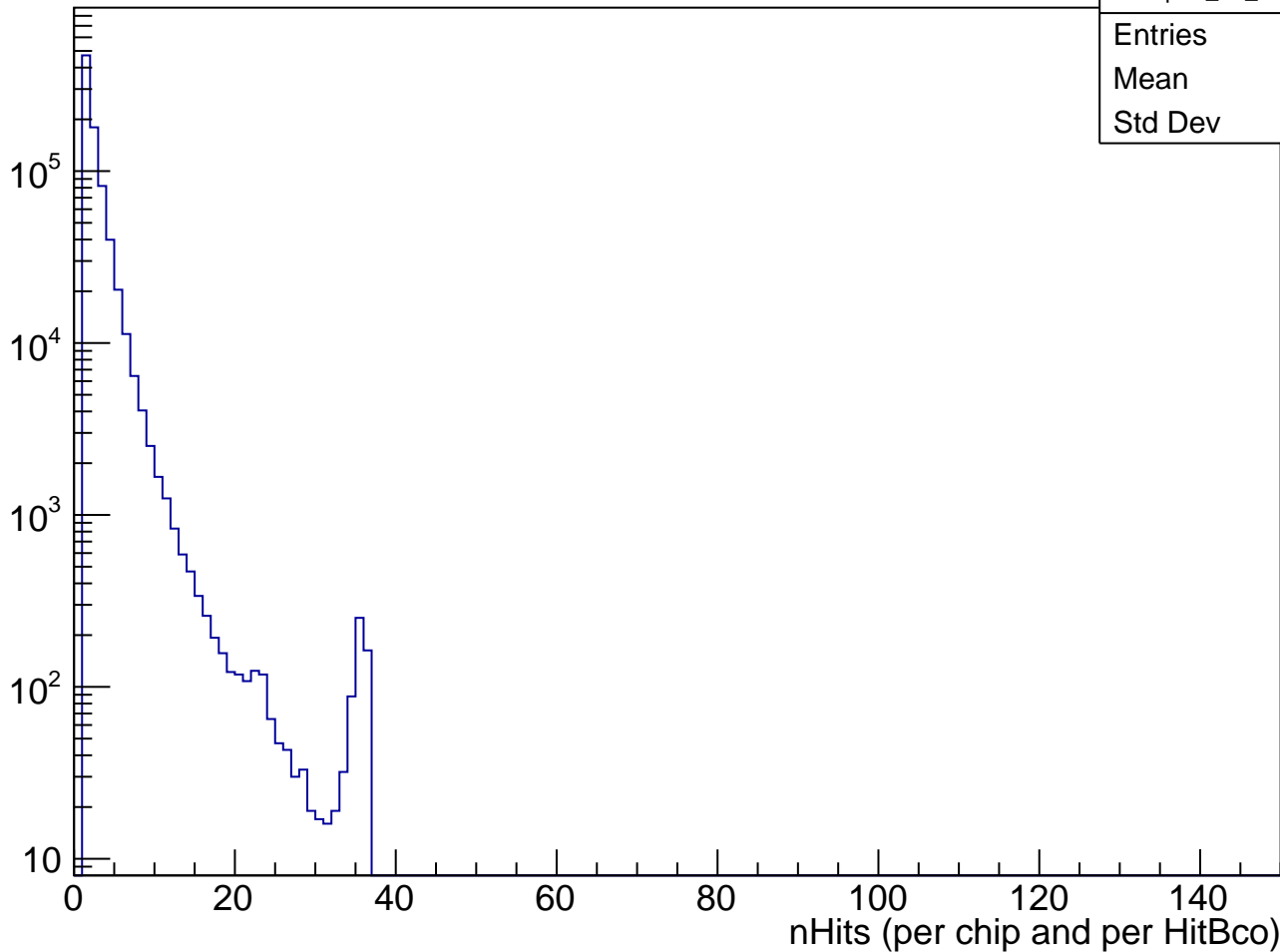


nChipHit_F0_Fch6_Chip19	
Entries	973393
Mean	3.01
Std Dev	3.465

nHits (per chip and per HitBco)

nChipHit_F0_Fch6_Chip2

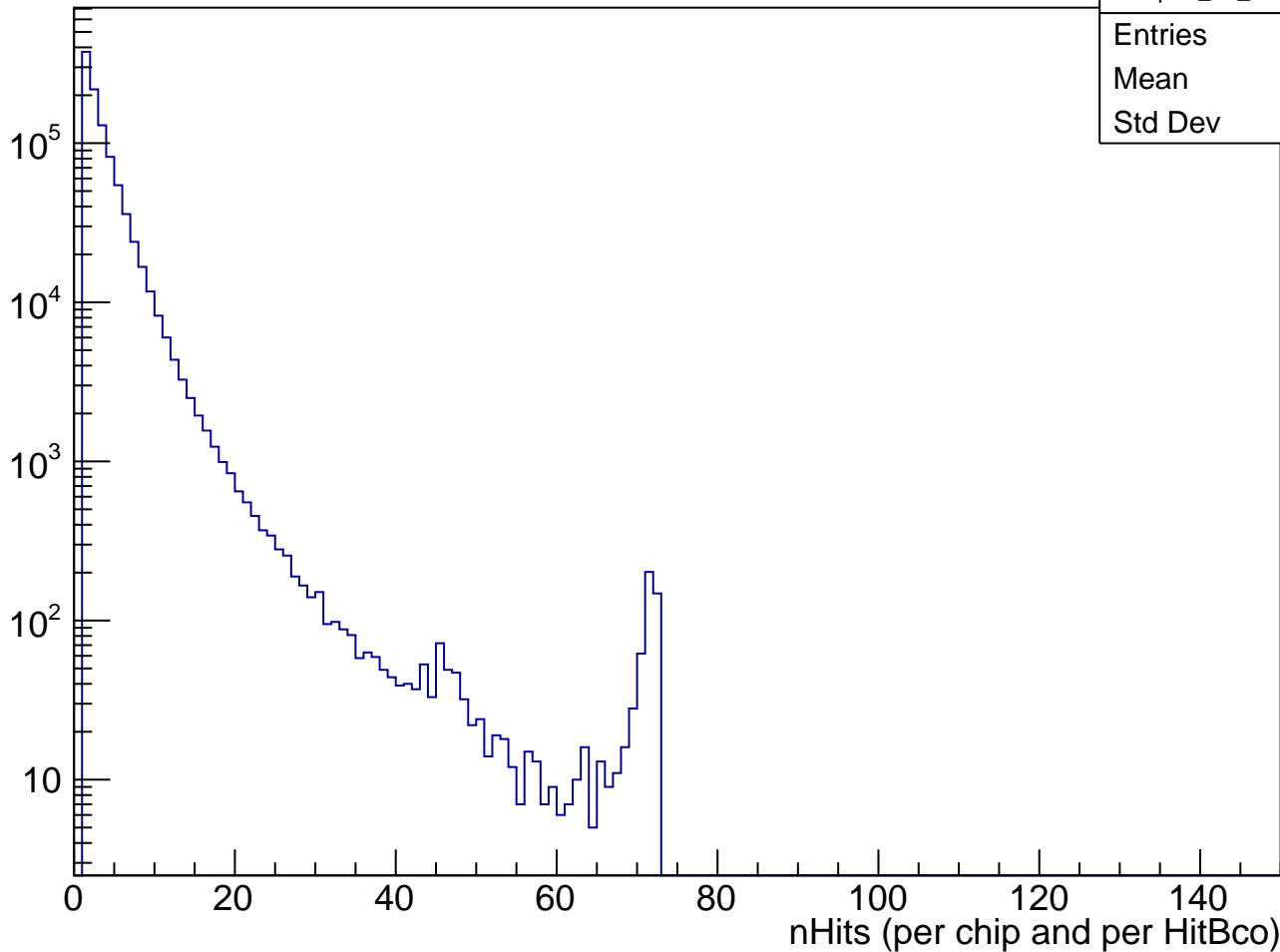
Entries



nChipHit_F0_Fch6_Chip2	
Entries	824515
Mean	1.958
Std Dev	1.94

nChipHit_F0_Fch6_Chip20

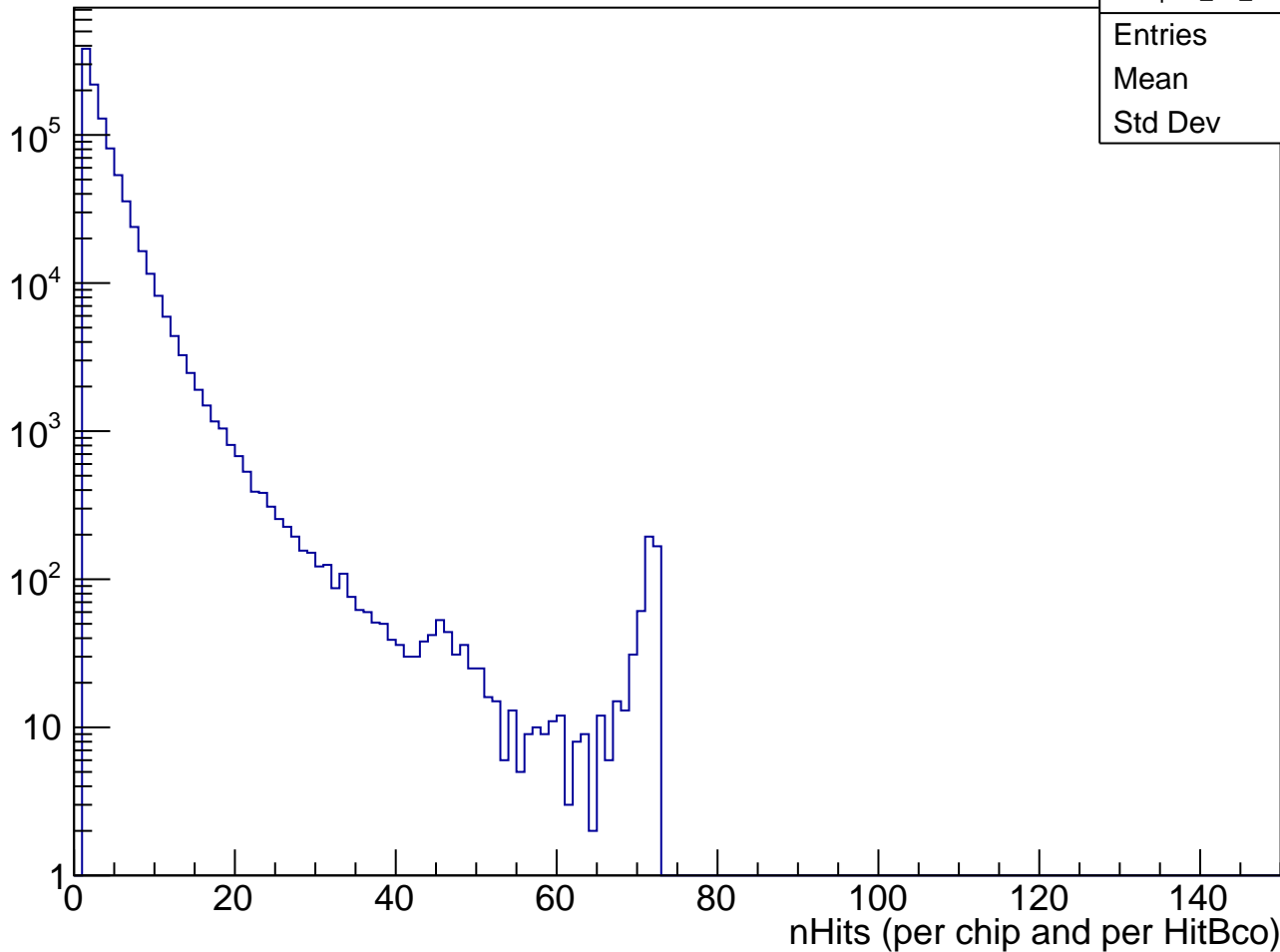
Entries



nChipHit_F0_Fch6_Chip20	
Entries	983224
Mean	3.028
Std Dev	3.519

nChipHit_F0_Fch6_Chip21

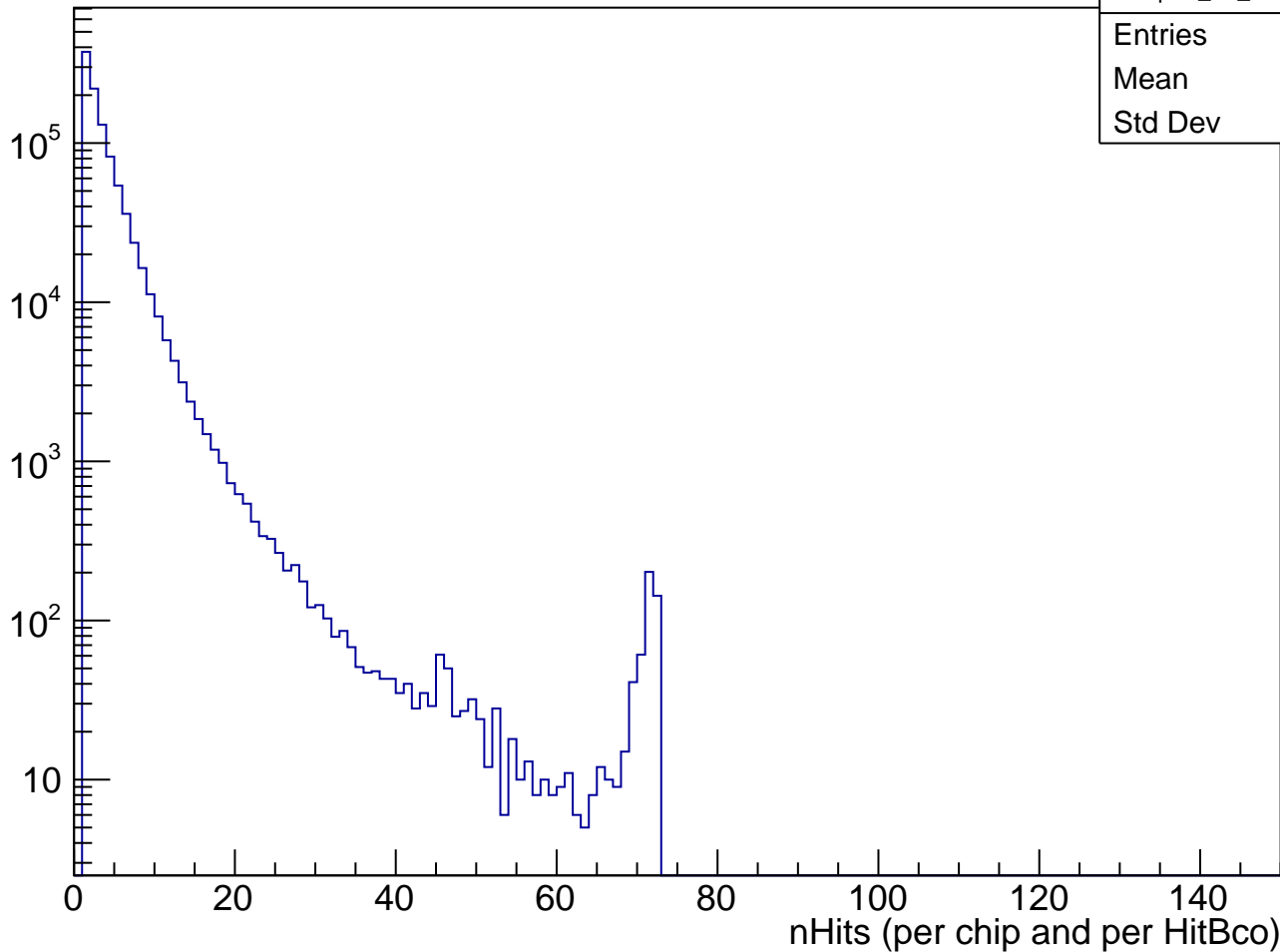
Entries



nChipHit_F0_Fch6_Chip21	
Entries	987767
Mean	2.997
Std Dev	3.479

nChipHit_F0_Fch6_Chip22

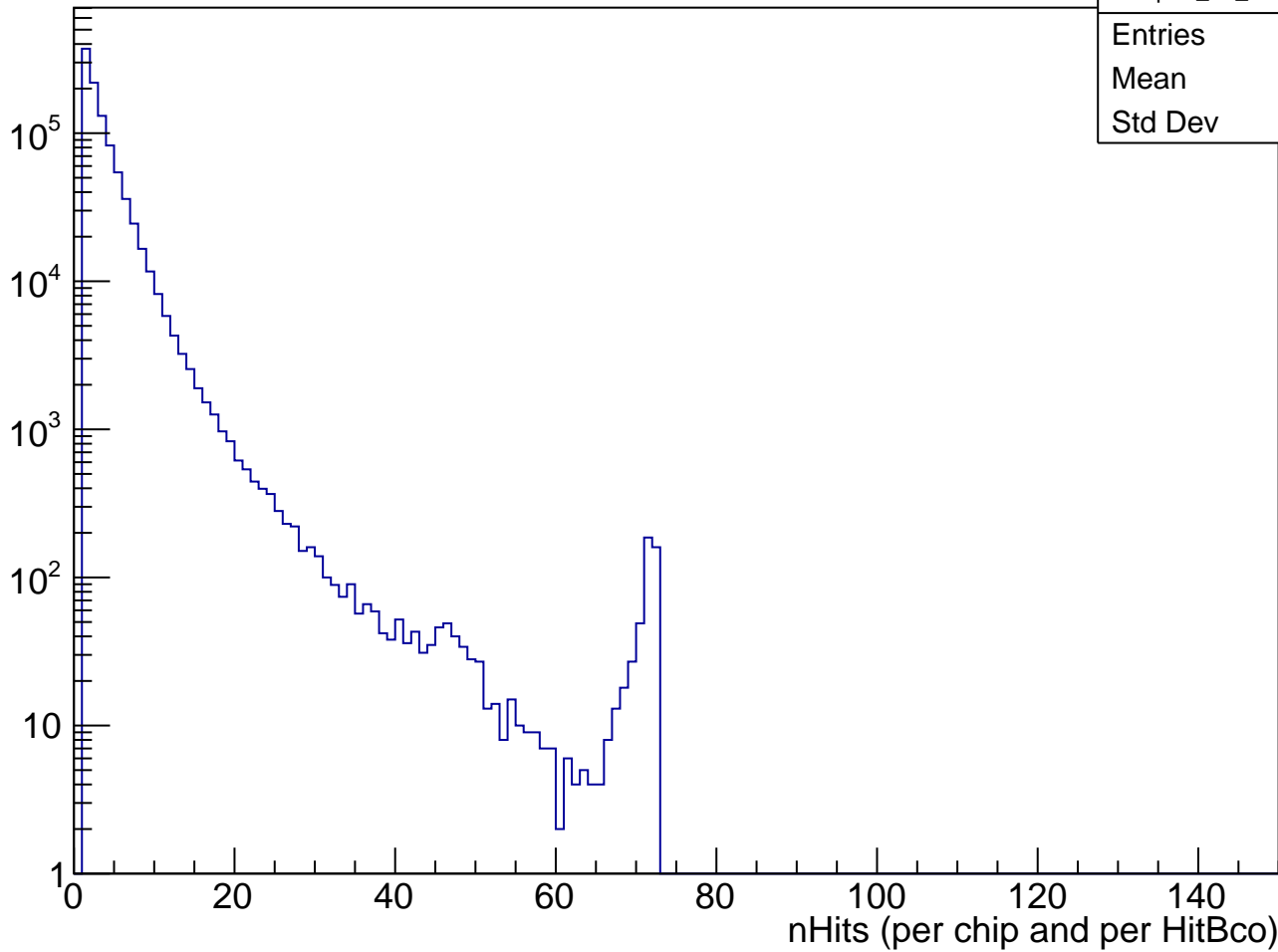
Entries



nChipHit_F0_Fch6_Chip22	
Entries	983280
Mean	3.001
Std Dev	3.458

nChipHit_F0_Fch6_Chip23

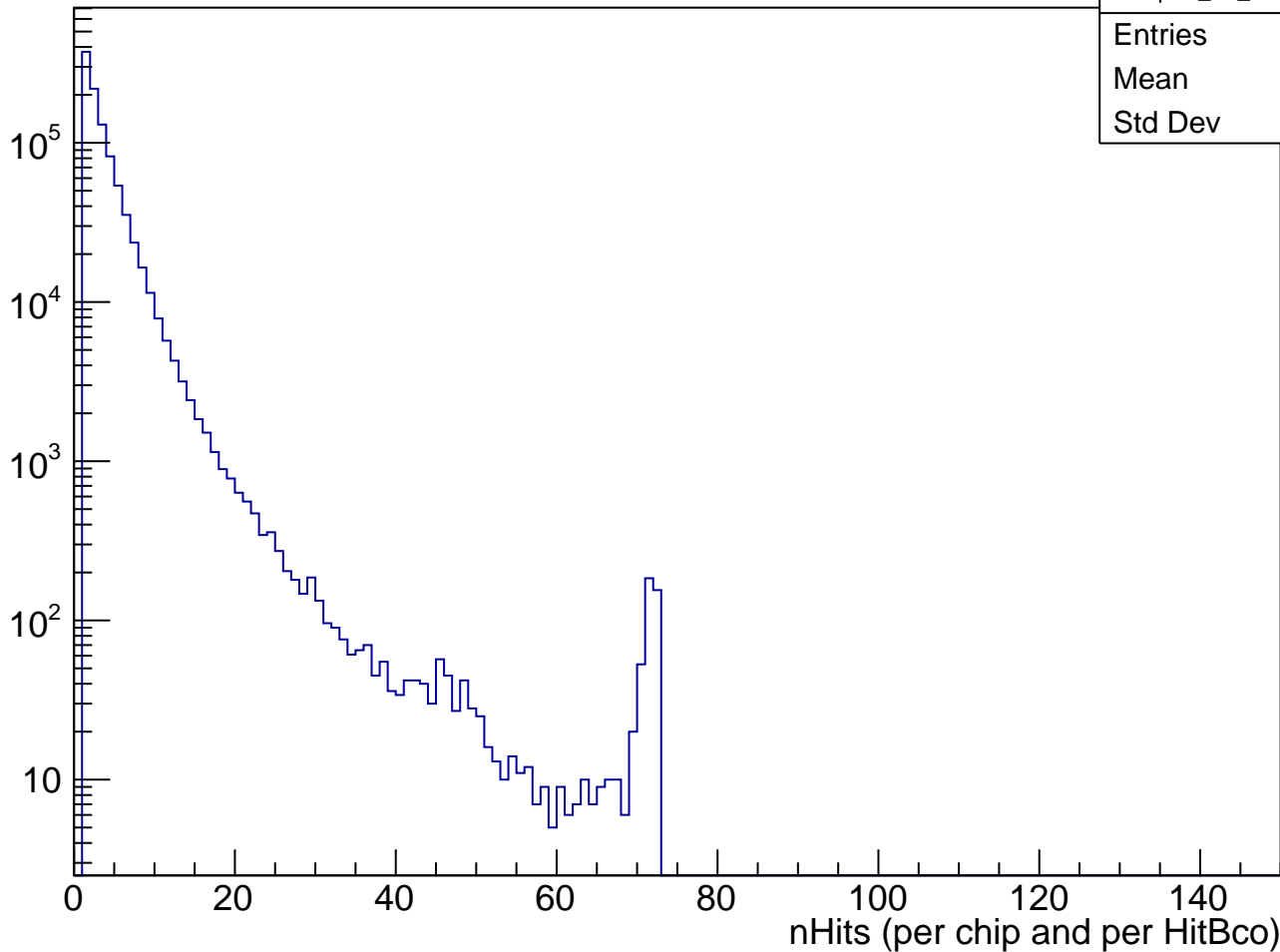
Entries



nChipHit_F0_Fch6_Chip23	
Entries	983267
Mean	3.026
Std Dev	3.469

nChipHit_F0_Fch6_Chip24

Entries

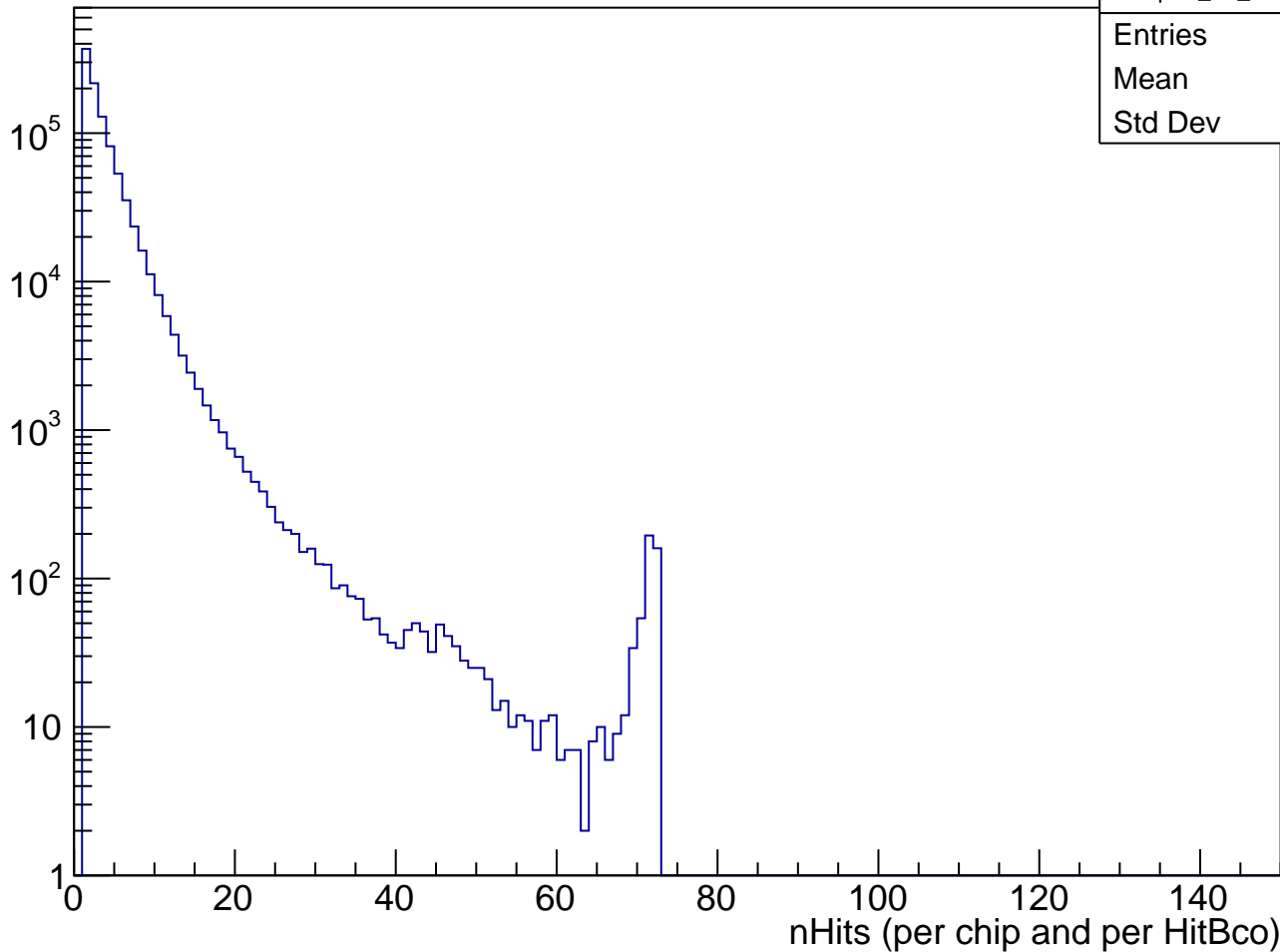


nChipHit_F0_Fch6_Chip24	
Entries	979280
Mean	3.002
Std Dev	3.445

nHits (per chip and per HitBco)

nChipHit_F0_Fch6_Chip25

Entries

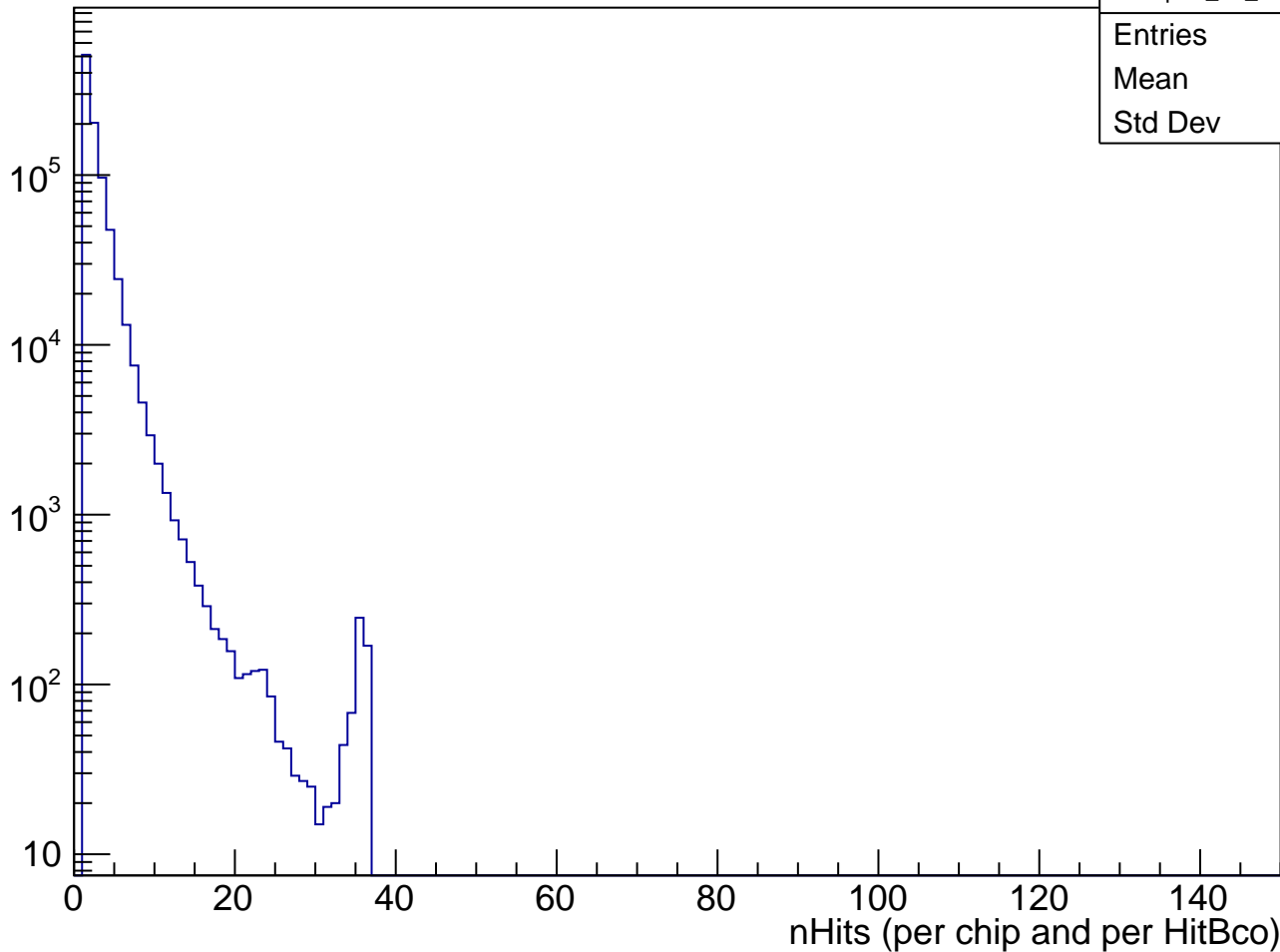


nChipHit_F0_Fch6_Chip25	
Entries	972278
Mean	3.013
Std Dev	3.479

nHits (per chip and per HitBco)

nChipHit_F0_Fch6_Chip3

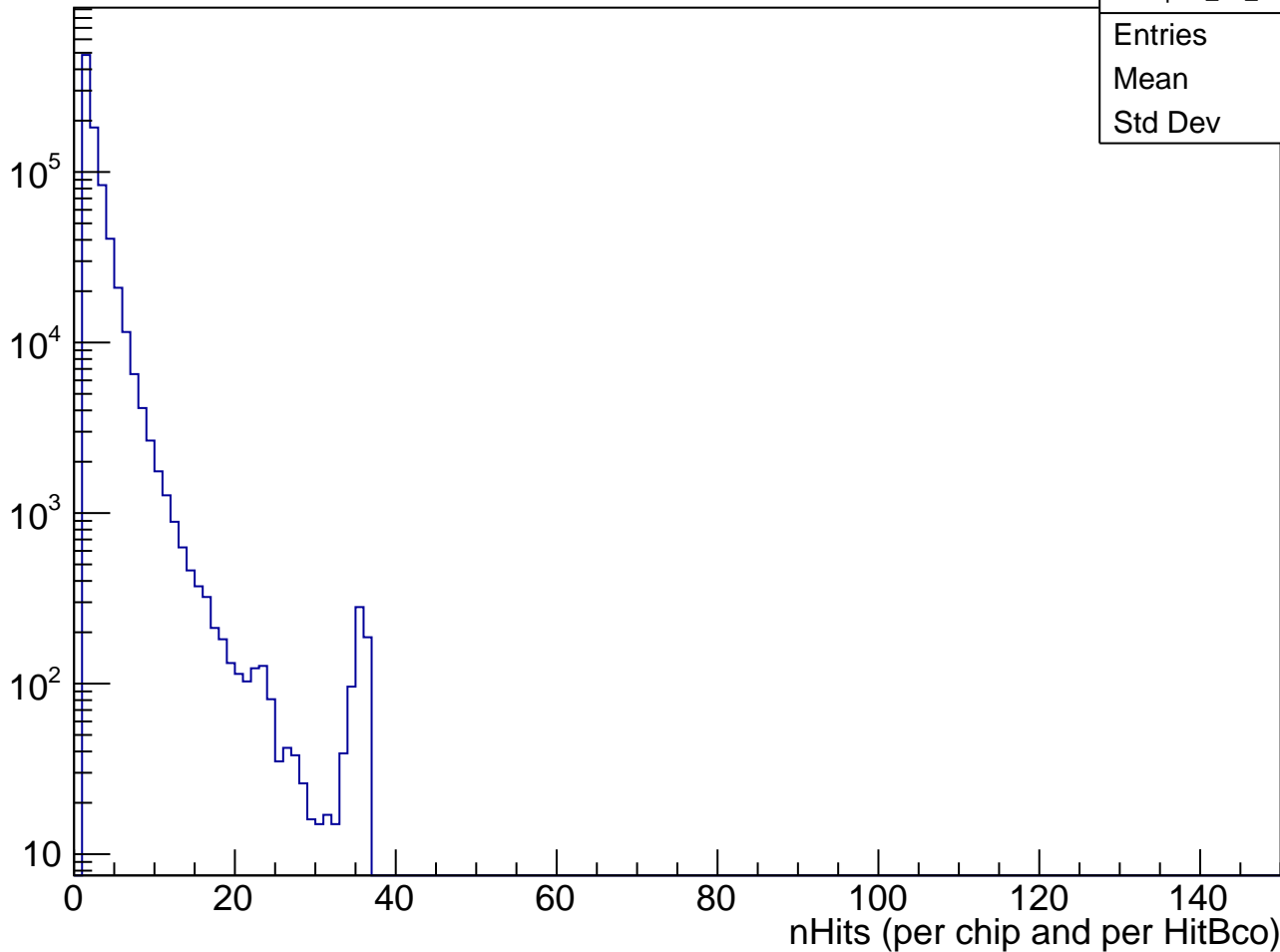
Entries



nChipHit_F0_Fch6_Chip3	
Entries	918869
Mean	1.995
Std Dev	1.93

nChipHit_F0_Fch6_Chip4

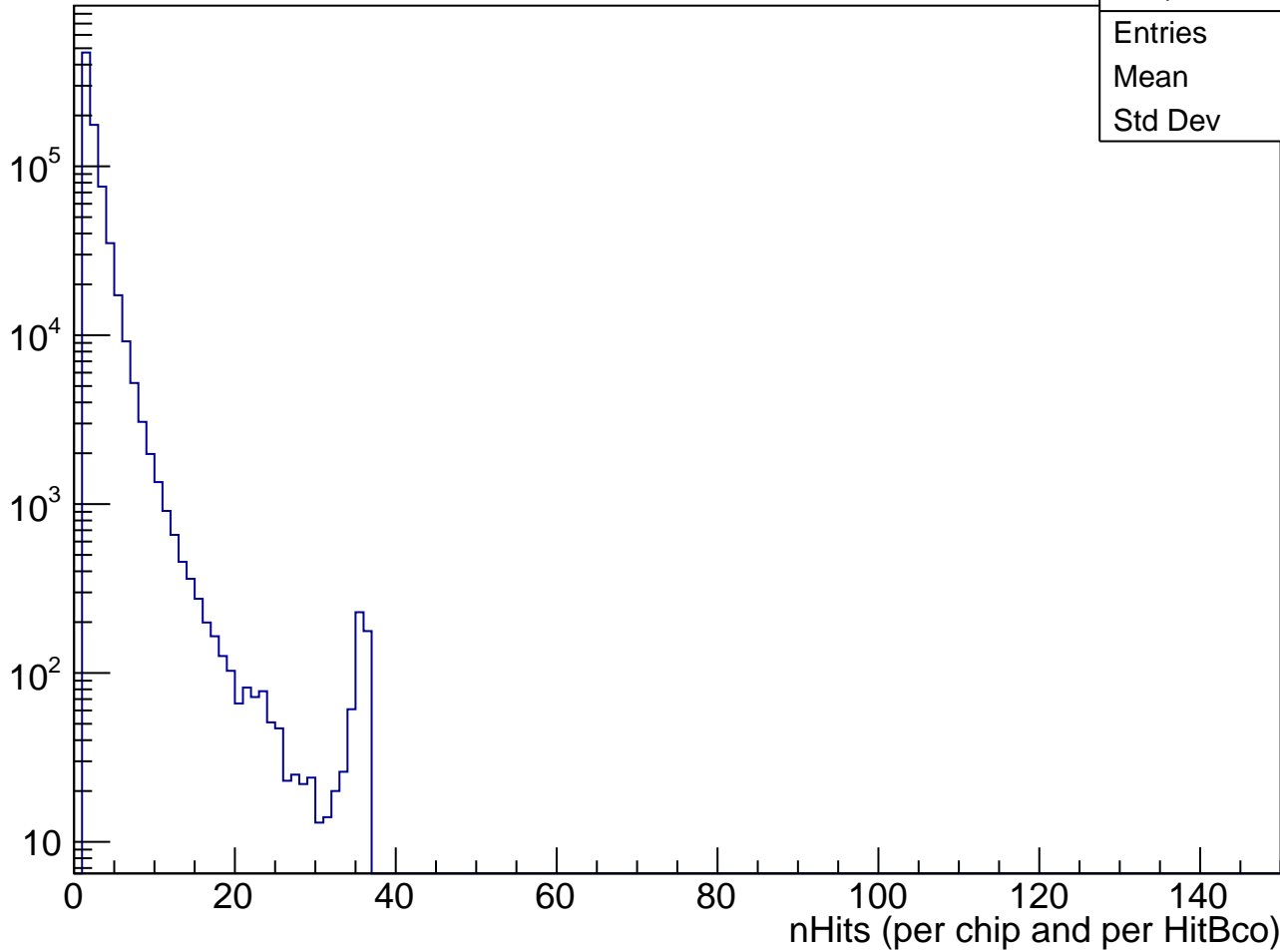
Entries



nChipHit_F0_Fch6_Chip4	
Entries	845540
Mean	1.959
Std Dev	1.964

nChipHit_F0_Fch6_Chip5

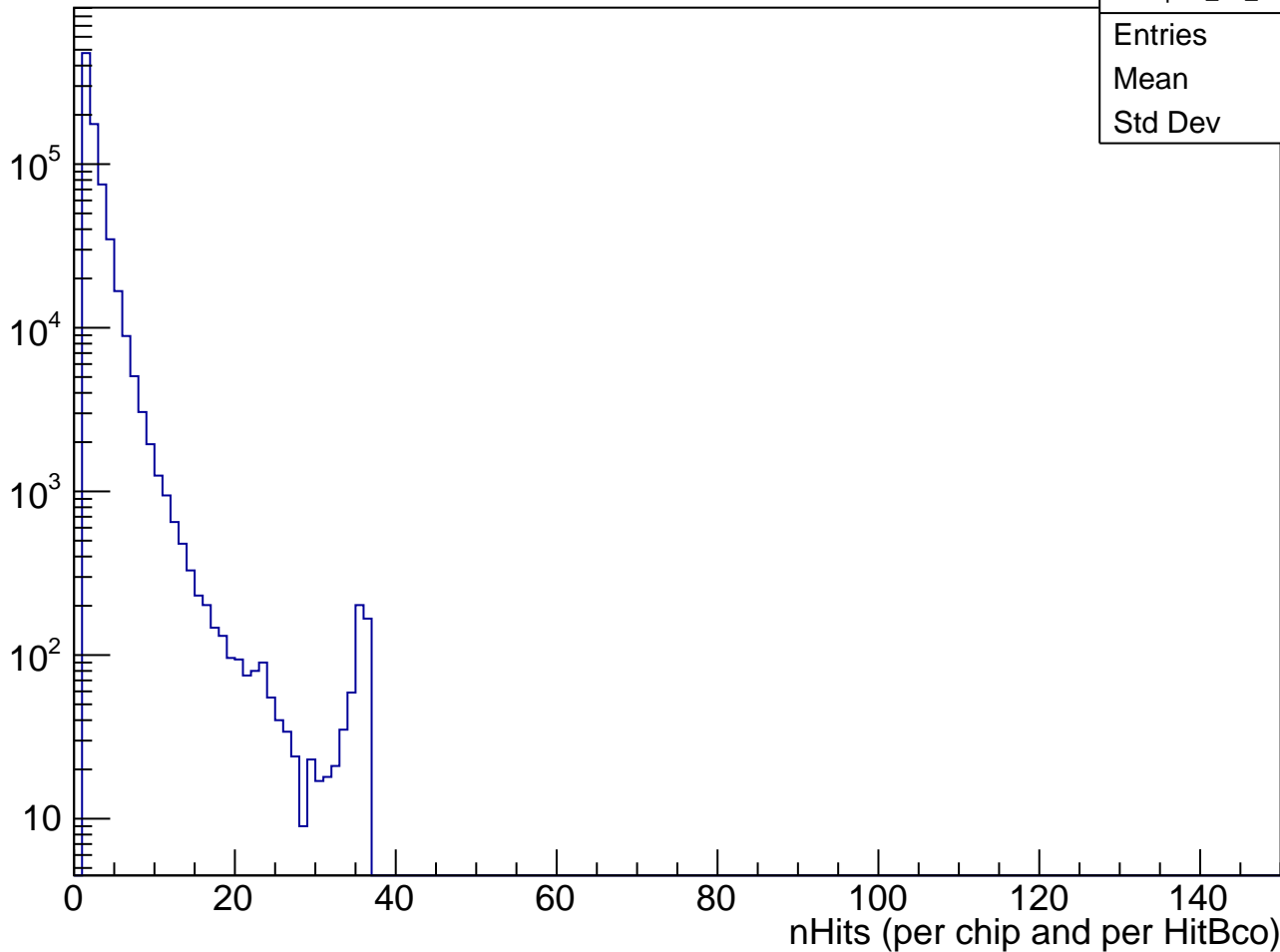
Entries



nChipHit_F0_Fch6_Chip5	
Entries	800985
Mean	1.871
Std Dev	1.815

nChipHit_F0_Fch6_Chip6

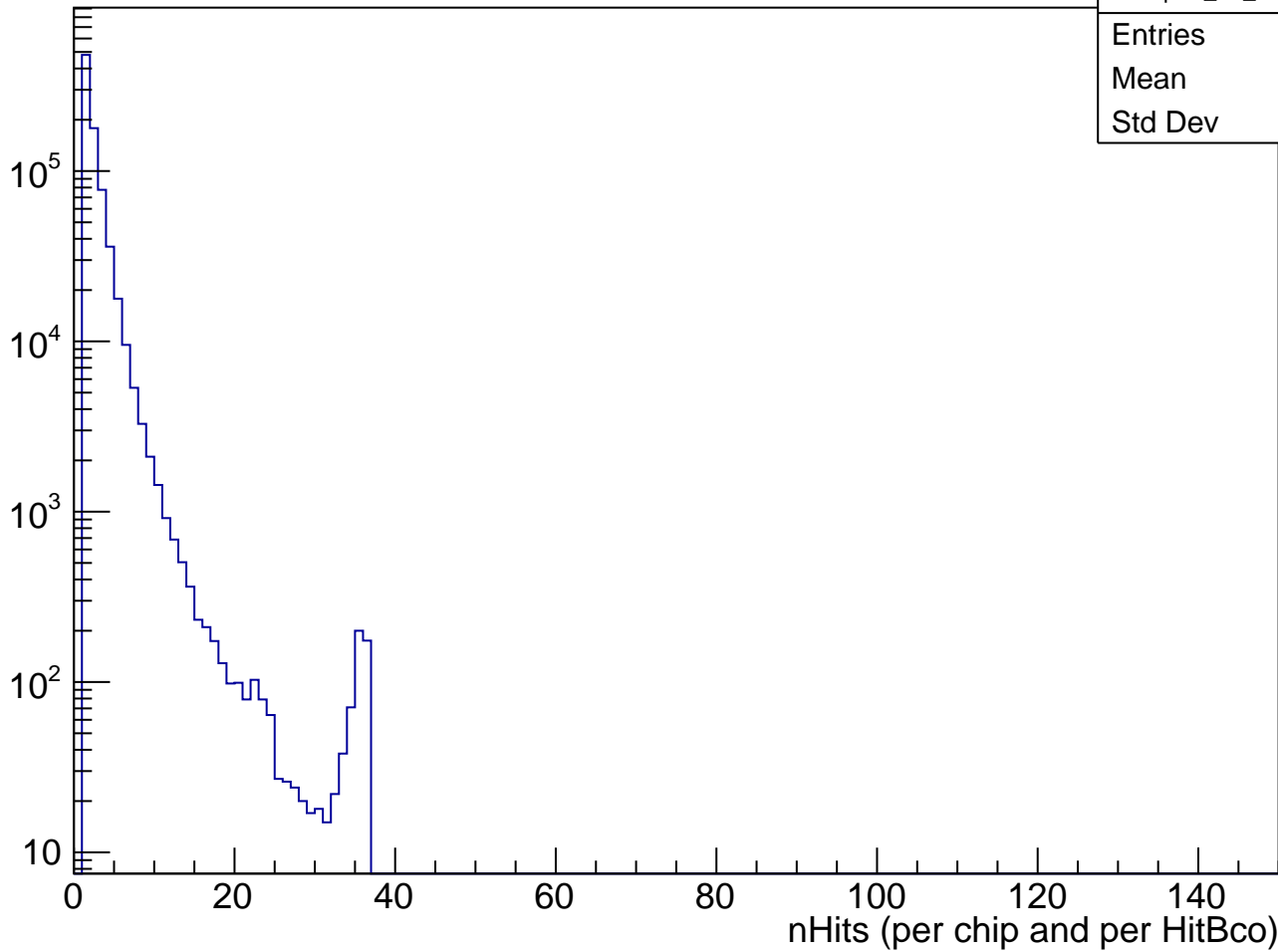
Entries



nChipHit_F0_Fch6_Chip6	
Entries	803929
Mean	1.856
Std Dev	1.794

nChipHit_F0_Fch6_Chip7

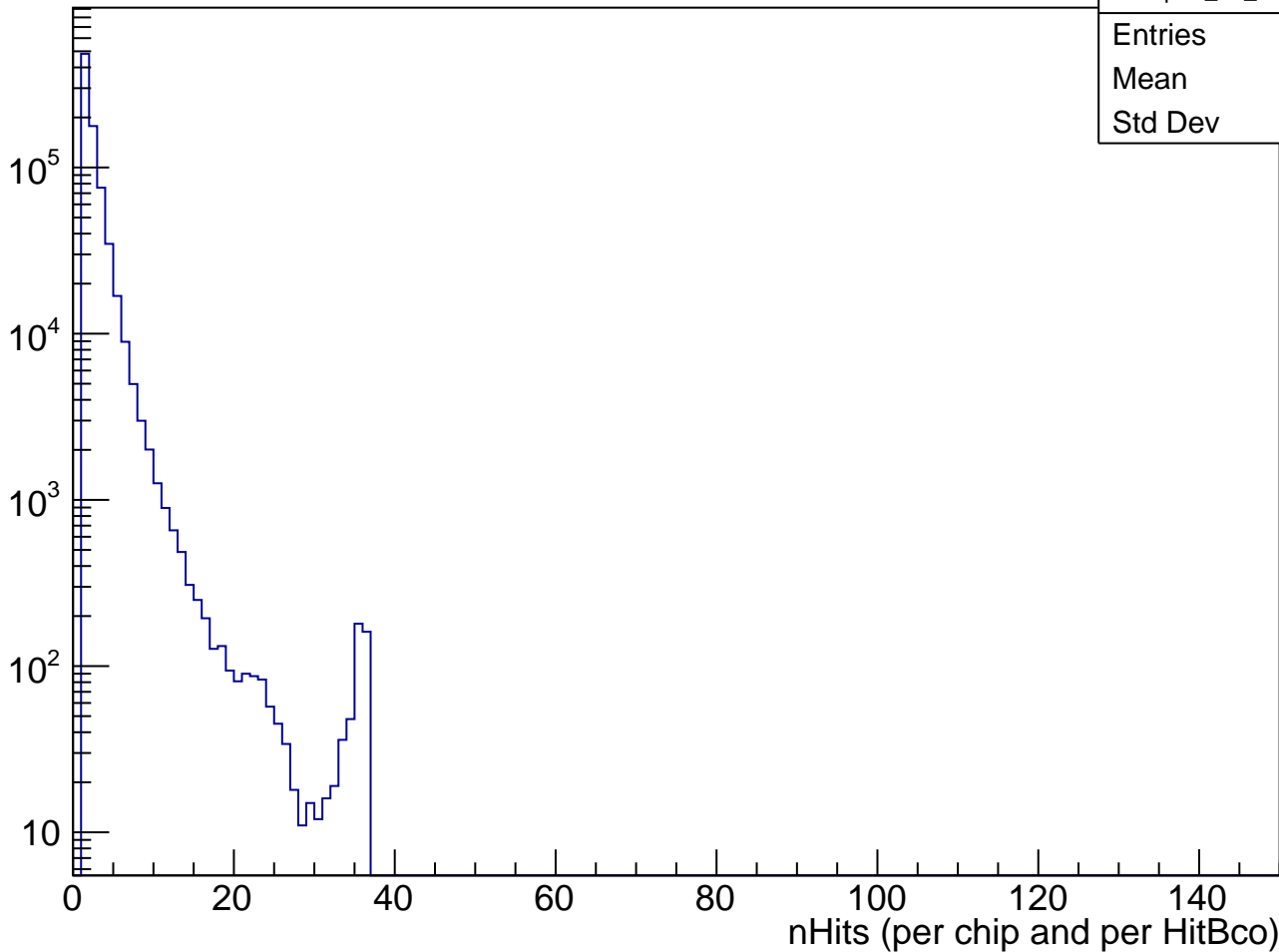
Entries



nChipHit_F0_Fch6_Chip7	
Entries	816558
Mean	1.877
Std Dev	1.818

nChipHit_F0_Fch6_Chip8

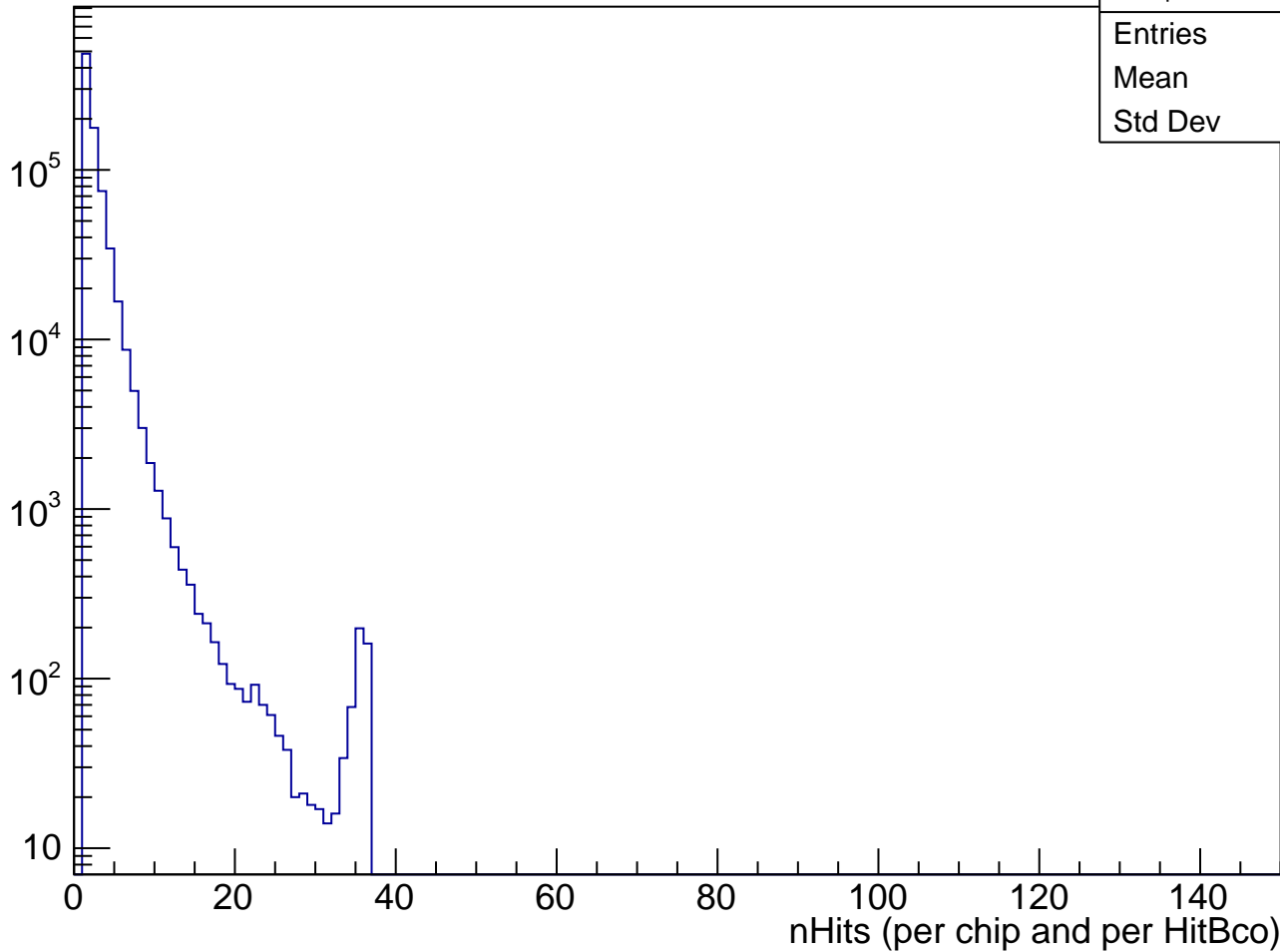
Entries



nChipHit_F0_Fch6_Chip8	
Entries	812075
Mean	1.848
Std Dev	1.766

nChipHit_F0_Fch6_Chip9

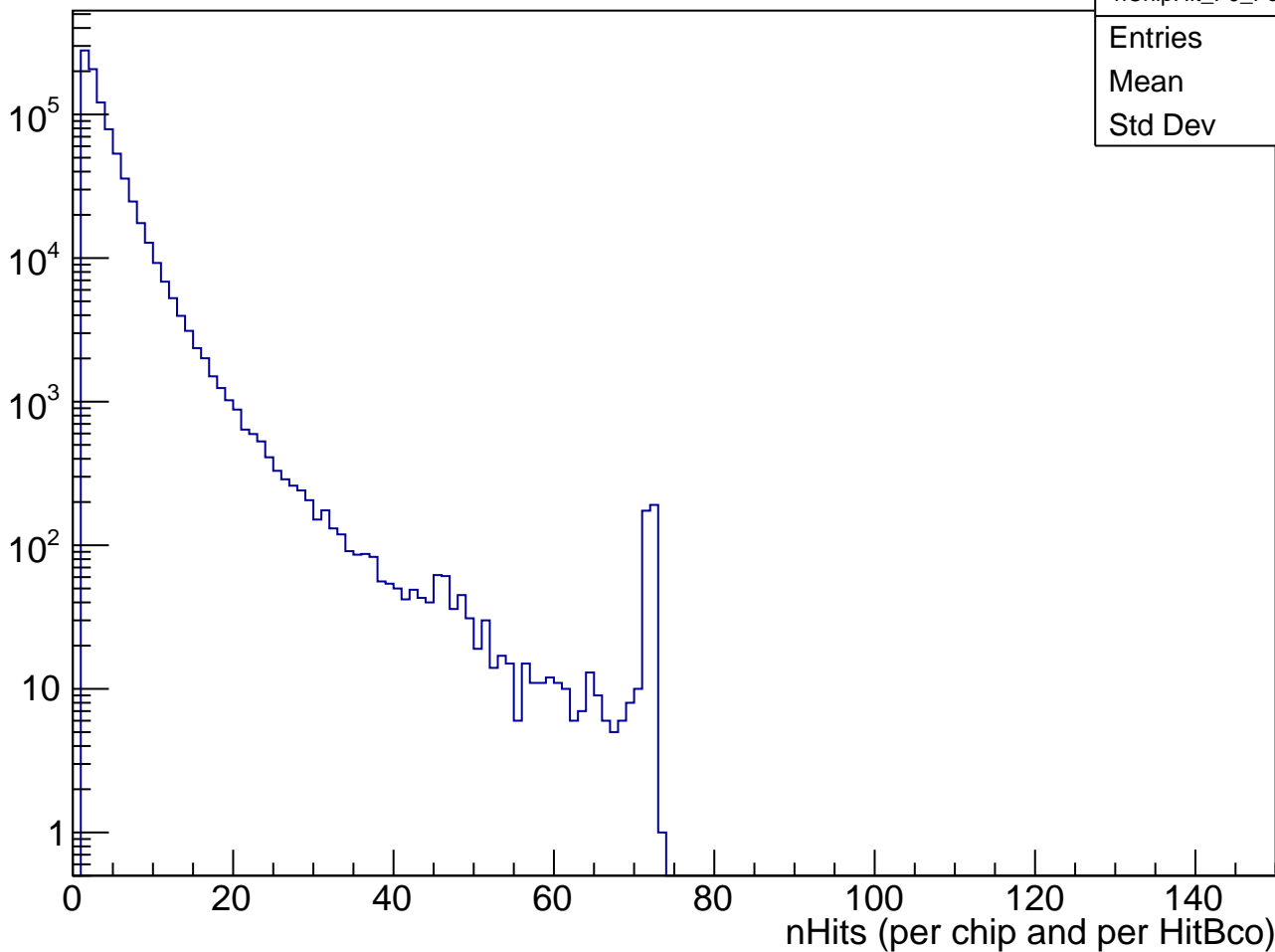
Entries



nChipHit_F0_Fch6_Chip9	
Entries	811594
Mean	1.844
Std Dev	1.779

nChipHit_F0_Fch7_Chip0

Entries



nChipHit_F0_Fch7_Chip0

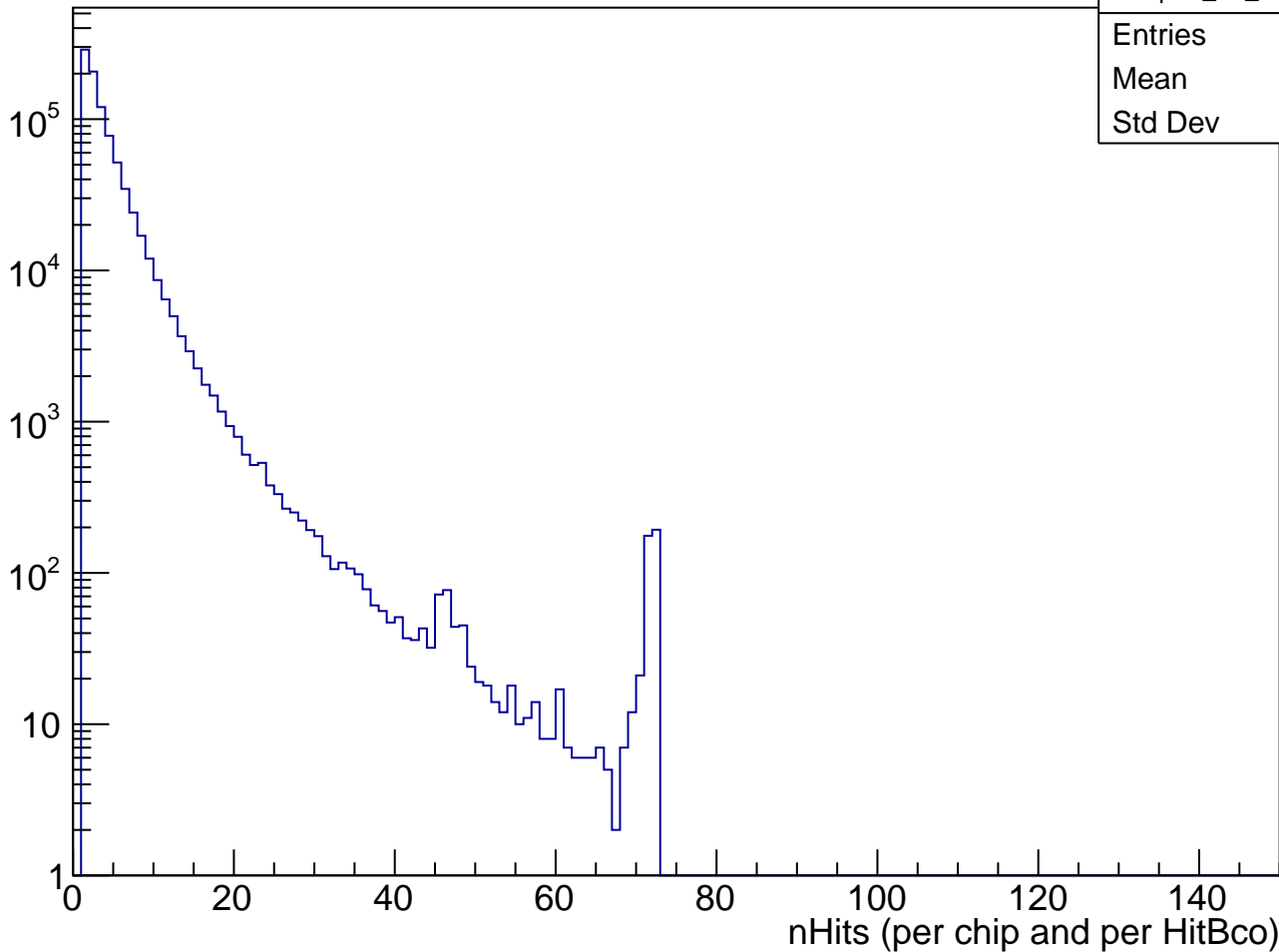
Entries 873213

Mean 3.366

Std Dev 3.821

nChipHit_F0_Fch7_Chip1

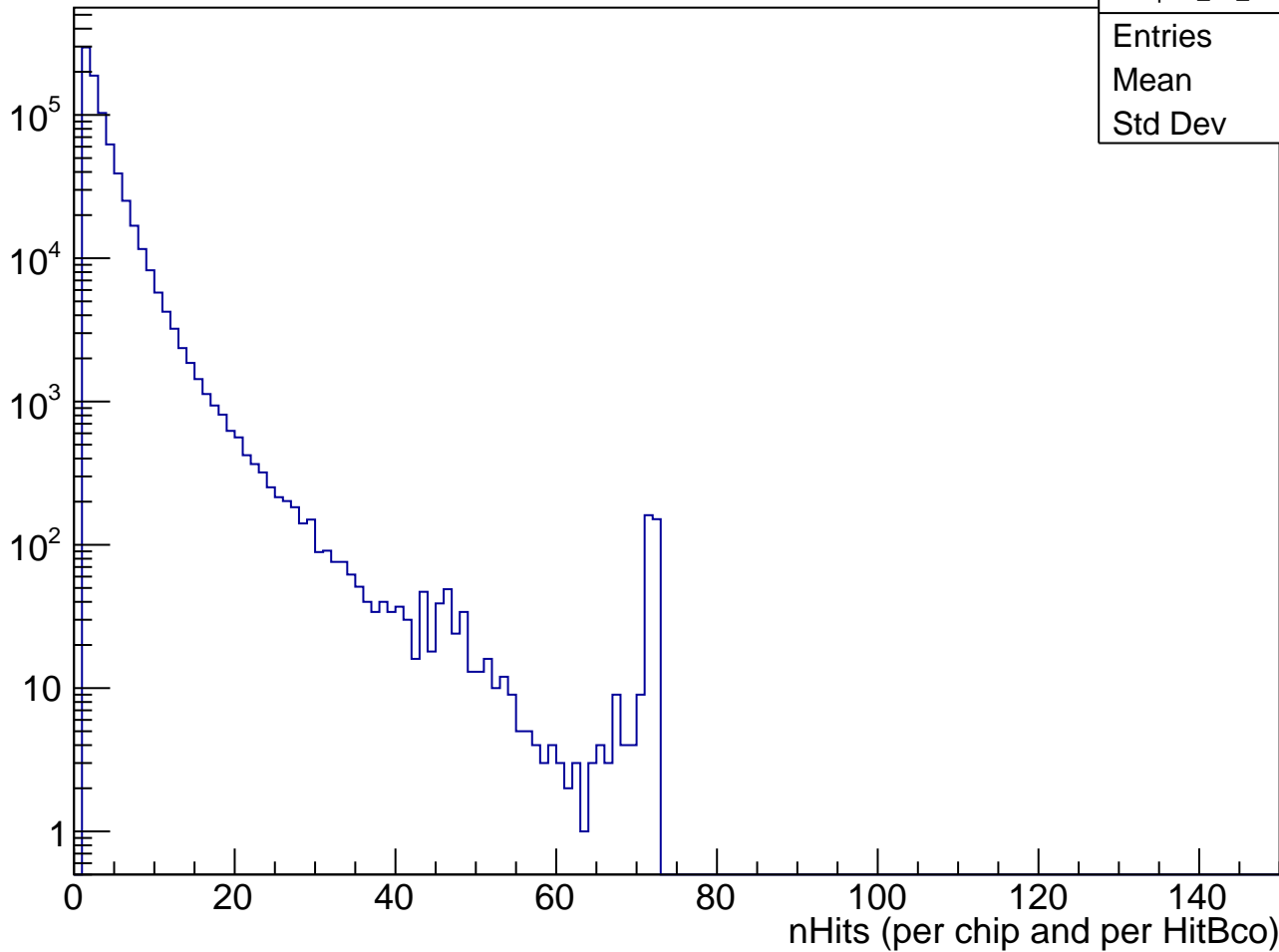
Entries



nChipHit_F0_Fch7_Chip1	
Entries	872090
Mean	3.29
Std Dev	3.765

nChipHit_F0_Fch7_Chip10

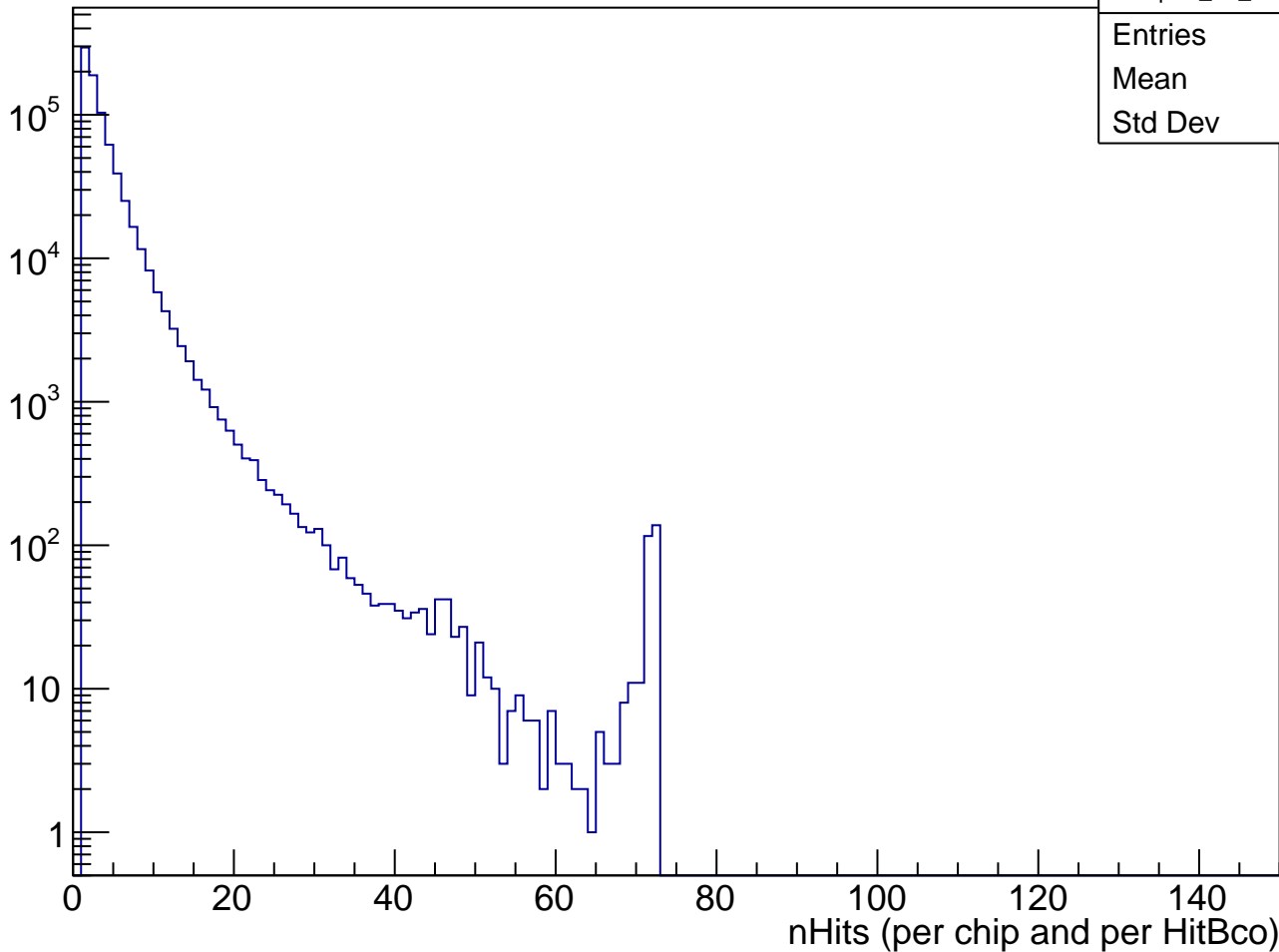
Entries



nChipHit_F0_Fch7_Chip10	
Entries	777041
Mean	2.924
Std Dev	3.398

nChipHit_F0_Fch7_Chip11

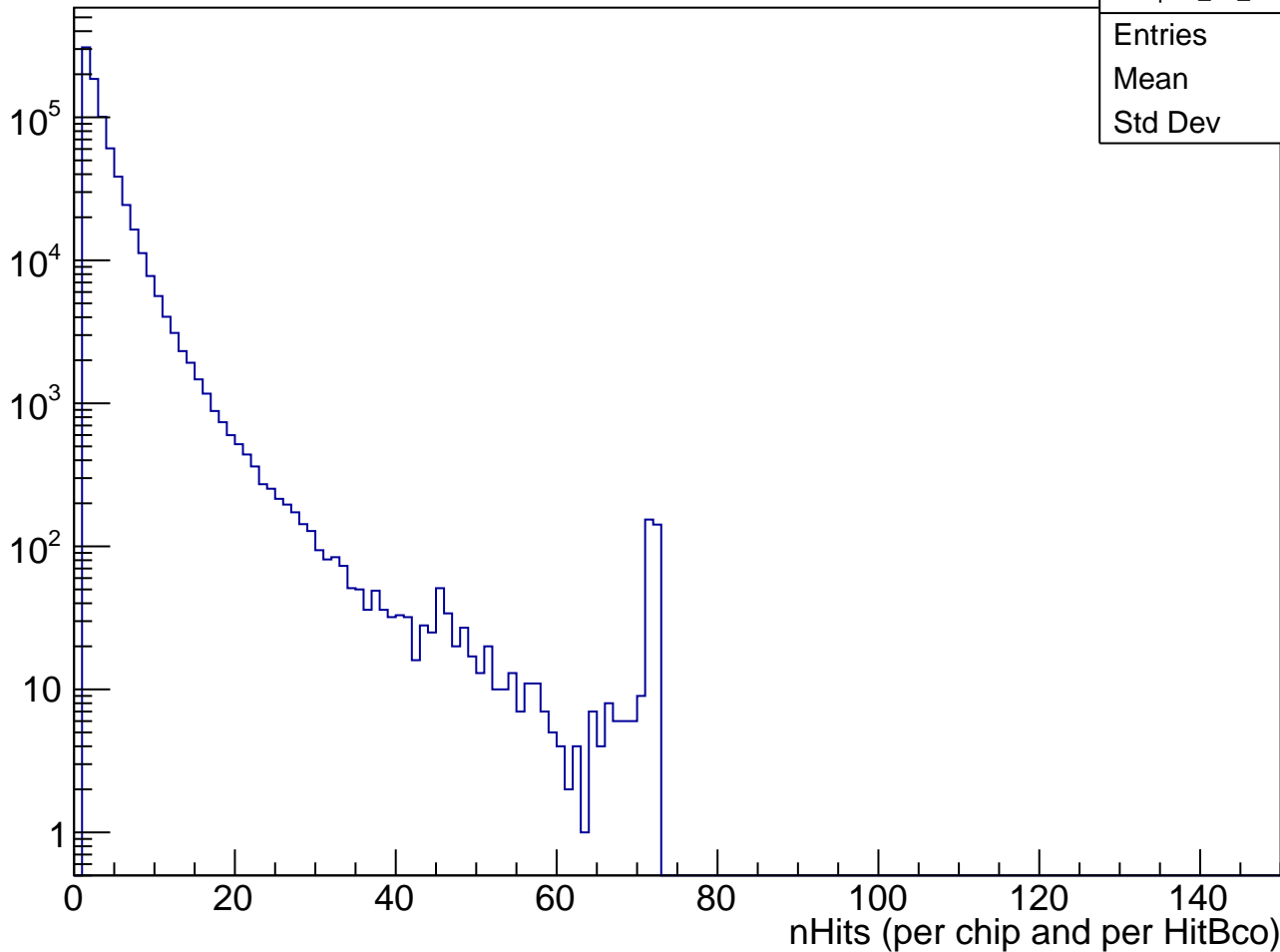
Entries



nChipHit_F0_Fch7_Chip11	
Entries	775718
Mean	2.921
Std Dev	3.353

nChipHit_F0_Fch7_Chip12

Entries

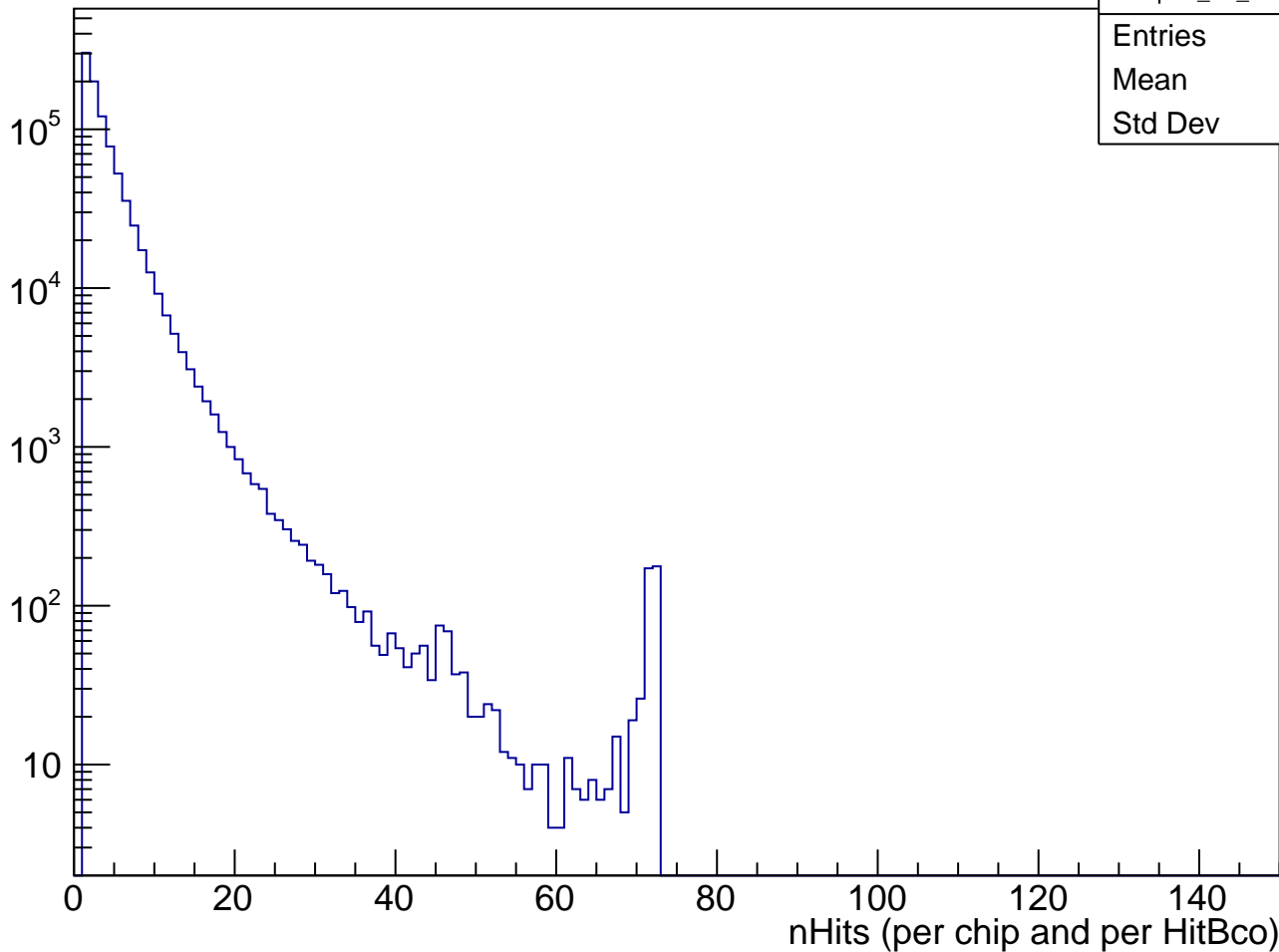


nChipHit_F0_Fch7_Chip12	
Entries	779602
Mean	2.872
Std Dev	3.373

nHits (per chip and per HitBco)

nChipHit_F0_Fch7_Chip13

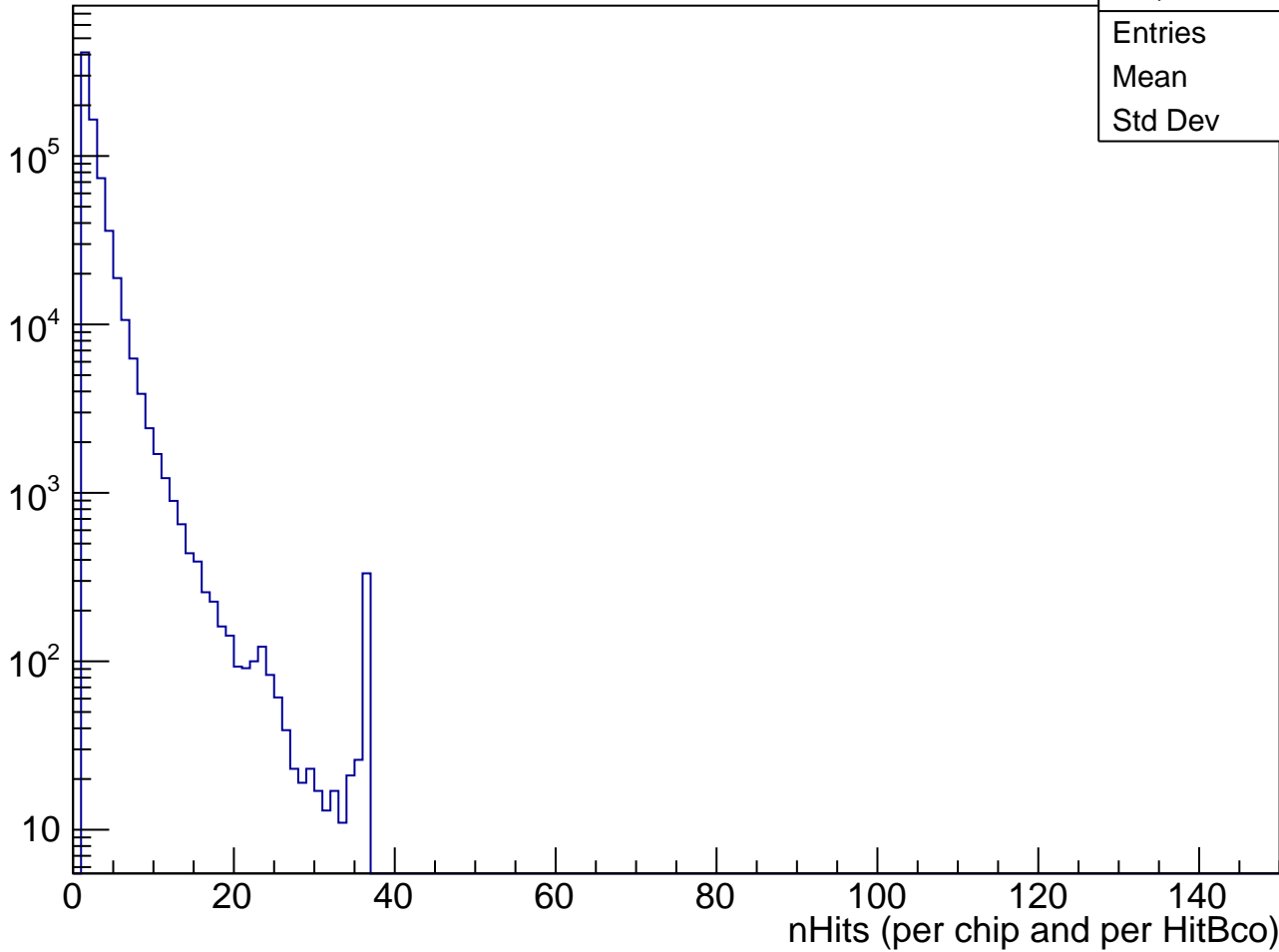
Entries



nChipHit_F0_Fch7_Chip13	
Entries	887764
Mean	3.303
Std Dev	3.799

nChipHit_F0_Fch7_Chip14

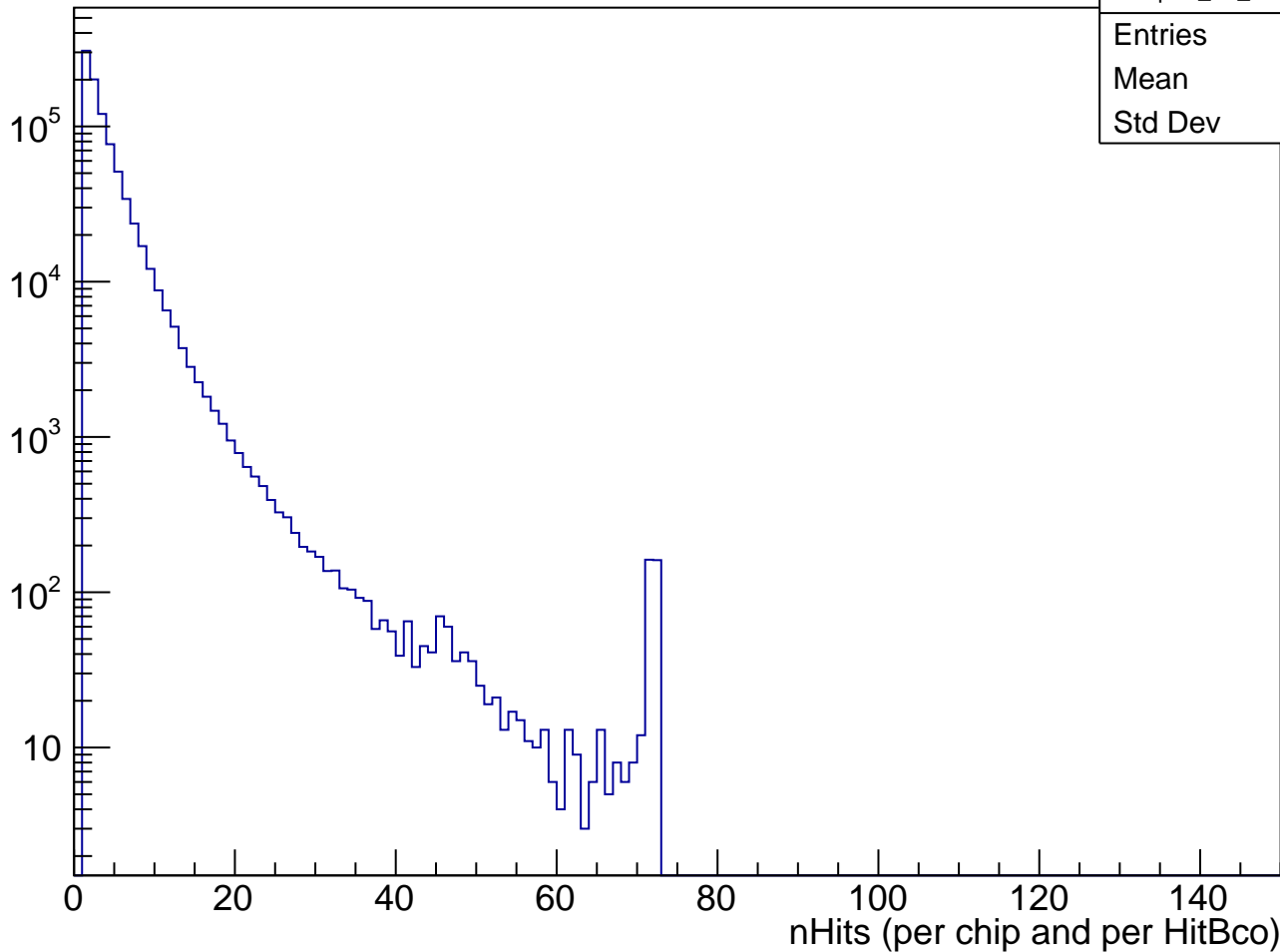
Entries



nChipHit_F0_Fch7_Chip14	
Entries	735804
Mean	1.994
Std Dev	1.969

nChipHit_F0_Fch7_Chip15

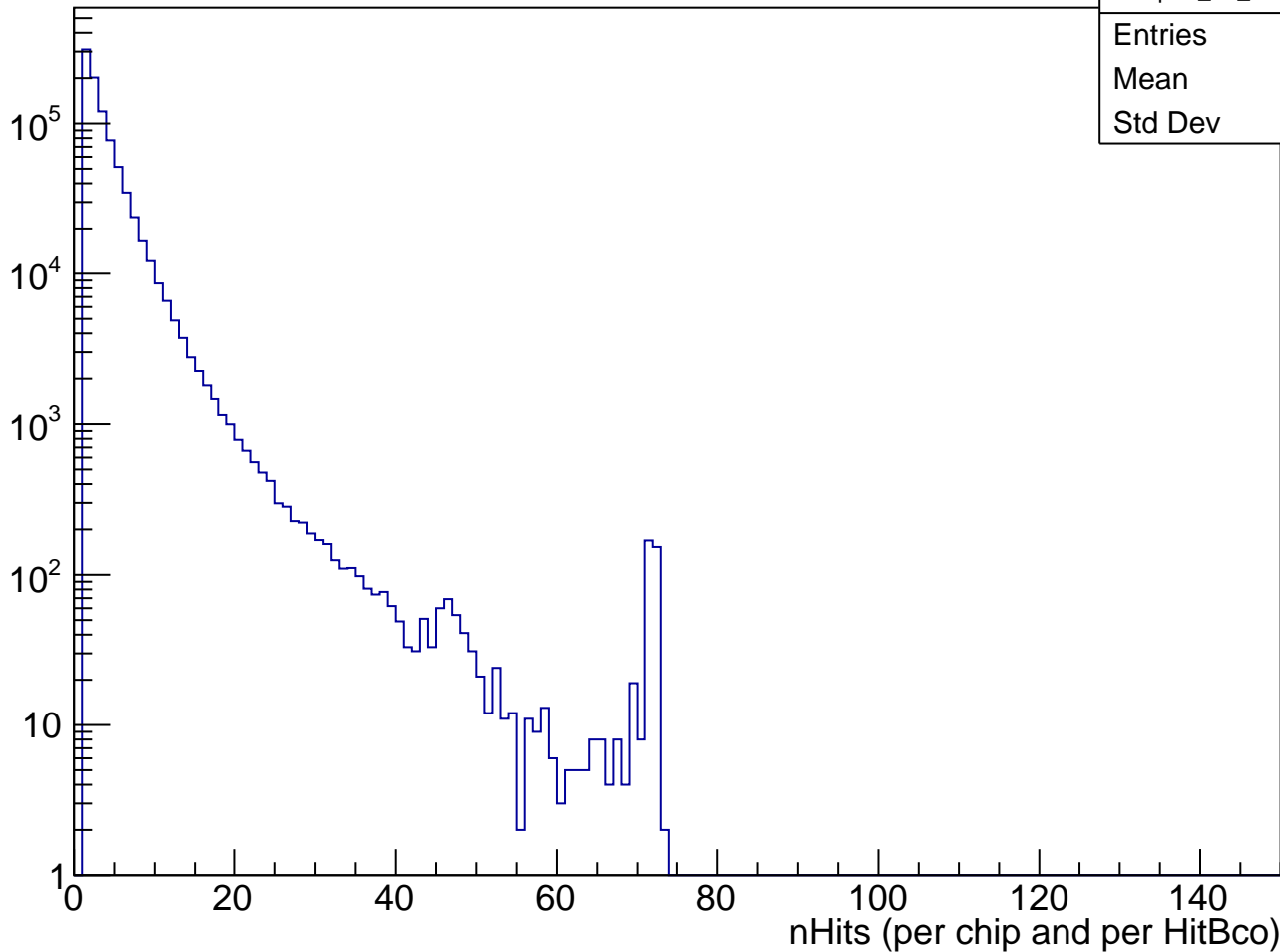
Entries



nChipHit_F0_Fch7_Chip15	
Entries	884003
Mean	3.249
Std Dev	3.733

nChipHit_F0_Fch7_Chip16

Entries

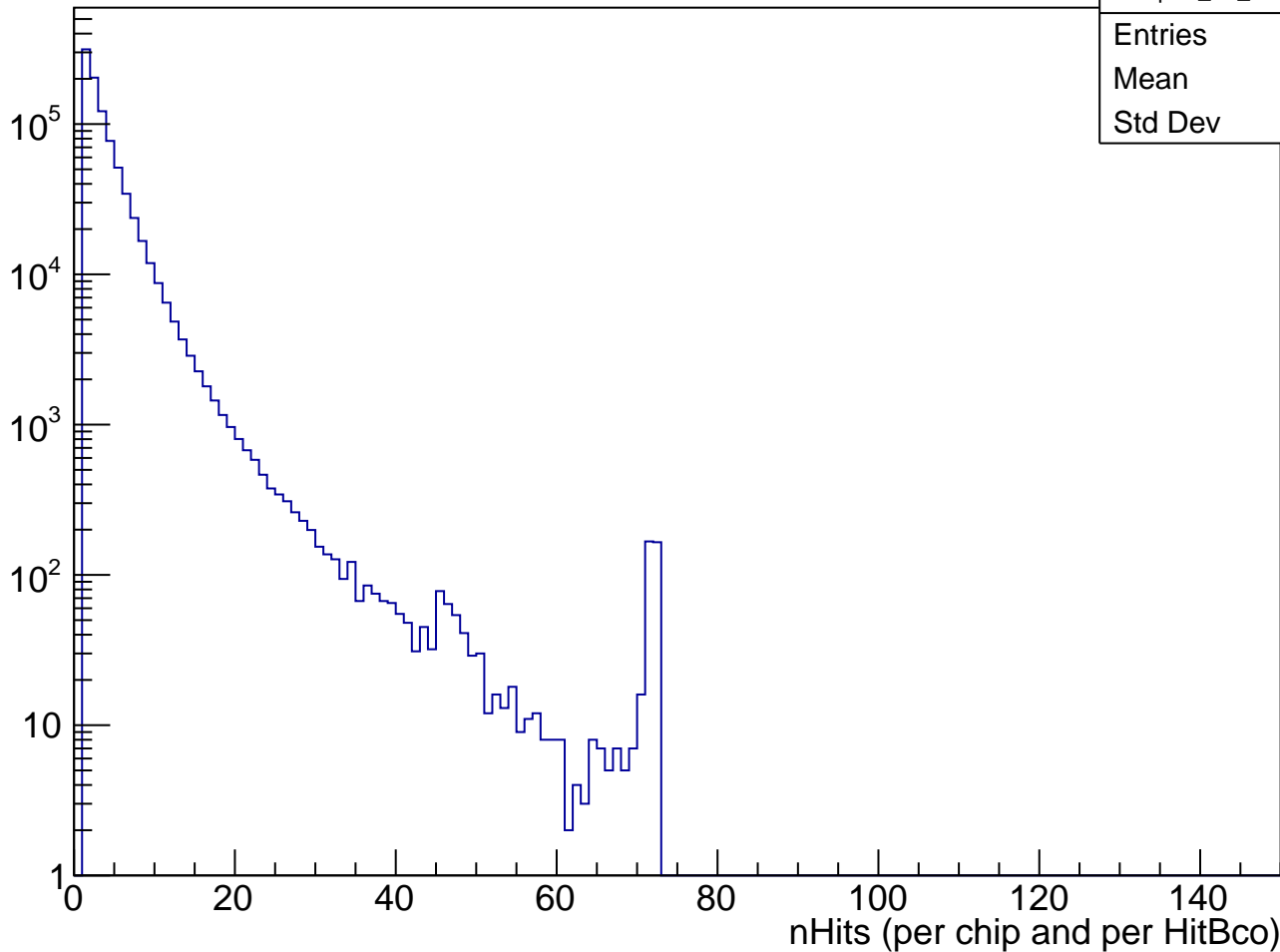


nChipHit_F0_Fch7_Chip16	
Entries	888234
Mean	3.236
Std Dev	3.711

nHits (per chip and per HitBco)

nChipHit_F0_Fch7_Chip17

Entries

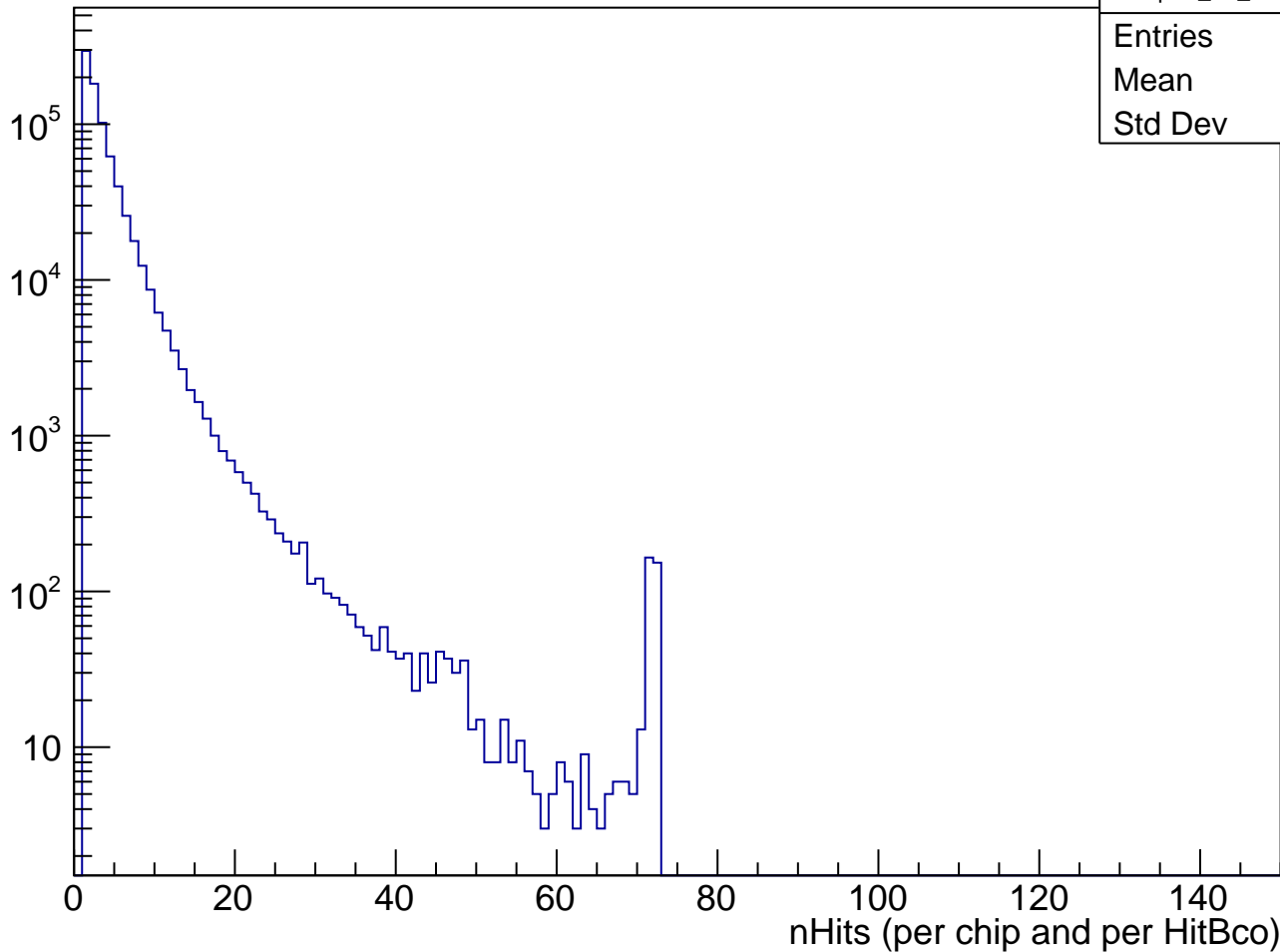


nChipHit_F0_Fch7_Chip17	
Entries	895296
Mean	3.223
Std Dev	3.712

nHits (per chip and per HitBco)

nChipHit_F0_Fch7_Chip18

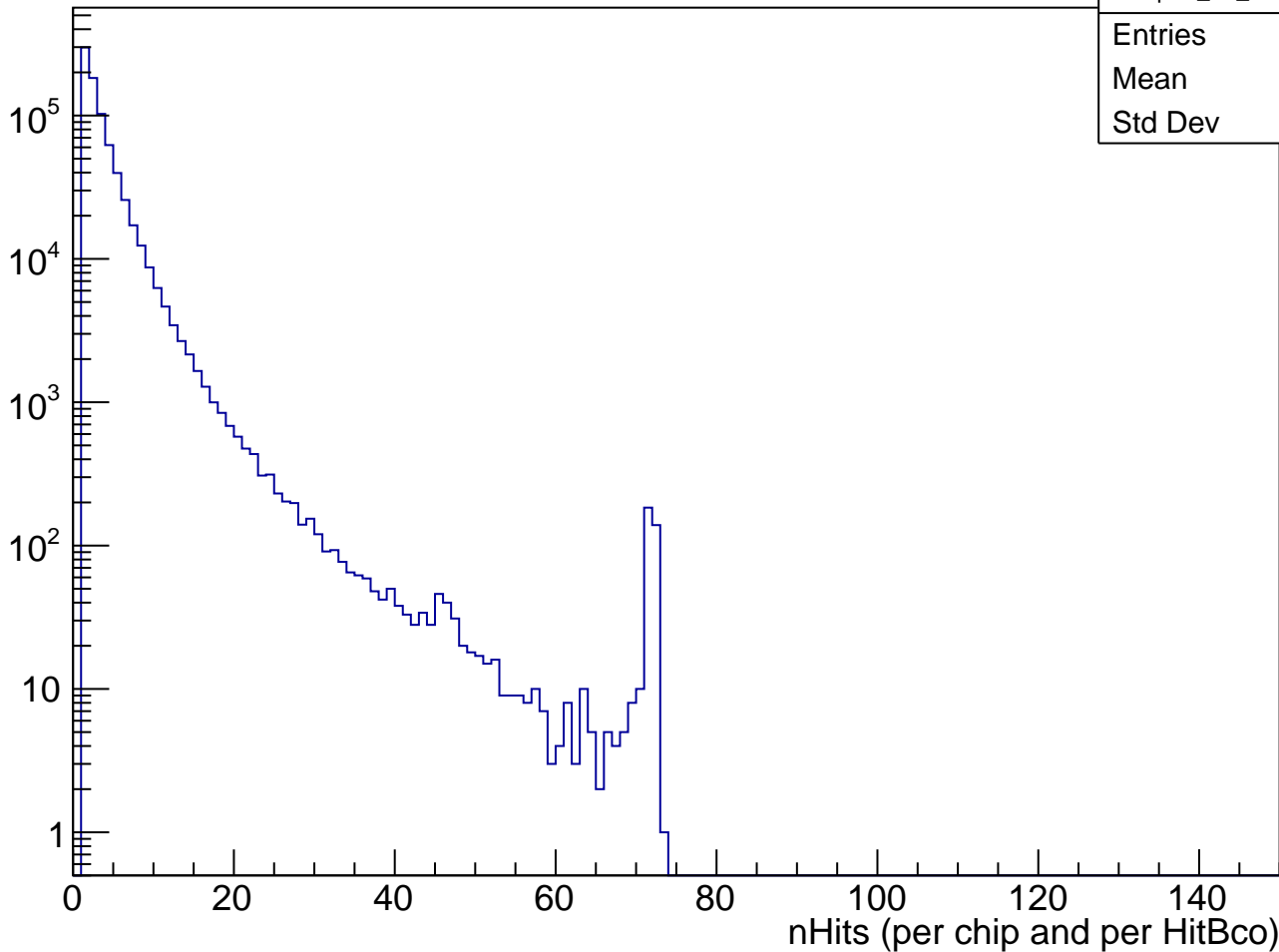
Entries



nChipHit_F0_Fch7_Chip18	
Entries	775711
Mean	2.99
Std Dev	3.517

nChipHit_F0_Fch7_Chip19

Entries

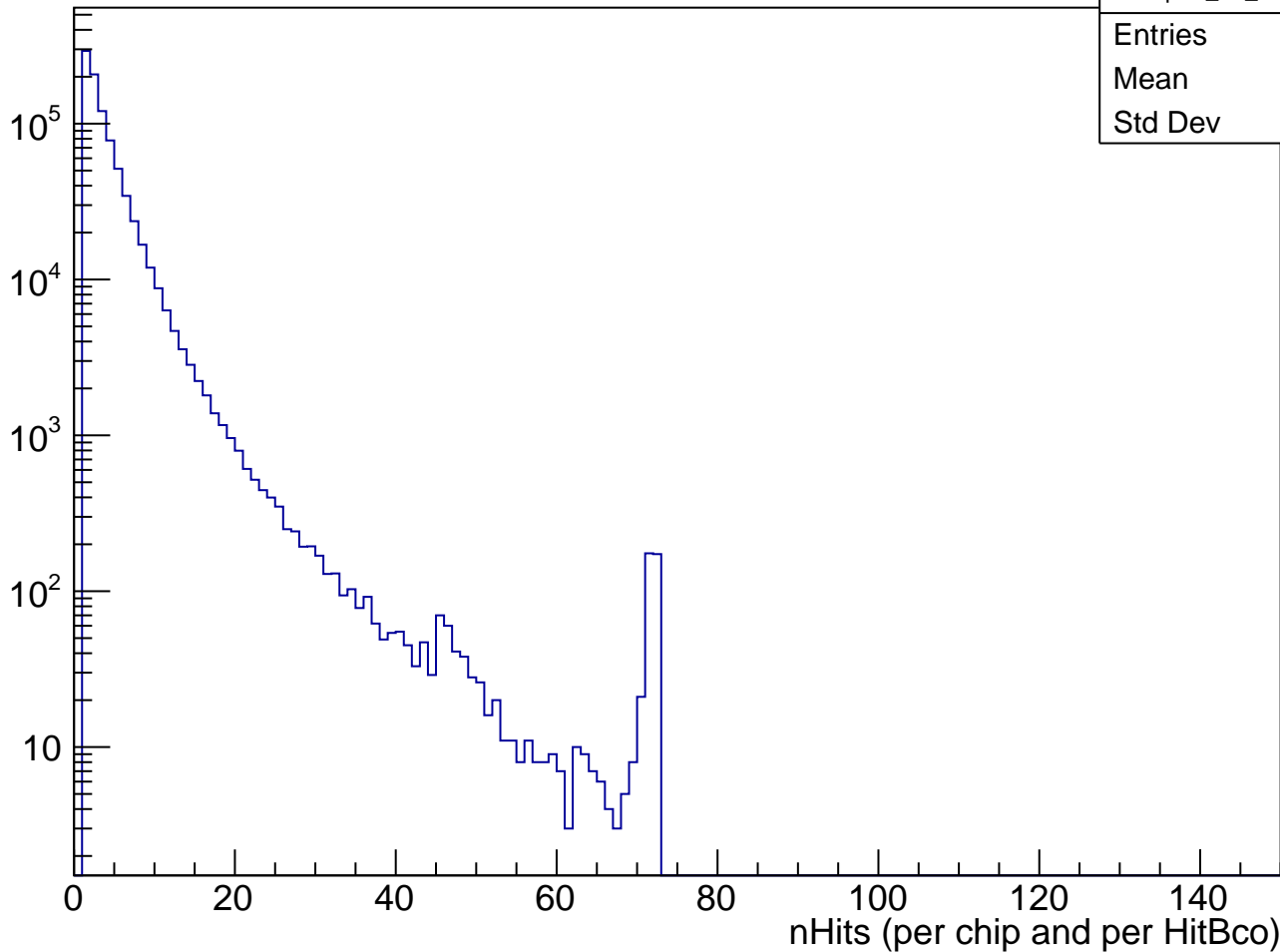


nChipHit_F0_Fch7_Chip19	
Entries	779089
Mean	2.982
Std Dev	3.52

nHits (per chip and per HitBco)

nChipHit_F0_Fch7_Chip2

Entries



nChipHit_F0_Fch7_Chip2

Entries 876274

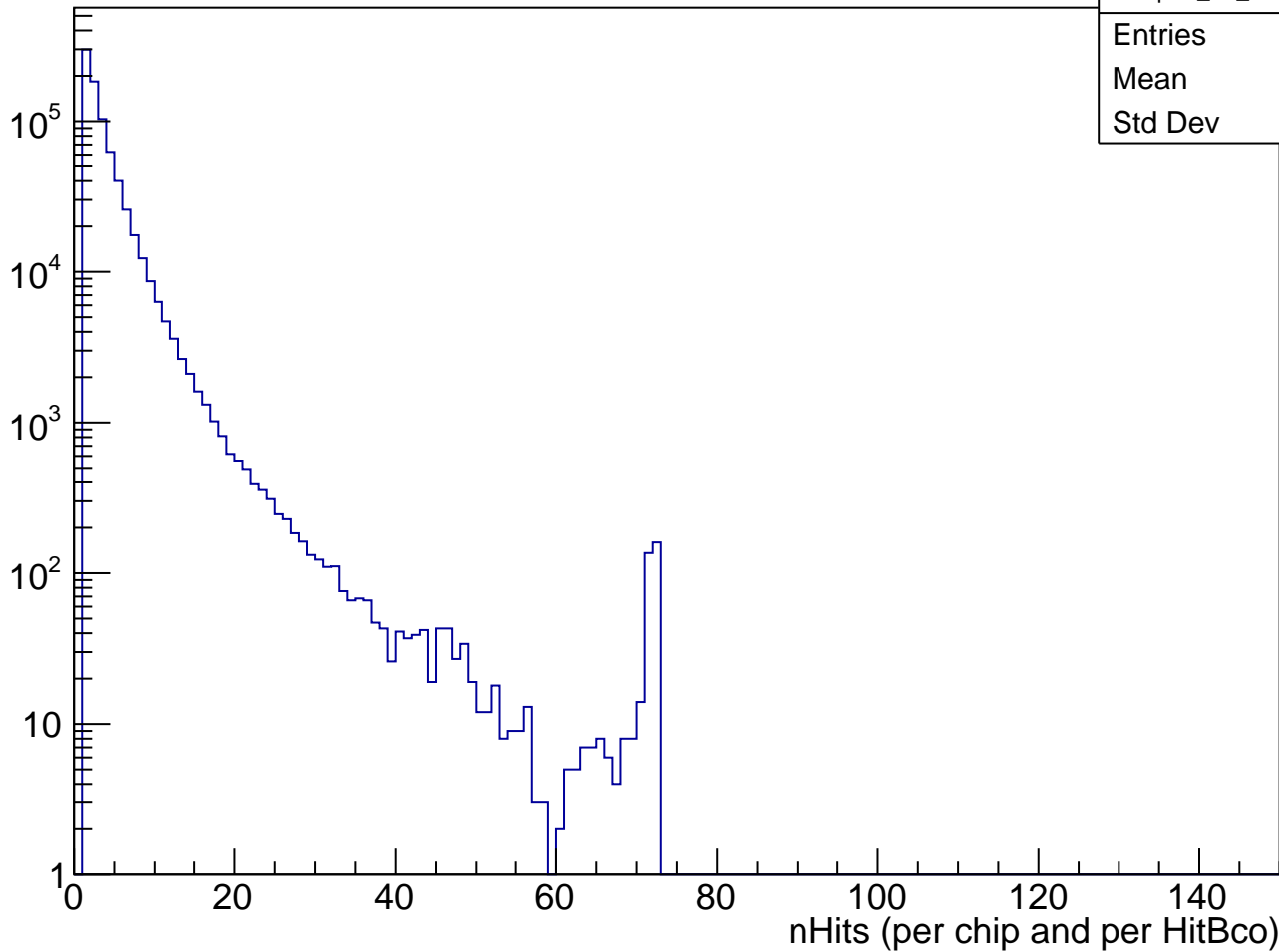
Mean 3.261

Std Dev 3.711

nHits (per chip and per HitBco)

nChipHit_F0_Fch7_Chip20

Entries

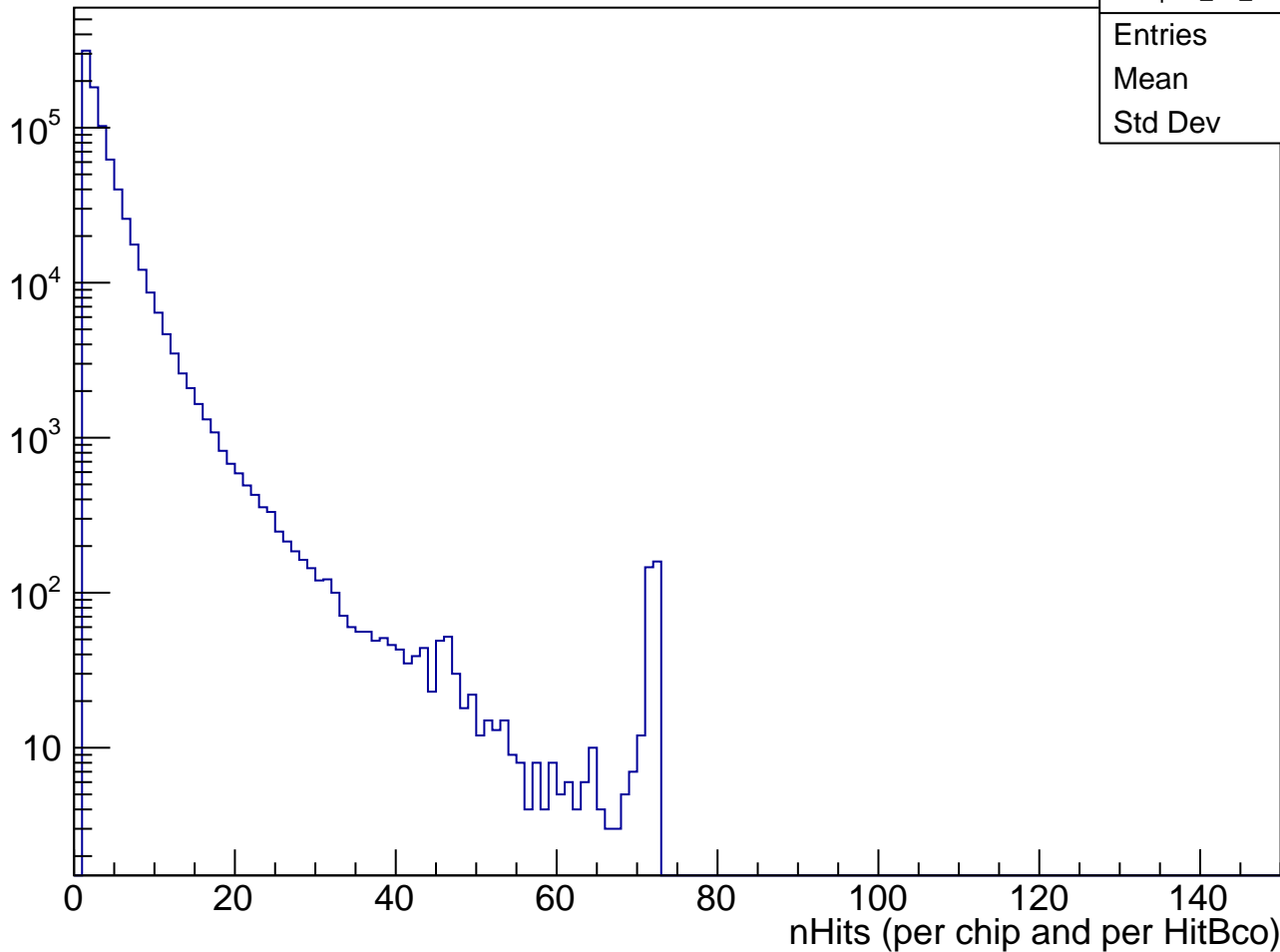


nChipHit_F0_Fch7_Chip20	
Entries	781948
Mean	2.983
Std Dev	3.503

nHits (per chip and per HitBco)

nChipHit_F0_Fch7_Chip21

Entries

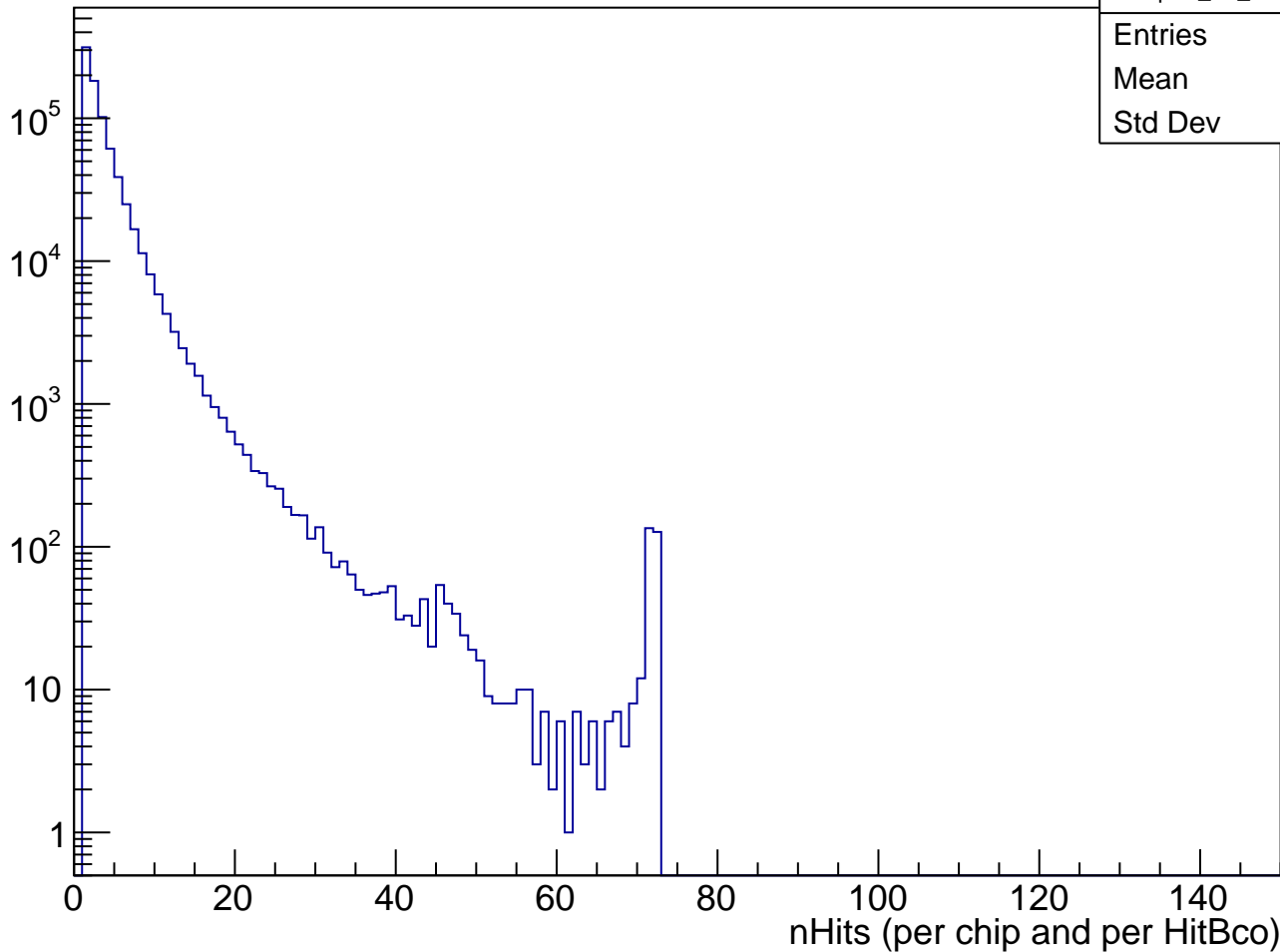


nChipHit_F0_Fch7_Chip21	
Entries	794662
Mean	2.95
Std Dev	3.504

nHits (per chip and per HitBco)

nChipHit_F0_Fch7_Chip22

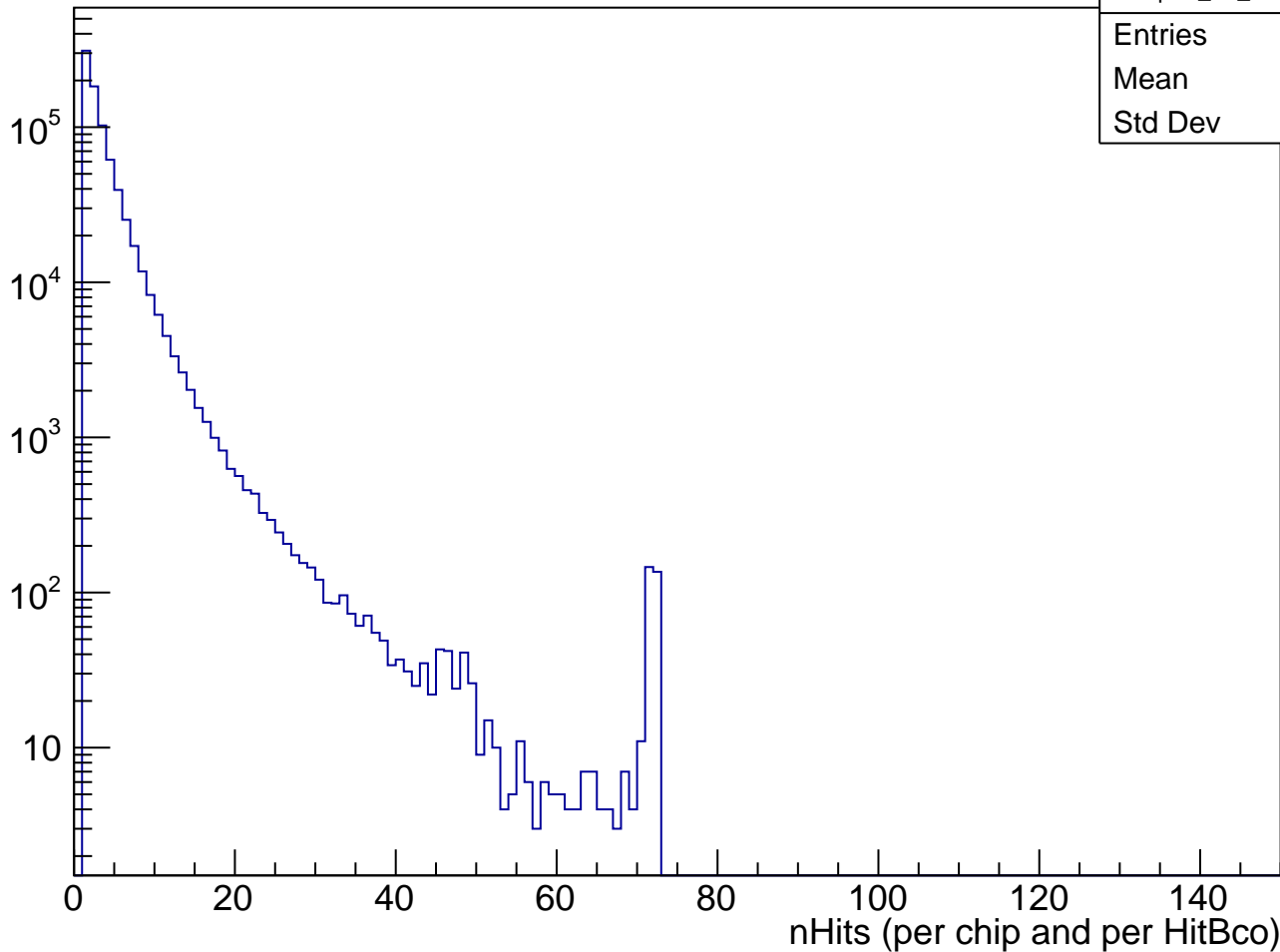
Entries



nChipHit_F0_Fch7_Chip22	
Entries	786846
Mean	2.887
Std Dev	3.384

nChipHit_F0_Fch7_Chip23

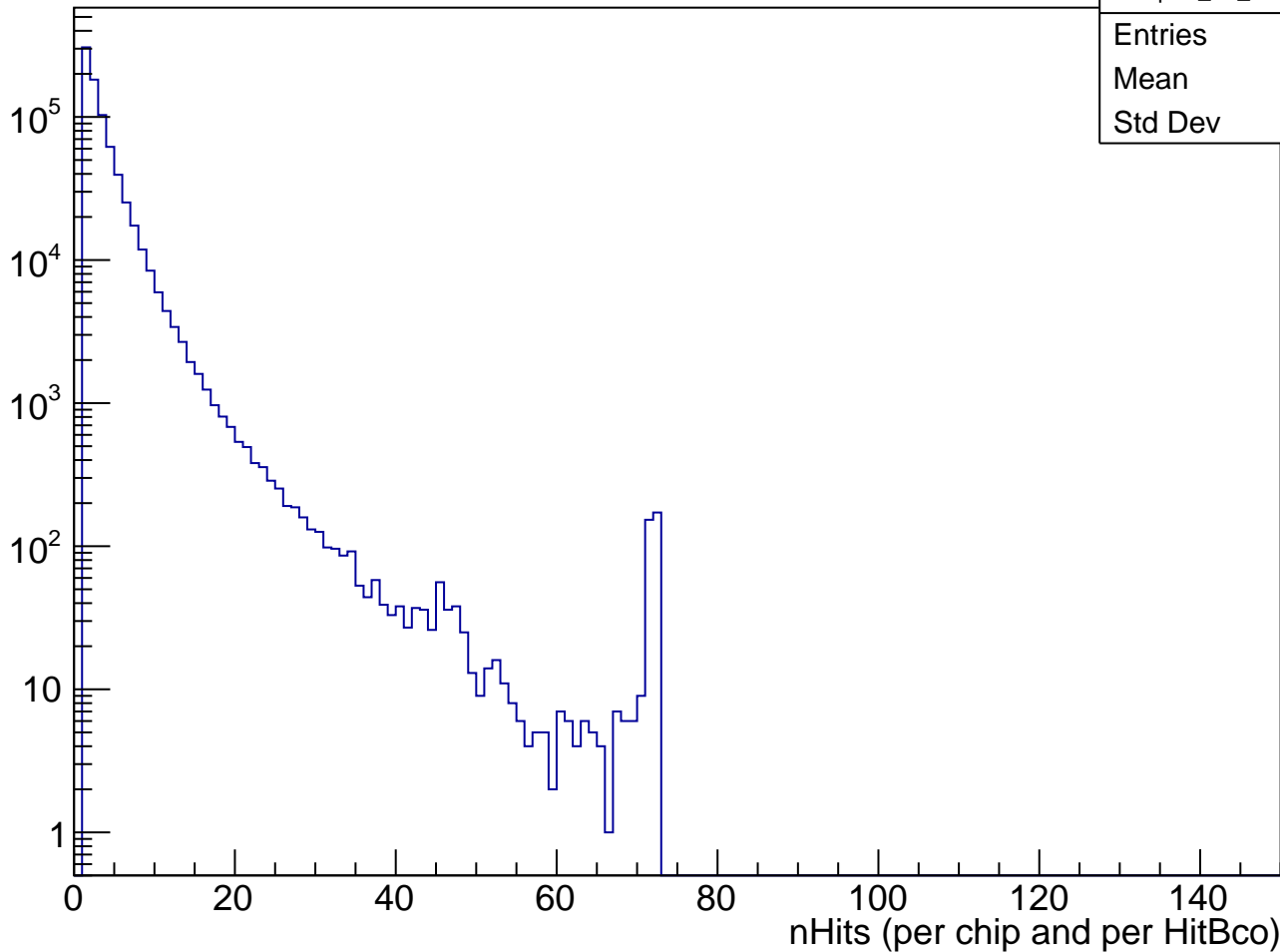
Entries



nChipHit_F0_Fch7_Chip23	
Entries	788739
Mean	2.924
Std Dev	3.44

nChipHit_F0_Fch7_Chip24

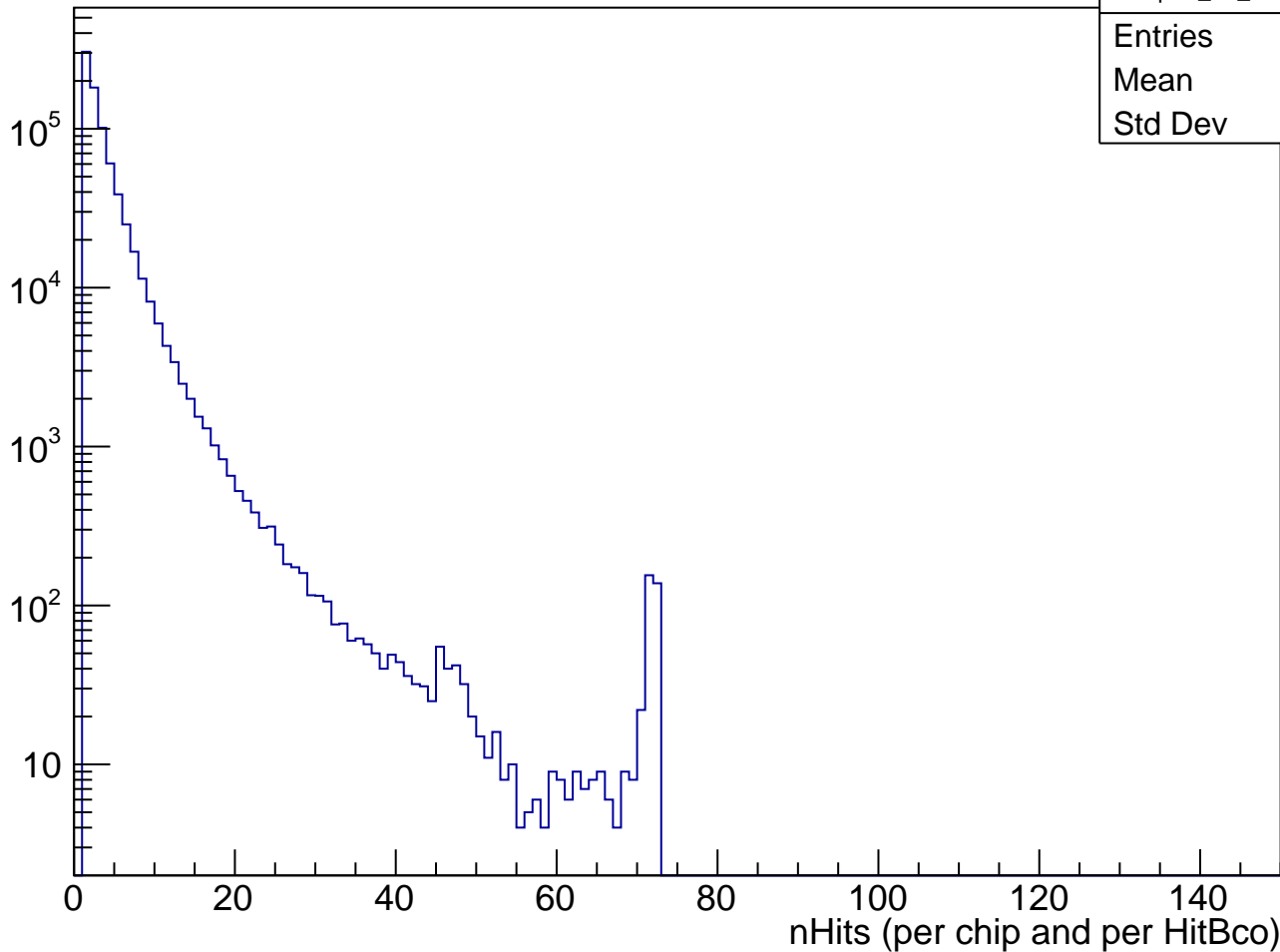
Entries



nChipHit_F0_Fch7_Chip24	
Entries	784242
Mean	2.941
Std Dev	3.483

nChipHit_F0_Fch7_Chip25

Entries

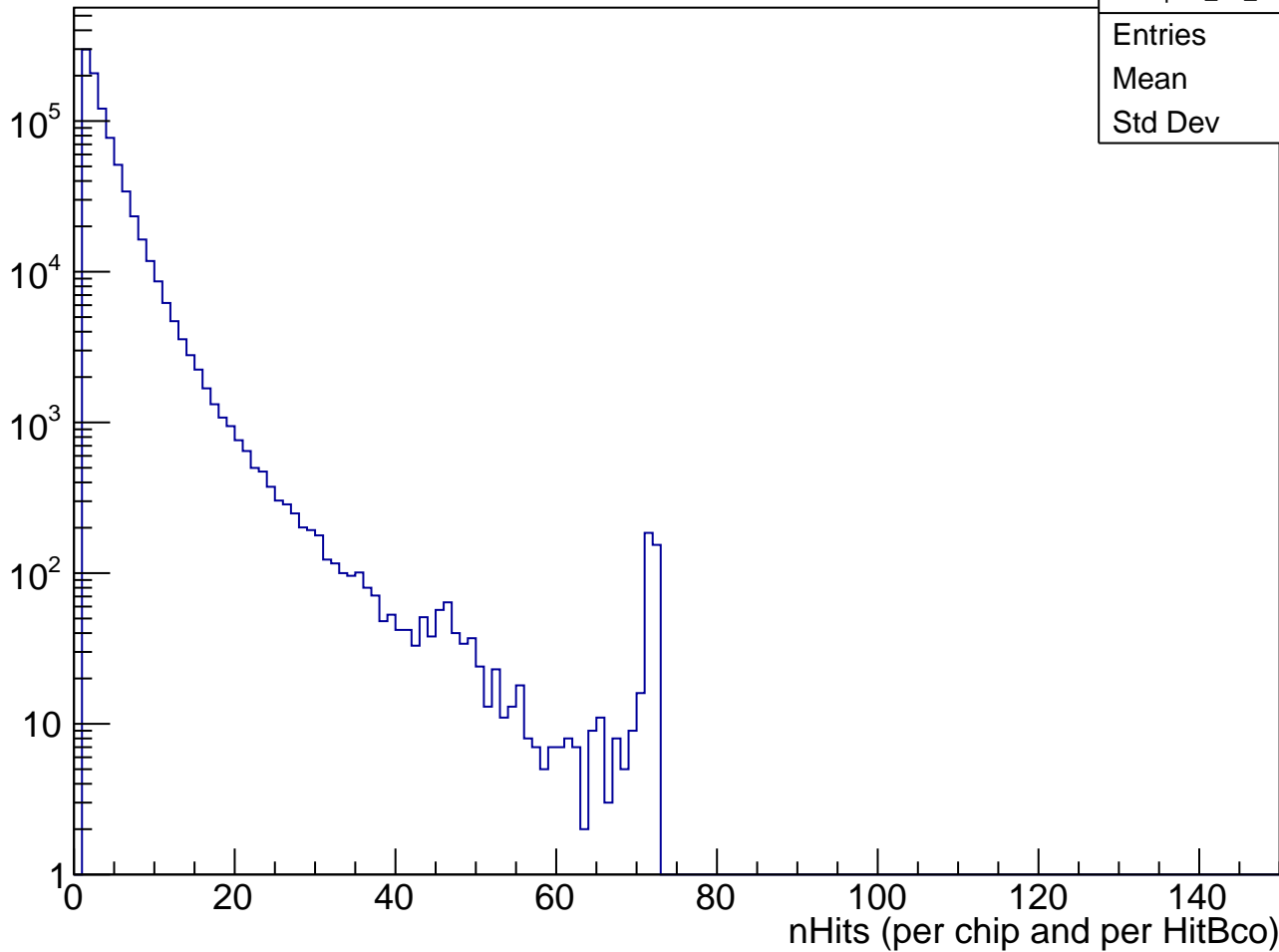


nChipHit_F0_Fch7_Chip25	
Entries	776911
Mean	2.929
Std Dev	3.488

nHits (per chip and per HitBco)

nChipHit_F0_Fch7_Chip3

Entries



nChipHit_F0_Fch7_Chip3

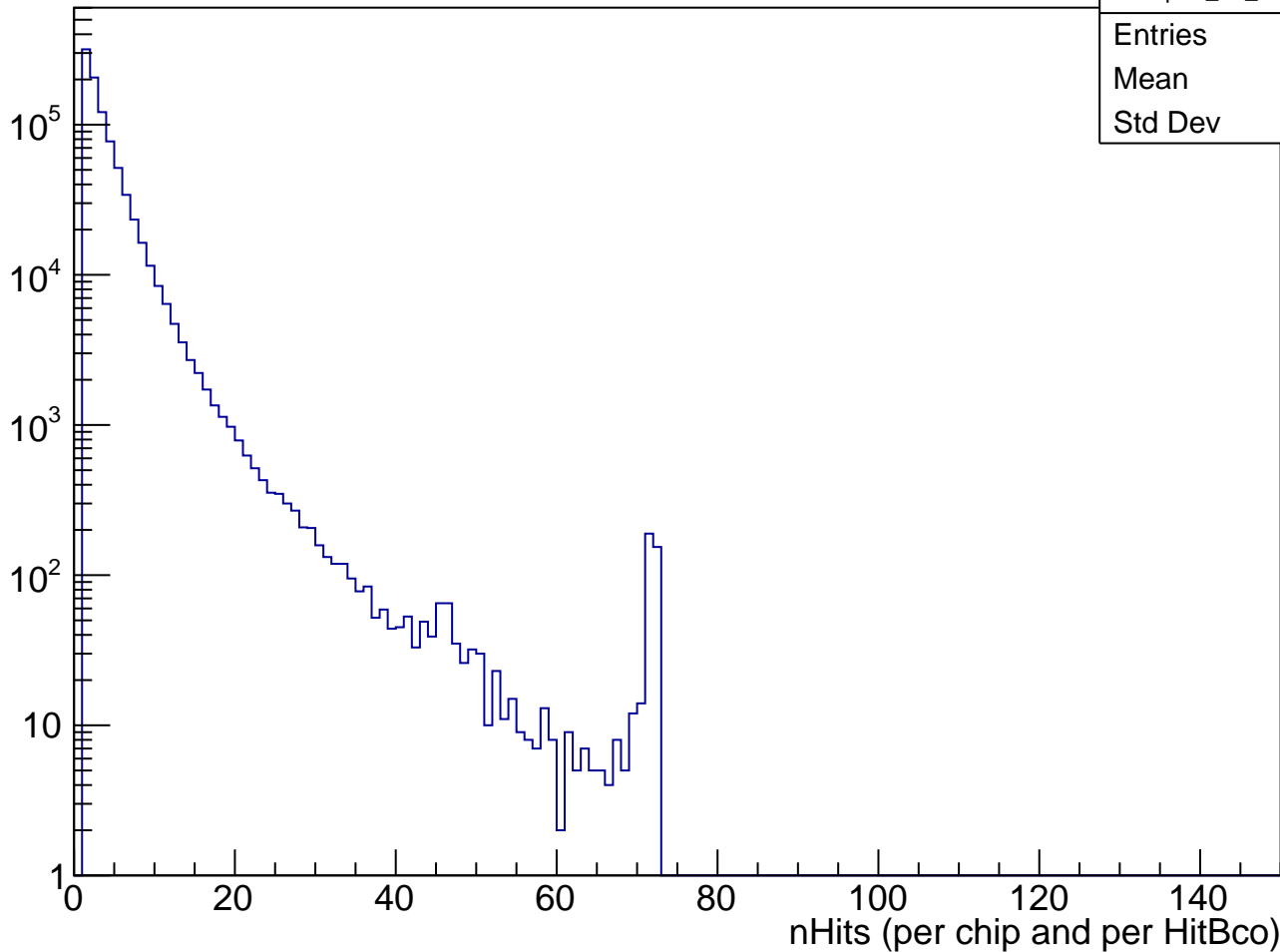
Entries 879969

Mean 3.232

Std Dev 3.688

nChipHit_F0_Fch7_Chip4

Entries



nChipHit_F0_Fch7_Chip4

Entries 898107

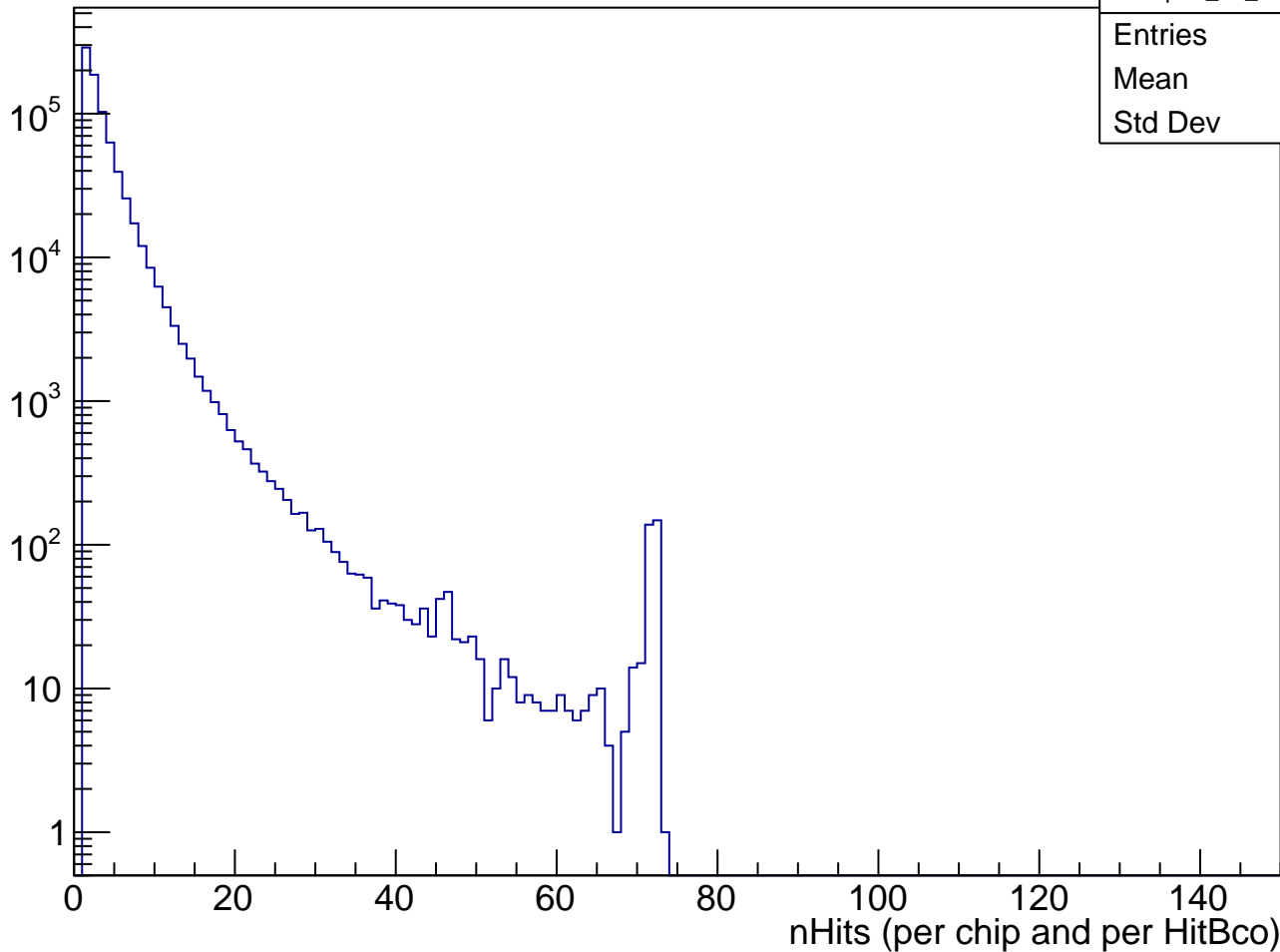
Mean 3.187

Std Dev 3.667

nHits (per chip and per HitBco)

nChipHit_F0_Fch7_Chip5

Entries



nChipHit_F0_Fch7_Chip5

Entries 771590

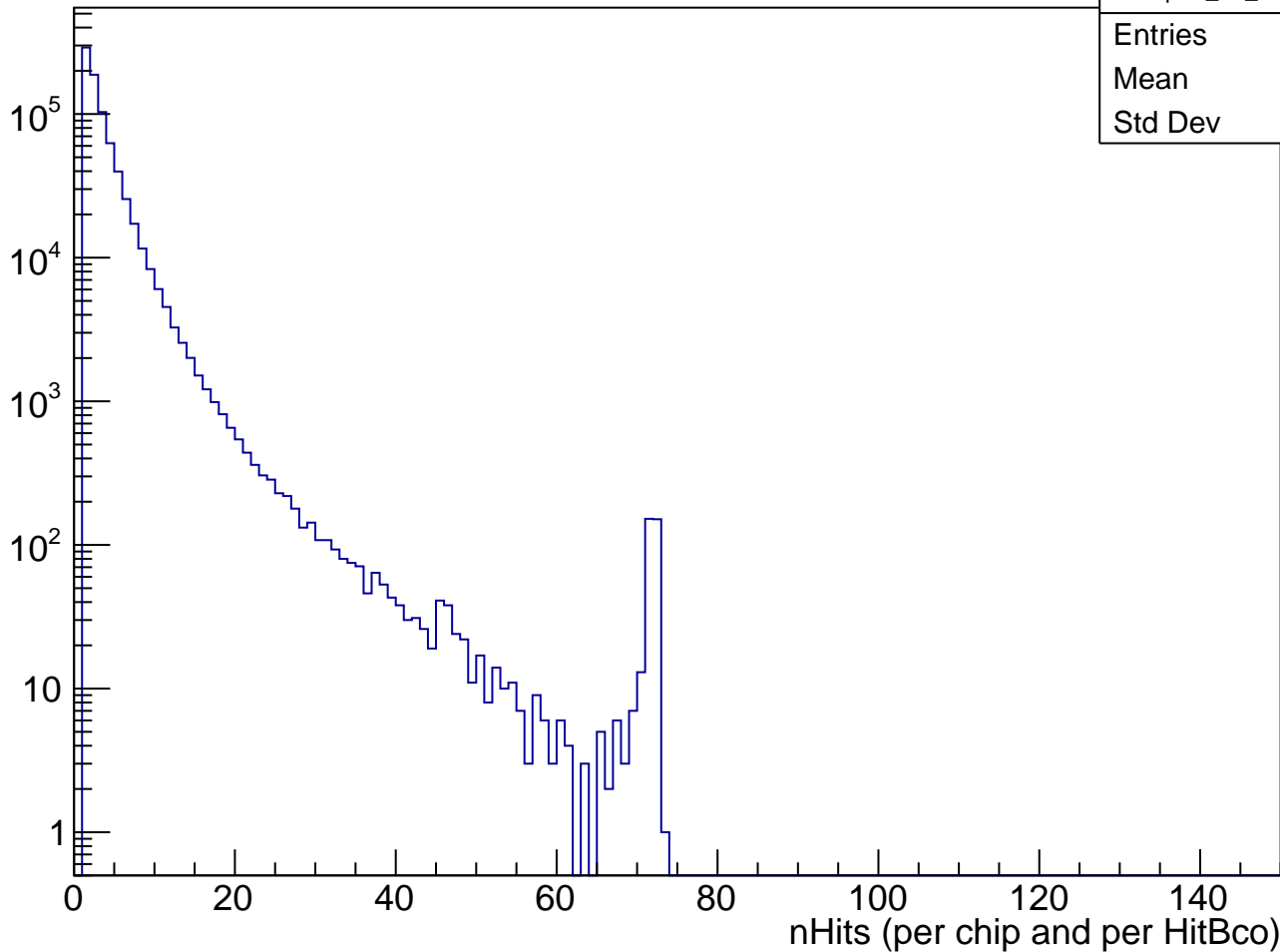
Mean 2.978

Std Dev 3.471

nHits (per chip and per HitBco)

nChipHit_F0_Fch7_Chip6

Entries



nChipHit_F0_Fch7_Chip6

Entries 774449

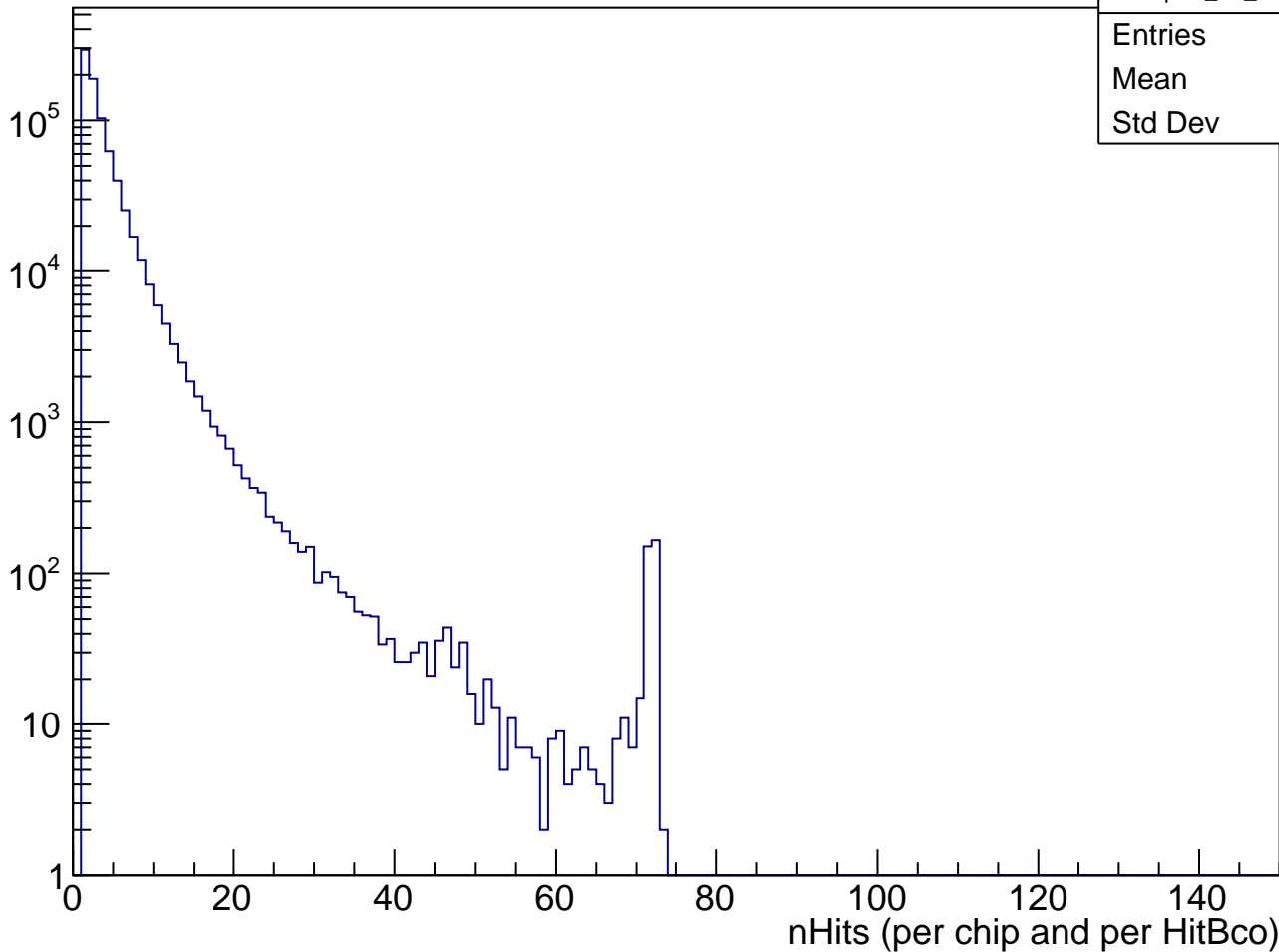
Mean 2.965

Std Dev 3.445

nHits (per chip and per HitBco)

nChipHit_F0_Fch7_Chip7

Entries

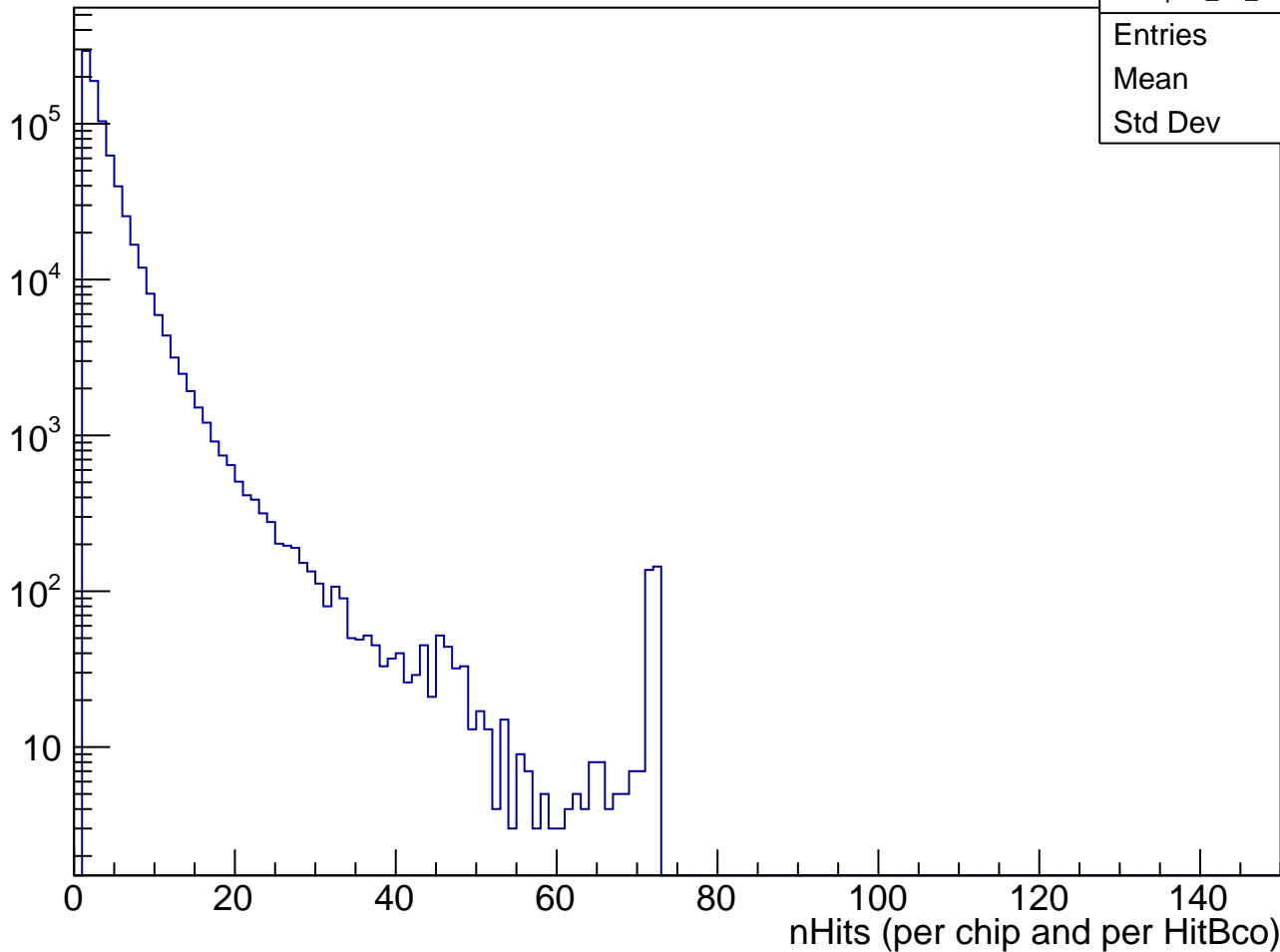


nChipHit_F0_Fch7_Chip7	
Entries	776628
Mean	2.949
Std Dev	3.45

nHits (per chip and per HitBco)

nChipHit_F0_Fch7_Chip8

Entries



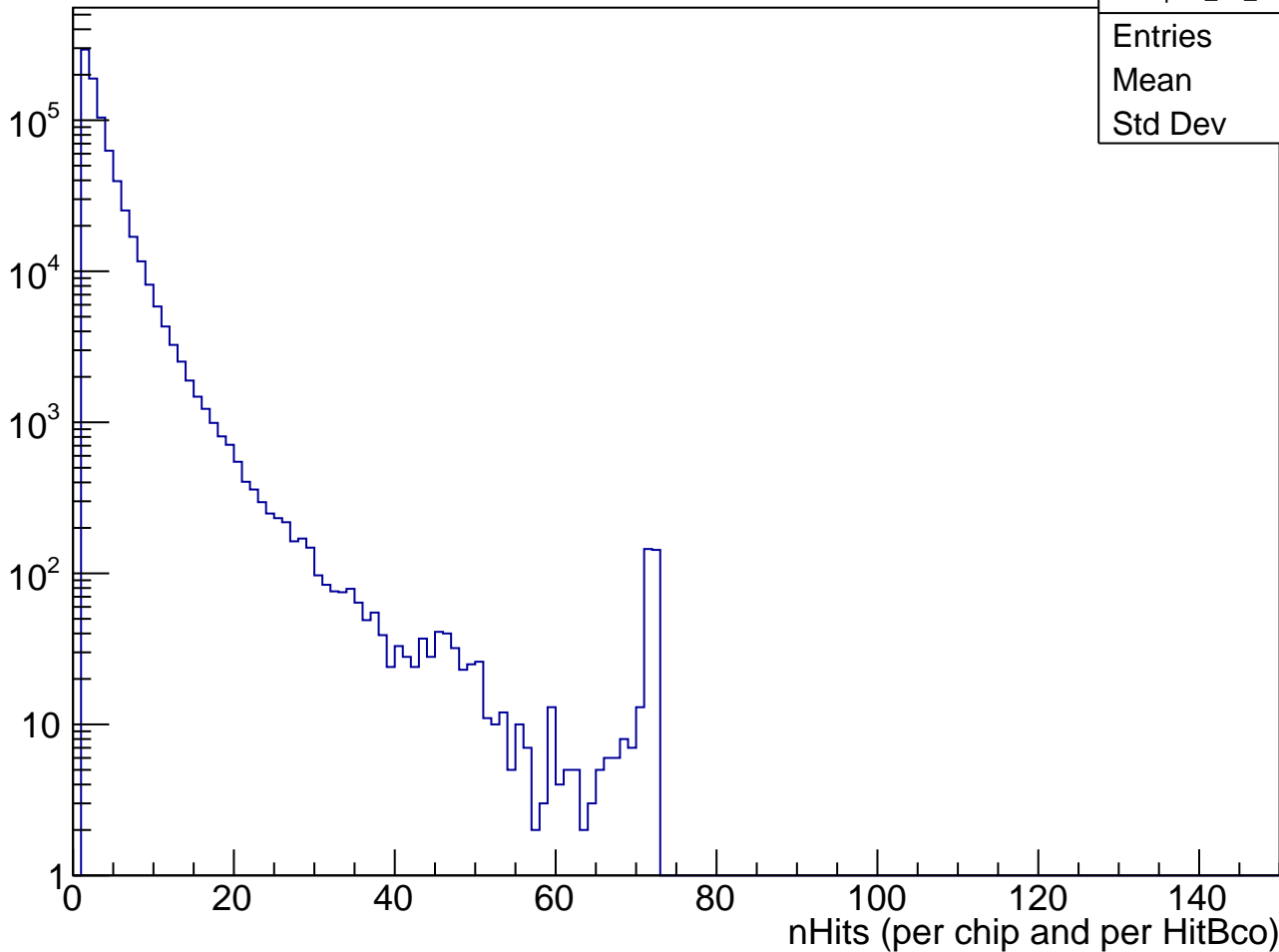
nChipHit_F0_Fch7_Chip8

Entries	776925
Mean	2.942
Std Dev	3.406

nHits (per chip and per HitBco)

nChipHit_F0_Fch7_Chip9

Entries



nChipHit_F0_Fch7_Chip9

Entries 778041

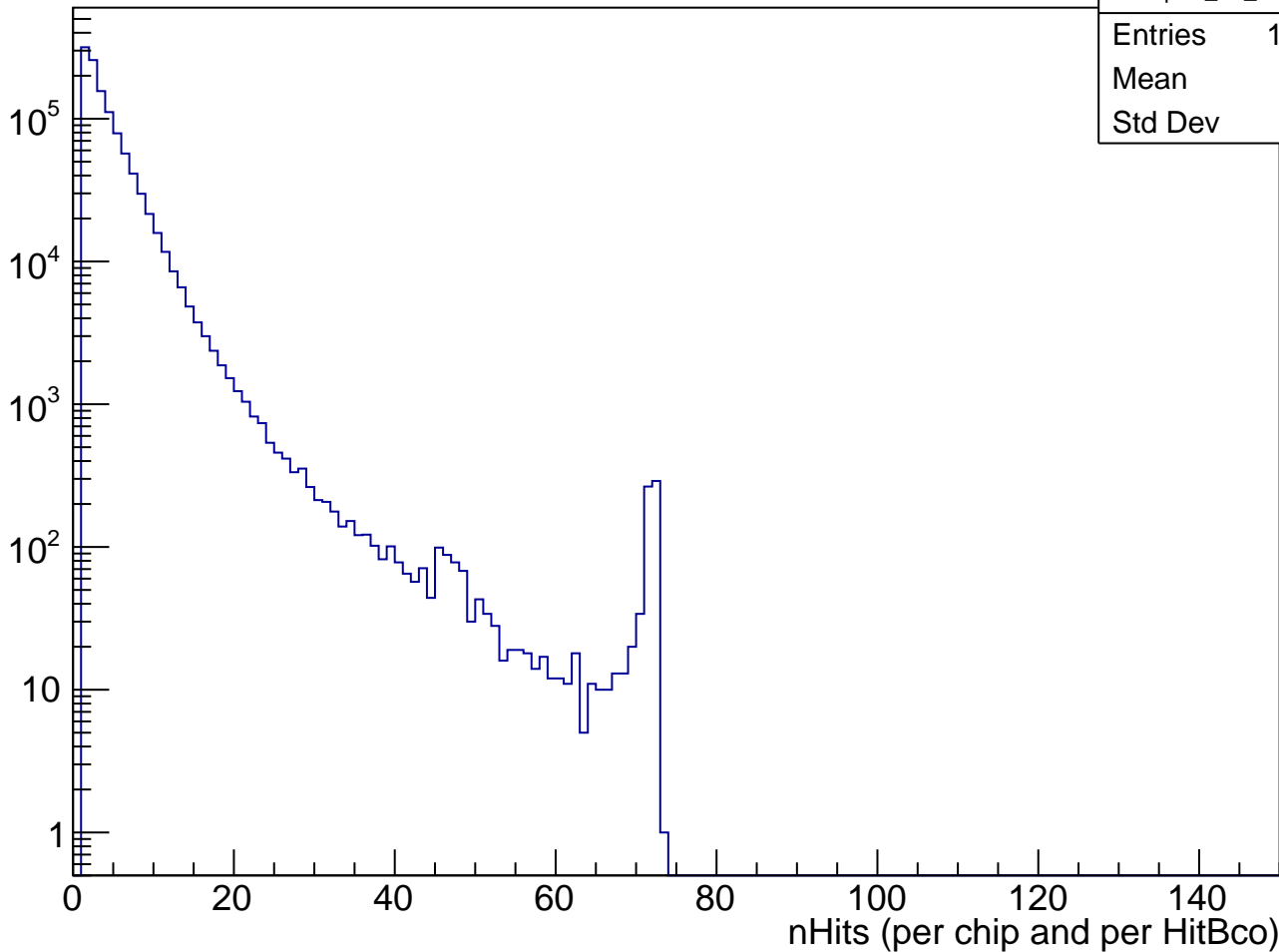
Mean 2.943

Std Dev 3.424

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip0

Entries



nChipHit_F0_Fch8_Chip0

Entries 1139508

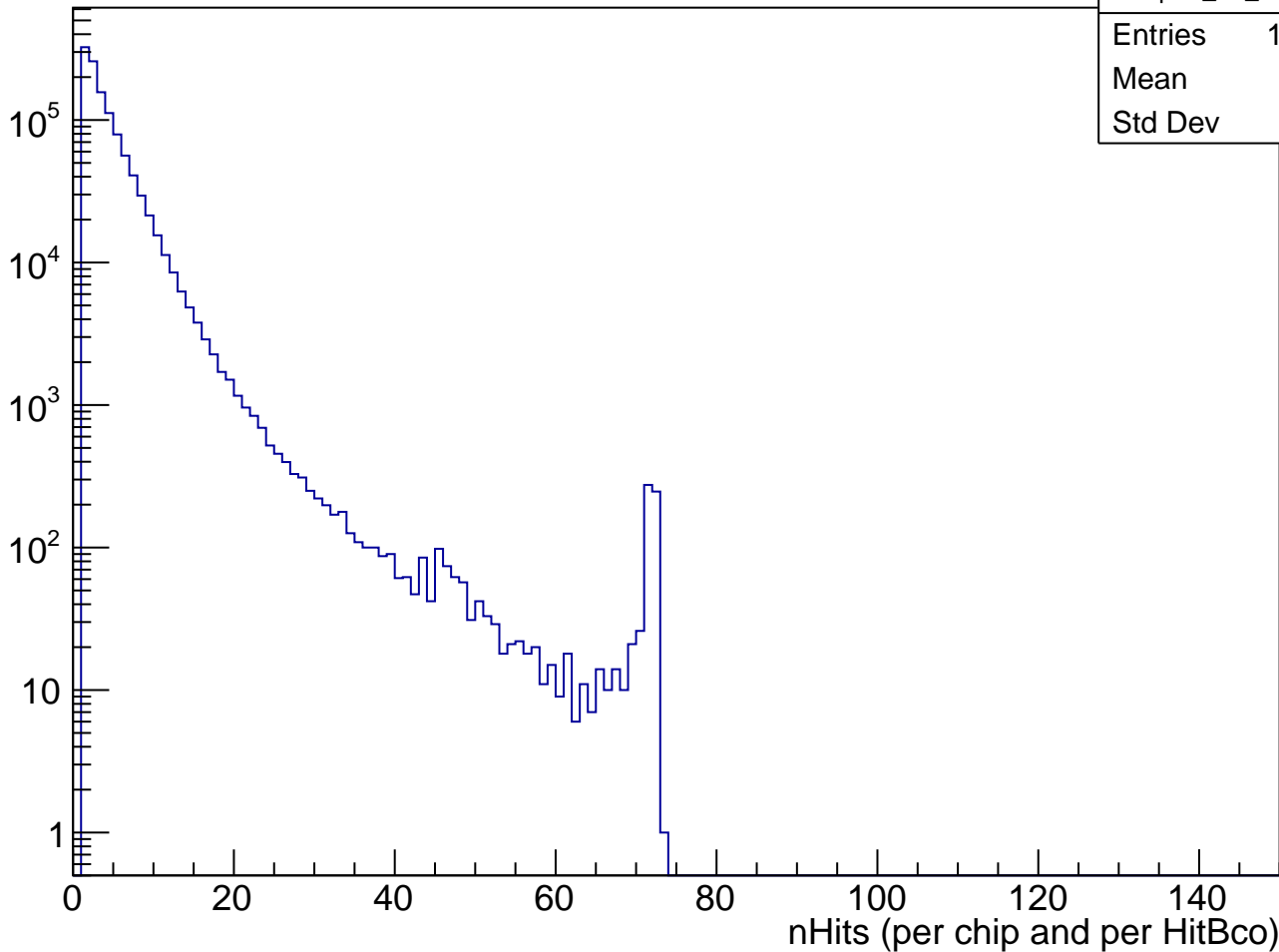
Mean 3.718

Std Dev 4.051

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip1

Entries

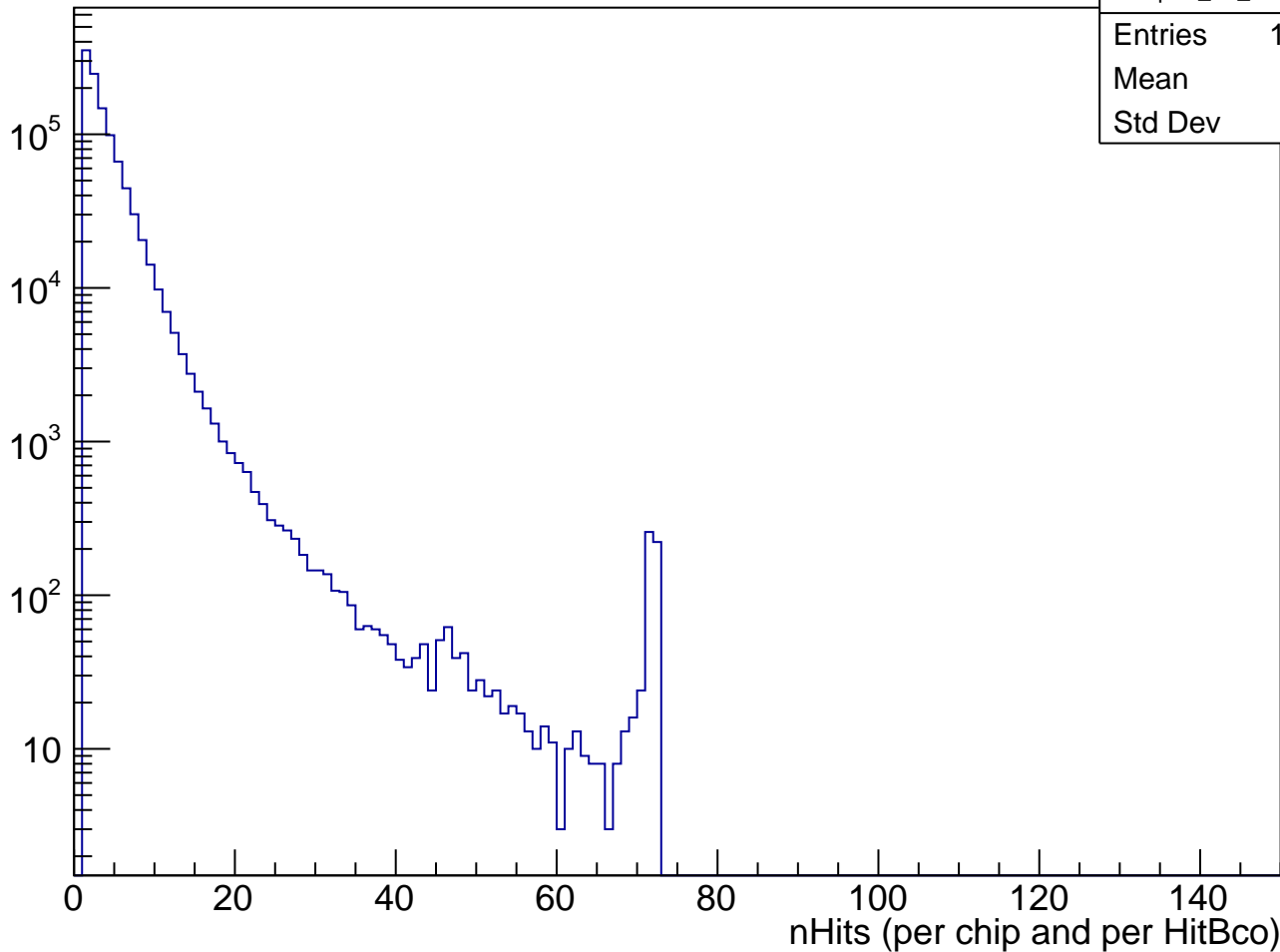


nChipHit_F0_Fch8_Chip1	
Entries	1145815
Mean	3.673
Std Dev	3.983

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip10

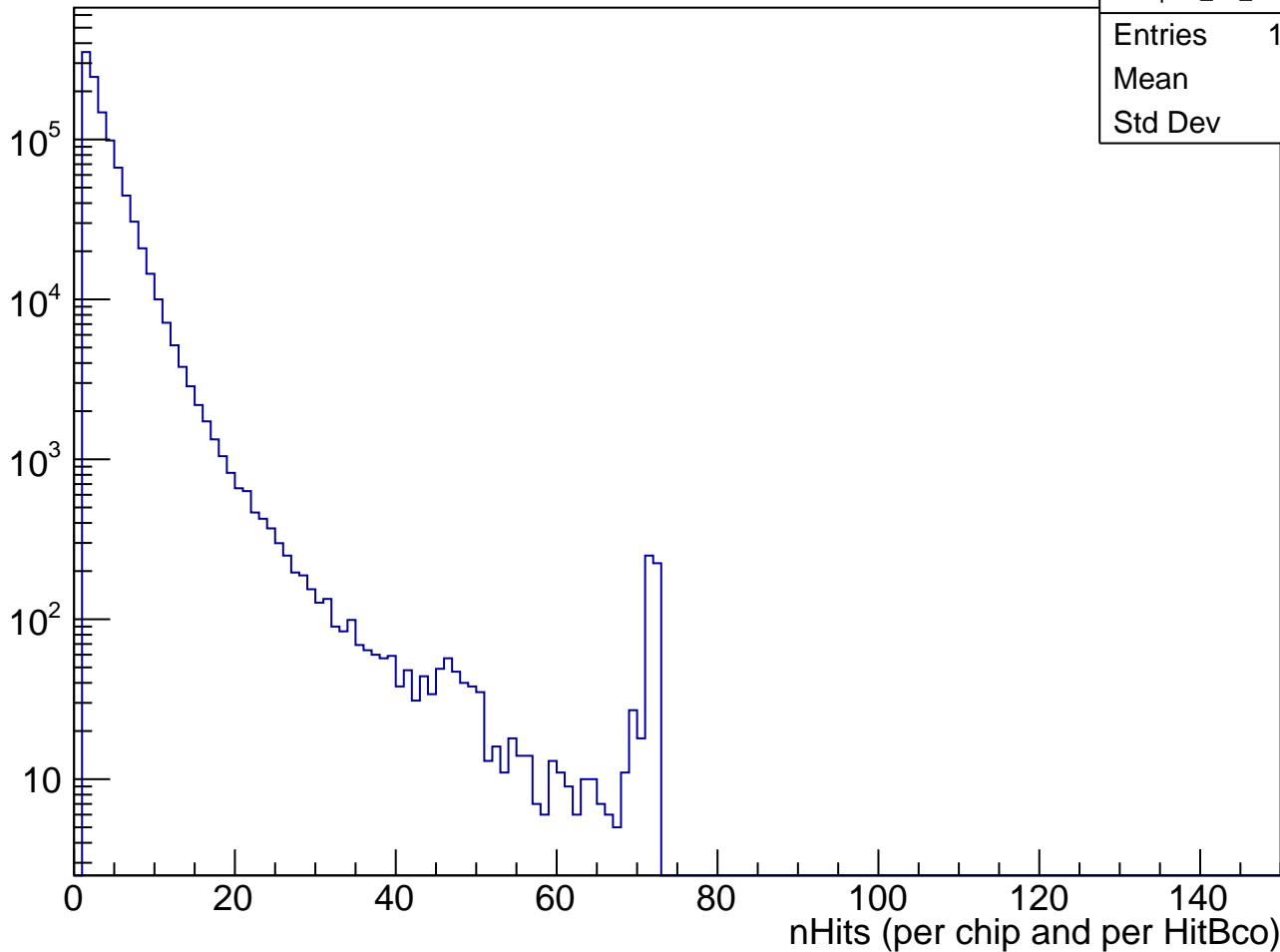
Entries



nChipHit_F0_Fch8_Chip10	
Entries	1062673
Mean	3.205
Std Dev	3.547

nChipHit_F0_Fch8_Chip11

Entries

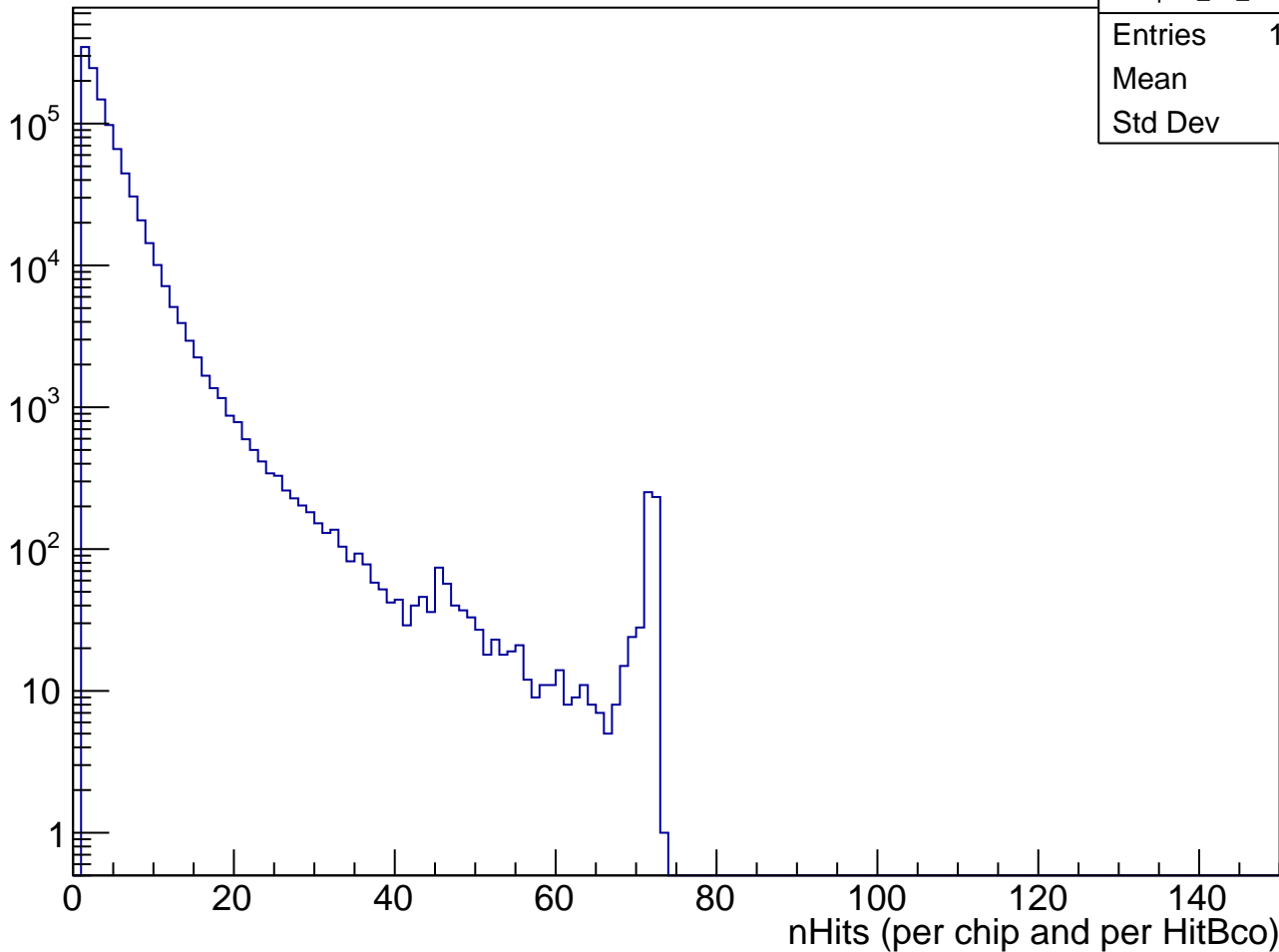


nChipHit_F0_Fch8_Chip11	
Entries	1063309
Mean	3.219
Std Dev	3.554

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip12

Entries

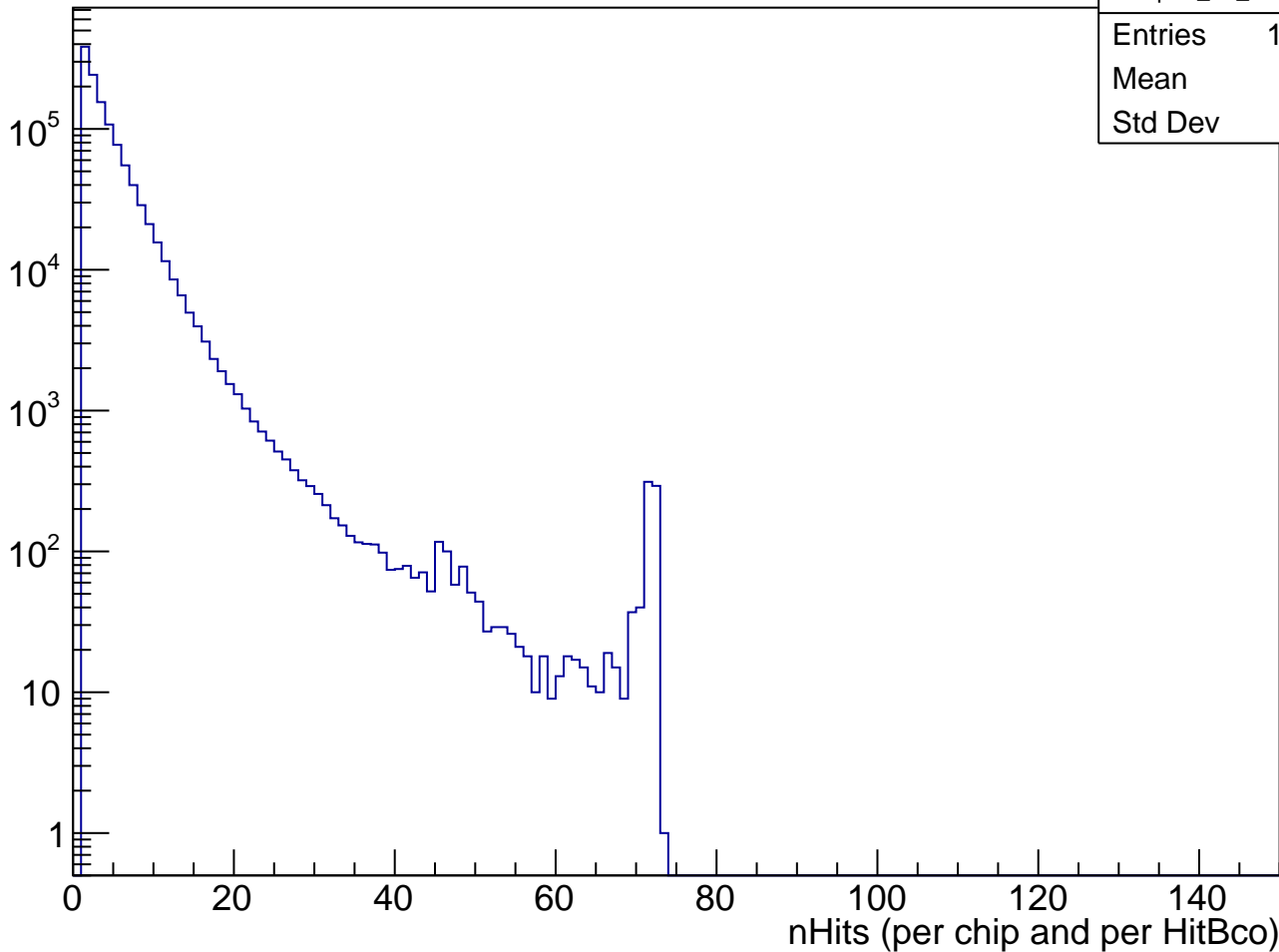


nChipHit_F0_Fch8_Chip12	
Entries	1057520
Mean	3.241
Std Dev	3.613

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip13

Entries

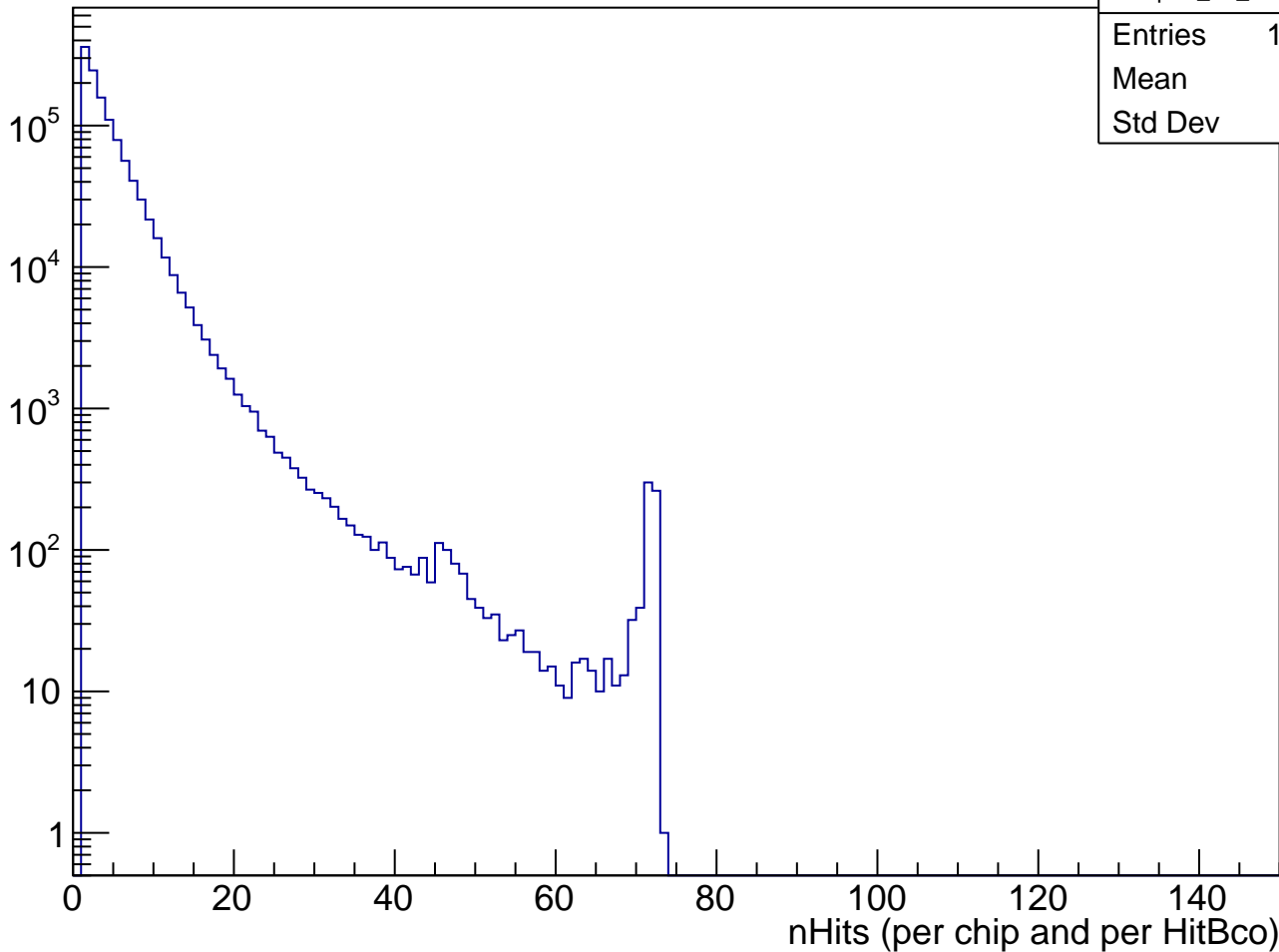


nChipHit_F0_Fch8_Chip13	
Entries	1180247
Mean	3.585
Std Dev	4.092

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip14

Entries

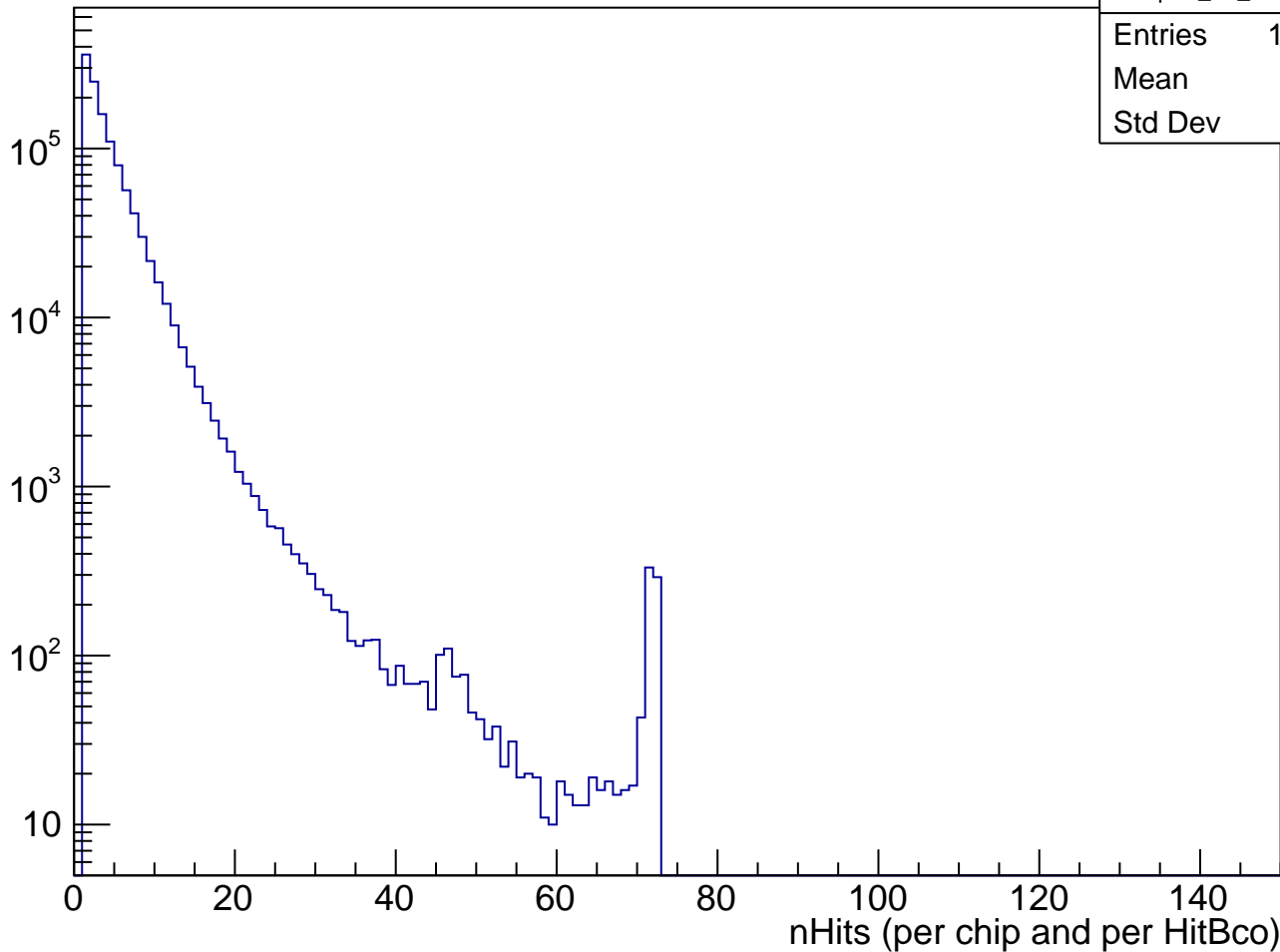


nChipHit_F0_Fch8_Chip14	
Entries	1171687
Mean	3.657
Std Dev	4.101

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip15

Entries

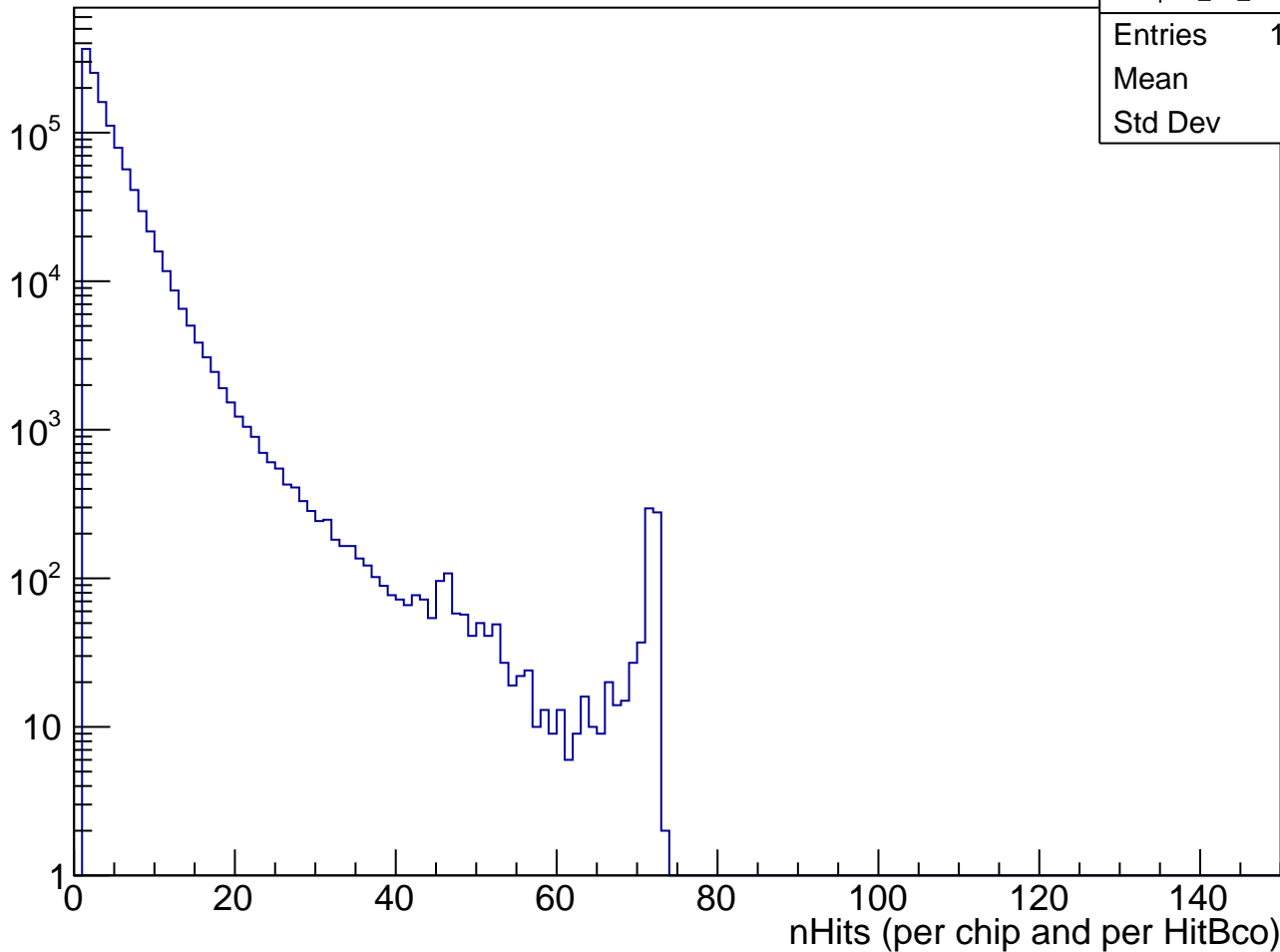


nChipHit_F0_Fch8_Chip15	
Entries	1178996
Mean	3.662
Std Dev	4.121

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip16

Entries

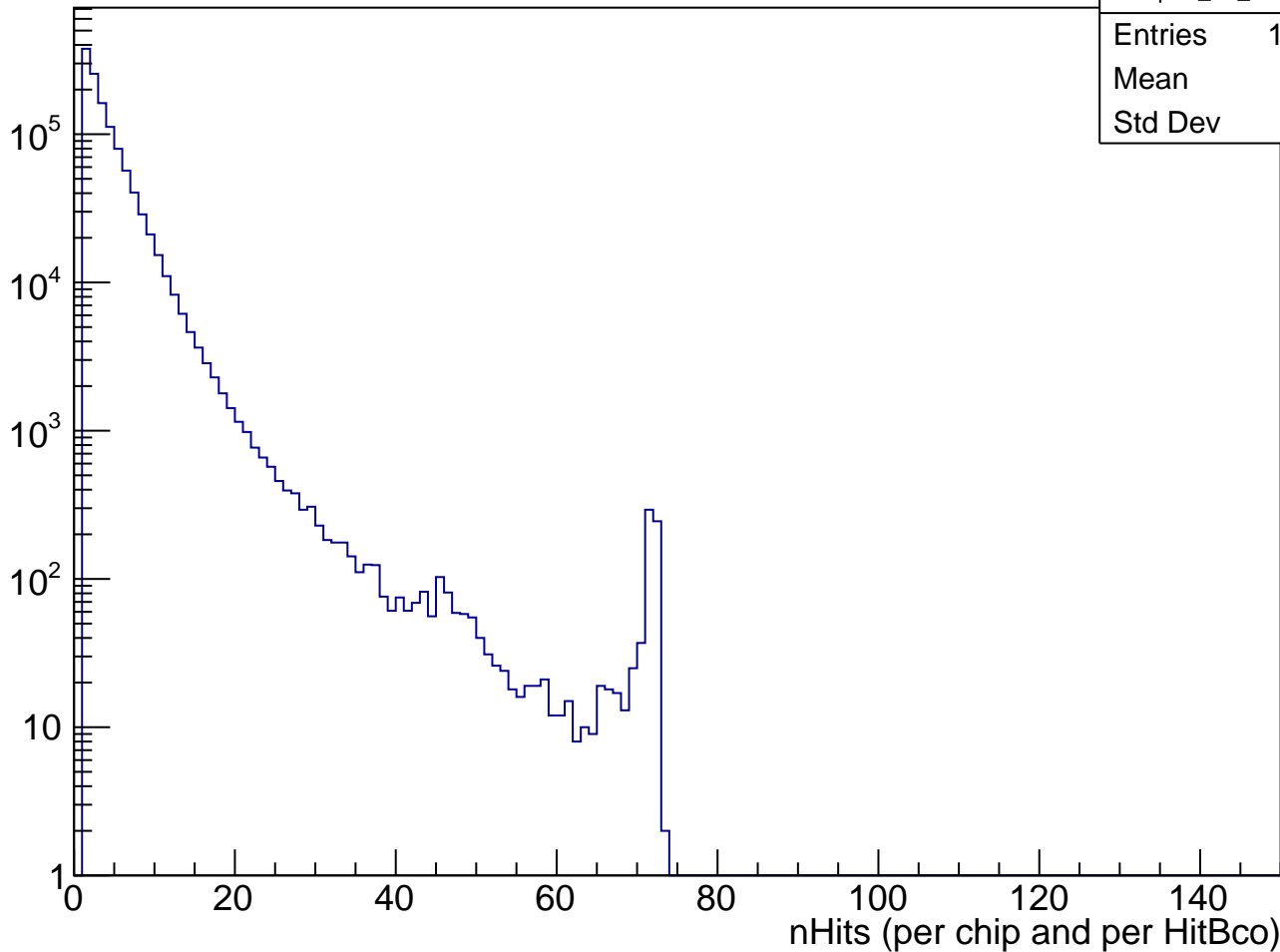


nChipHit_F0_Fch8_Chip16	
Entries	1189843
Mean	3.625
Std Dev	4.061

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip17

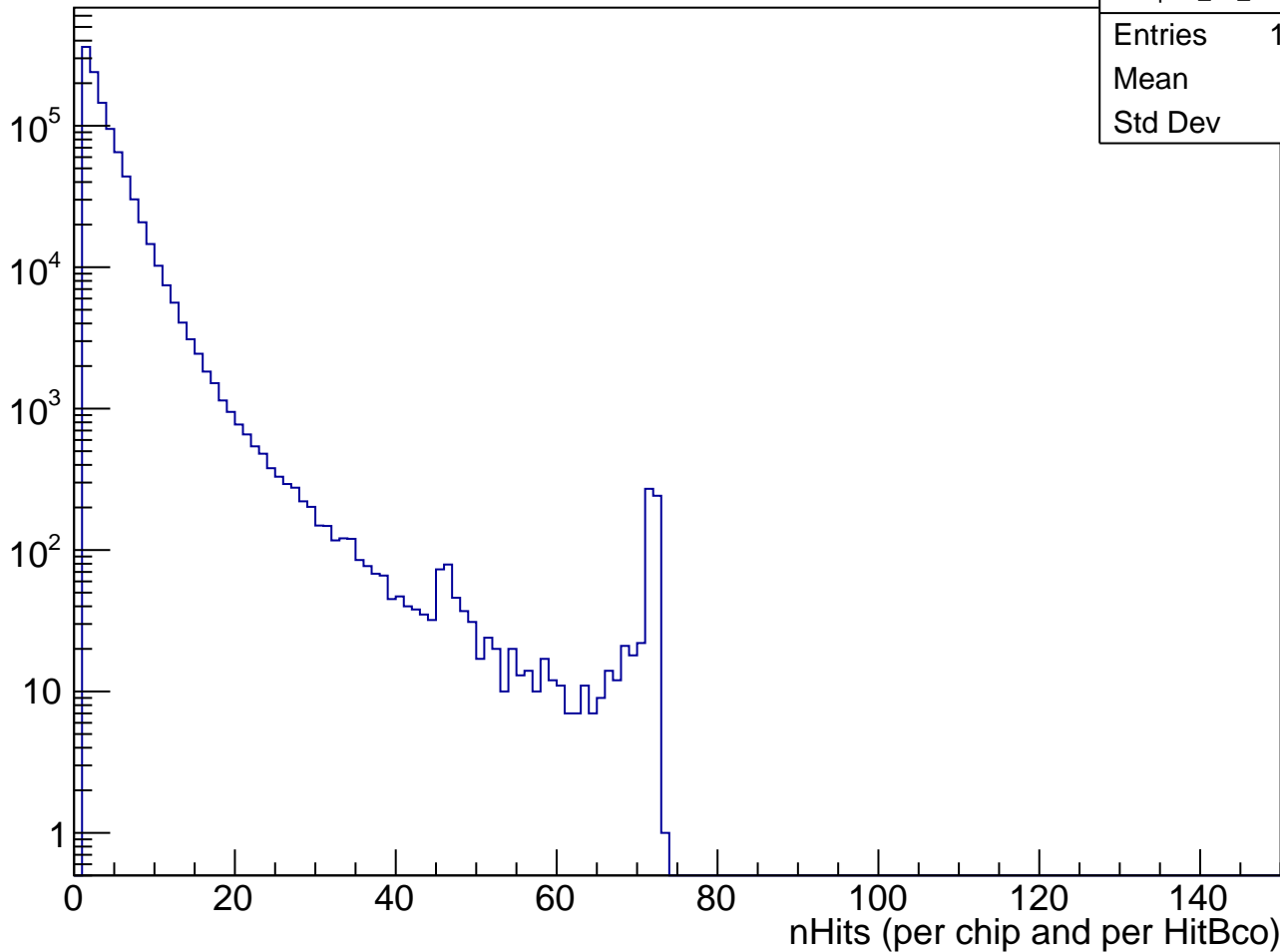
Entries



nChipHit_F0_Fch8_Chip17	
Entries	1199665
Mean	3.551
Std Dev	3.962

nChipHit_F0_Fch8_Chip18

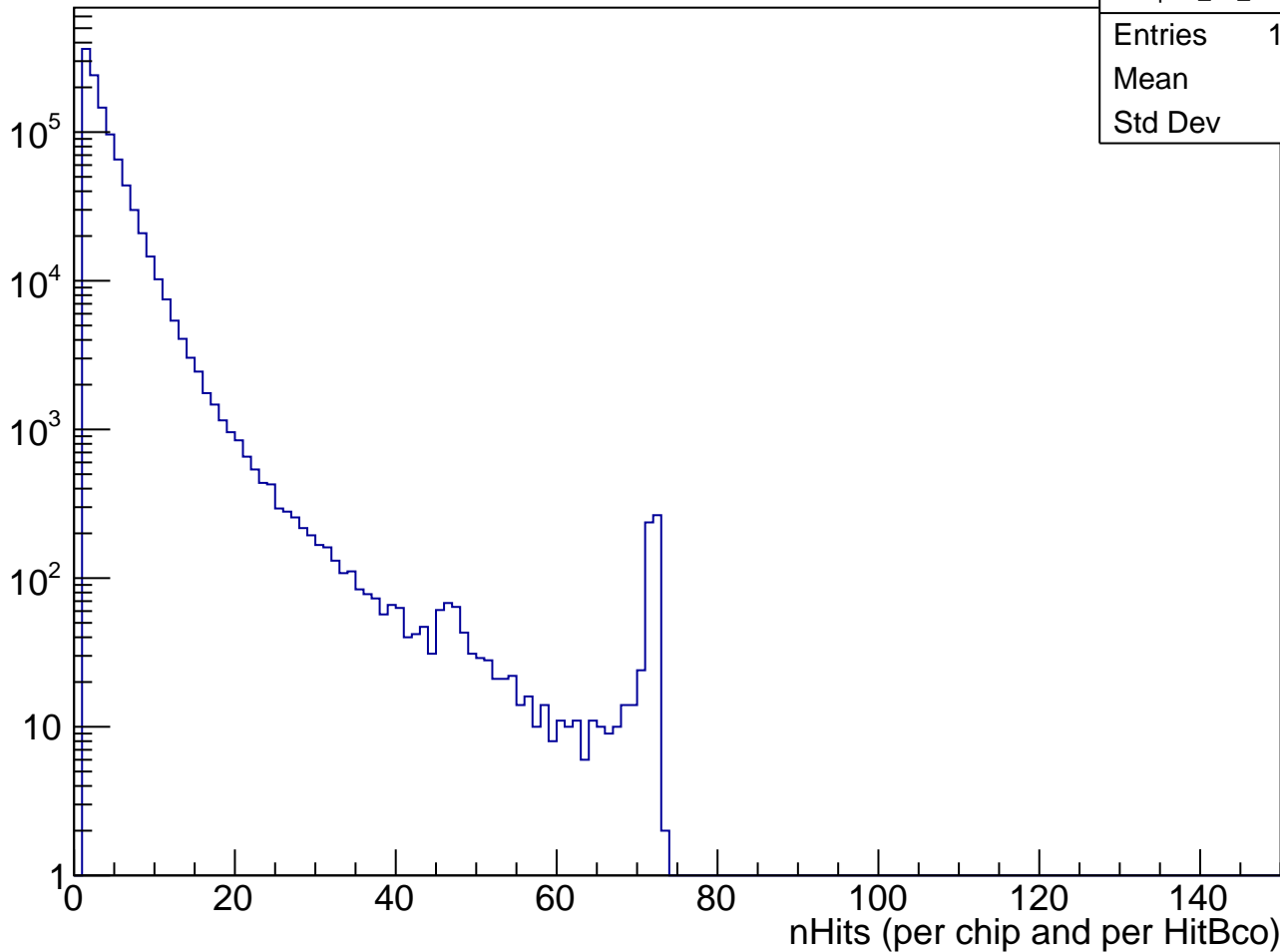
Entries



nChipHit_F0_Fch8_Chip18	
Entries	1060540
Mean	3.24
Std Dev	3.687

nChipHit_F0_Fch8_Chip19

Entries

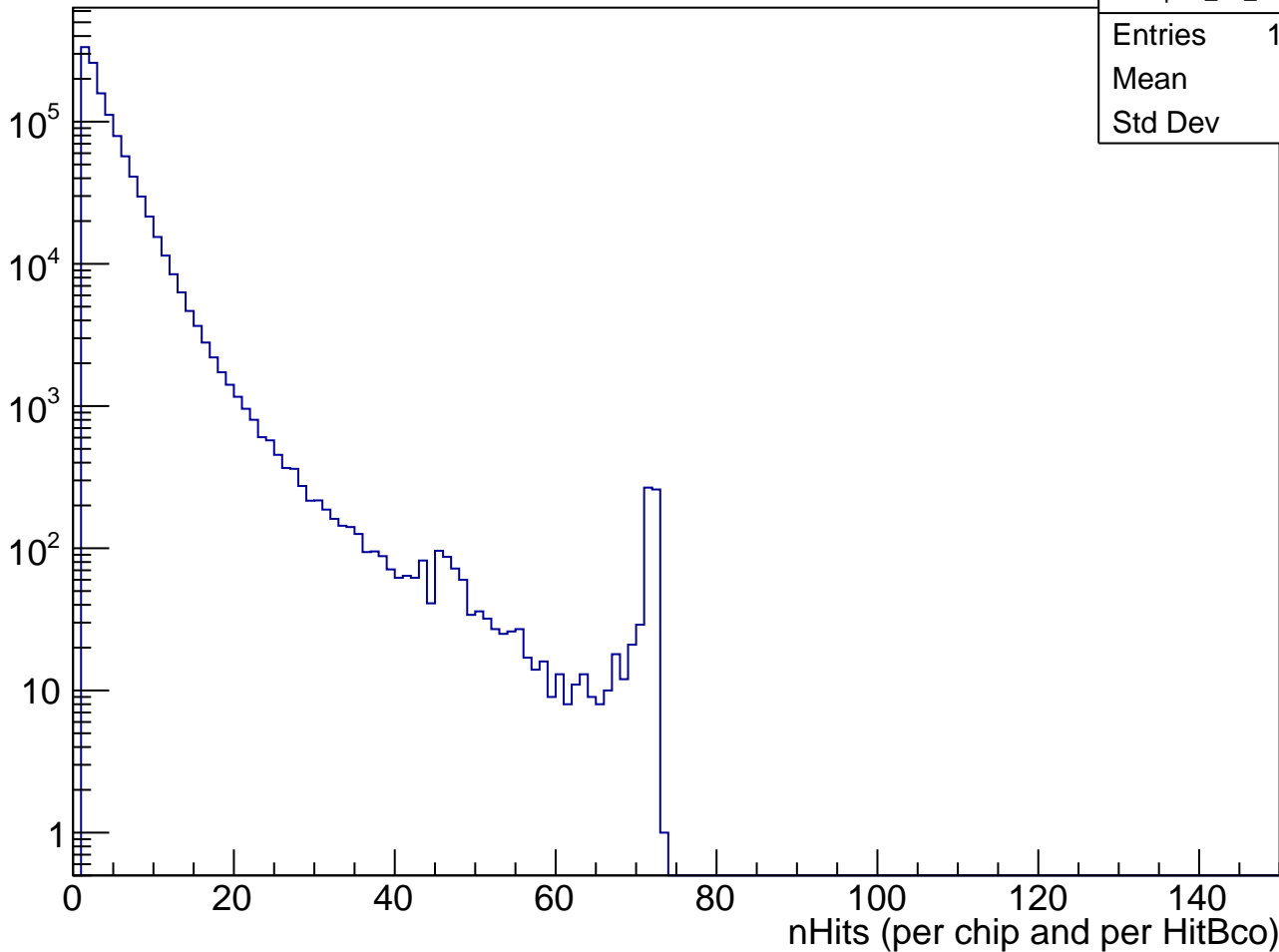


nChipHit_F0_Fch8_Chip19	
Entries	1065624
Mean	3.235
Std Dev	3.68

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip2

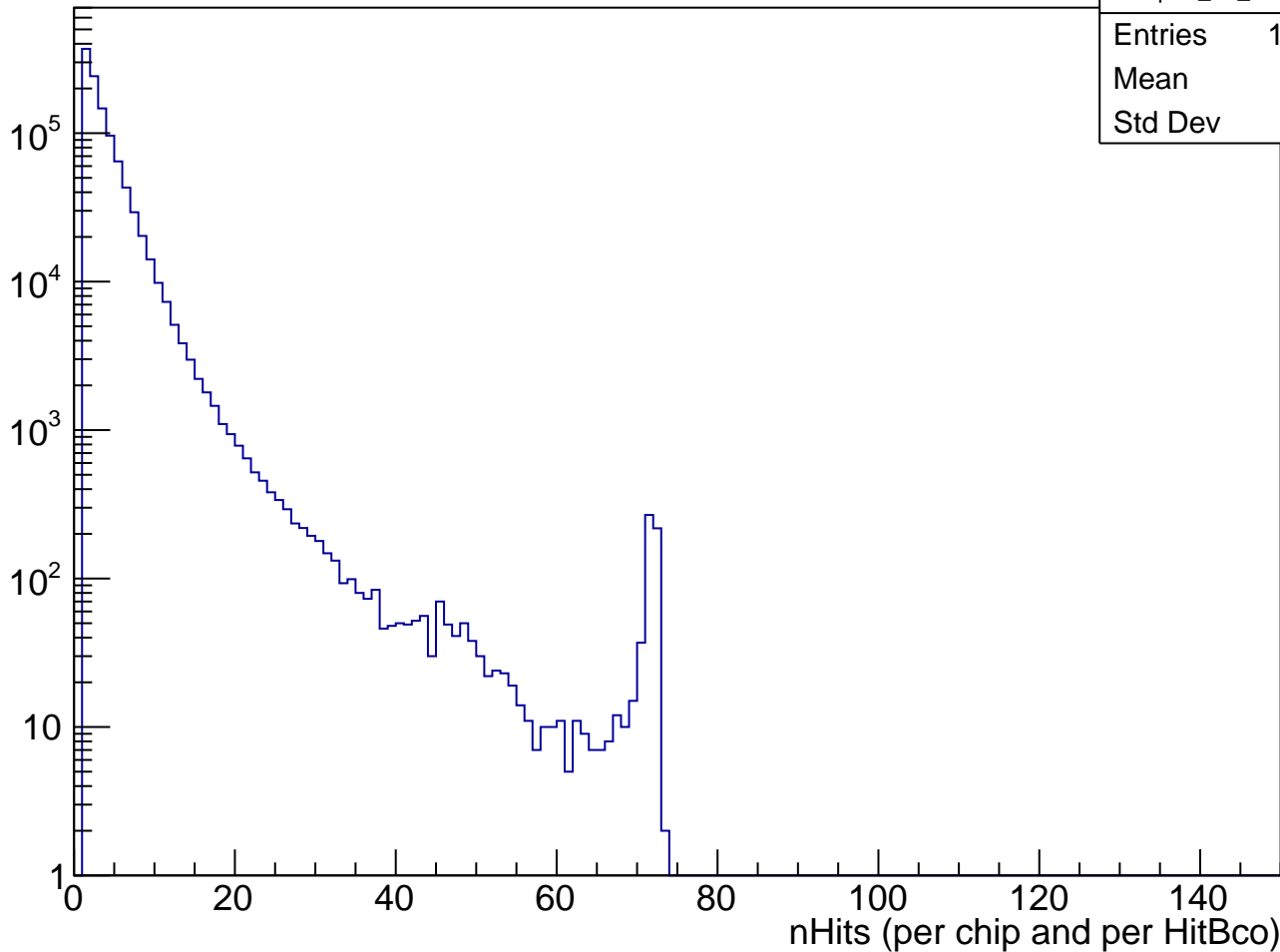
Entries



nChipHit_F0_Fch8_Chip2	
Entries	1159025
Mean	3.643
Std Dev	3.962

nChipHit_F0_Fch8_Chip20

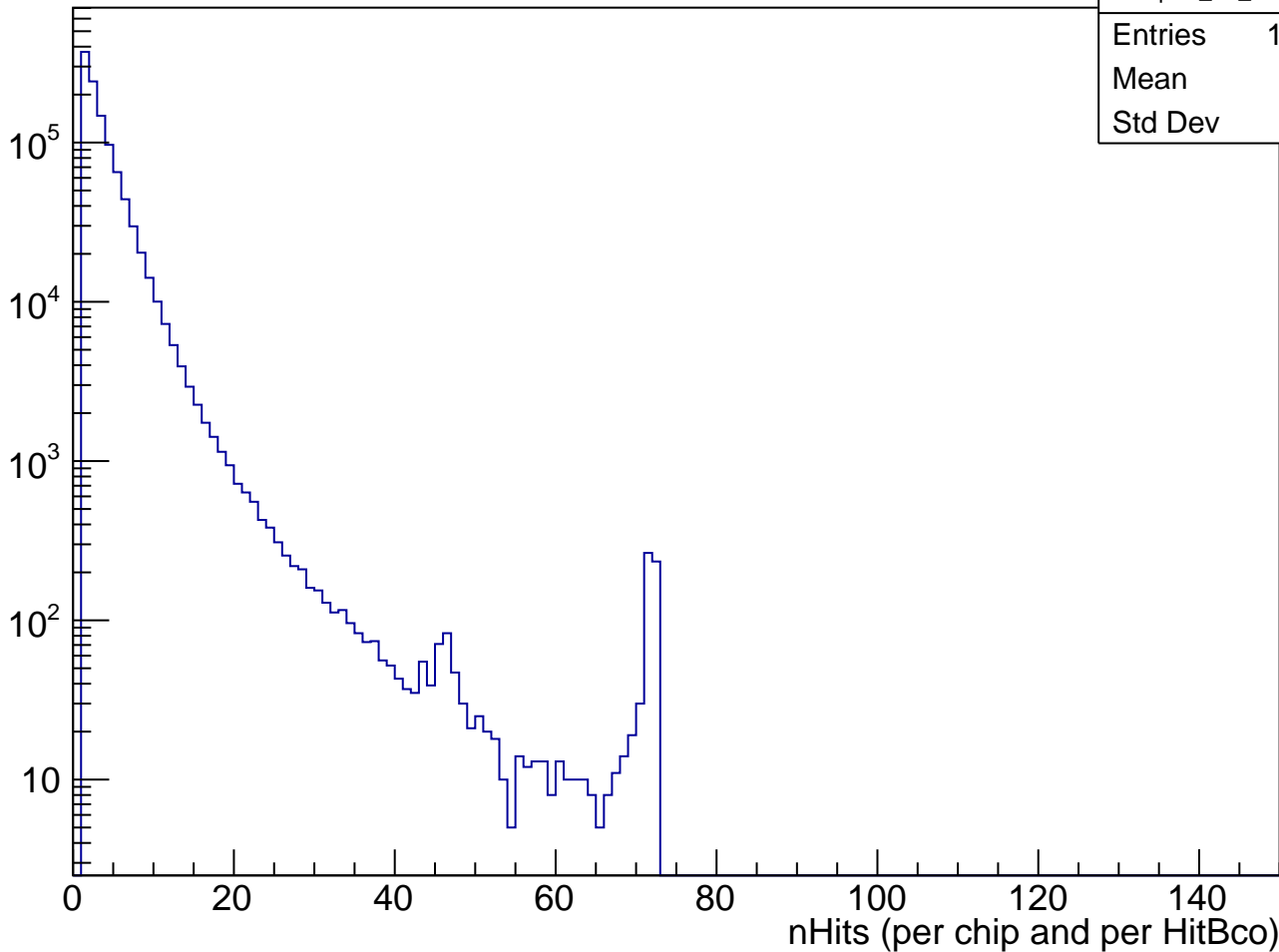
Entries



nChipHit_F0_Fch8_Chip20	
Entries	1069138
Mean	3.192
Std Dev	3.632

nChipHit_F0_Fch8_Chip21

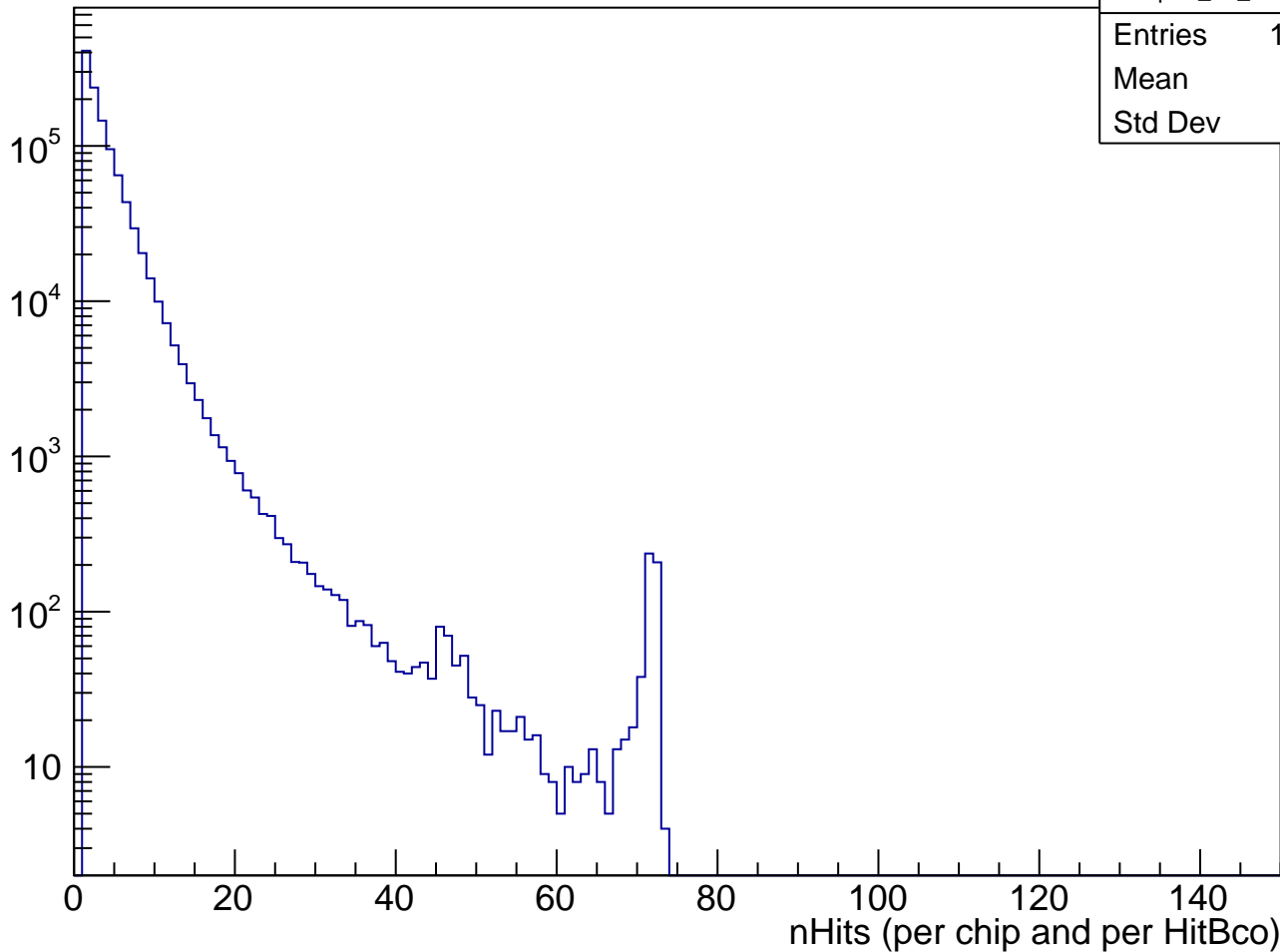
Entries



nChipHit_F0_Fch8_Chip21	
Entries	1073865
Mean	3.192
Std Dev	3.611

nChipHit_F0_Fch8_Chip22

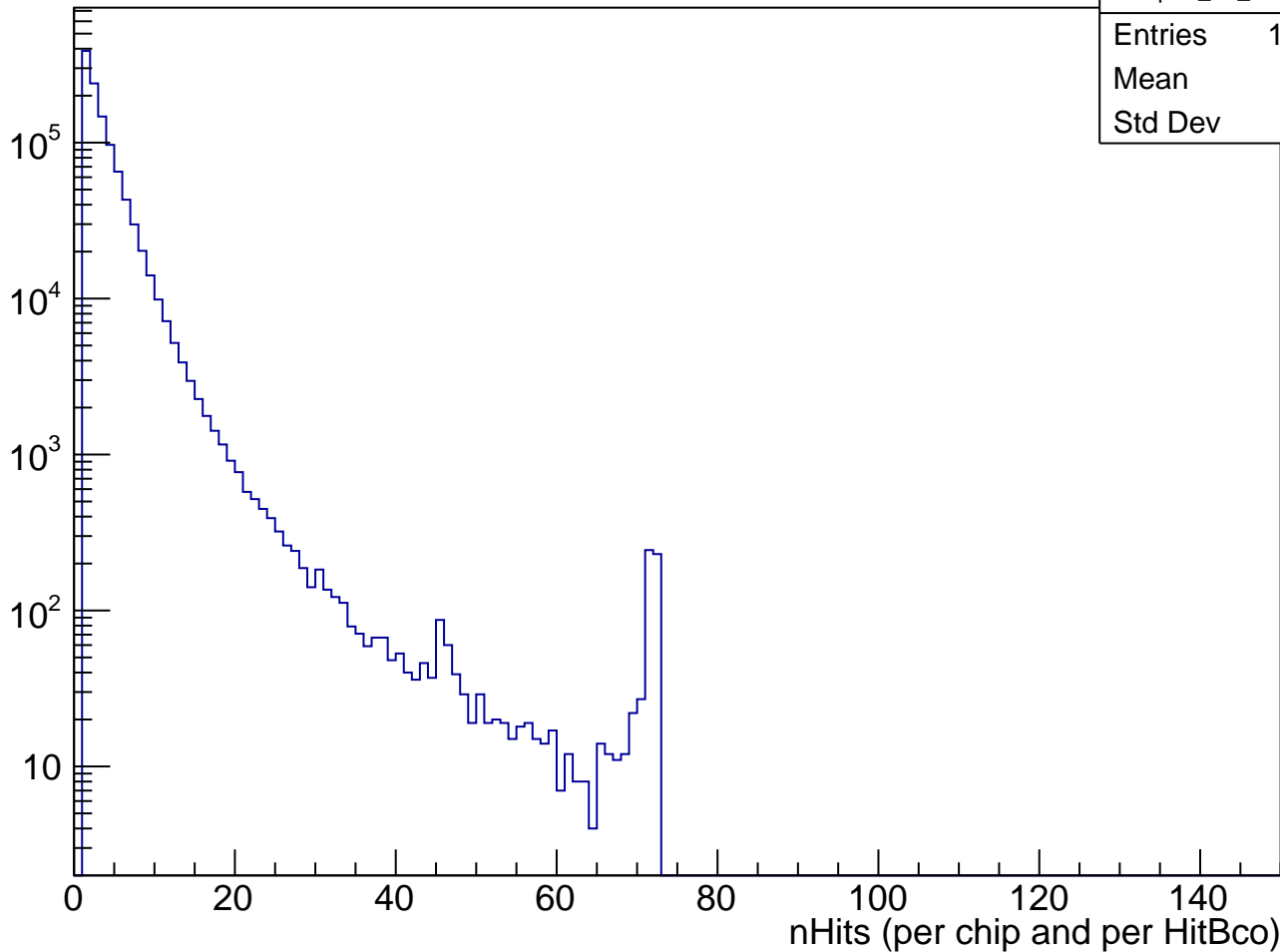
Entries



nChipHit_F0_Fch8_Chip22	
Entries	1103700
Mean	3.113
Std Dev	3.571

nChipHit_F0_Fch8_Chip23

Entries



nChipHit_F0_Fch8_Chip23

Entries 1085789

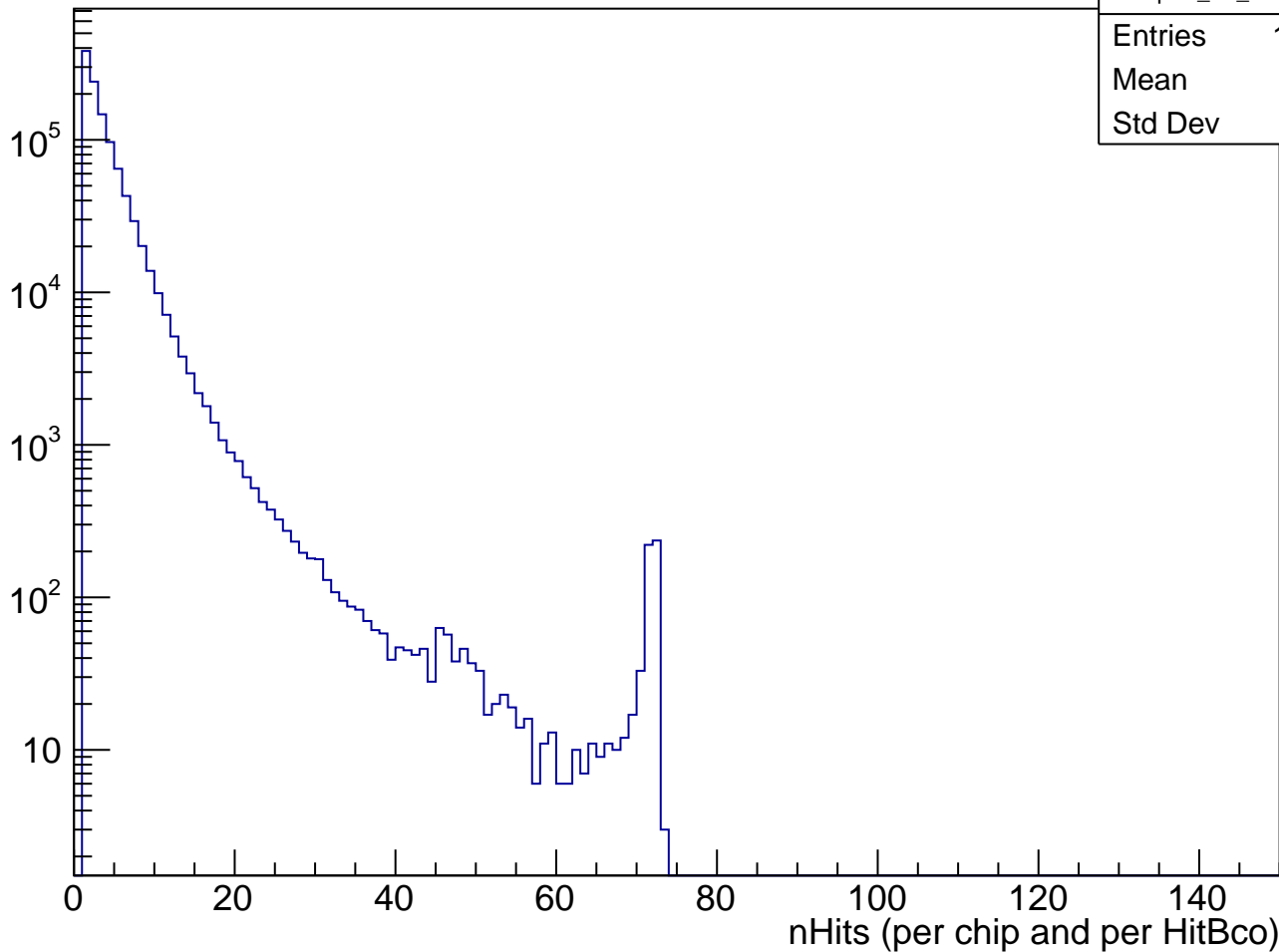
Mean 3.156

Std Dev 3.59

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip24

Entries

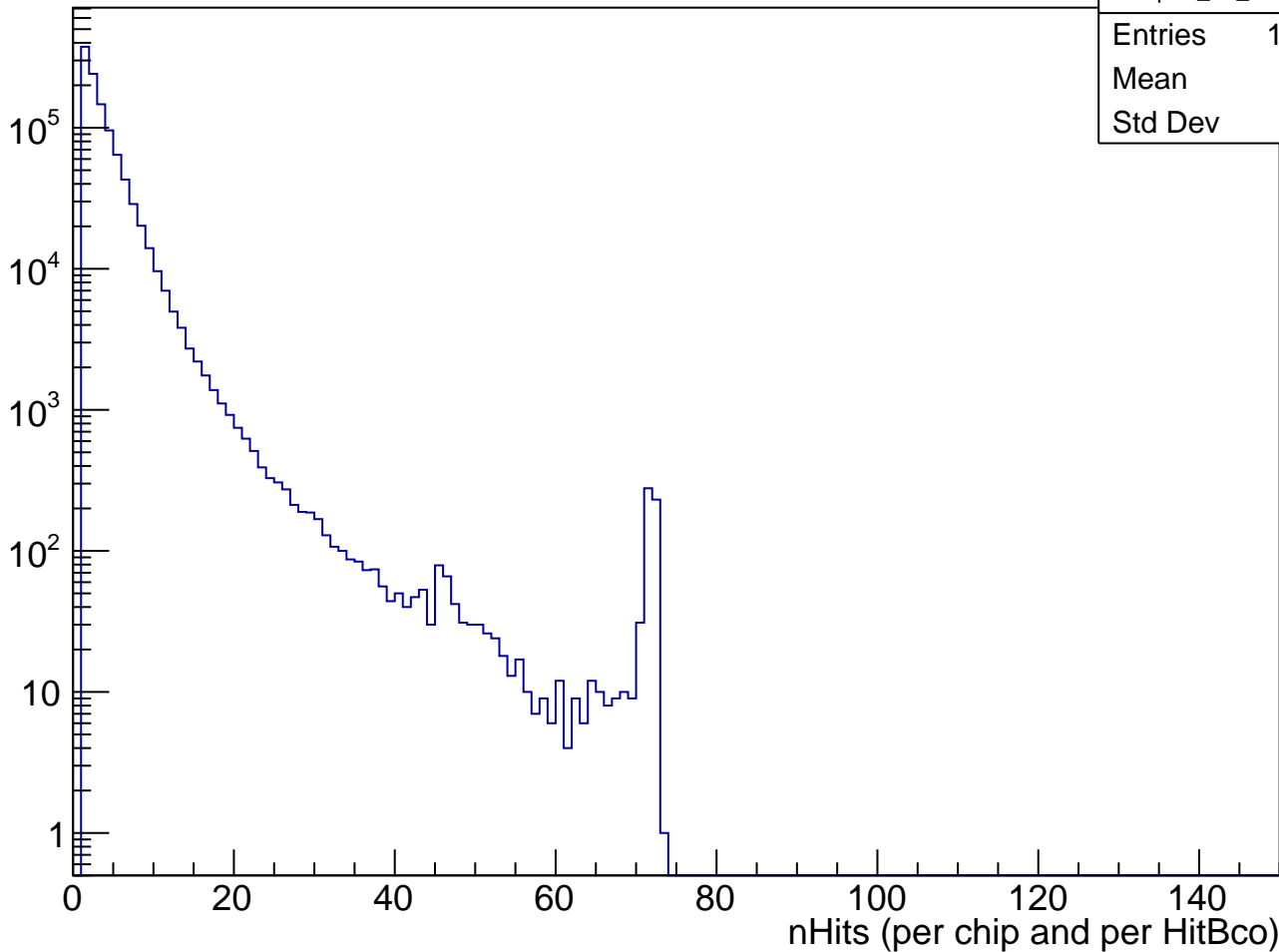


nChipHit_F0_Fch8_Chip24	
Entries	1080321
Mean	3.153
Std Dev	3.573

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip25

Entries

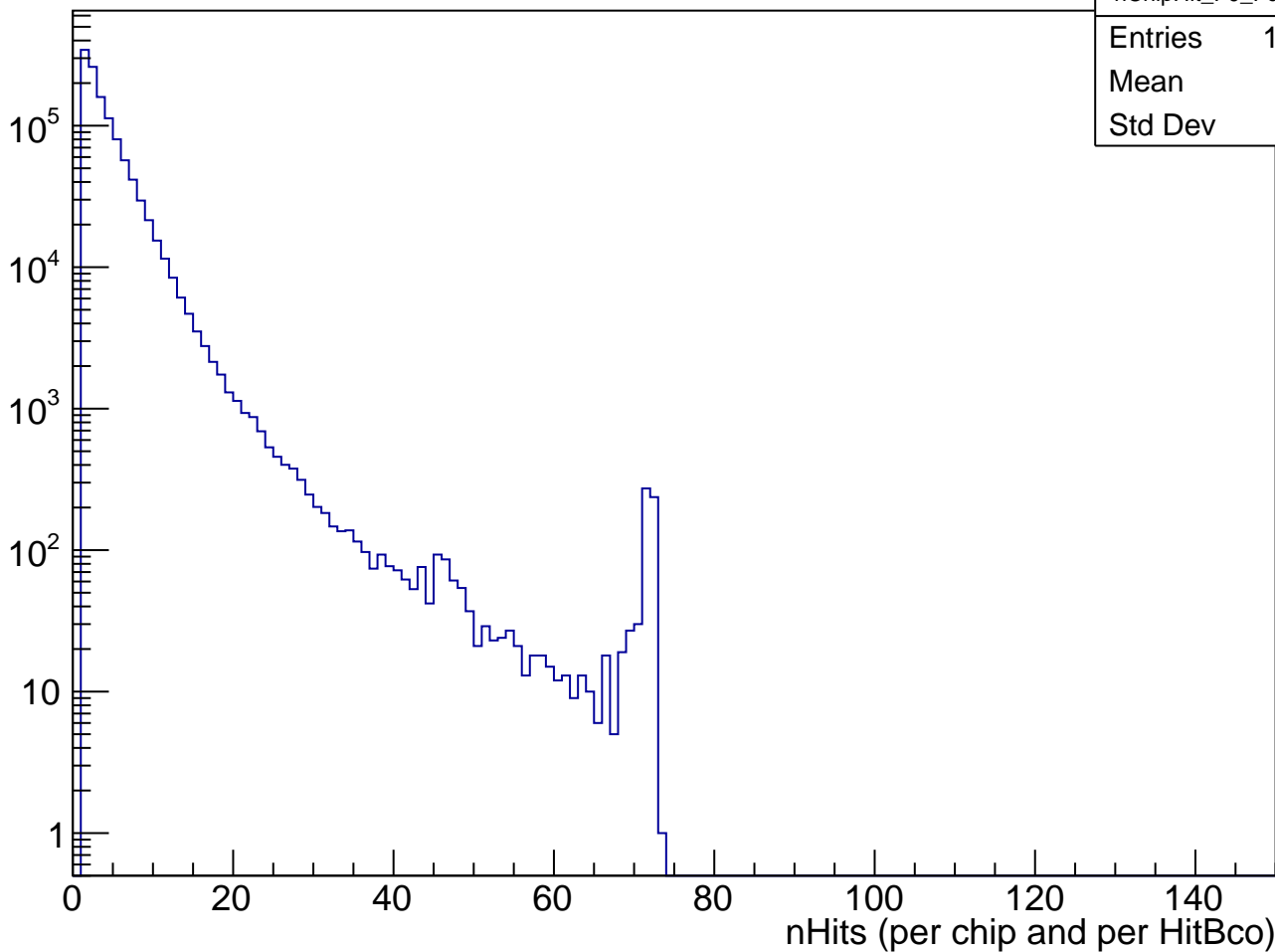


nChipHit_F0_Fch8_Chip25	
Entries	1070306
Mean	3.161
Std Dev	3.594

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip3

Entries

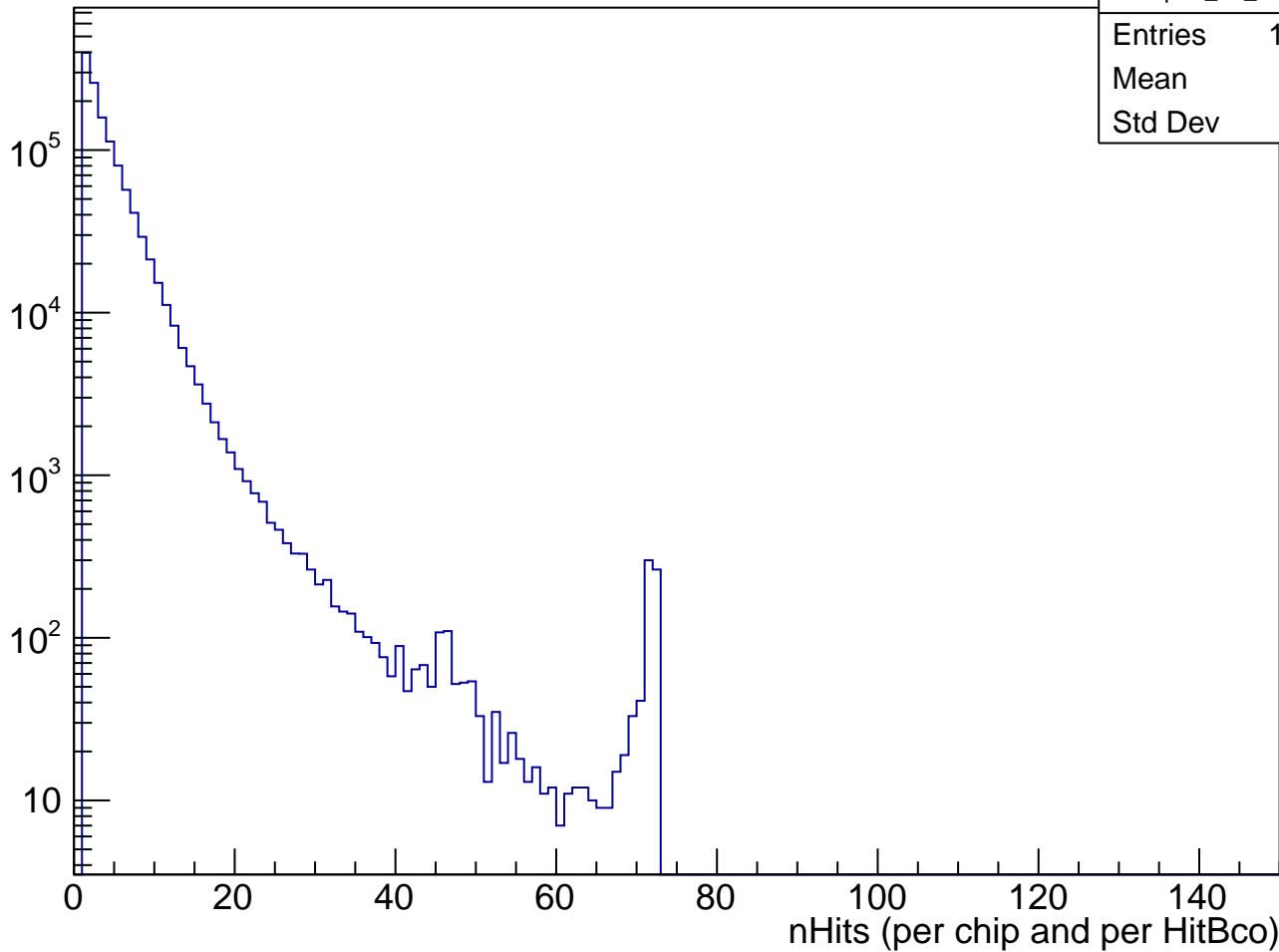


nChipHit_F0_Fch8_Chip3	
Entries	1172207
Mean	3.617
Std Dev	3.931

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip4

Entries



nChipHit_F0_Fch8_Chip4

Entries 1221810

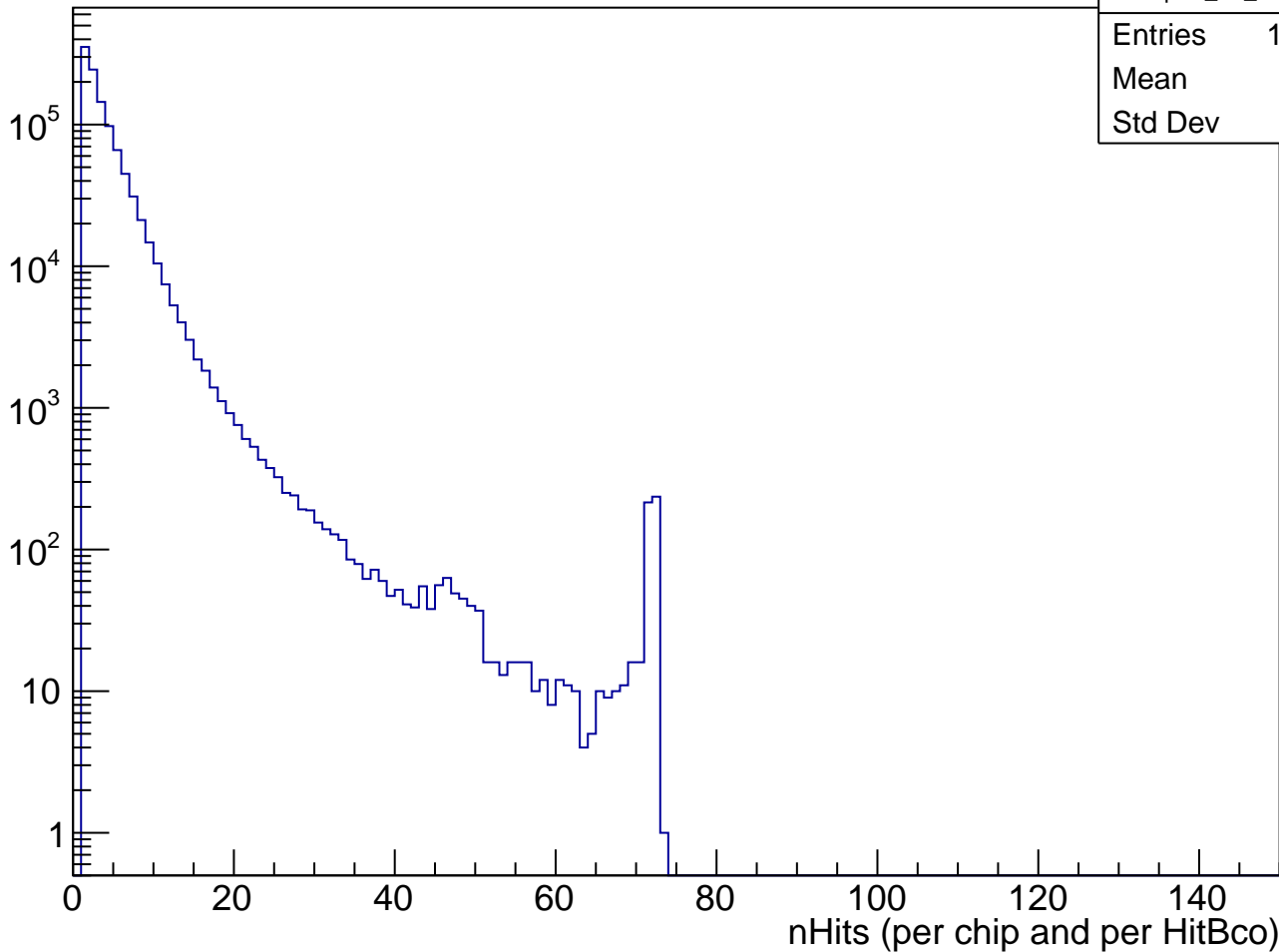
Mean 3.502

Std Dev 3.918

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip5

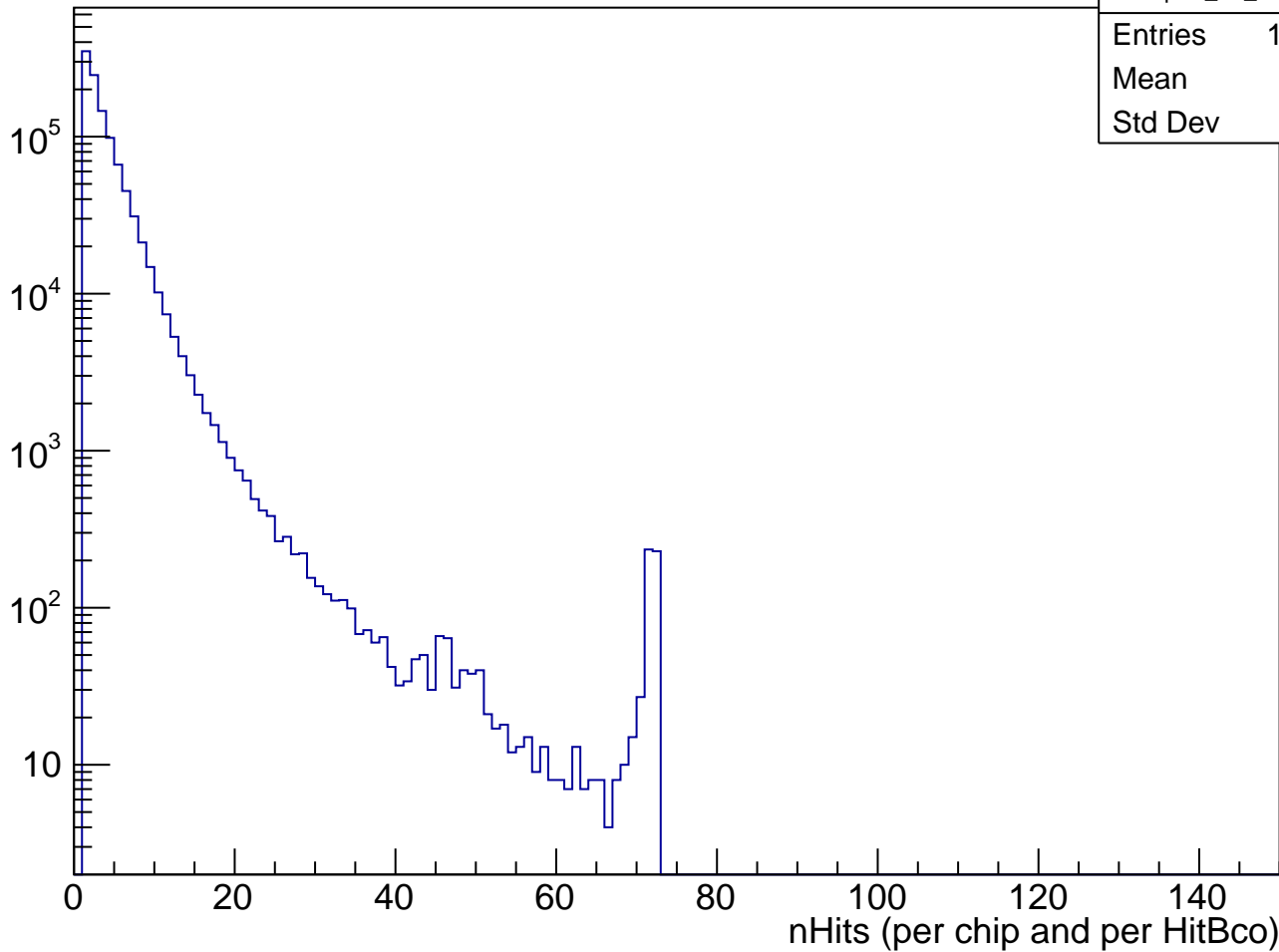
Entries



nChipHit_F0_Fch8_Chip5	
Entries	1061235
Mean	3.246
Std Dev	3.605

nChipHit_F0_Fch8_Chip6

Entries



nChipHit_F0_Fch8_Chip6

Entries 1062624

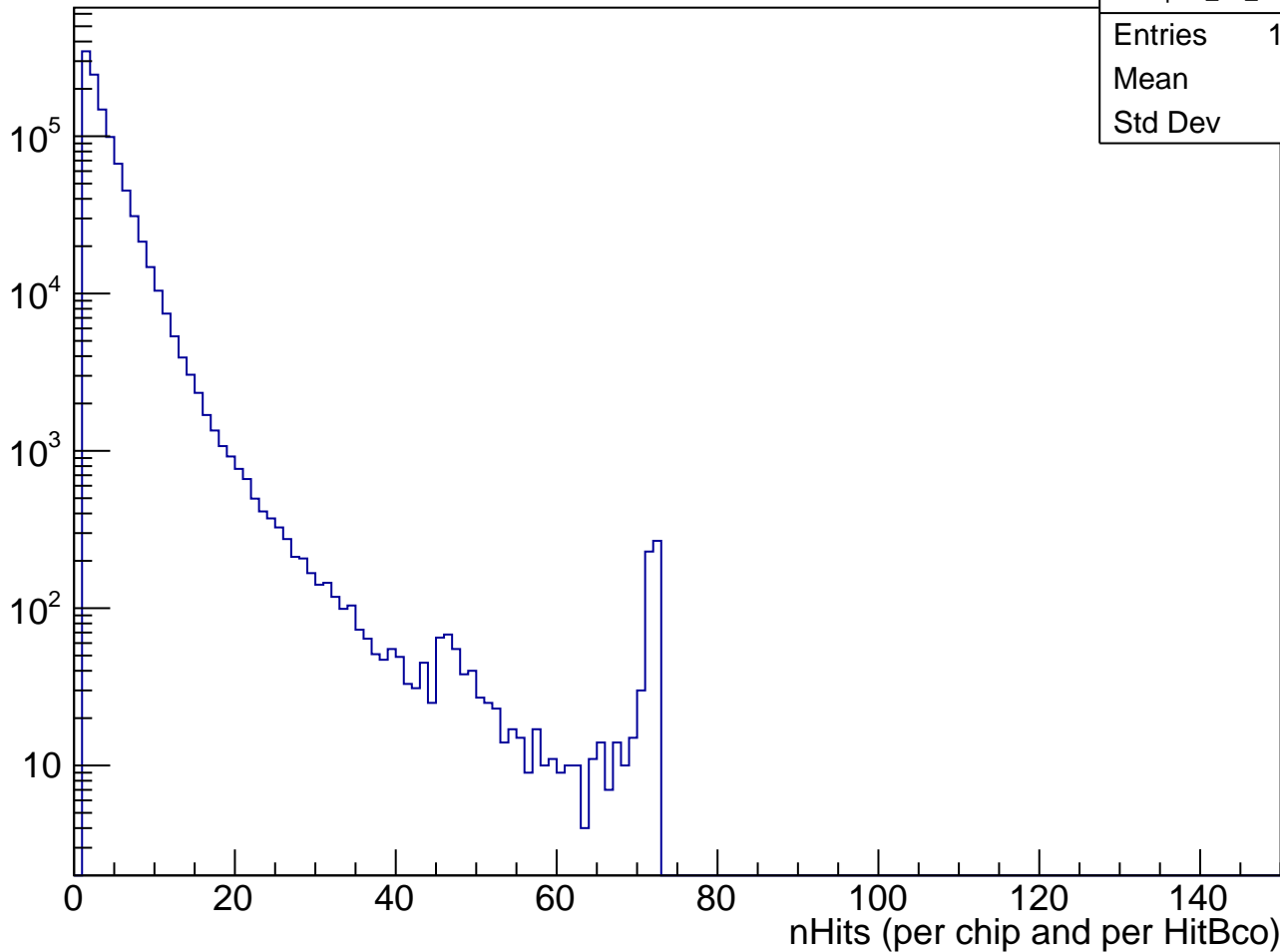
Mean 3.246

Std Dev 3.591

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip7

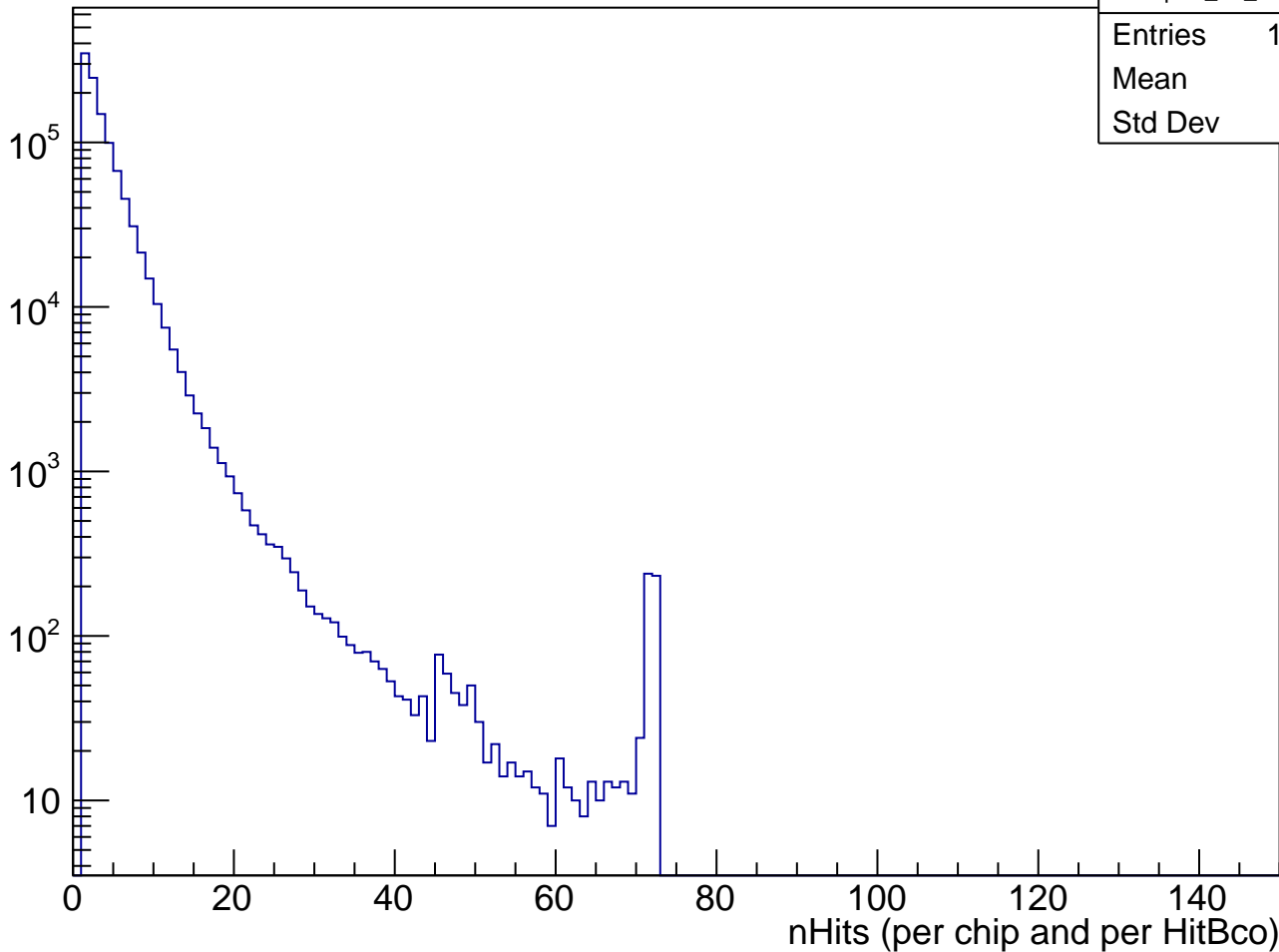
Entries



nChipHit_F0_Fch8_Chip7	
Entries	1062354
Mean	3.26
Std Dev	3.627

nChipHit_F0_Fch8_Chip8

Entries



nChipHit_F0_Fch8_Chip8

Entries 1066522

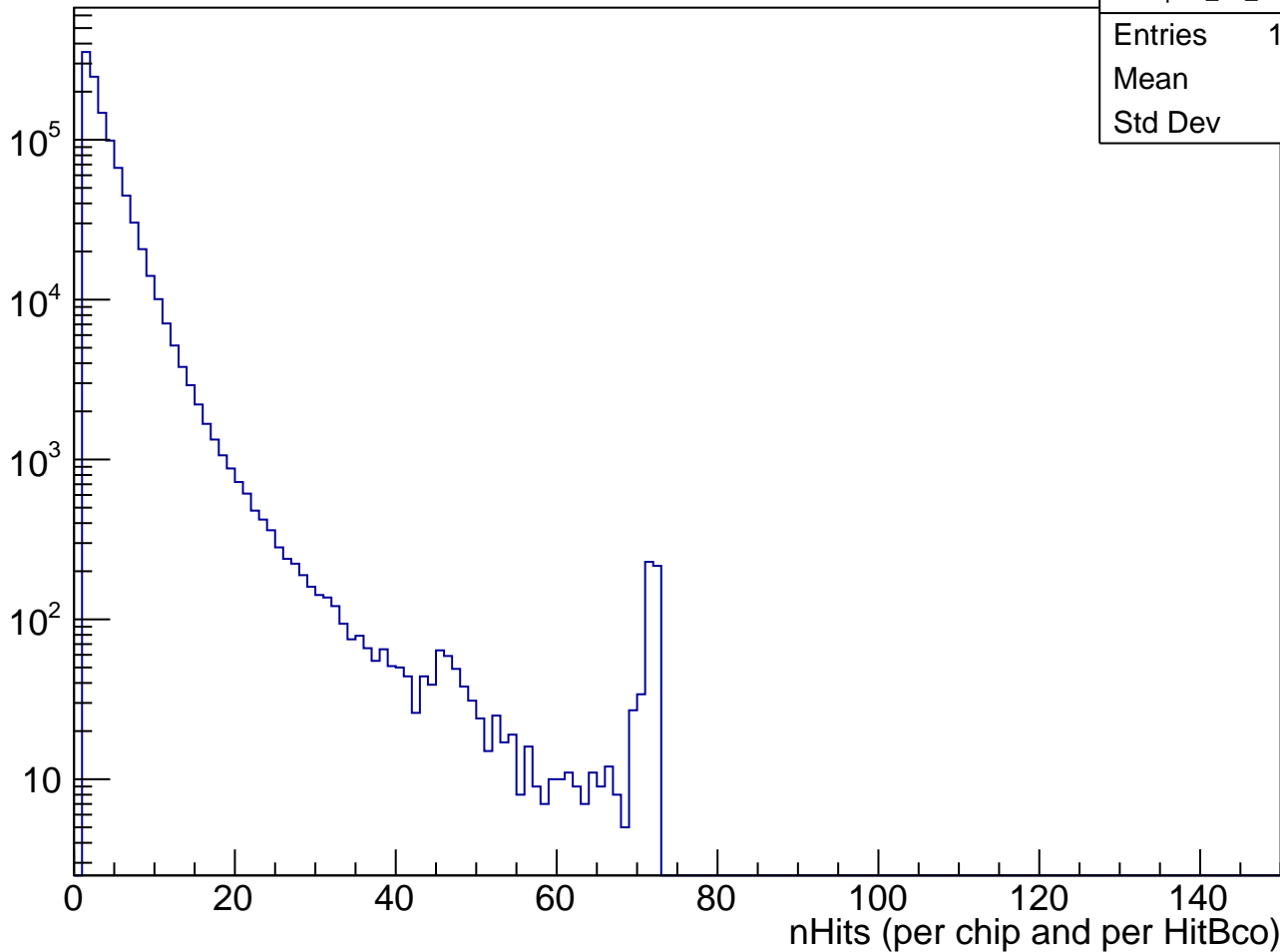
Mean 3.259

Std Dev 3.612

nHits (per chip and per HitBco)

nChipHit_F0_Fch8_Chip9

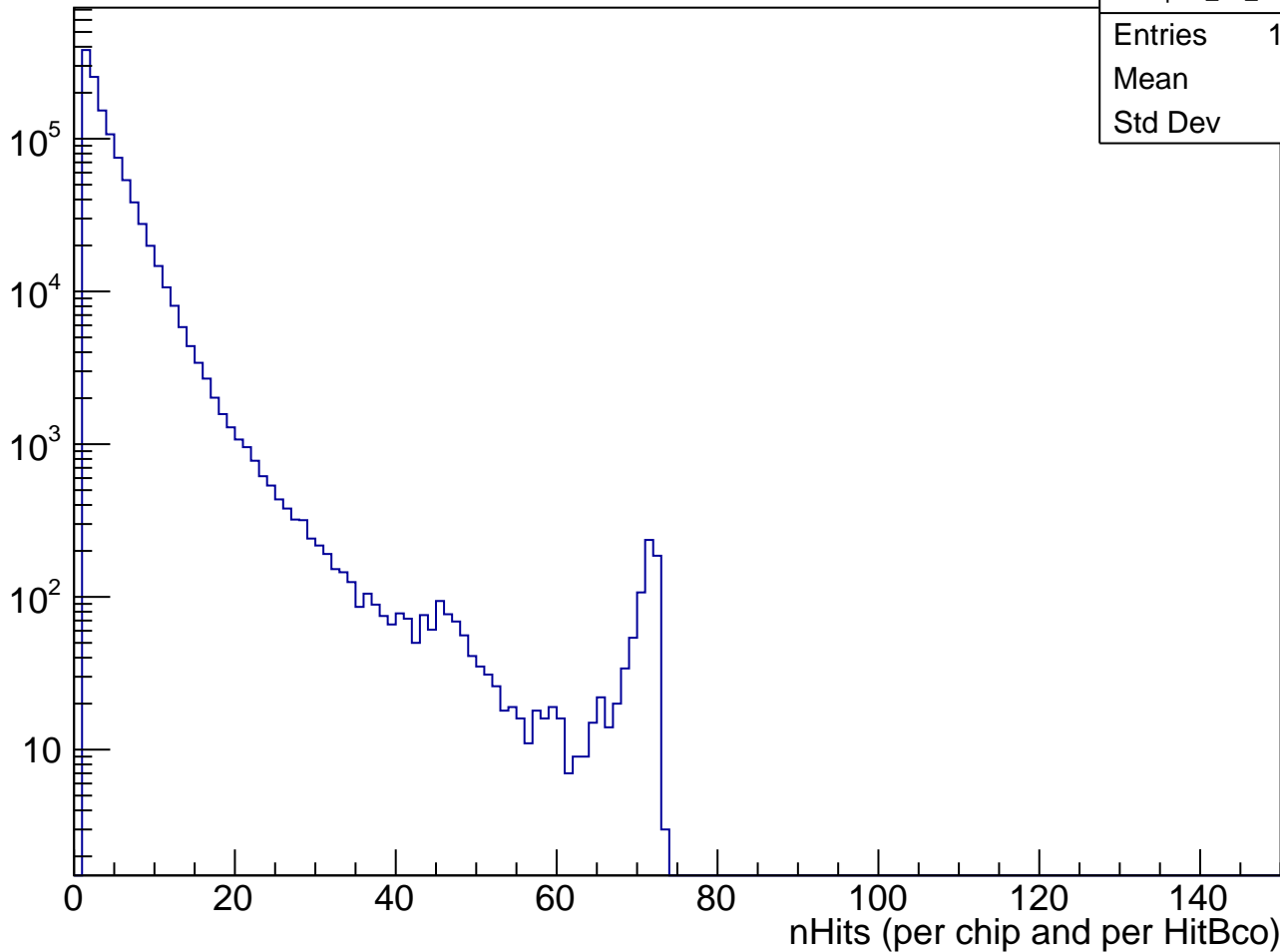
Entries



nChipHit_F0_Fch8_Chip9	
Entries	1067047
Mean	3.213
Std Dev	3.555

nChipHit_F0_Fch9_Chip0

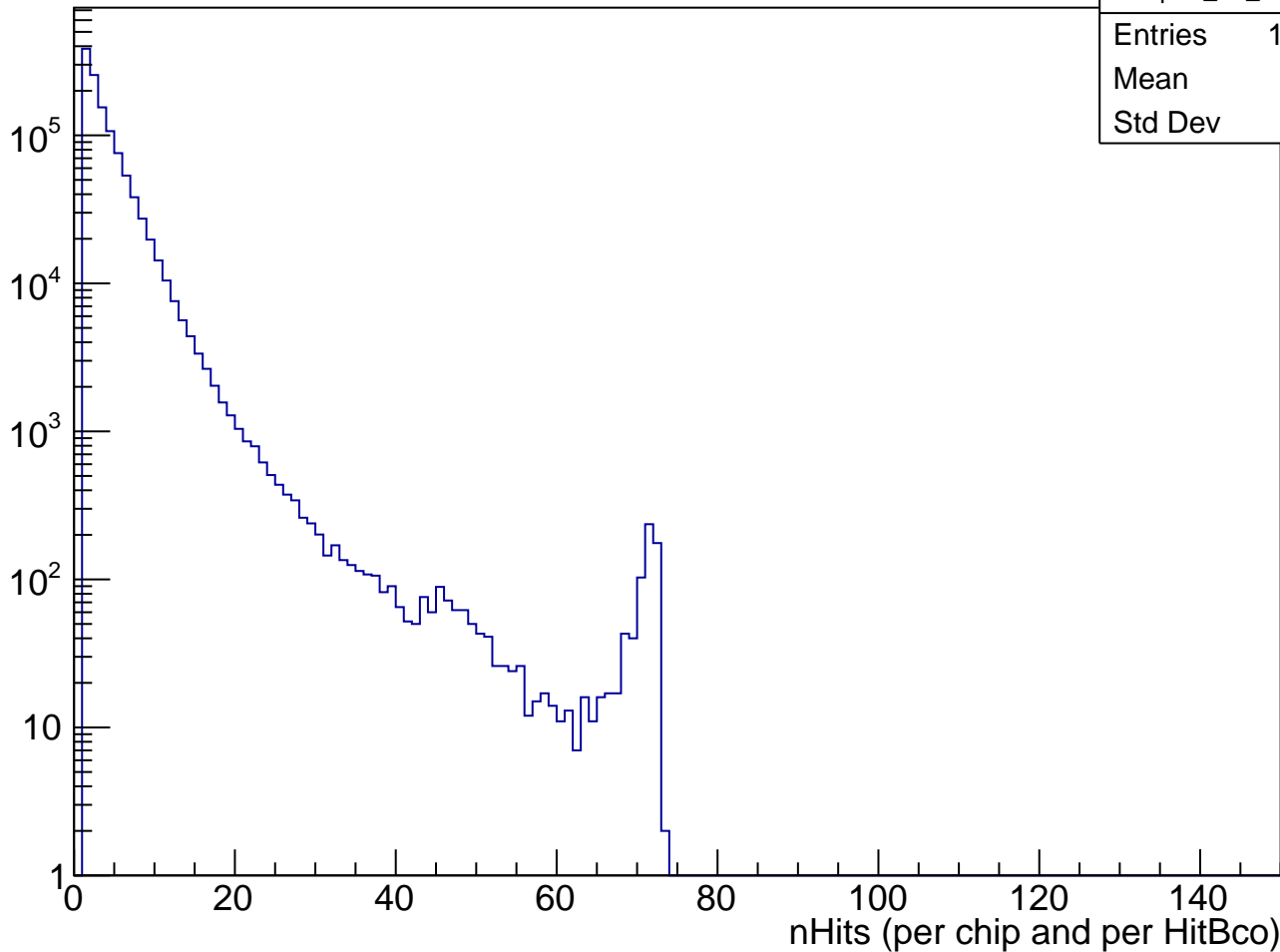
Entries



nChipHit_F0_Fch9_Chip0	
Entries	1172335
Mean	3.484
Std Dev	3.927

nChipHit_F0_Fch9_Chip1

Entries

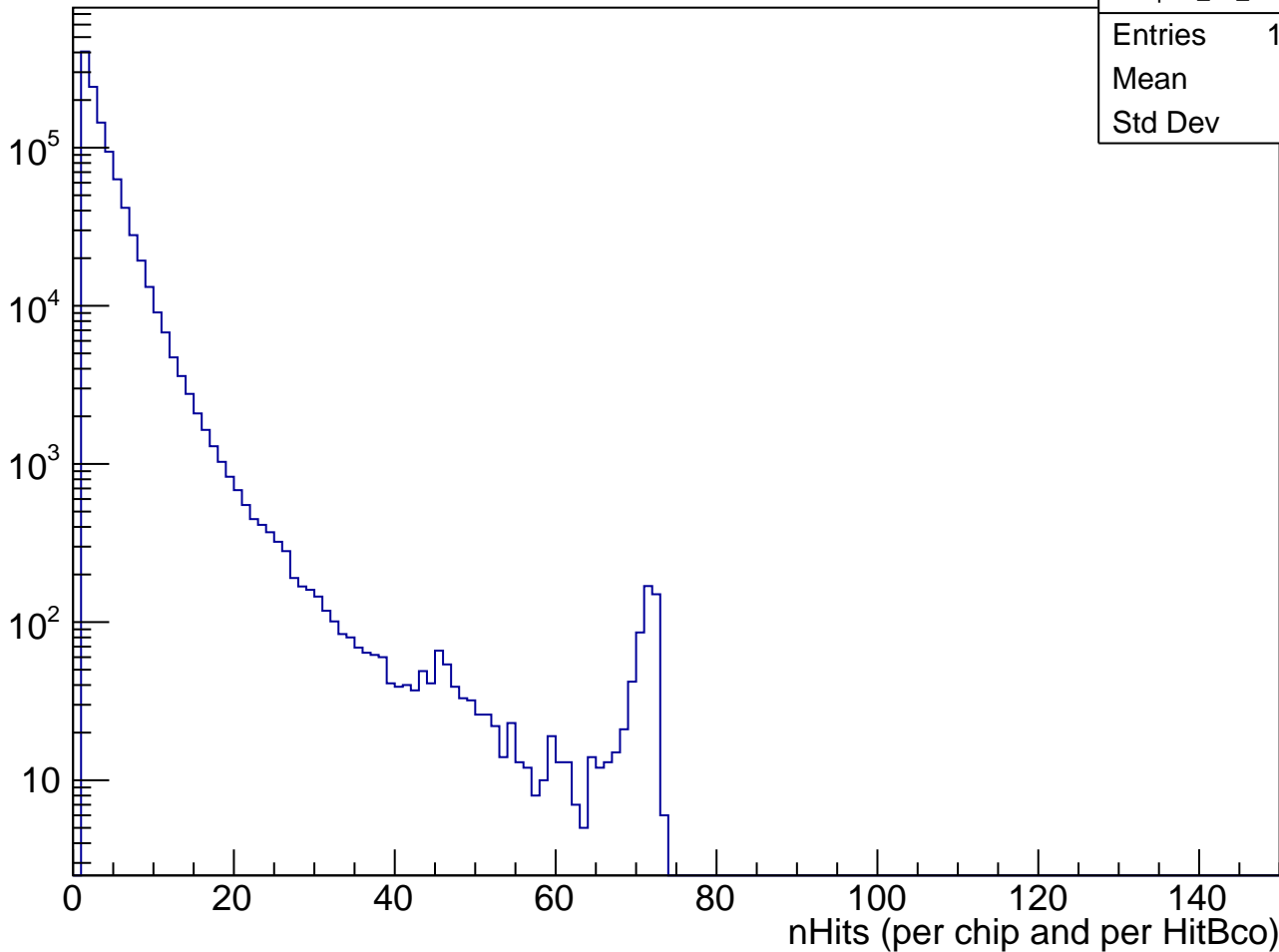


nChipHit_F0_Fch9_Chip1	
Entries	1177695
Mean	3.46
Std Dev	3.899

nHits (per chip and per HitBco)

nChipHit_F0_Fch9_Chip10

Entries

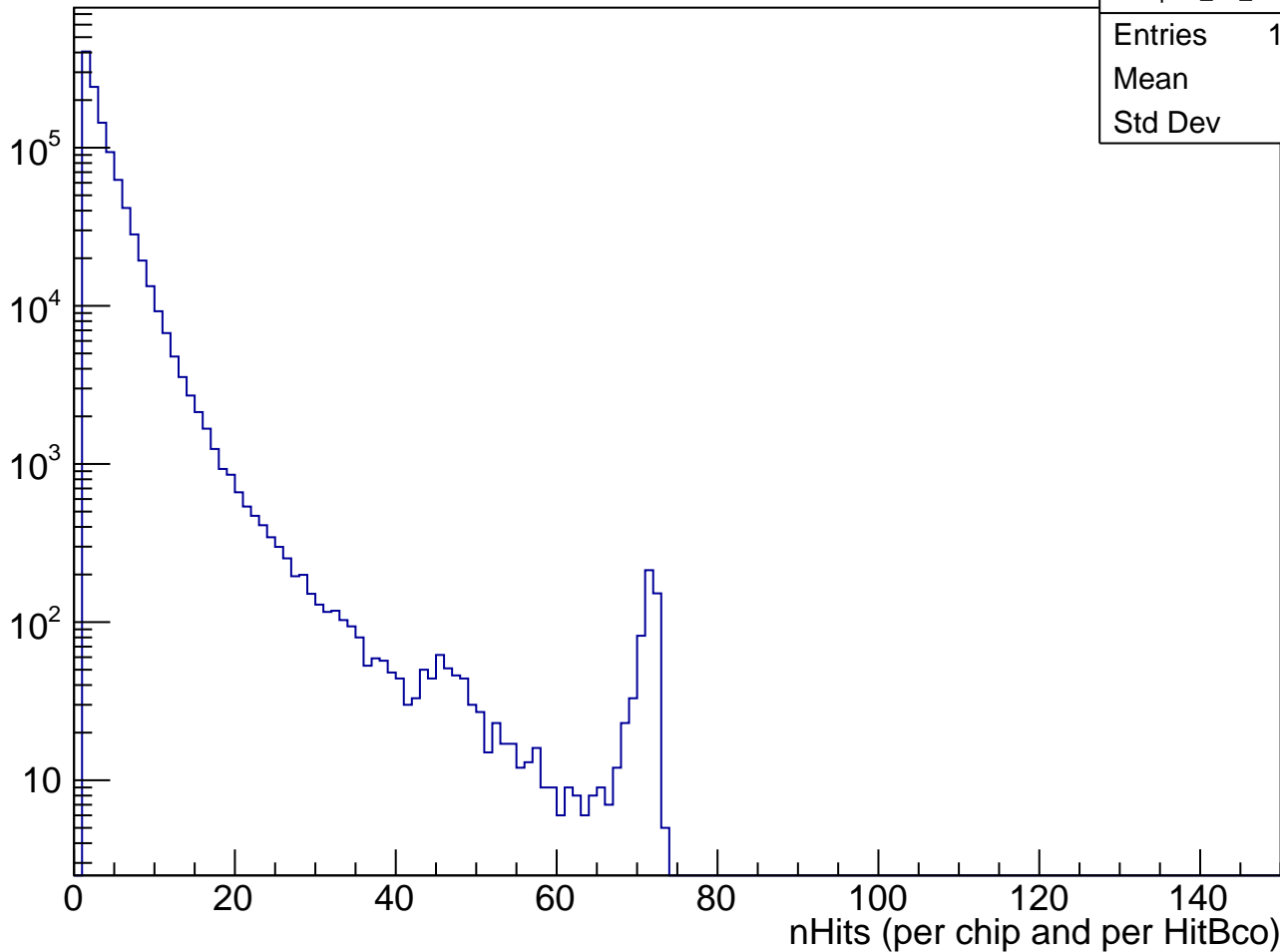


nChipHit_F0_Fch9_Chip10	
Entries	1090609
Mean	3.057
Std Dev	3.478

nHits (per chip and per HitBco)

nChipHit_F0_Fch9_Chip11

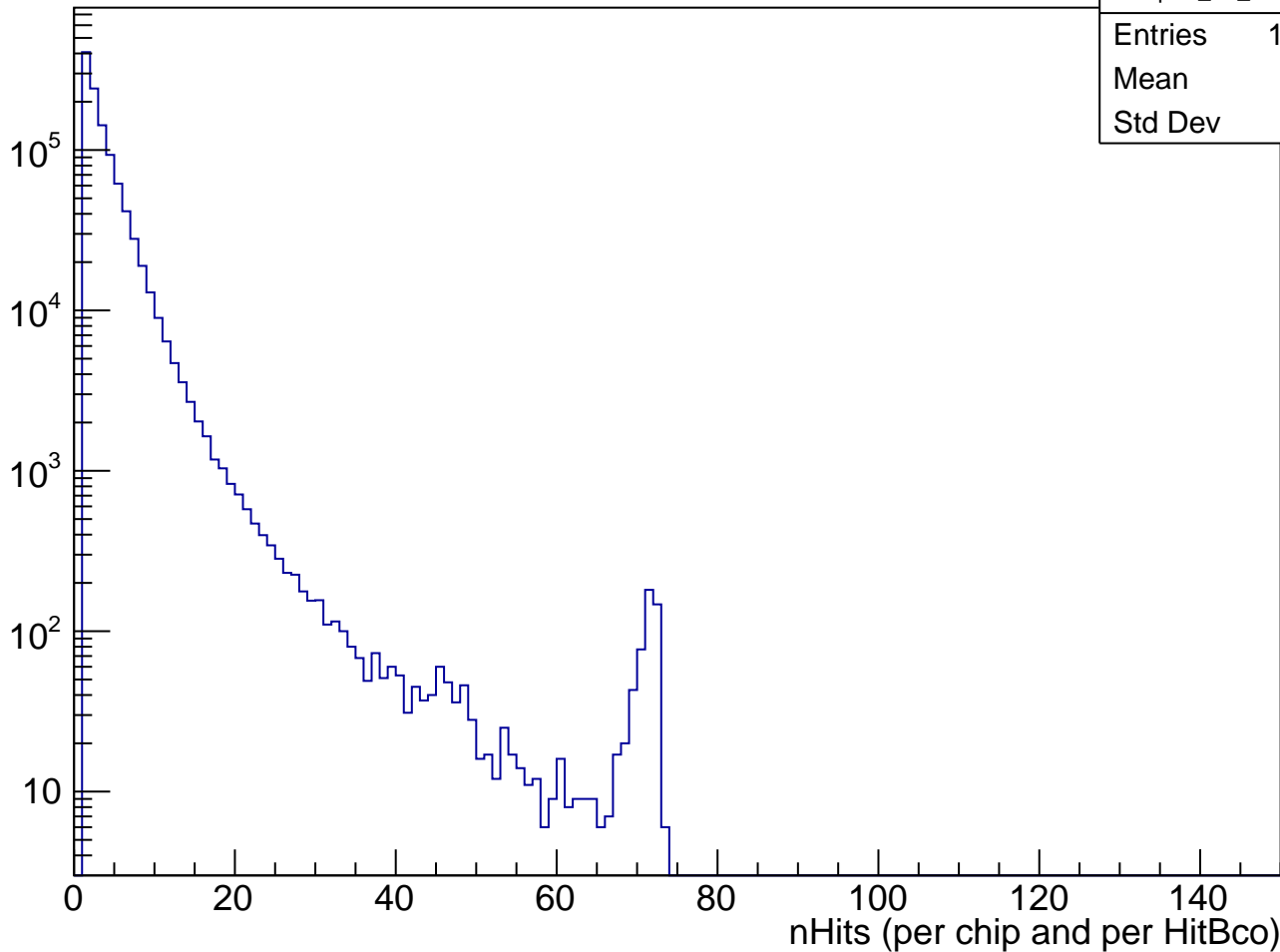
Entries



nChipHit_F0_Fch9_Chip11	
Entries	1090142
Mean	3.056
Std Dev	3.483

nChipHit_F0_Fch9_Chip12

Entries

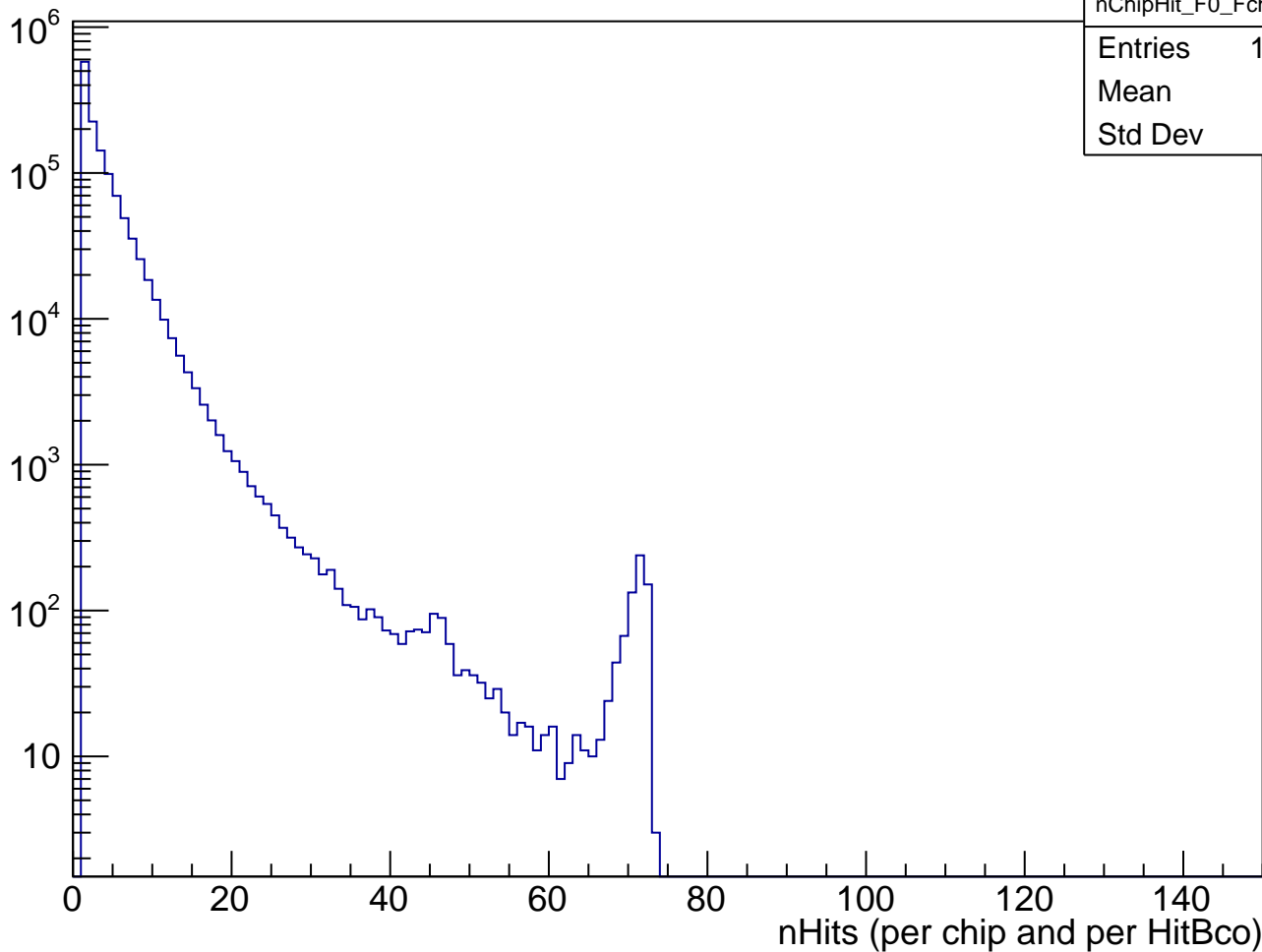


nChipHit_F0_Fch9_Chip12	
Entries	1086842
Mean	3.038
Std Dev	3.456

nHits (per chip and per HitBco)

nChipHit_F0_Fch9_Chip13

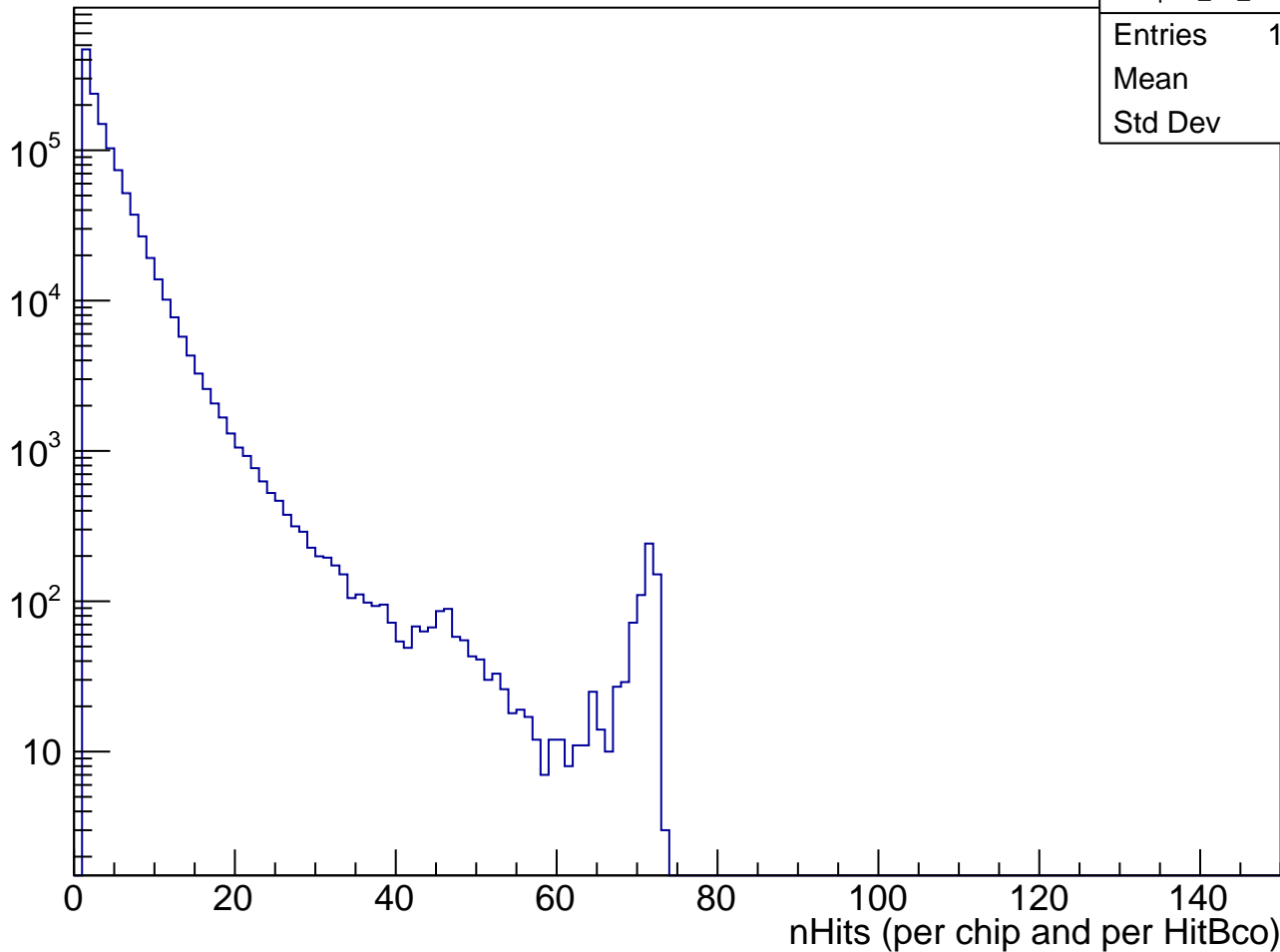
Entries



nChipHit_F0_Fch9_Chip13	
Entries	1302822
Mean	3.084
Std Dev	3.782

nChipHit_F0_Fch9_Chip14

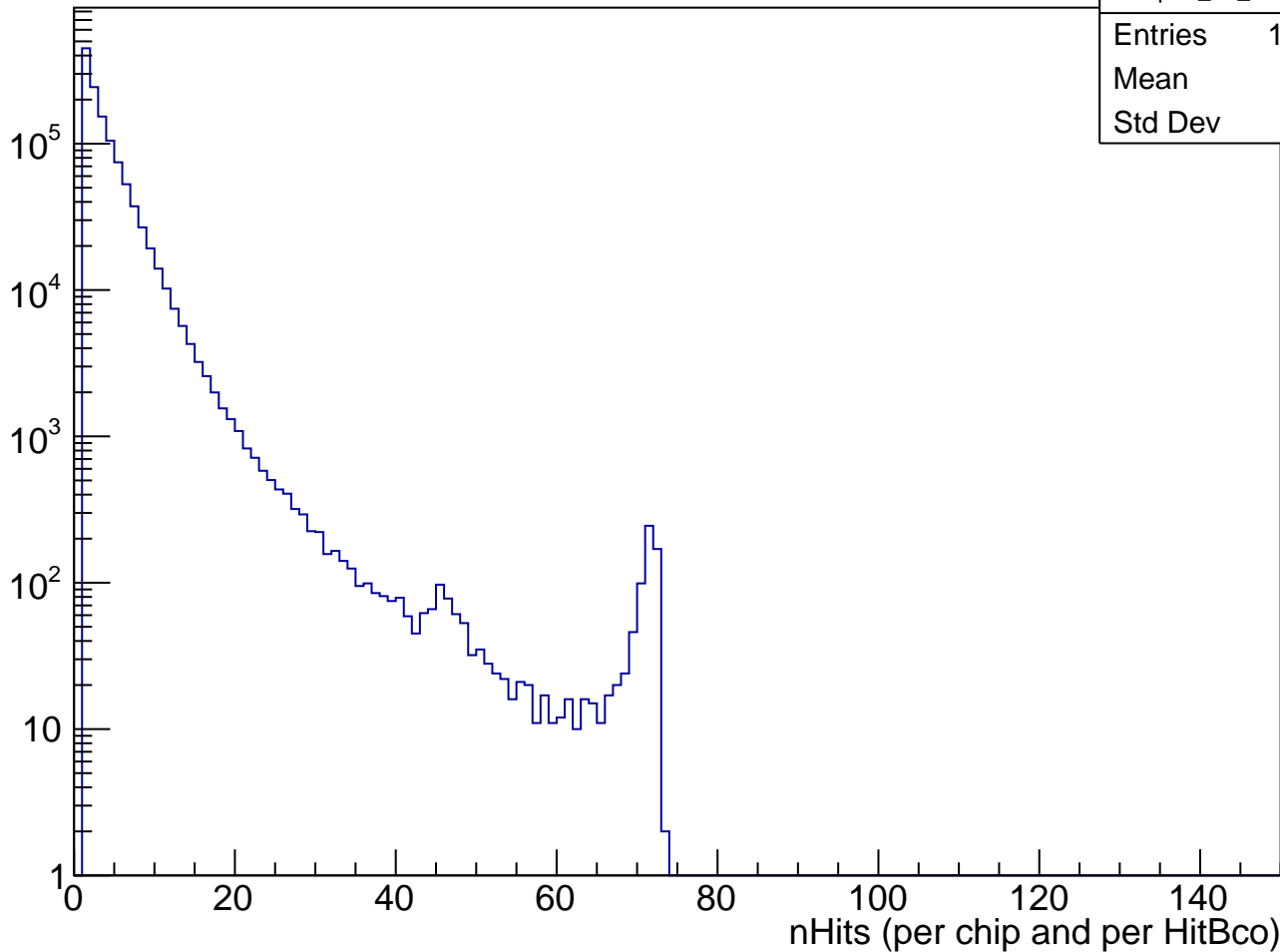
Entries



nChipHit_F0_Fch9_Chip14	
Entries	1229207
Mean	3.298
Std Dev	3.856

nChipHit_F0_Fch9_Chip15

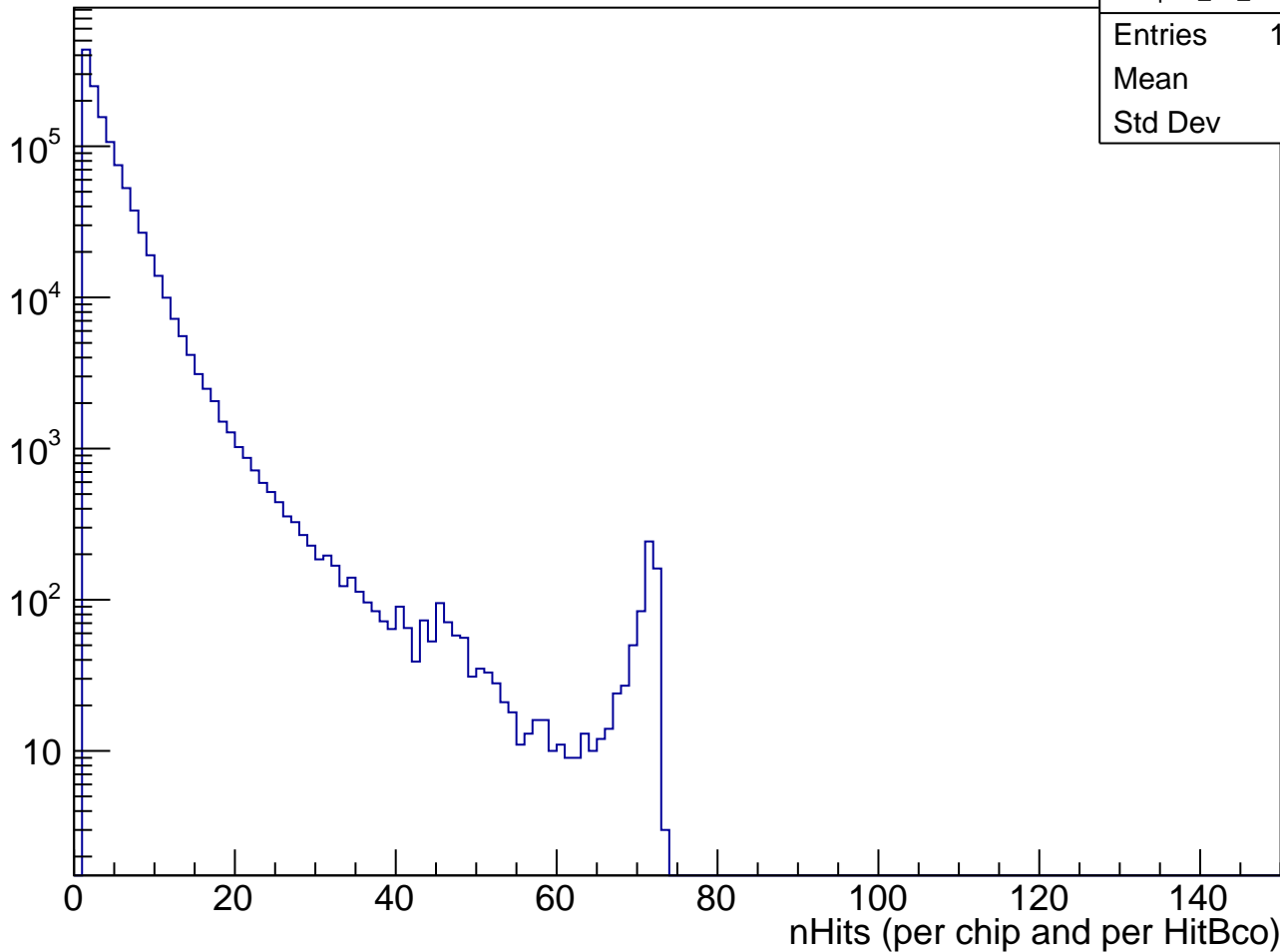
Entries



nChipHit_F0_Fch9_Chip15	
Entries	1221347
Mean	3.325
Std Dev	3.828

nChipHit_F0_Fch9_Chip16

Entries



nChipHit_F0_Fch9_Chip16

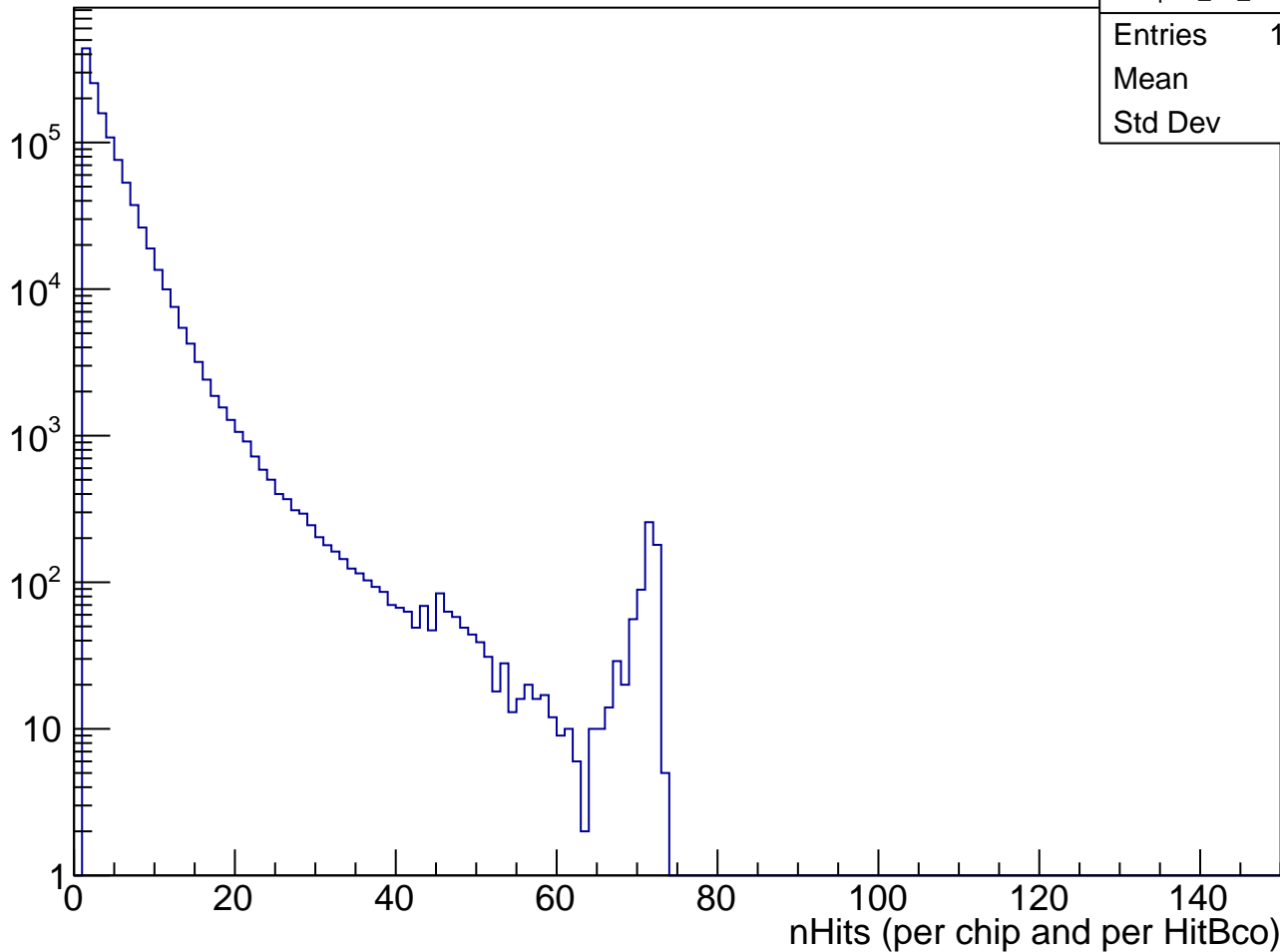
Entries 1218770

Mean 3.331

Std Dev 3.795

nChipHit_F0_Fch9_Chip17

Entries



nChipHit_F0_Fch9_Chip17

Entries 1230673

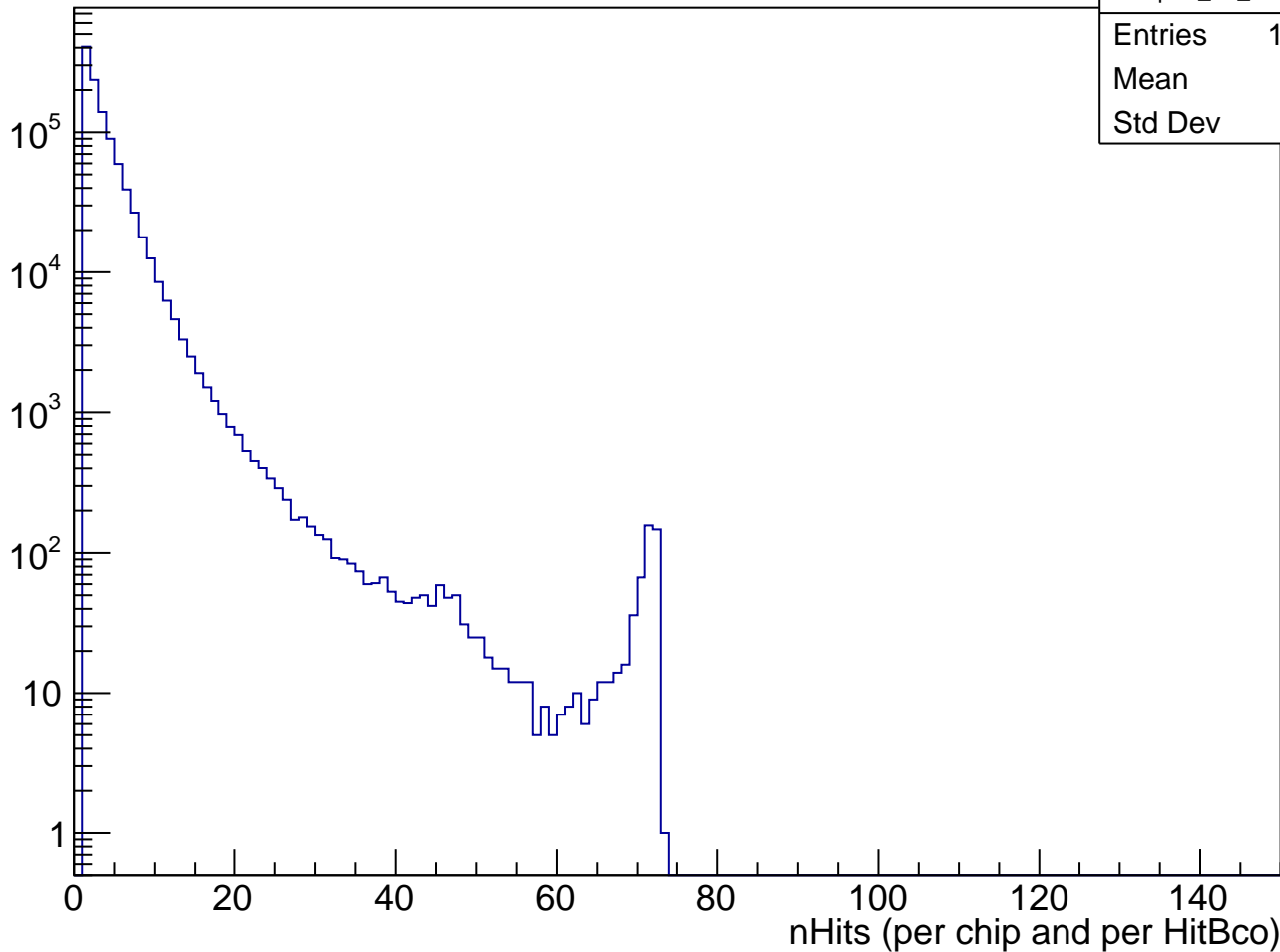
Mean 3.321

Std Dev 3.795

nHits (per chip and per HitBco)

nChipHit_F0_Fch9_Chip18

Entries

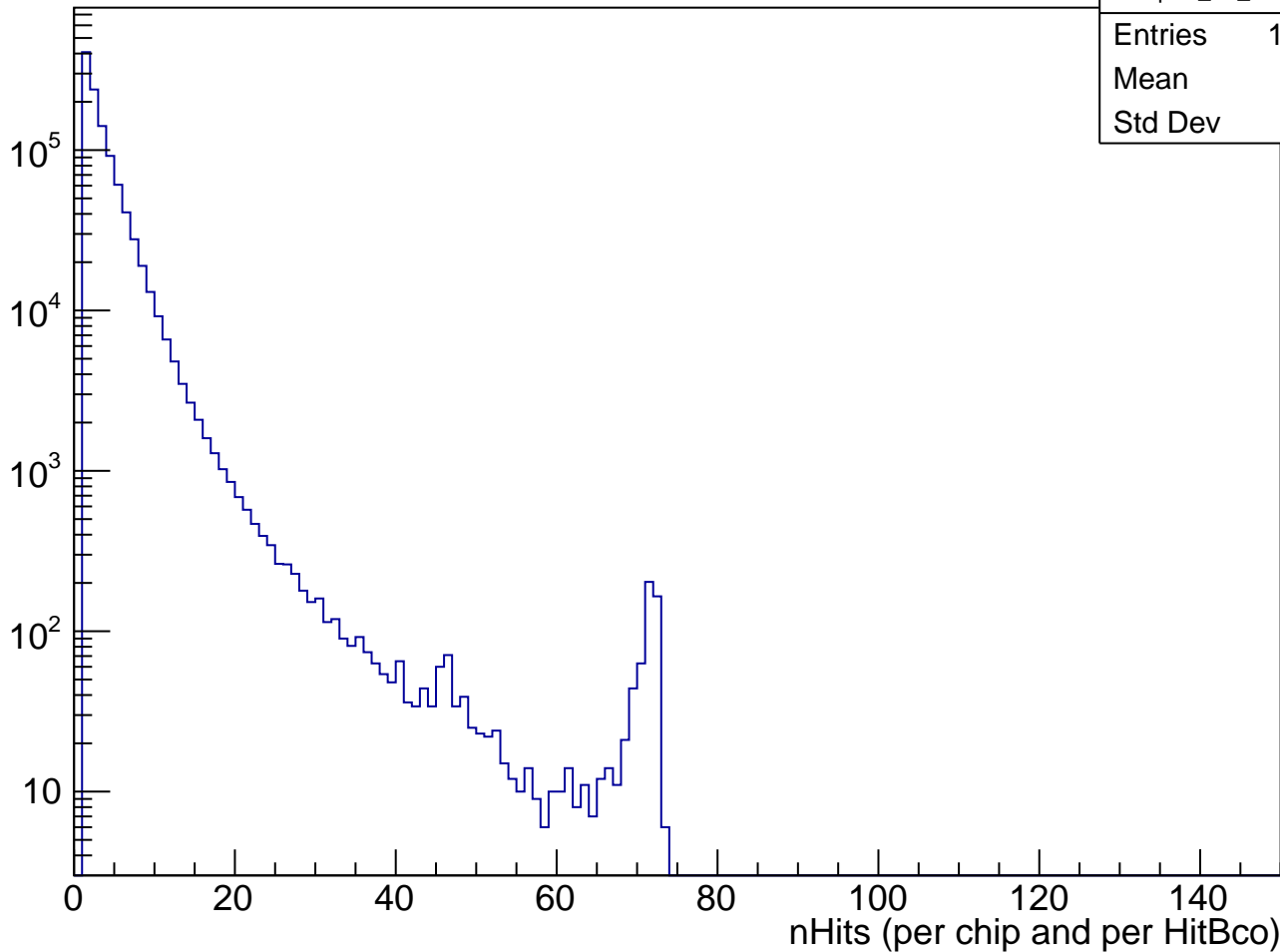


nChipHit_F0_Fch9_Chip18	
Entries	1064942
Mean	2.998
Std Dev	3.415

nHits (per chip and per HitBco)

nChipHit_F0_Fch9_Chip19

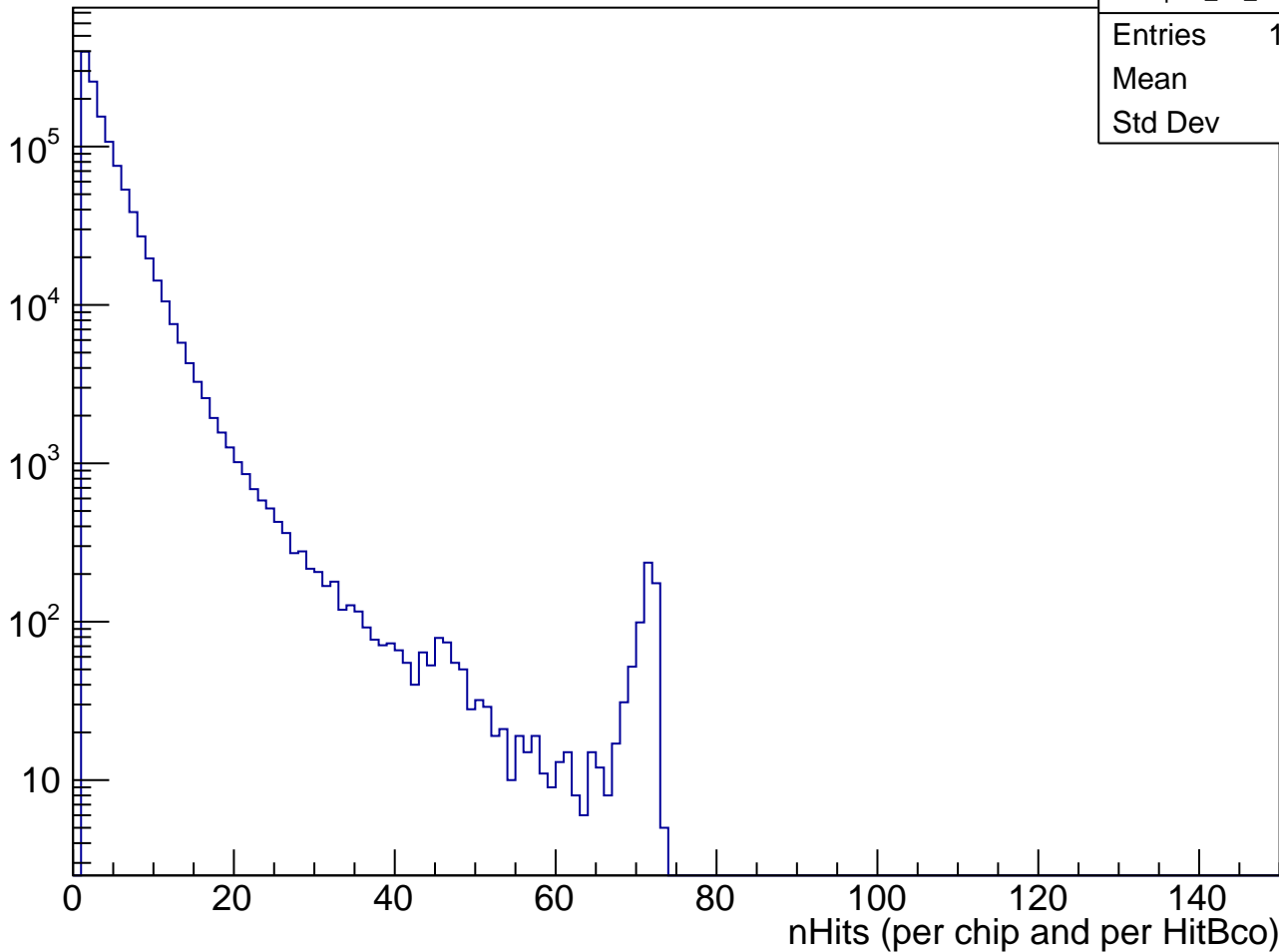
Entries



nChipHit_F0_Fch9_Chip19	
Entries	1080646
Mean	3.044
Std Dev	3.5

nChipHit_F0_Fch9_Chip2

Entries

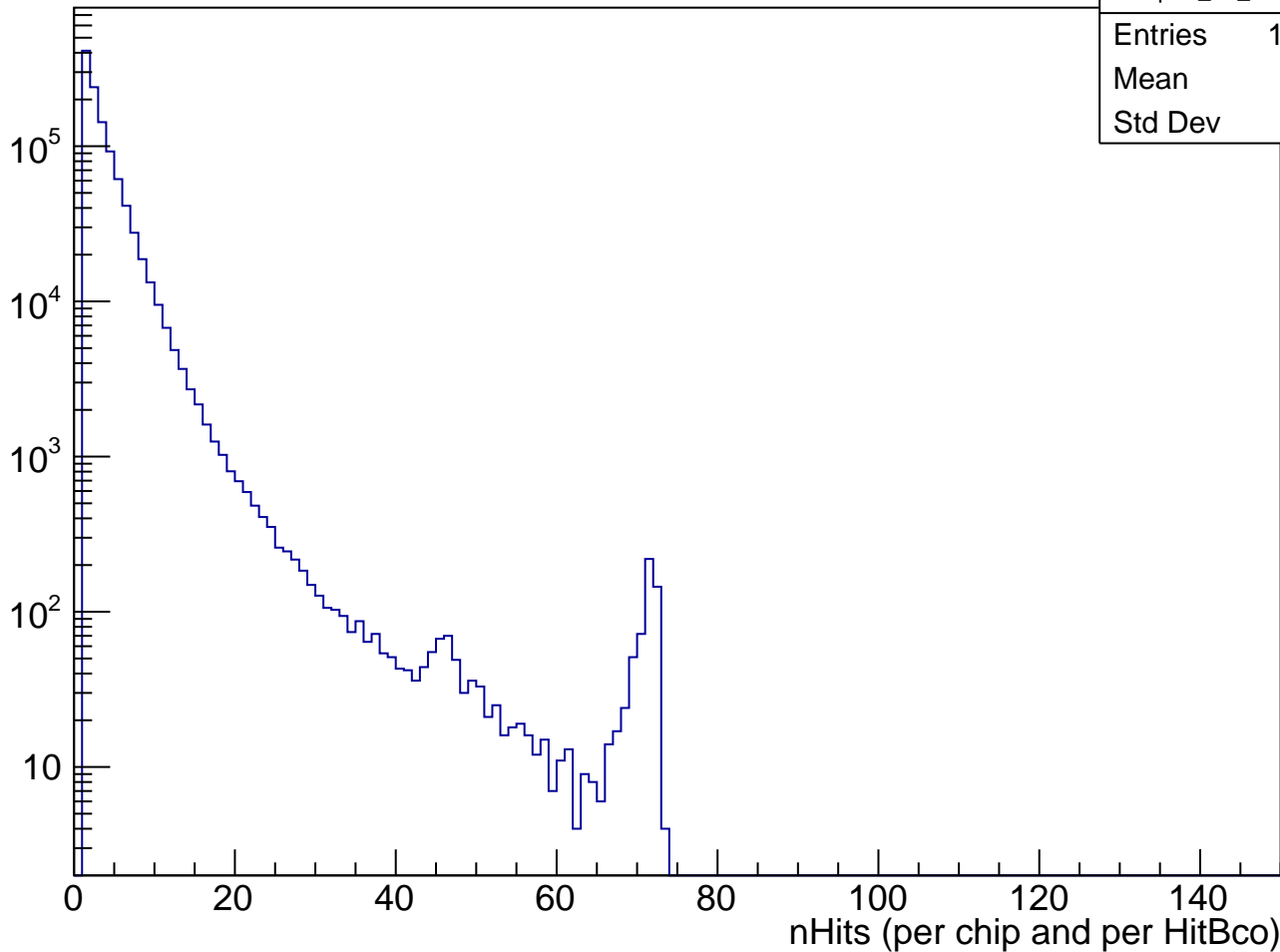


nChipHit_F0_Fch9_Chip2	
Entries	1191623
Mean	3.416
Std Dev	3.823

nHits (per chip and per HitBco)

nChipHit_F0_Fch9_Chip20

Entries

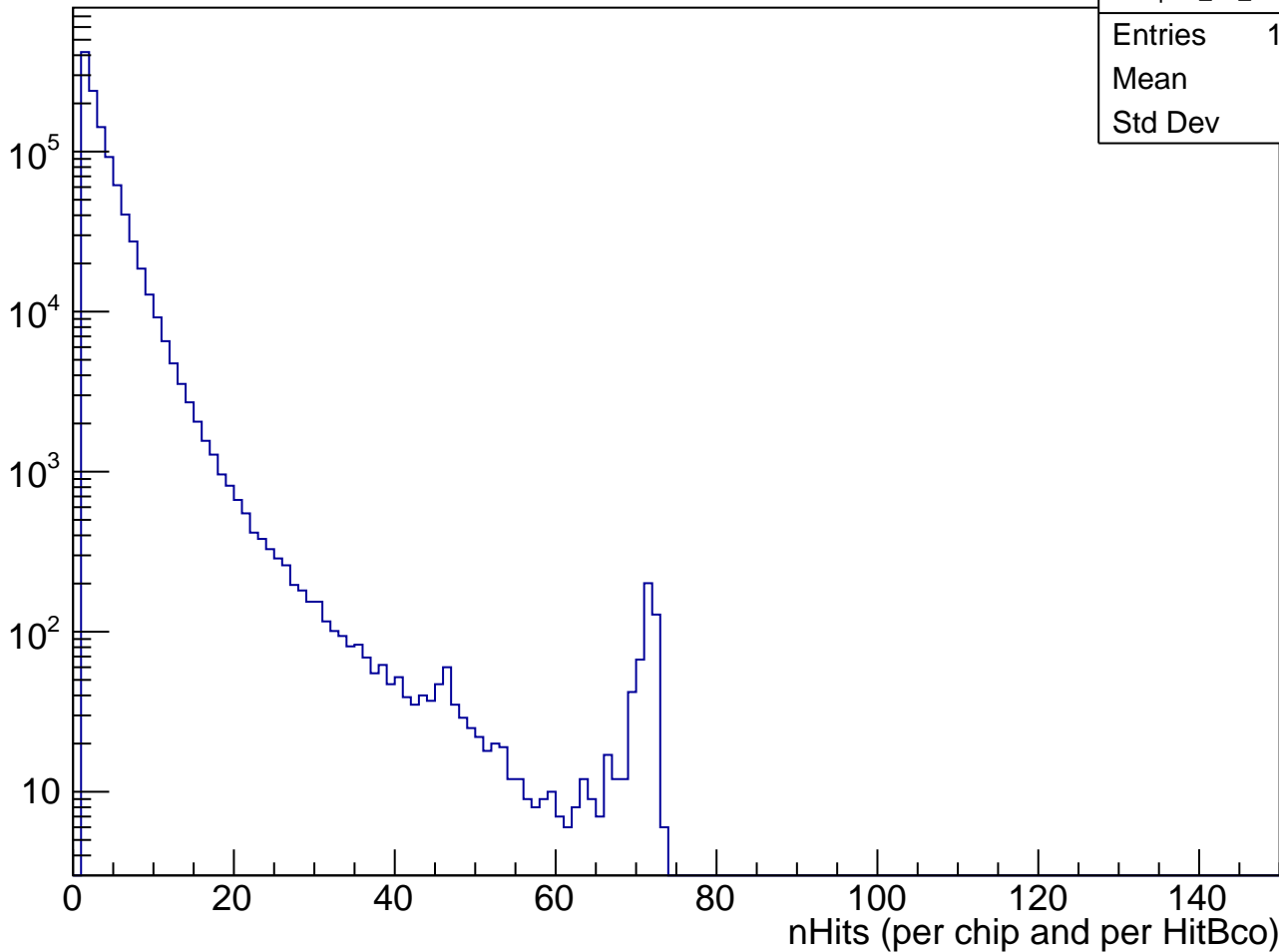


nChipHit_F0_Fch9_Chip20	
Entries	1090128
Mean	3.045
Std Dev	3.515

nHits (per chip and per HitBco)

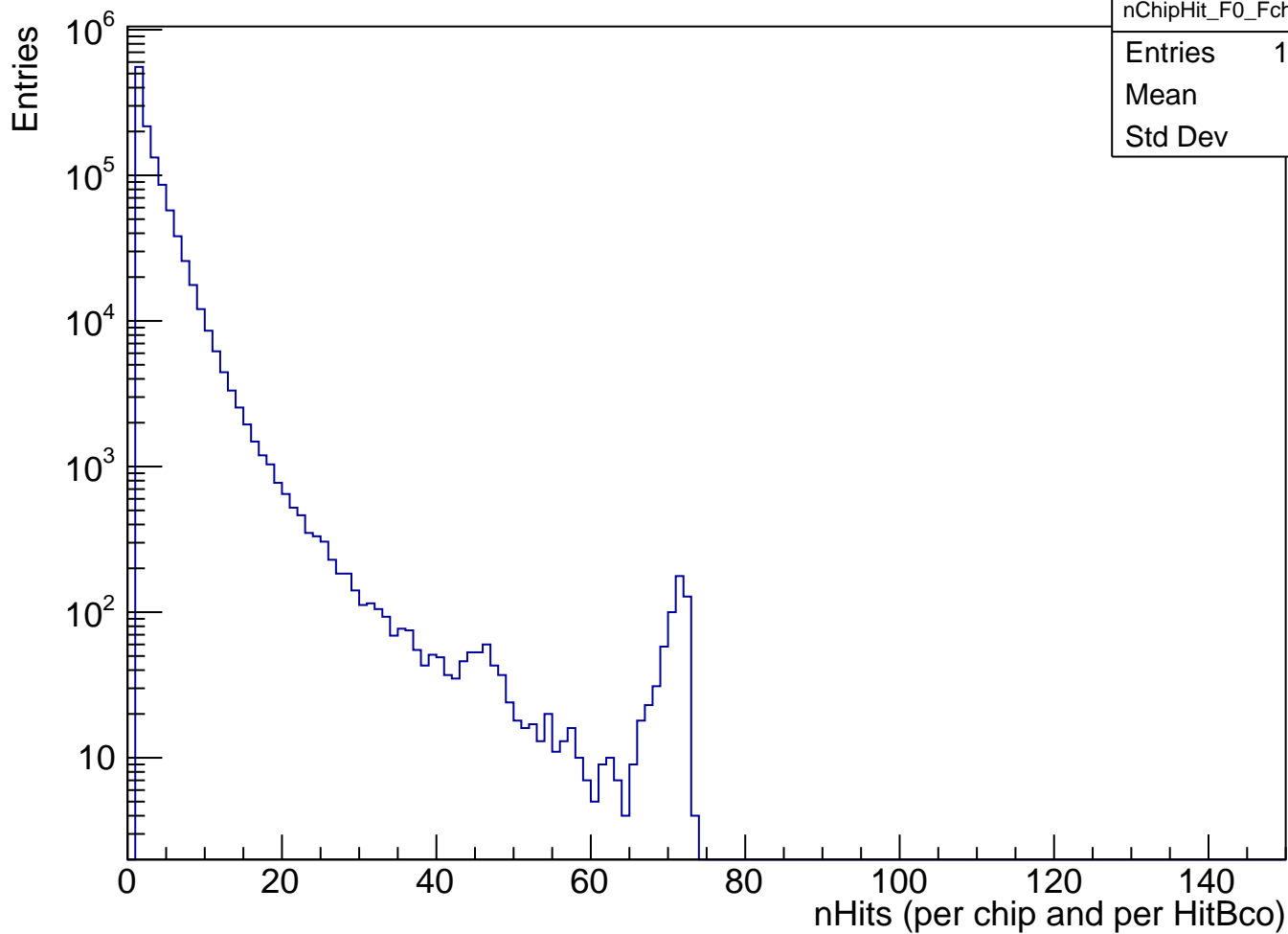
nChipHit_F0_Fch9_Chip21

Entries



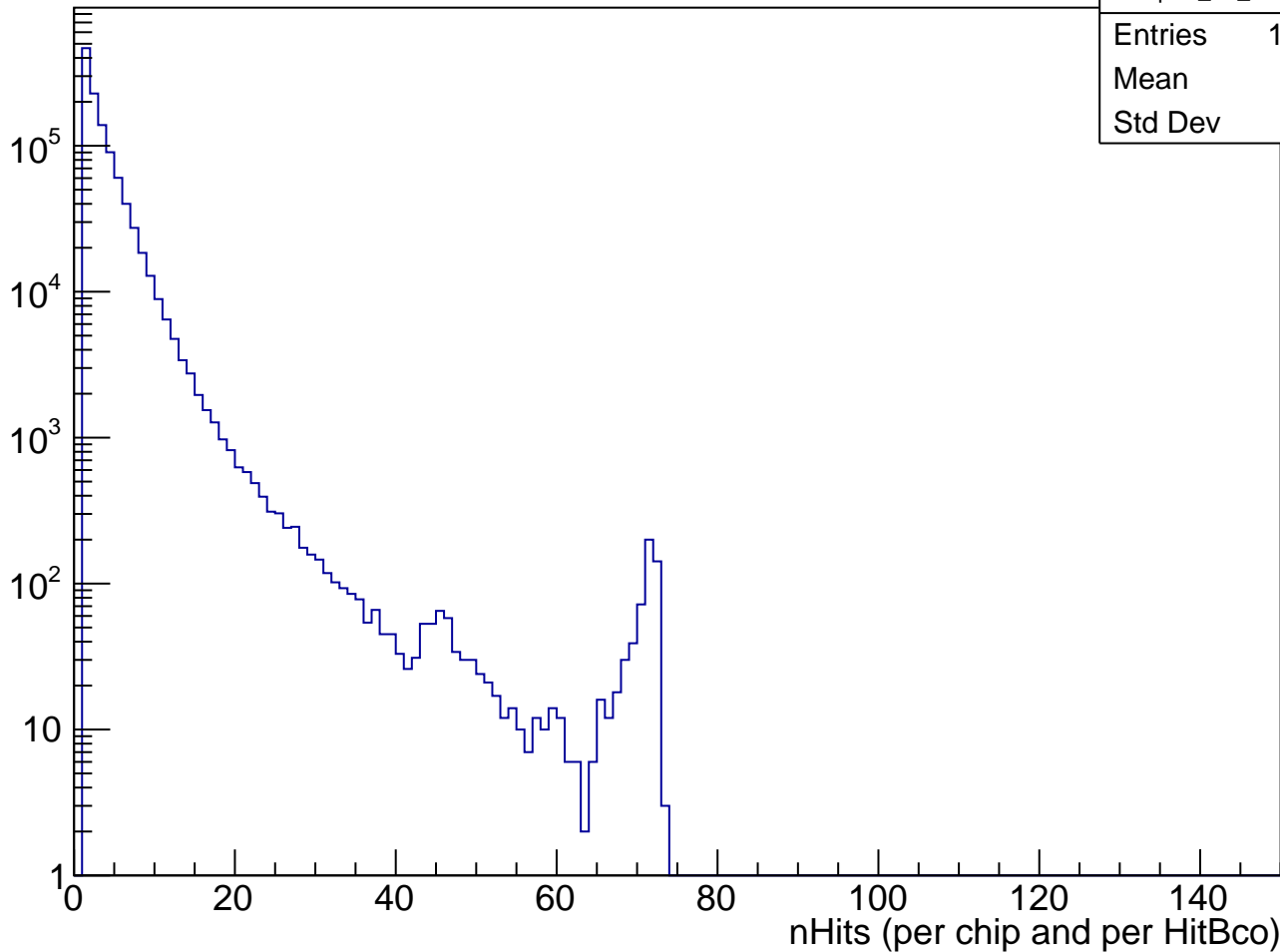
nChipHit_F0_Fch9_Chip21	
Entries	1092415
Mean	3.008
Std Dev	3.425

nChipHit_F0_Fch9_Chip22



nChipHit_F0_Fch9_Chip23

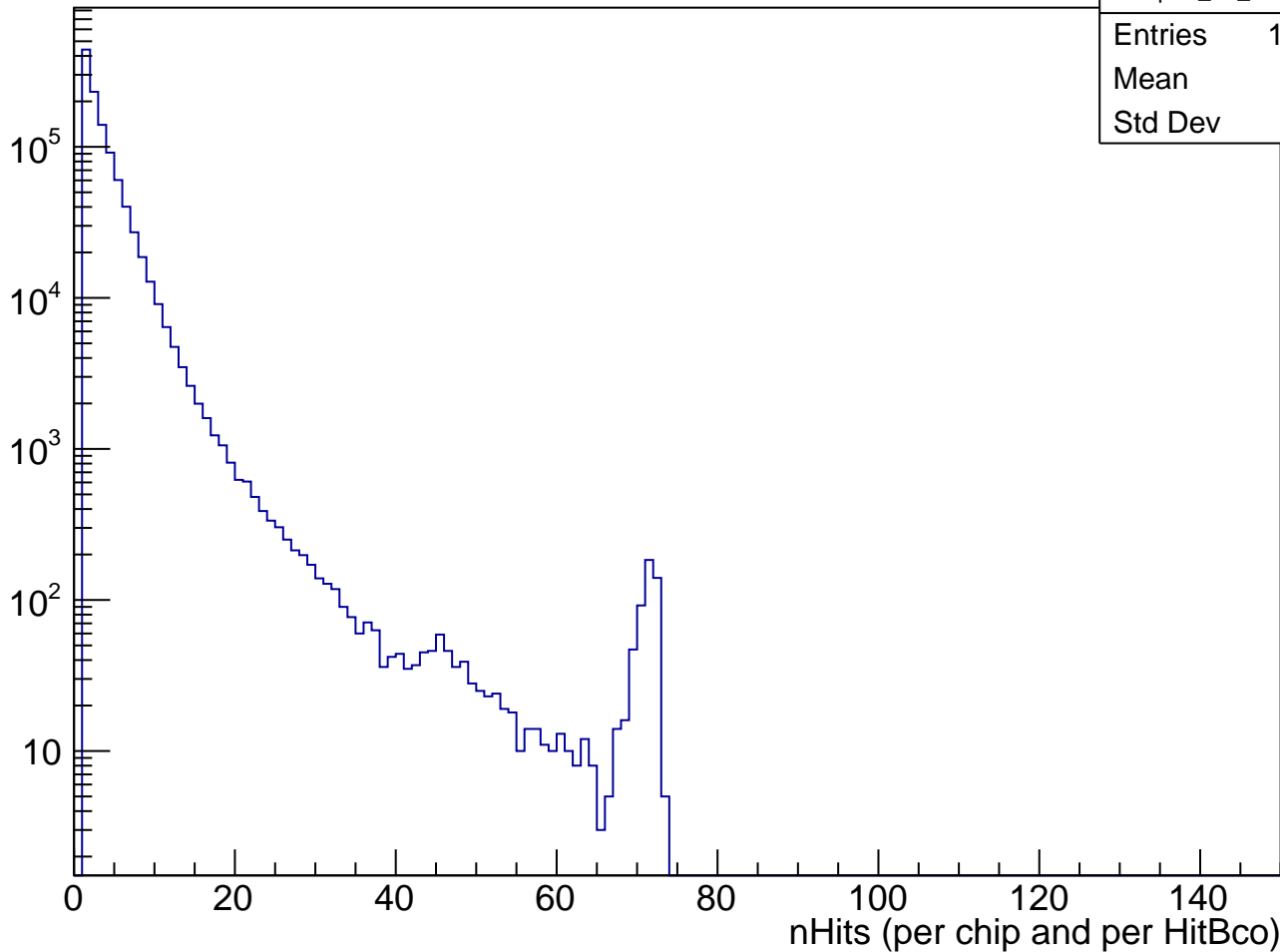
Entries



nChipHit_F0_Fch9_Chip23	
Entries	1121024
Mean	2.926
Std Dev	3.422

nChipHit_F0_Fch9_Chip24

Entries

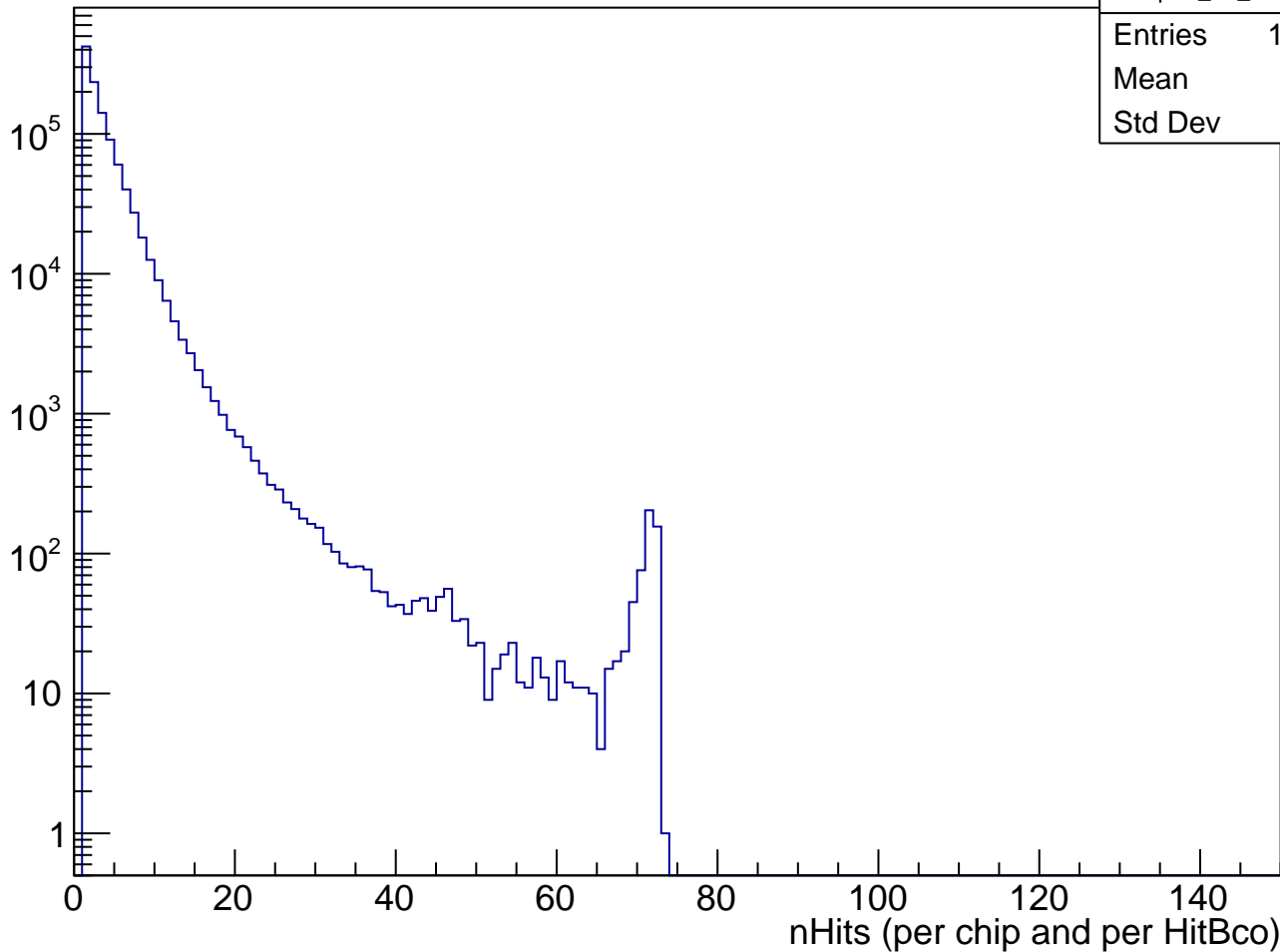


nChipHit_F0_Fch9_Chip24	
Entries	1100939
Mean	2.974
Std Dev	3.449

nHits (per chip and per HitBco)

nChipHit_F0_Fch9_Chip25

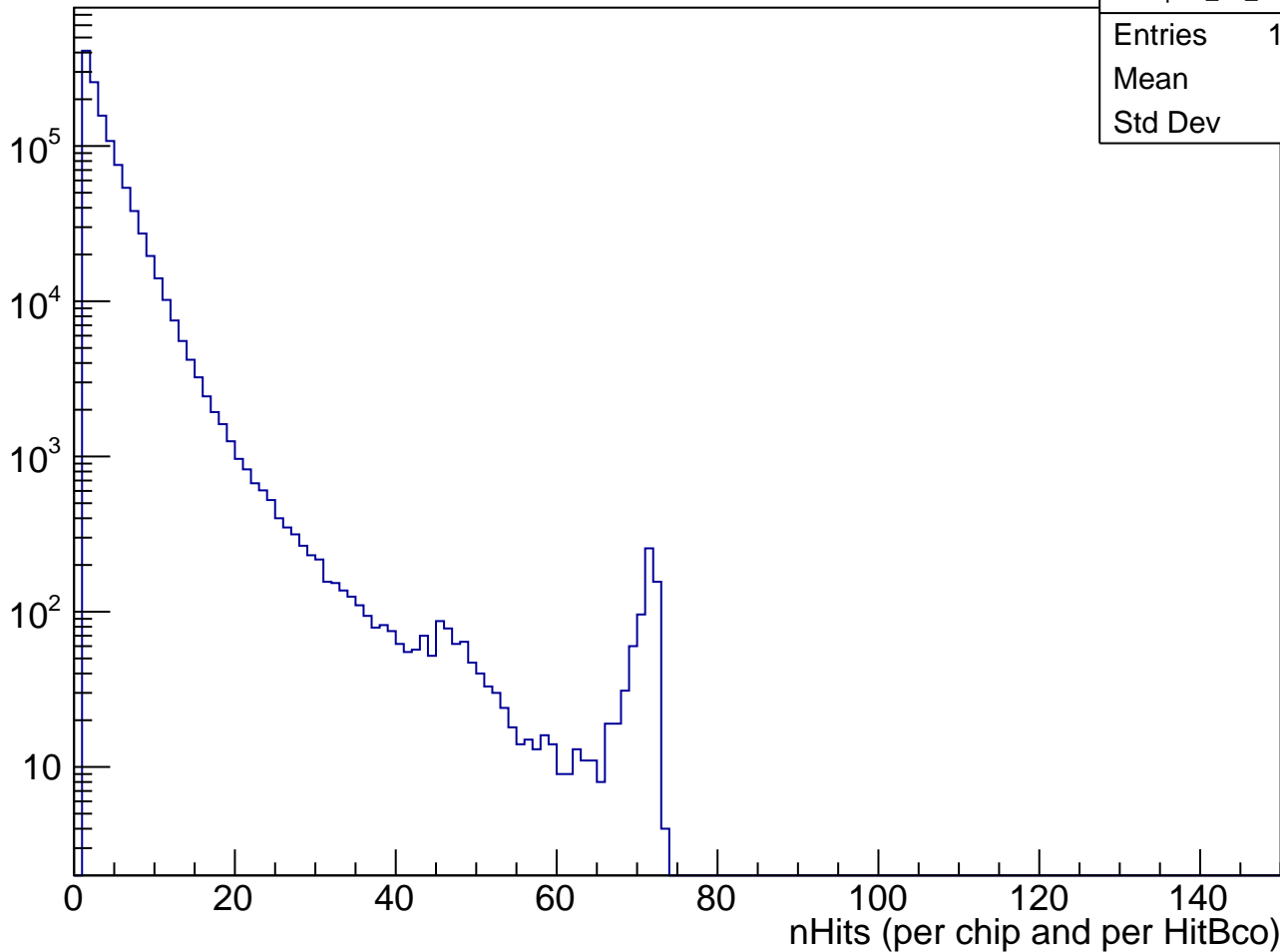
Entries



nChipHit_F0_Fch9_Chip25	
Entries	1085337
Mean	2.998
Std Dev	3.468

nChipHit_F0_Fch9_Chip3

Entries



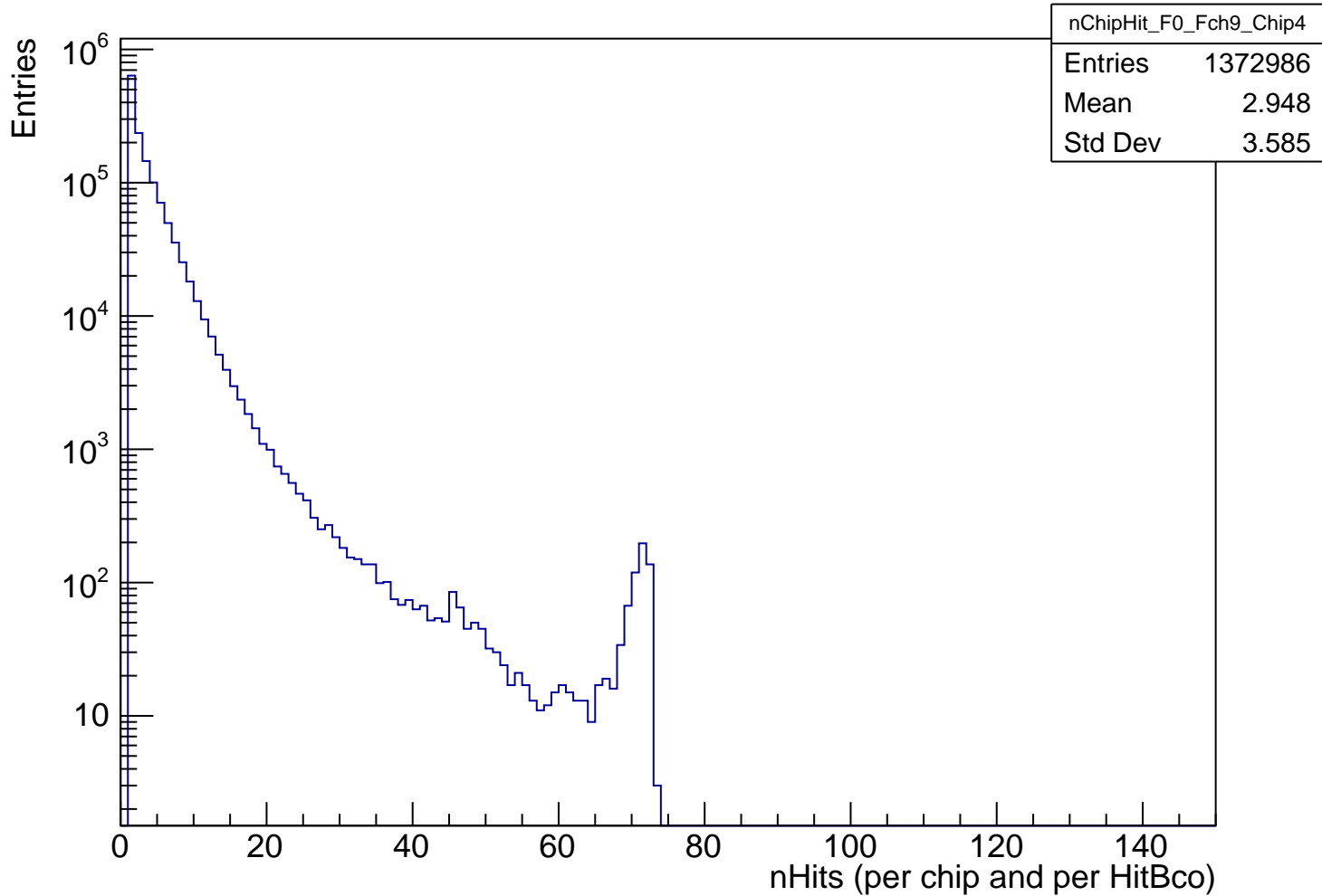
nChipHit_F0_Fch9_Chip3

Entries 1207357

Mean 3.384

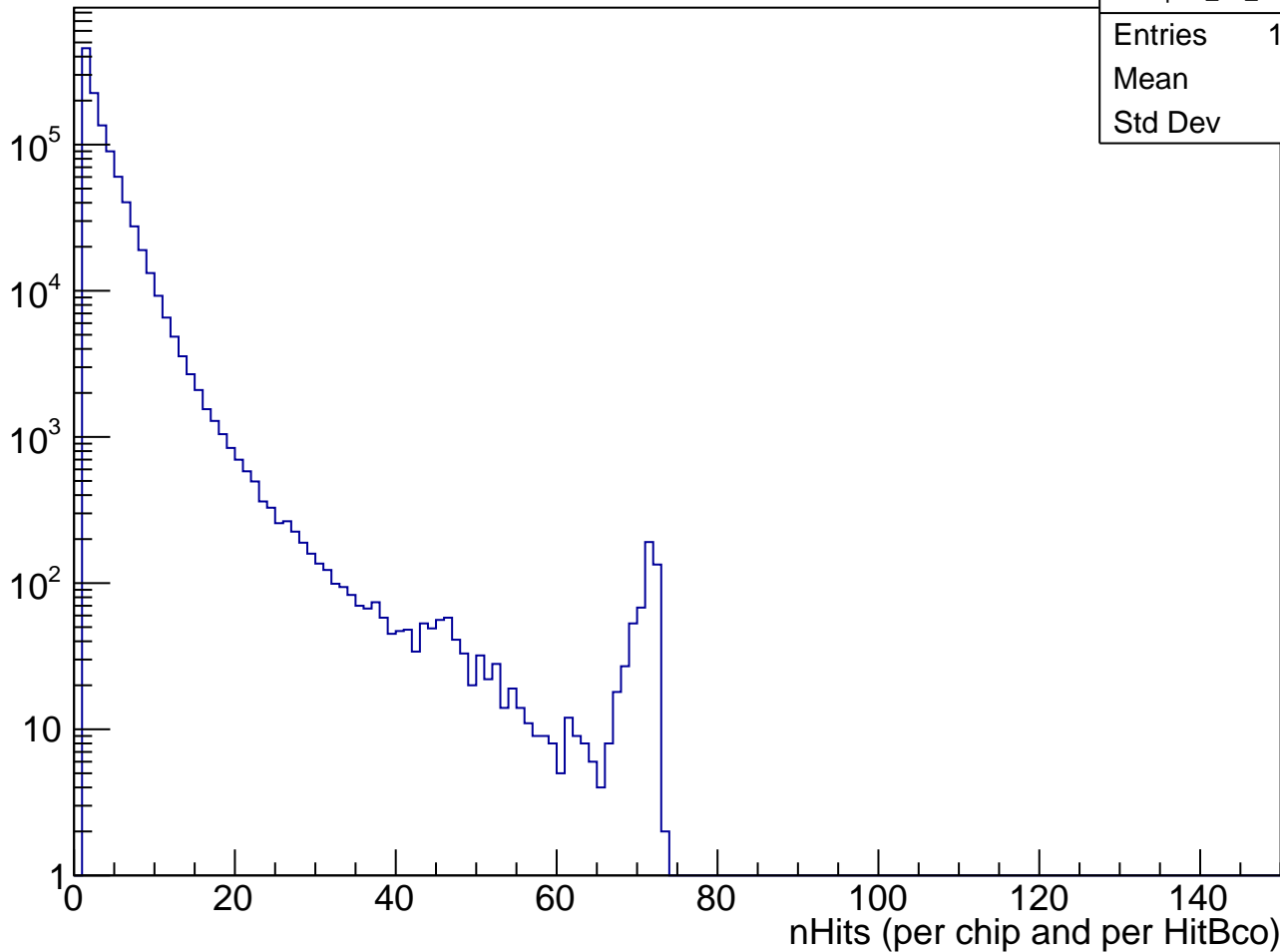
Std Dev 3.824

nChipHit_F0_Fch9_Chip4



nChipHit_F0_Fch9_Chip5

Entries



nChipHit_F0_Fch9_Chip5

Entries 1106794

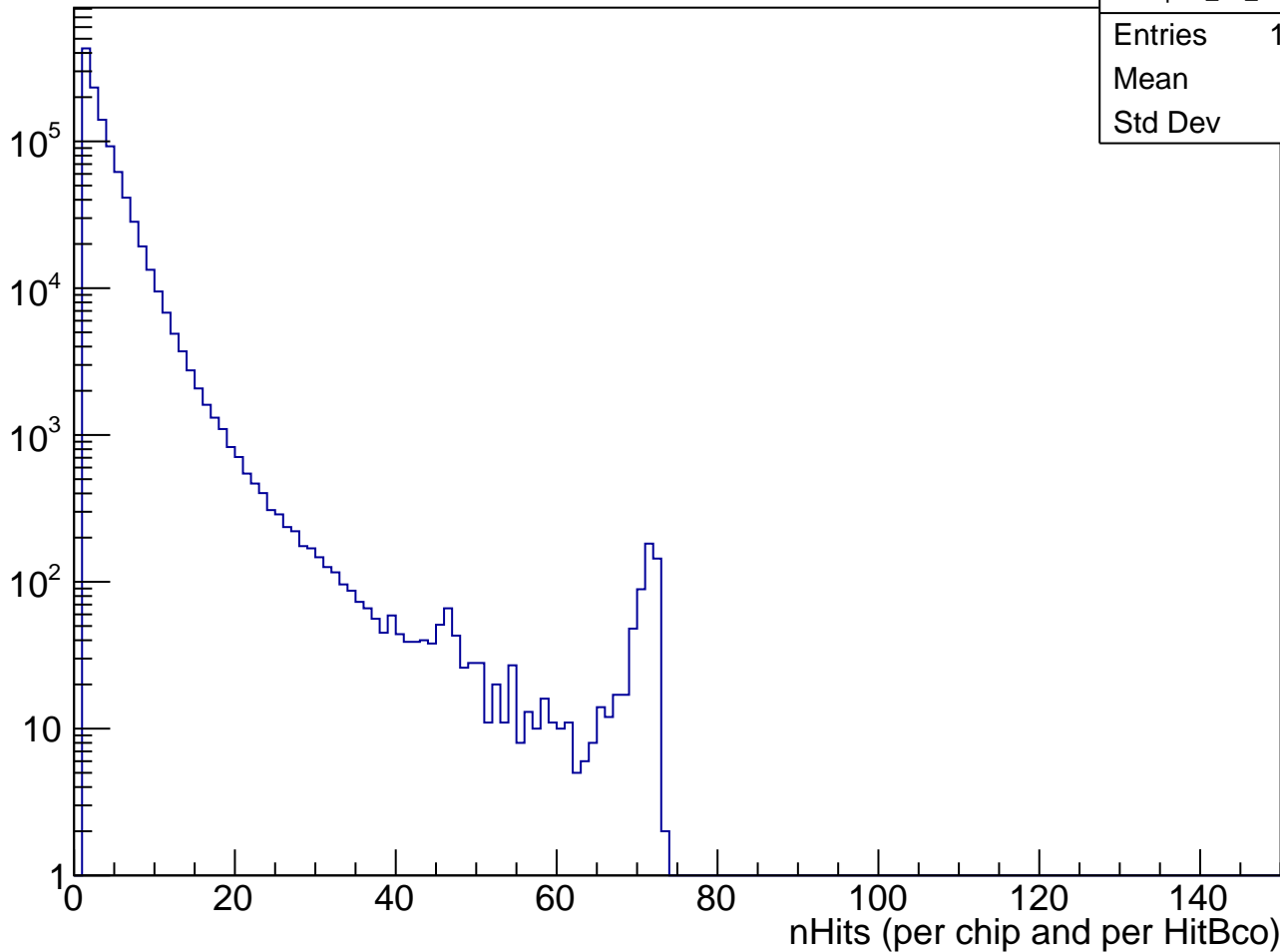
Mean 2.961

Std Dev 3.458

nHits (per chip and per HitBco)

nChipHit_F0_Fch9_Chip6

Entries



nChipHit_F0_Fch9_Chip6

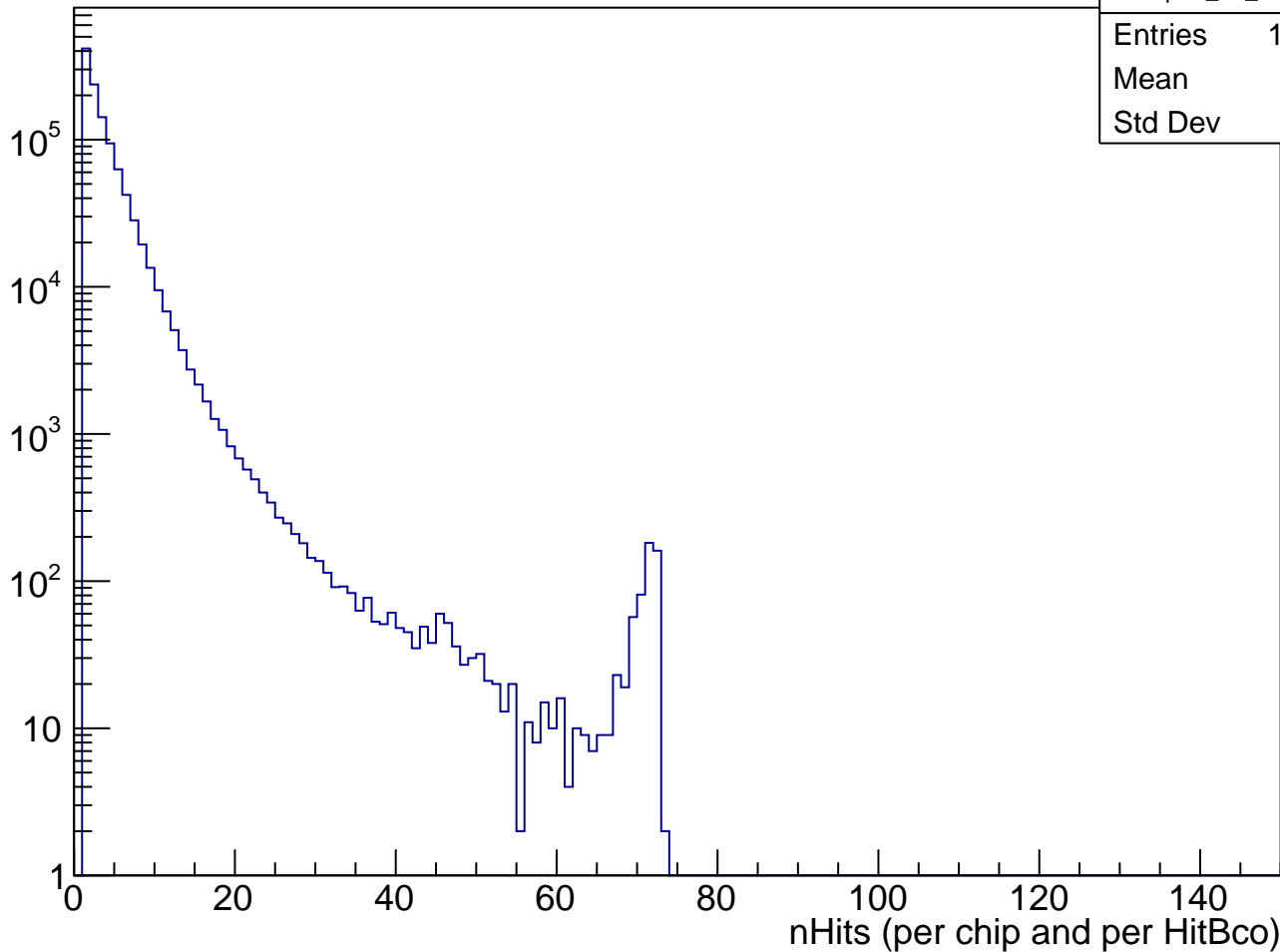
Entries 1099463

Mean 3.022

Std Dev 3.476

nChipHit_F0_Fch9_Chip7

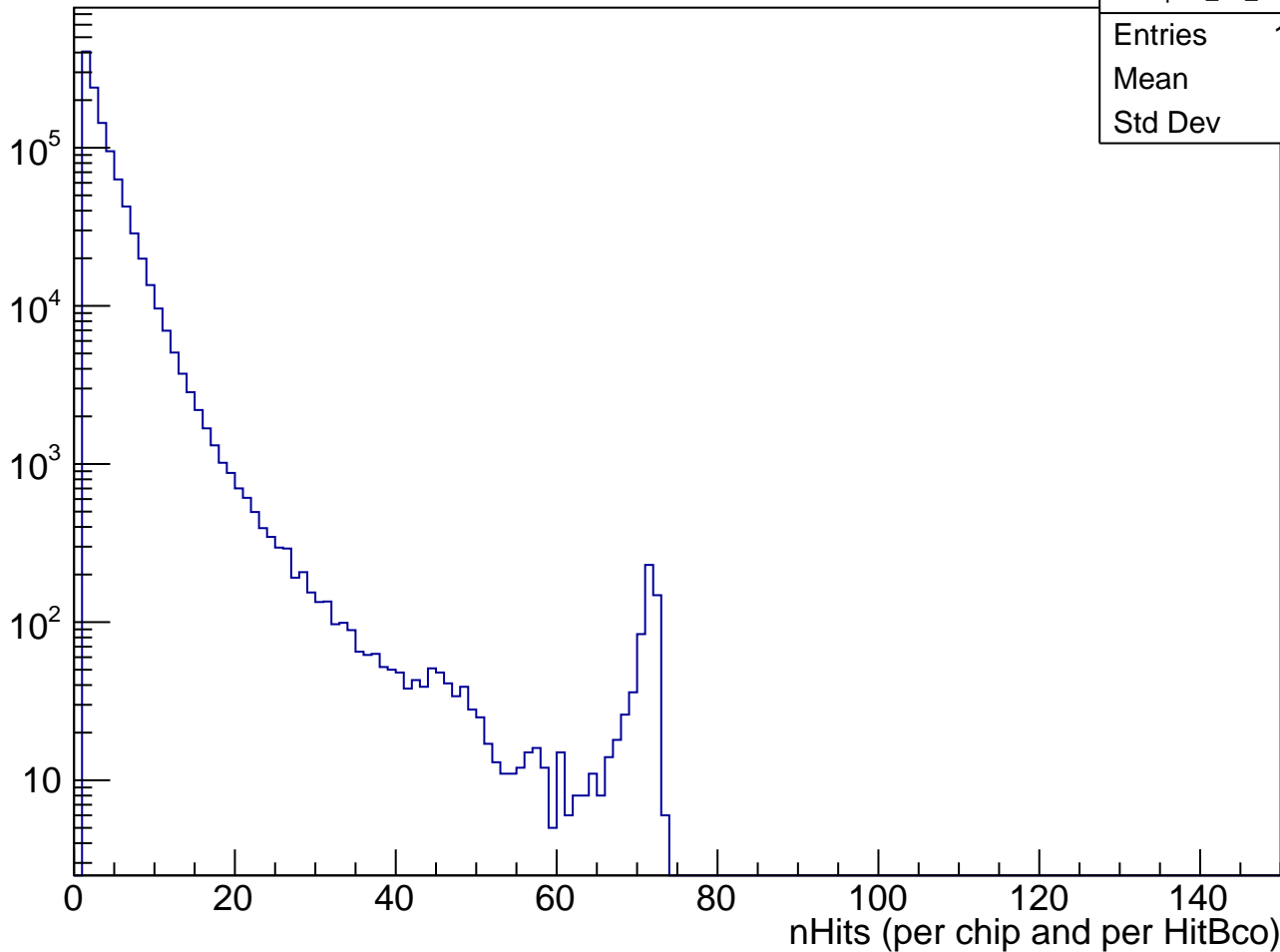
Entries



nChipHit_F0_Fch9_Chip7	
Entries	1096452
Mean	3.052
Std Dev	3.487

nChipHit_F0_Fch9_Chip8

Entries



nChipHit_F0_Fch9_Chip8

Entries 1093361

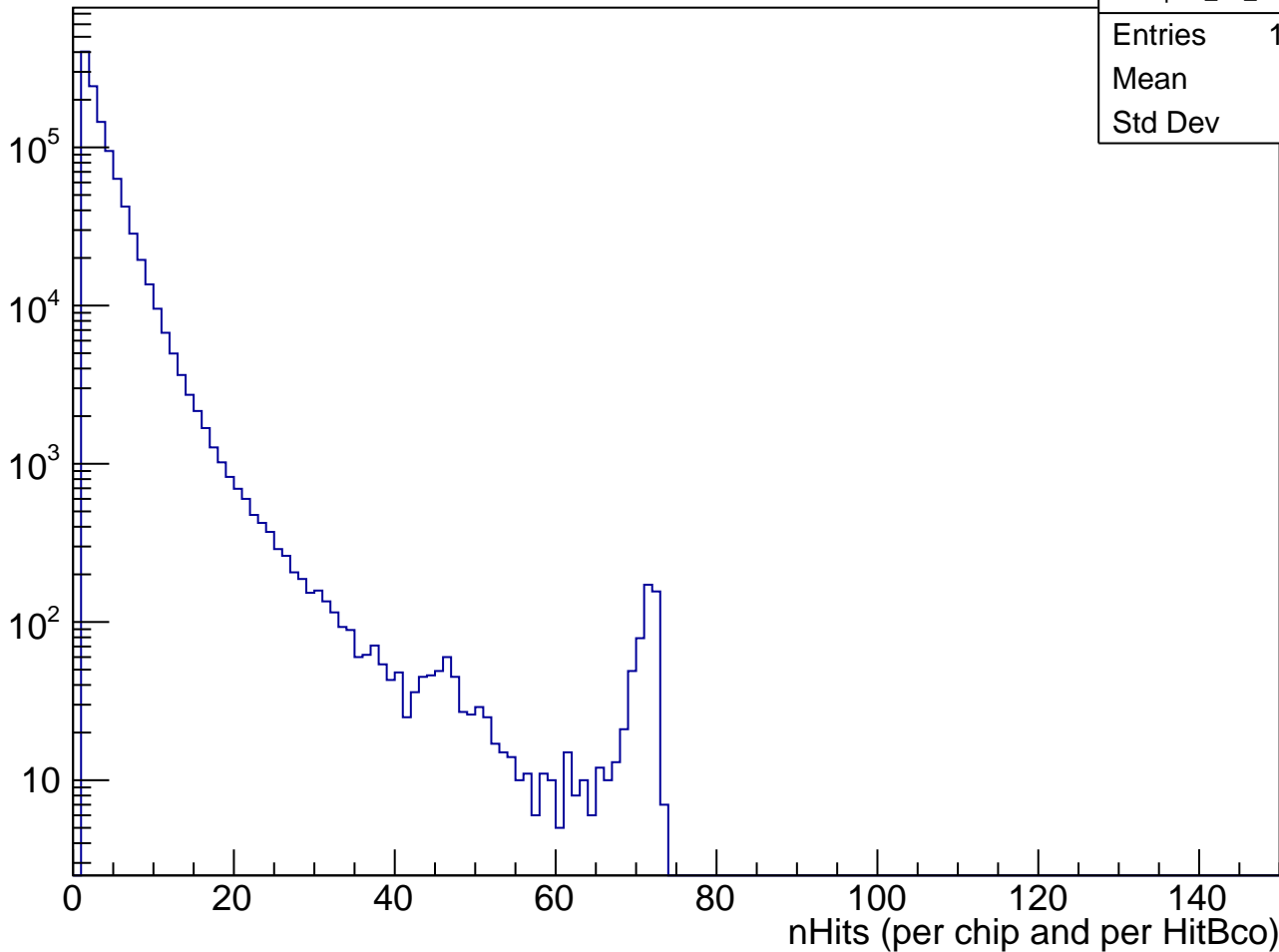
Mean 3.083

Std Dev 3.518

nHits (per chip and per HitBco)

nChipHit_F0_Fch9_Chip9

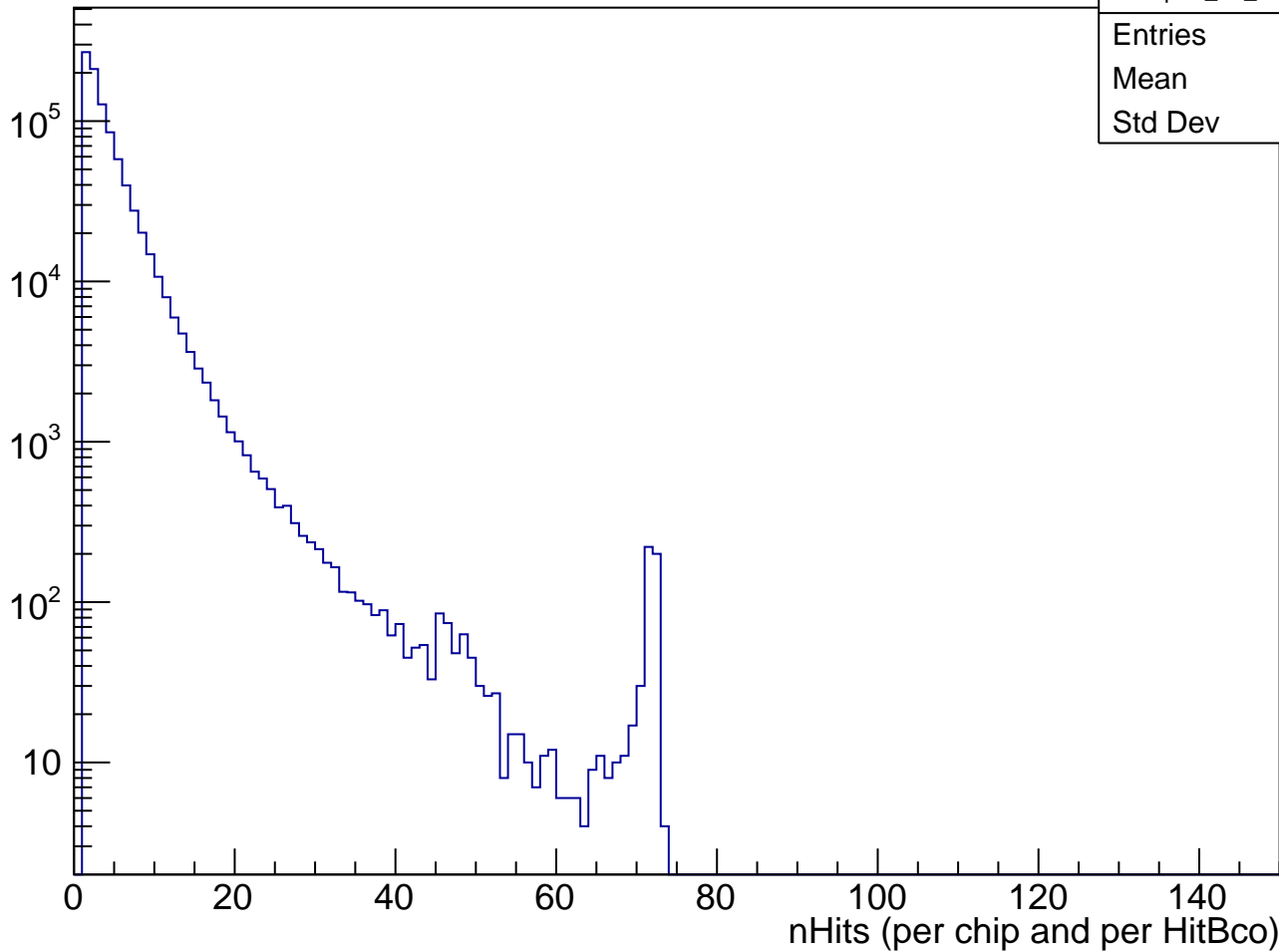
Entries



nChipHit_F0_Fch9_Chip9	
Entries	1093800
Mean	3.073
Std Dev	3.477

nChipHit_F1_Fch0_Chip0

Entries



nChipHit_F1_Fch0_Chip0

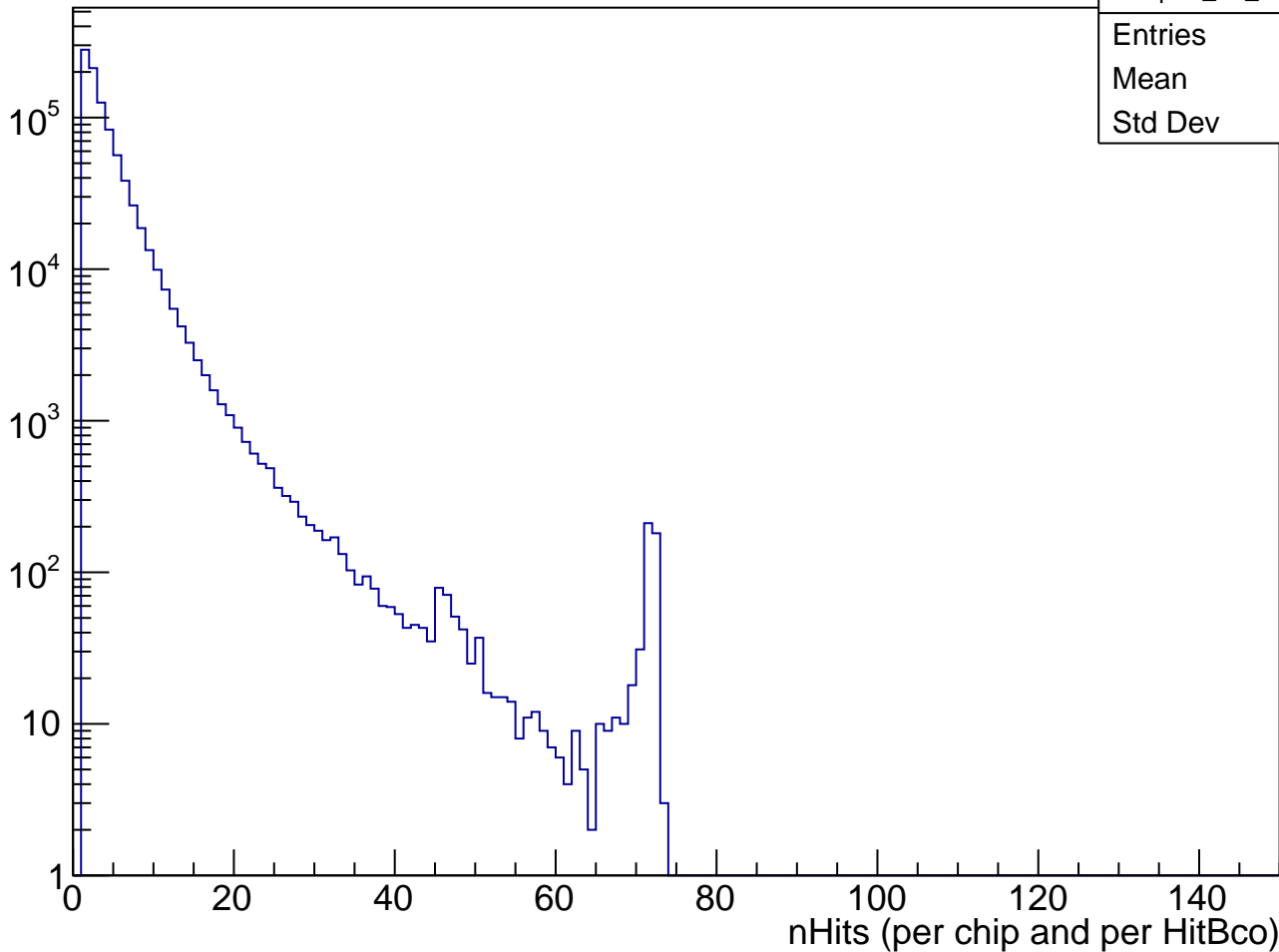
Entries 903178

Mean 3.543

Std Dev 4.014

nChipHit_F1_Fch0_Chip1

Entries

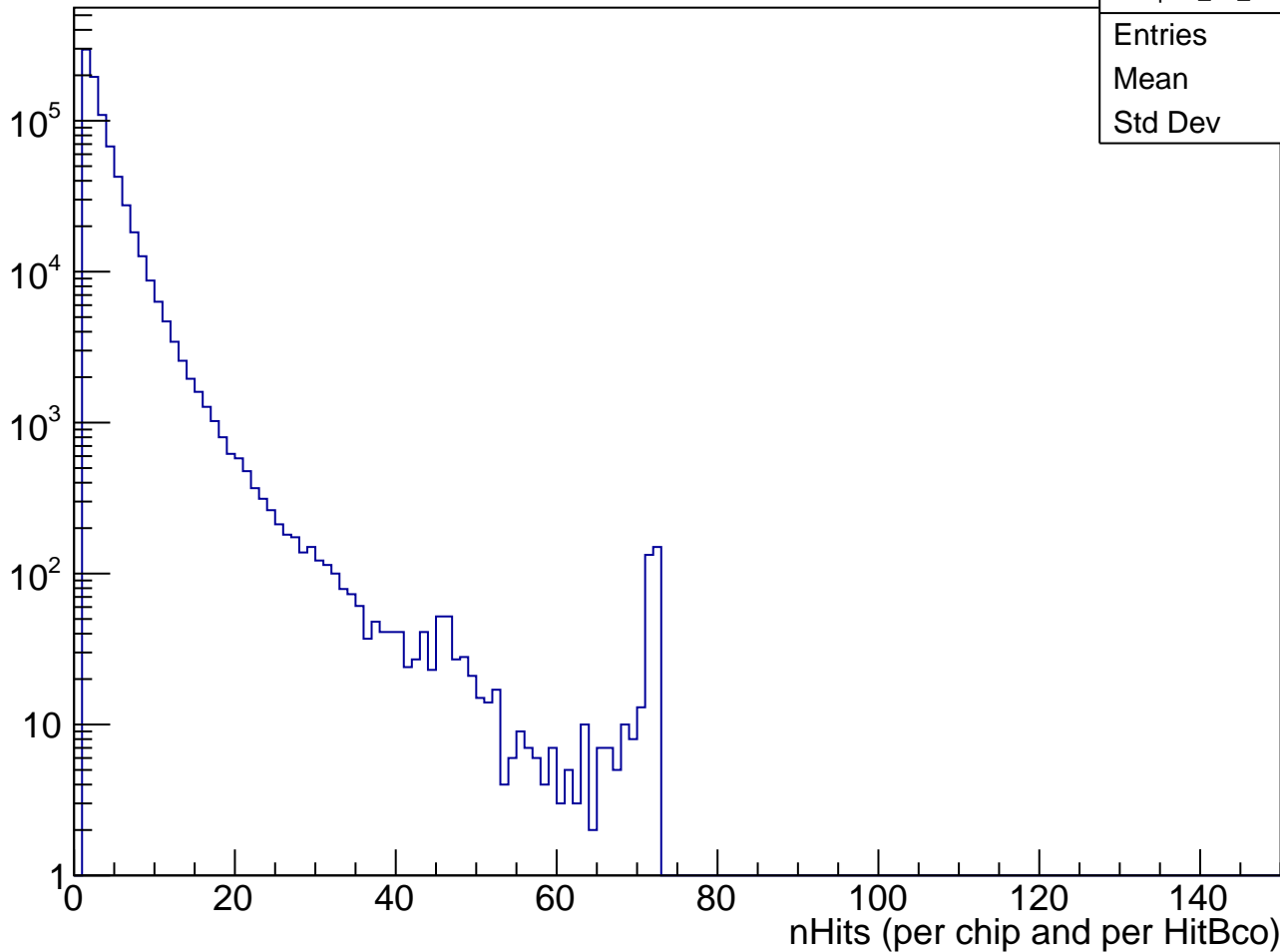


nChipHit_F1_Fch0_Chip1	
Entries	900018
Mean	3.419
Std Dev	3.873

nHits (per chip and per HitBco)

nChipHit_F1_Fch0_Chip10

Entries

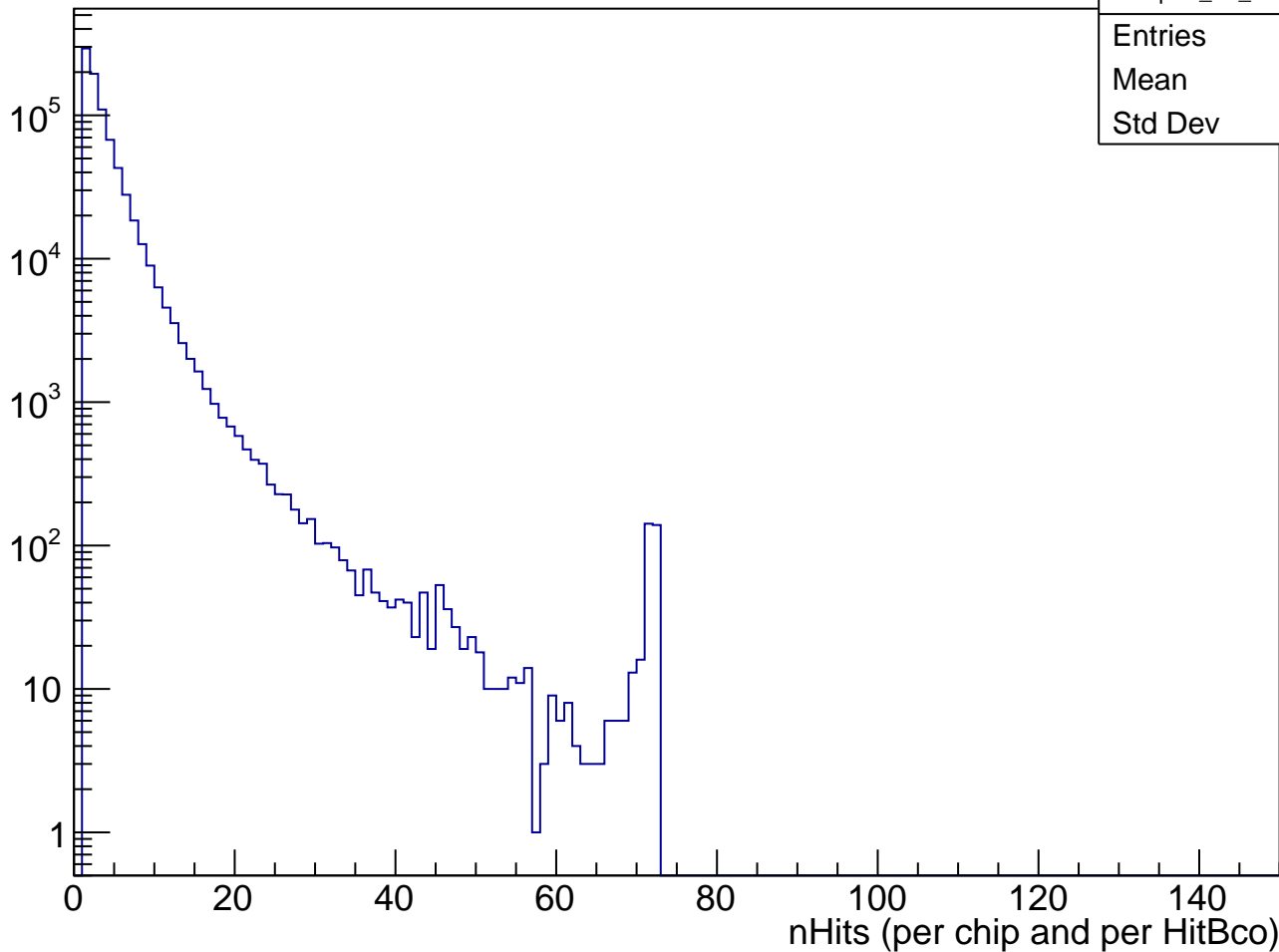


nChipHit_F1_Fch0_Chip10	
Entries	806574
Mean	2.987
Std Dev	3.419

nHits (per chip and per HitBco)

nChipHit_F1_Fch0_Chip11

Entries

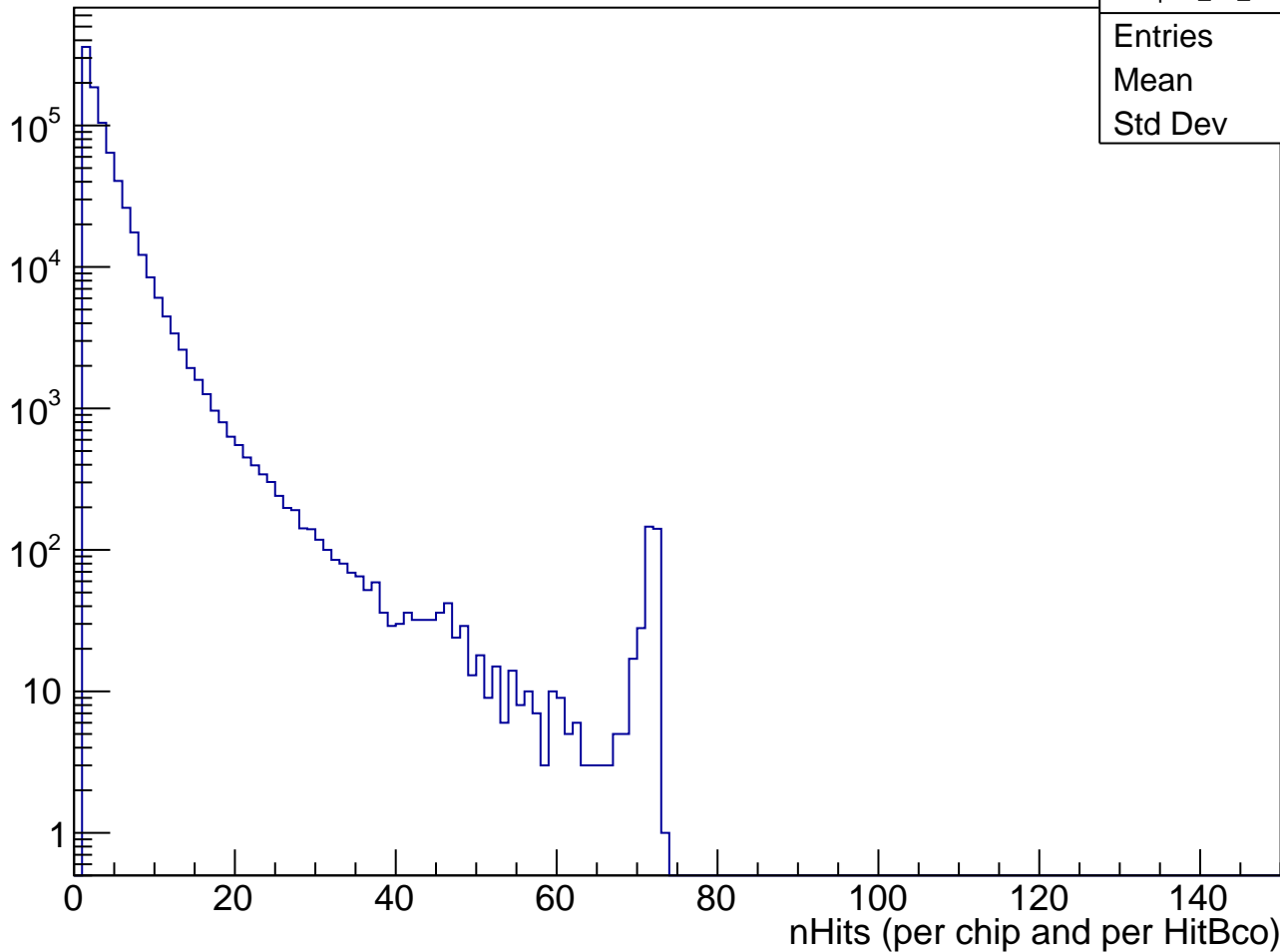


nChipHit_F1_Fch0_Chip11	
Entries	804160
Mean	3.006
Std Dev	3.435

nHits (per chip and per HitBco)

nChipHit_F1_Fch0_Chip12

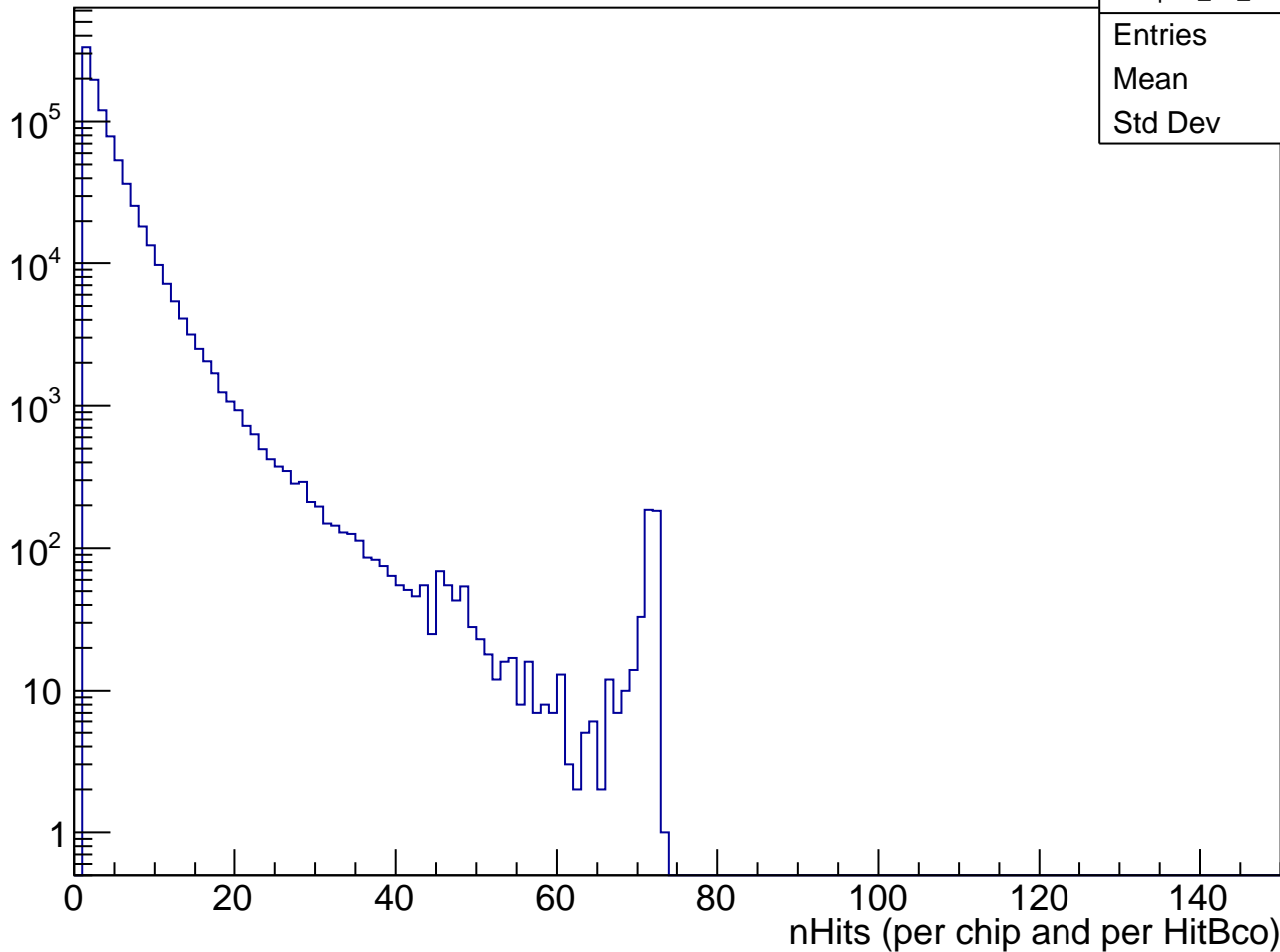
Entries



nChipHit_F1_Fch0_Chip12	
Entries	846871
Mean	2.825
Std Dev	3.373

nChipHit_F1_Fch0_Chip13

Entries

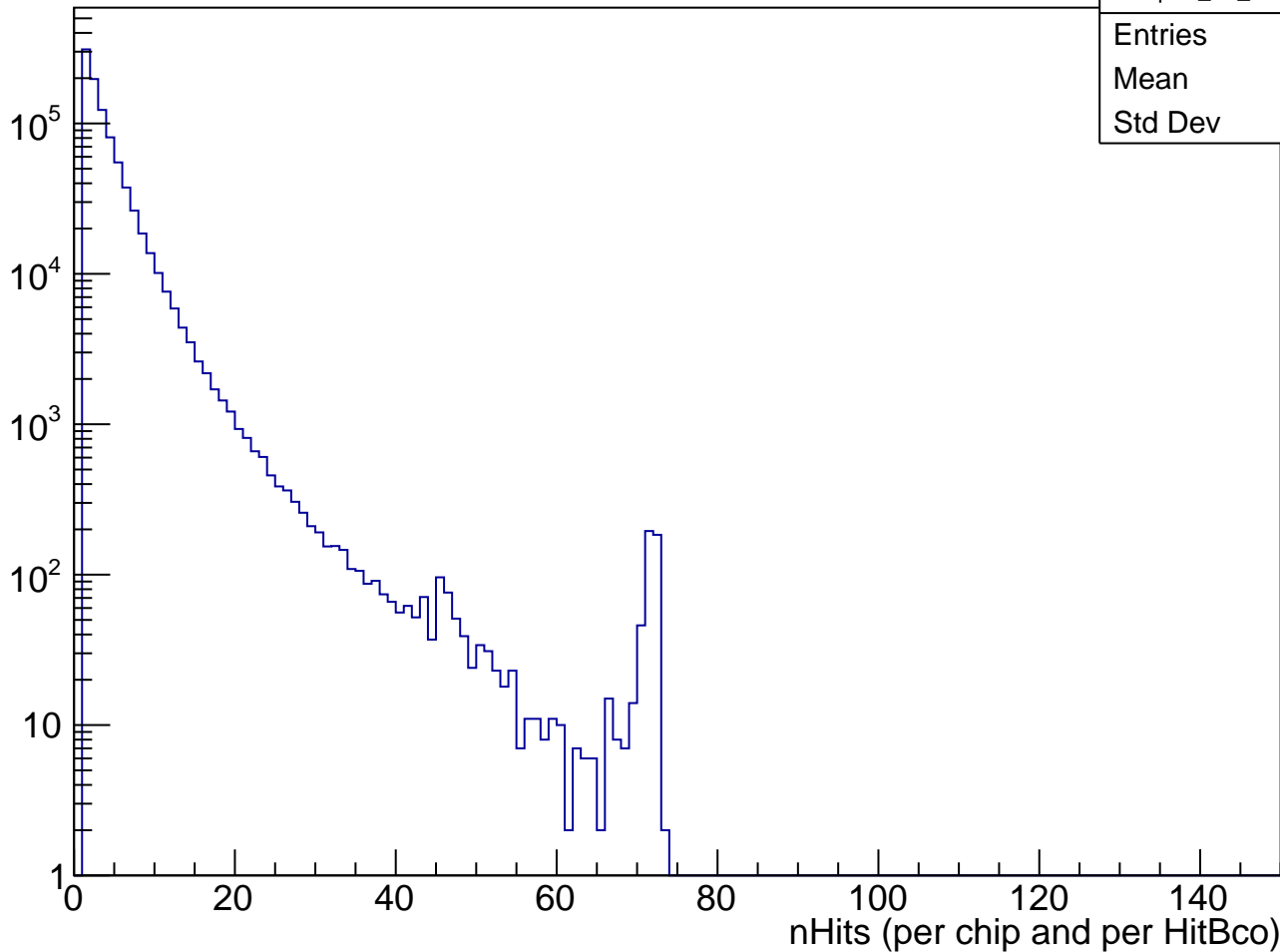


nChipHit_F1_Fch0_Chip13	
Entries	919465
Mean	3.287
Std Dev	3.842

nHits (per chip and per HitBco)

nChipHit_F1_Fch0_Chip14

Entries

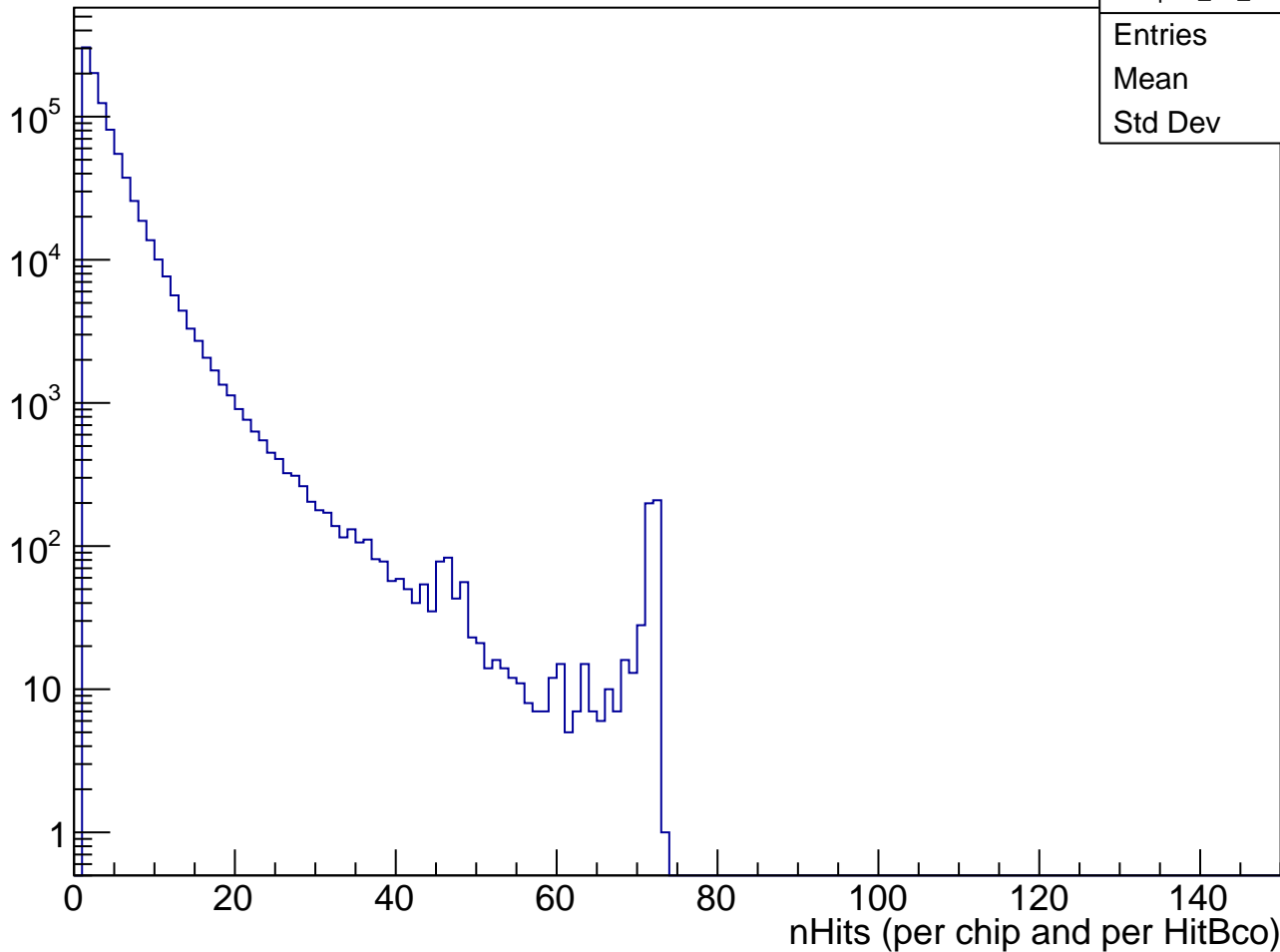


nChipHit_F1_Fch0_Chip14	
Entries	911170
Mean	3.395
Std Dev	3.953

nHits (per chip and per HitBco)

nChipHit_F1_Fch0_Chip15

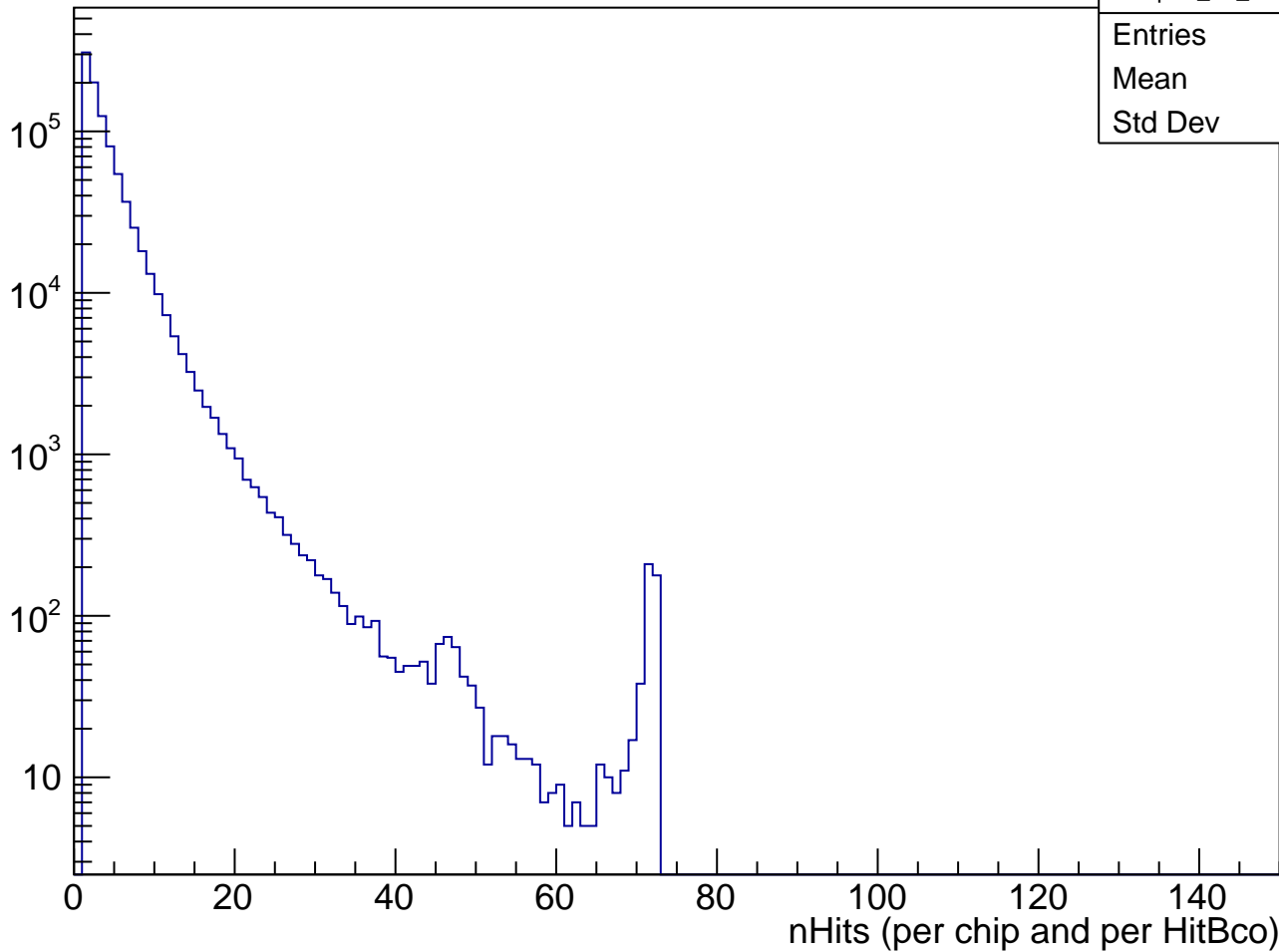
Entries



nChipHit_F1_Fch0_Chip15	
Entries	909554
Mean	3.386
Std Dev	3.921

nChipHit_F1_Fch0_Chip16

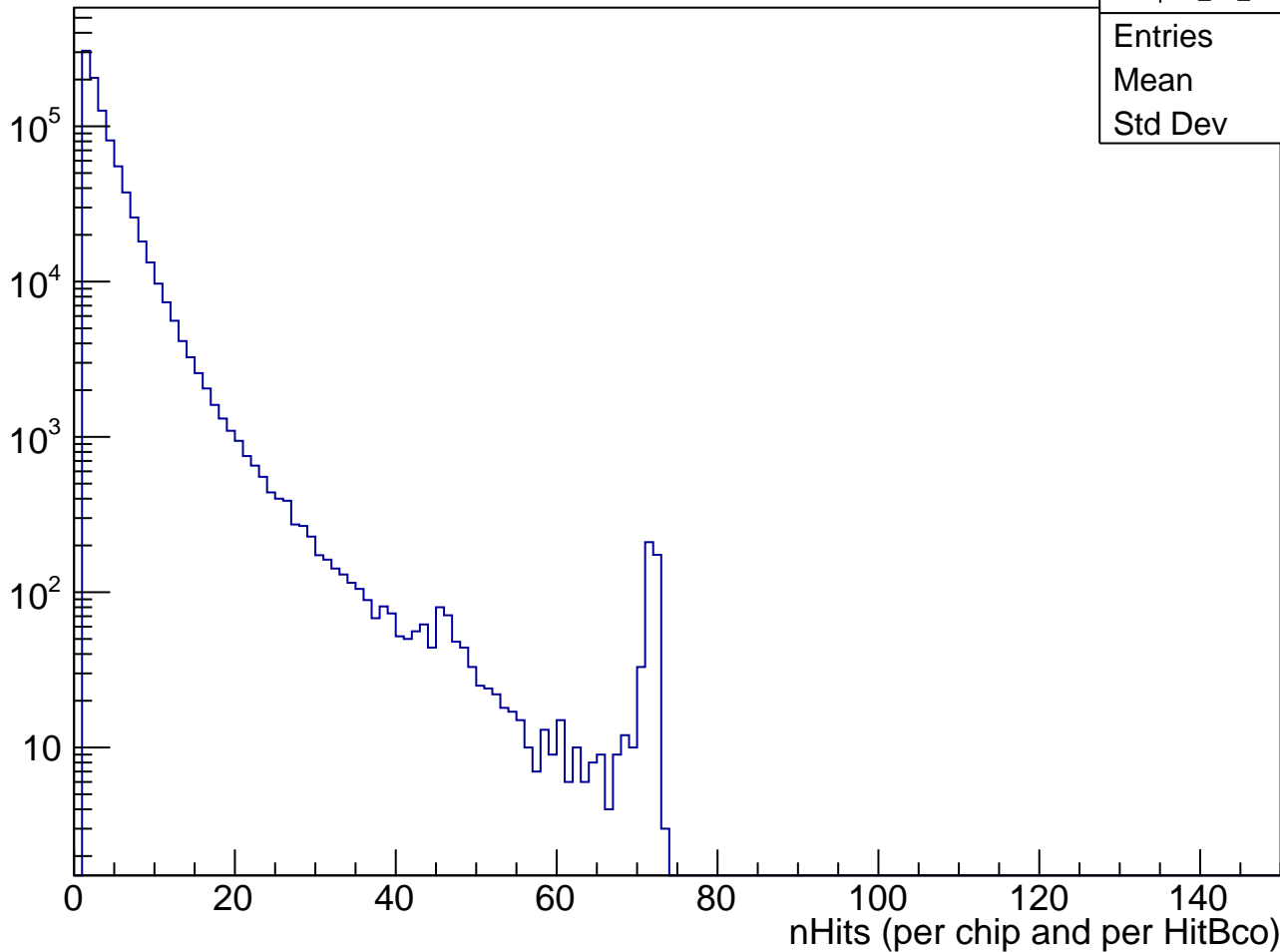
Entries



nChipHit_F1_Fch0_Chip16	
Entries	907254
Mean	3.345
Std Dev	3.877

nChipHit_F1_Fch0_Chip17

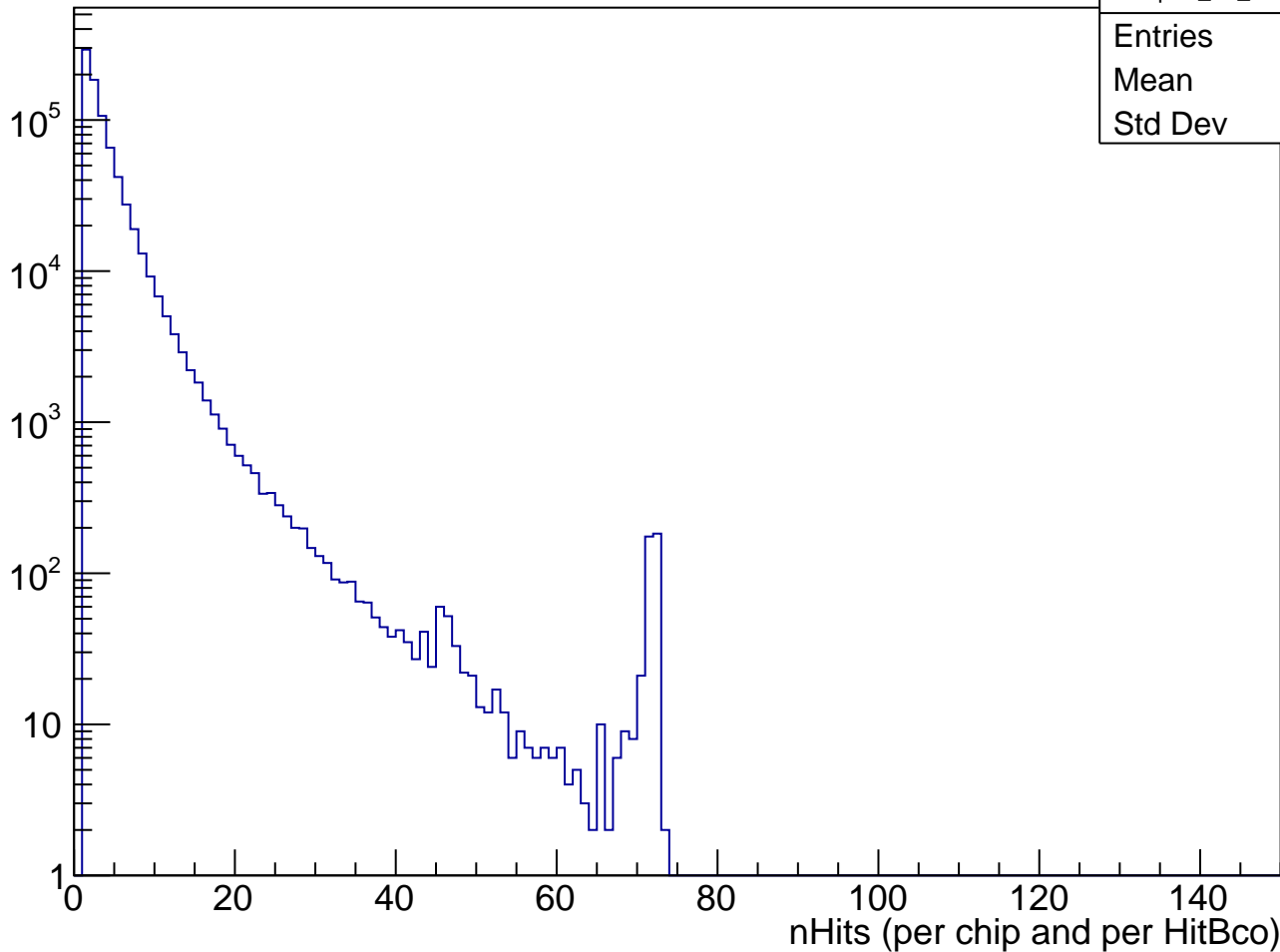
Entries



nChipHit_F1_Fch0_Chip17	
Entries	914761
Mean	3.361
Std Dev	3.894

nChipHit_F1_Fch0_Chip18

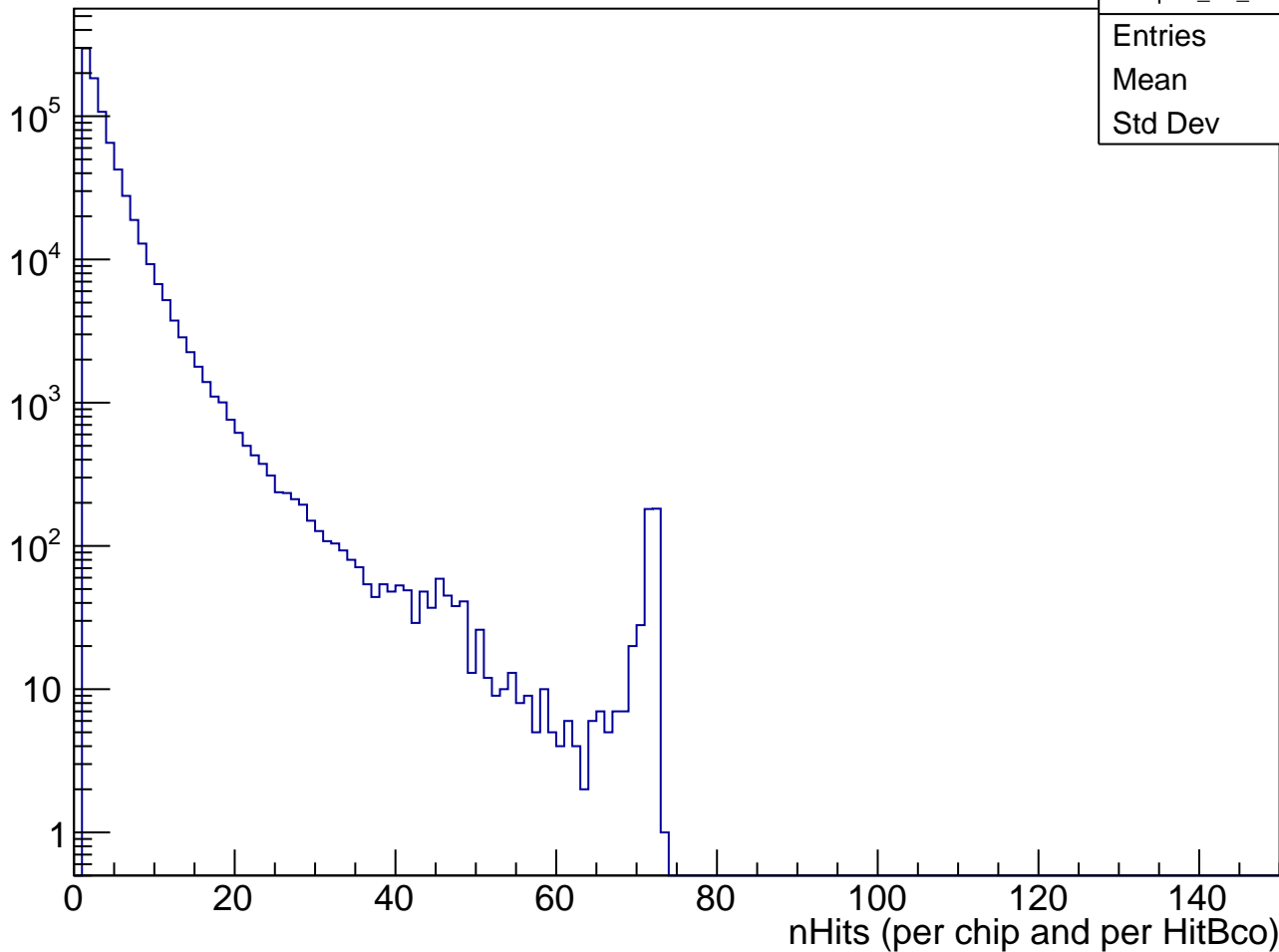
Entries



nChipHit_F1_Fch0_Chip18	
Entries	791621
Mean	3.07
Std Dev	3.623

nChipHit_F1_Fch0_Chip19

Entries

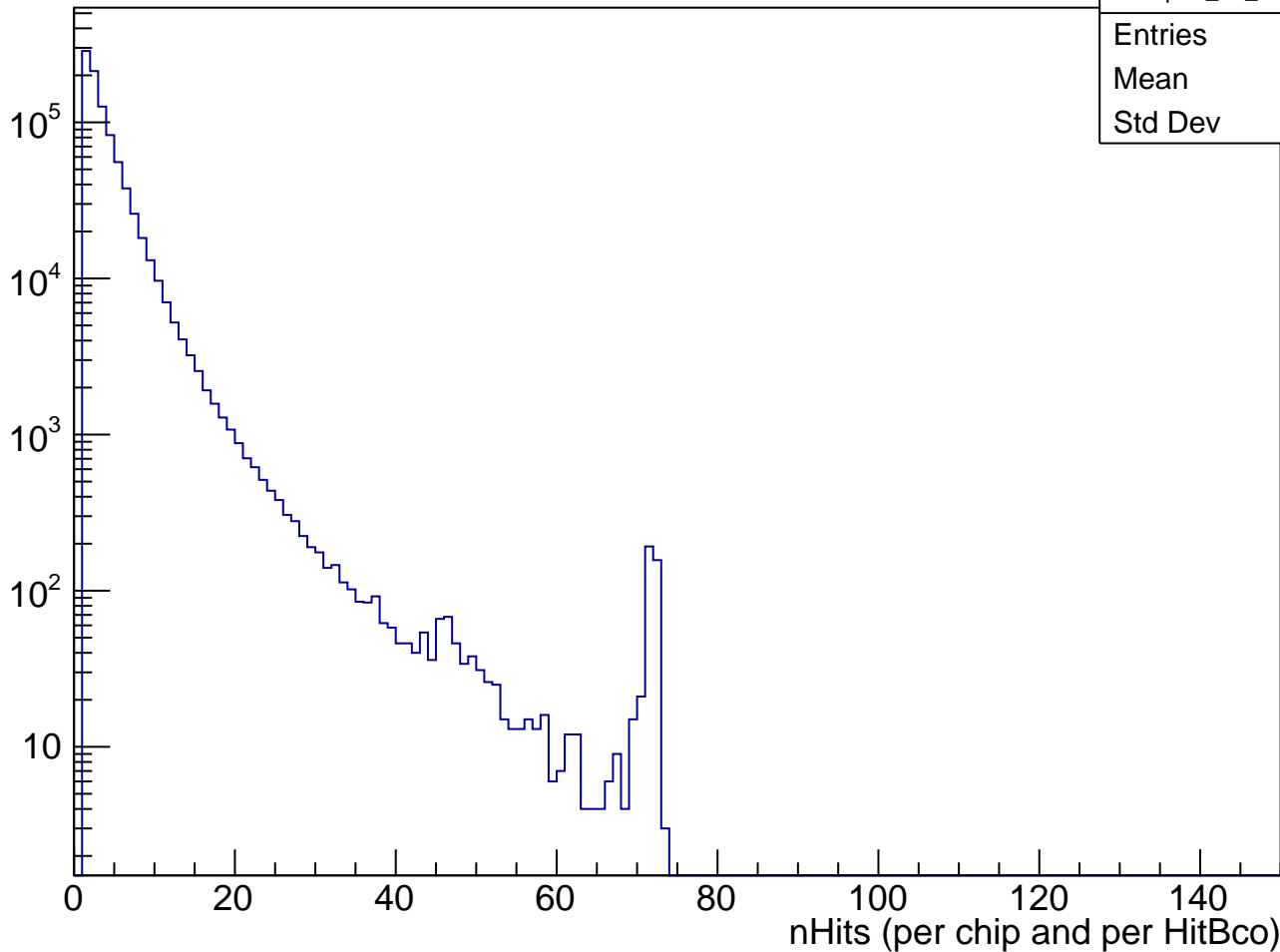


nChipHit_F1_Fch0_Chip19	
Entries	797207
Mean	3.064
Std Dev	3.654

nHits (per chip and per HitBco)

nChipHit_F1_Fch0_Chip2

Entries

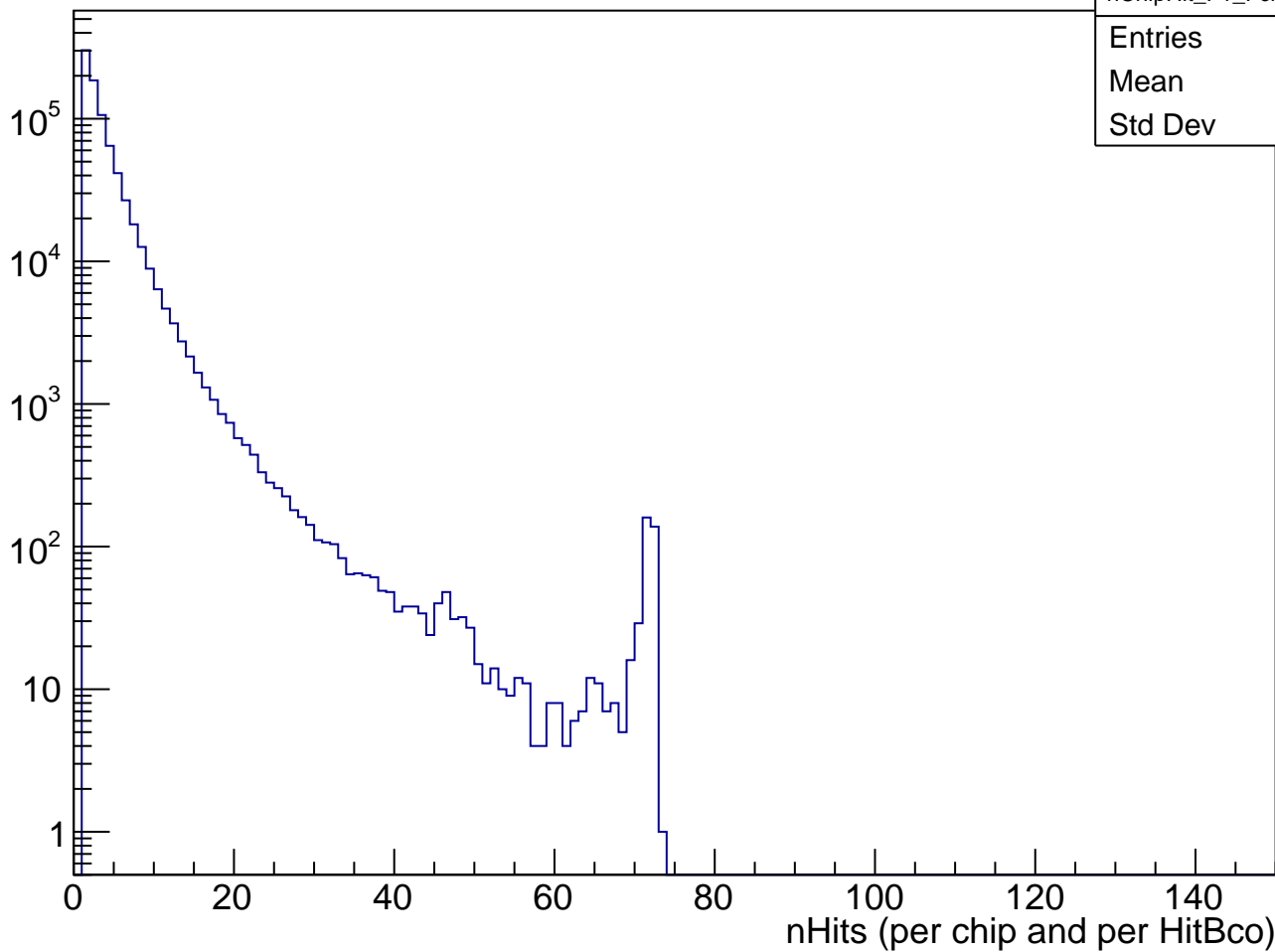


nChipHit_F1_Fch0_Chip2

Entries	903375
Mean	3.374
Std Dev	3.808

nChipHit_F1_Fch0_Chip20

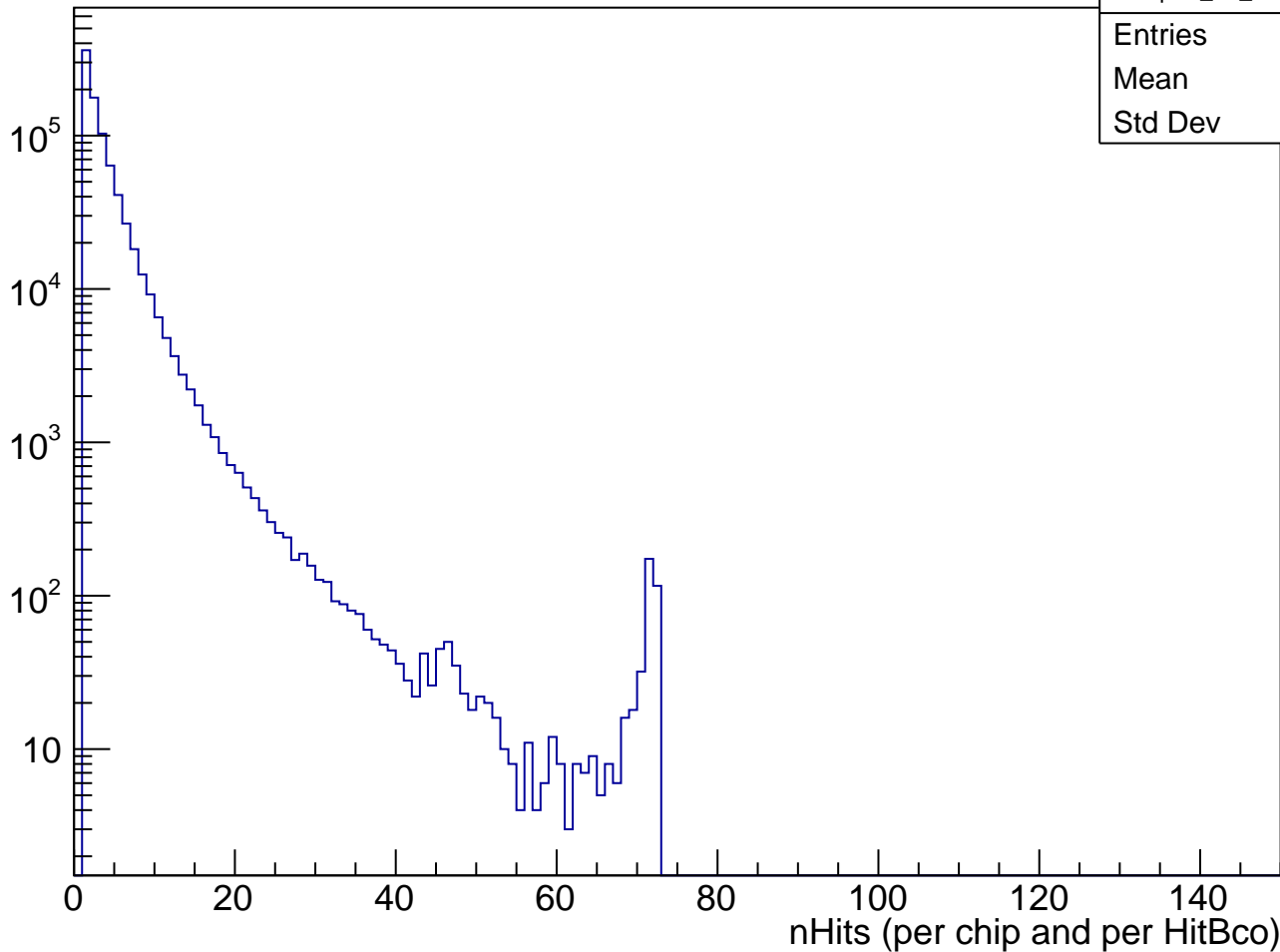
Entries



nChipHit_F1_Fch0_Chip20	
Entries	796903
Mean	3.002
Std Dev	3.543

nChipHit_F1_Fch0_Chip21

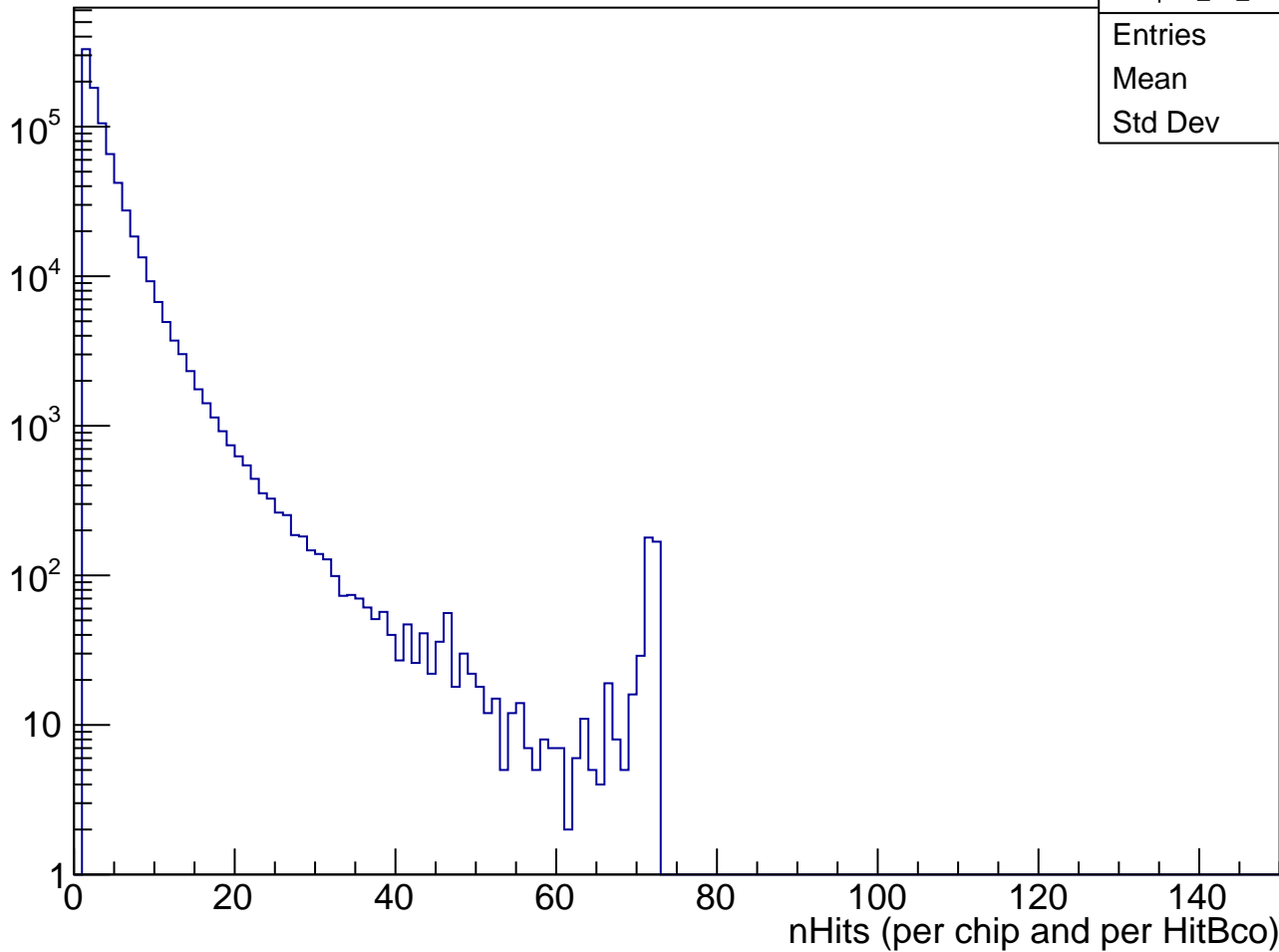
Entries



nChipHit_F1_Fch0_Chip21	
Entries	842146
Mean	2.882
Std Dev	3.5

nChipHit_F1_Fch0_Chip22

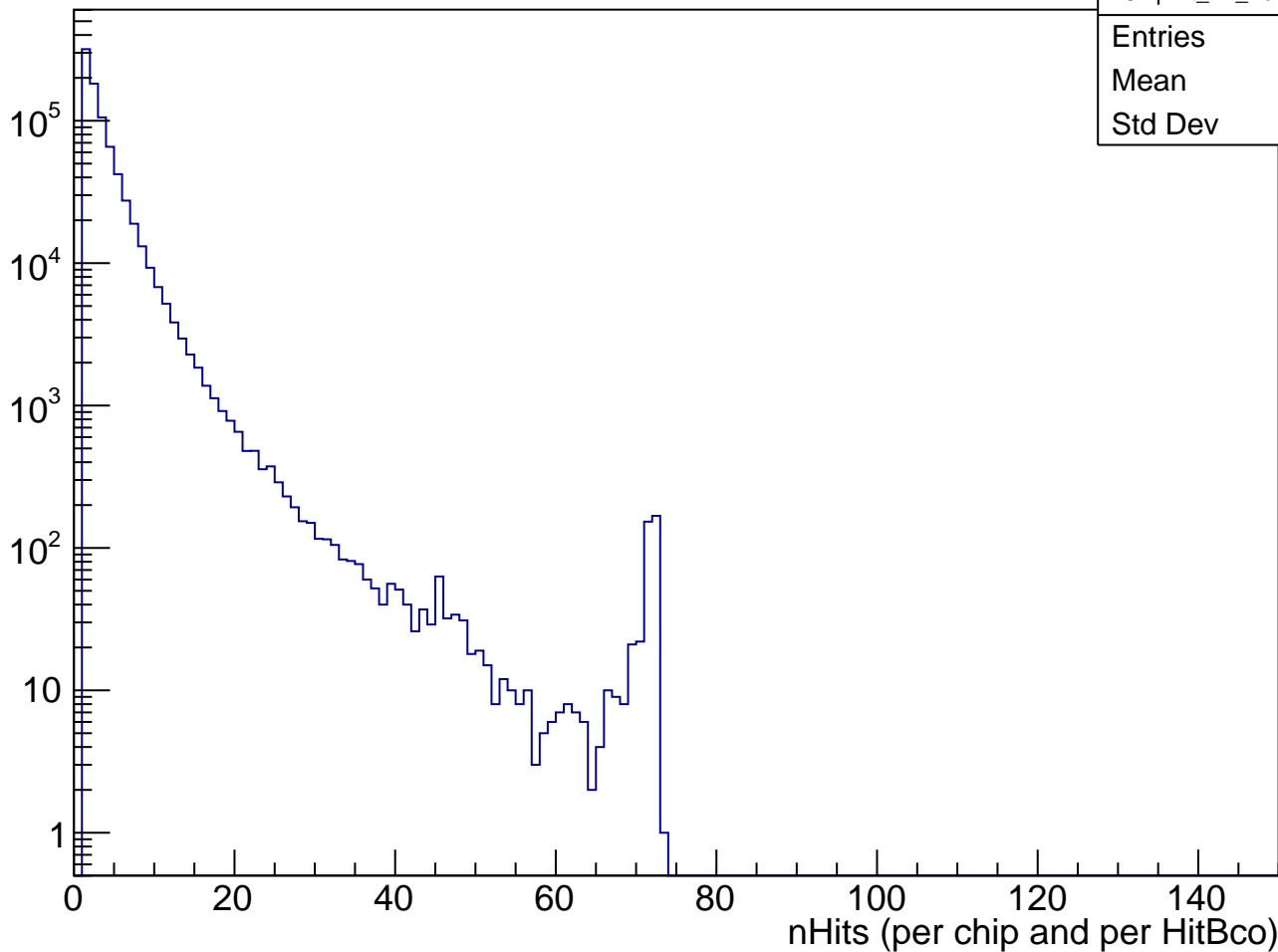
Entries



nChipHit_F1_Fch0_Chip22	
Entries	824664
Mean	2.982
Std Dev	3.587

nChipHit_F1_Fch0_Chip23

Entries

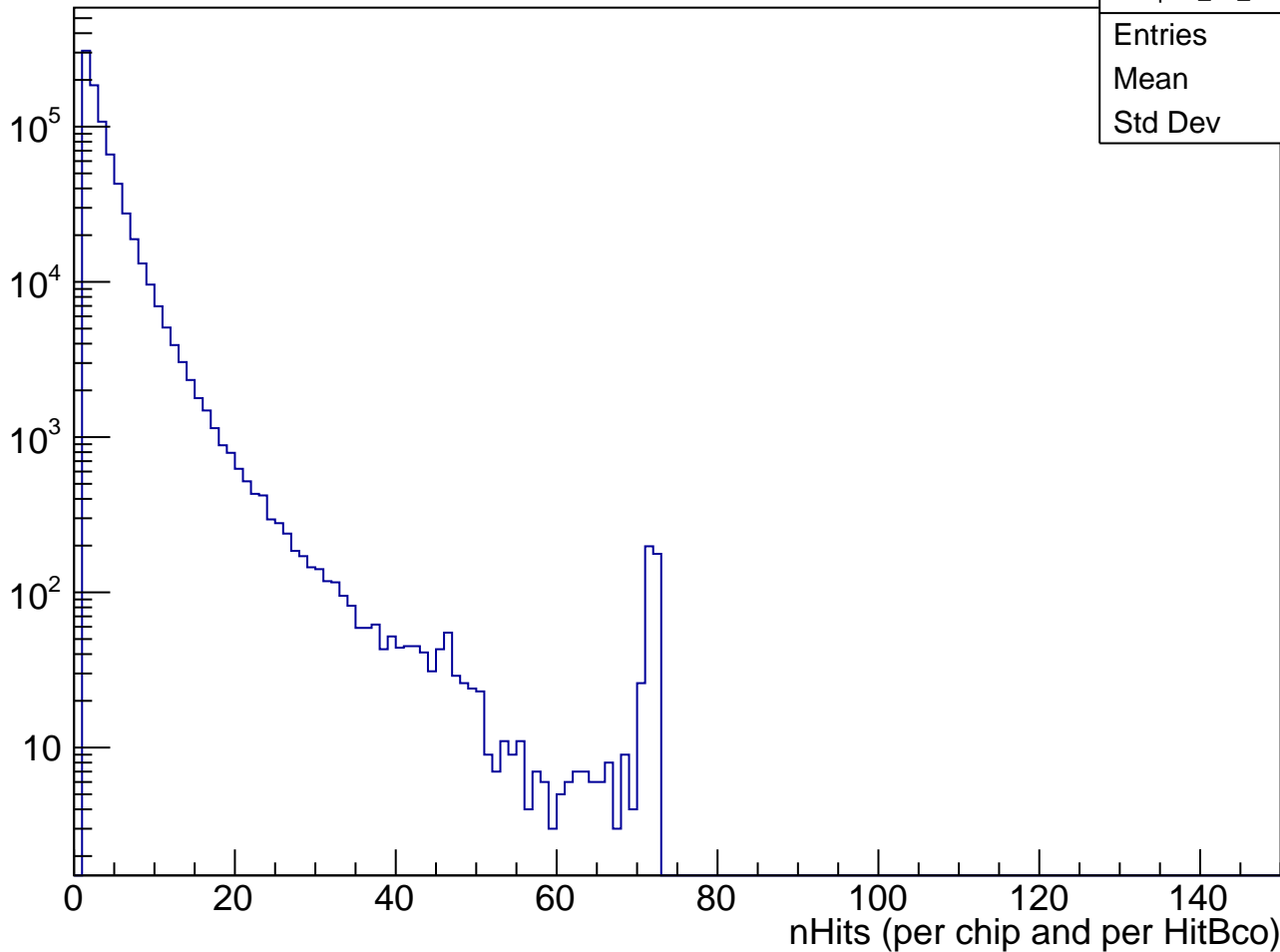


nChipHit_F1_Fch0_Chip23	
Entries	814789
Mean	3.014
Std Dev	3.59

nHits (per chip and per HitBco)

nChipHit_F1_Fch0_Chip24

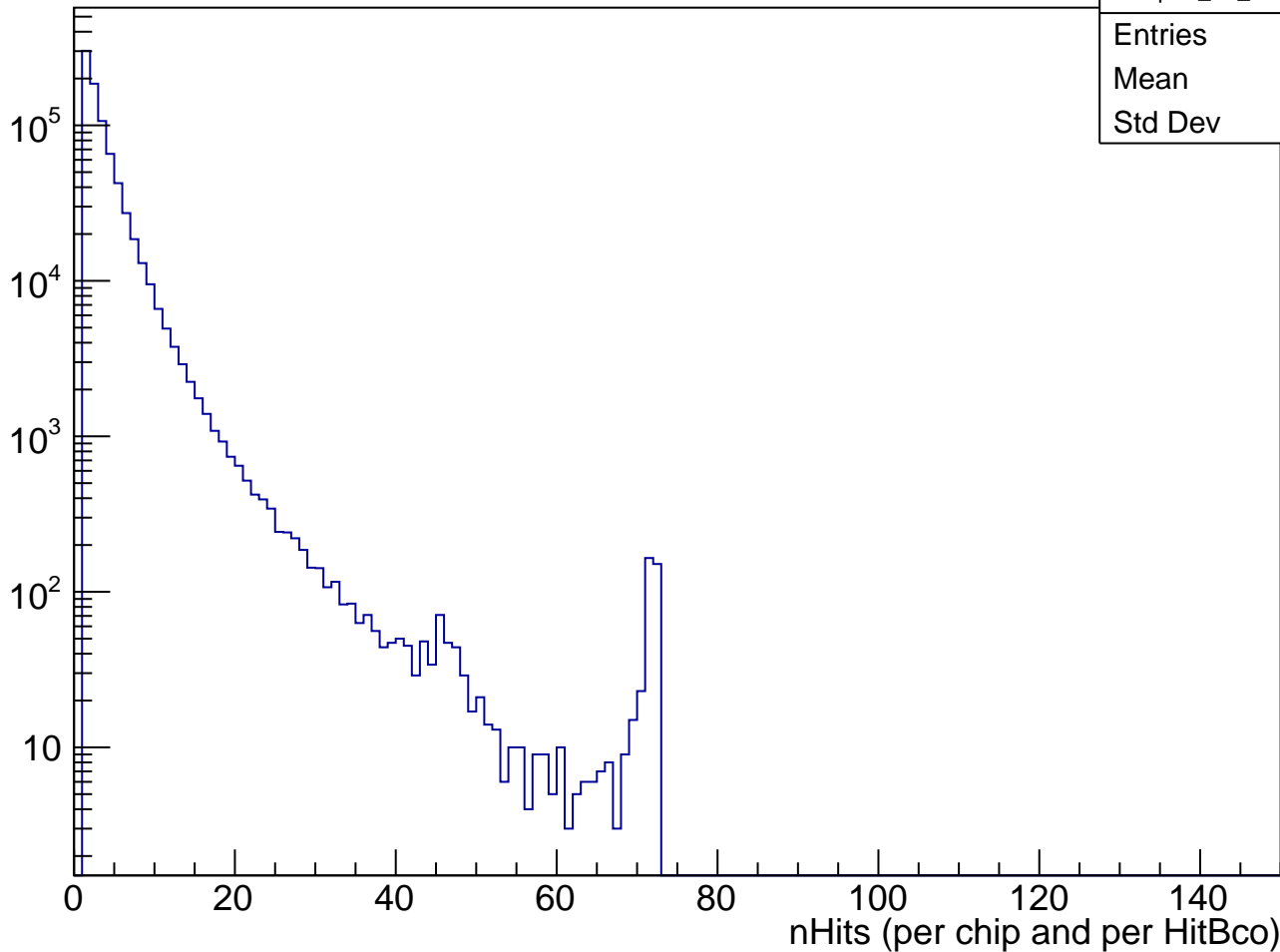
Entries



nChipHit_F1_Fch0_Chip24	
Entries	811588
Mean	3.048
Std Dev	3.633

nChipHit_F1_Fch0_Chip25

Entries



nChipHit_F1_Fch0_Chip25

Entries 801169

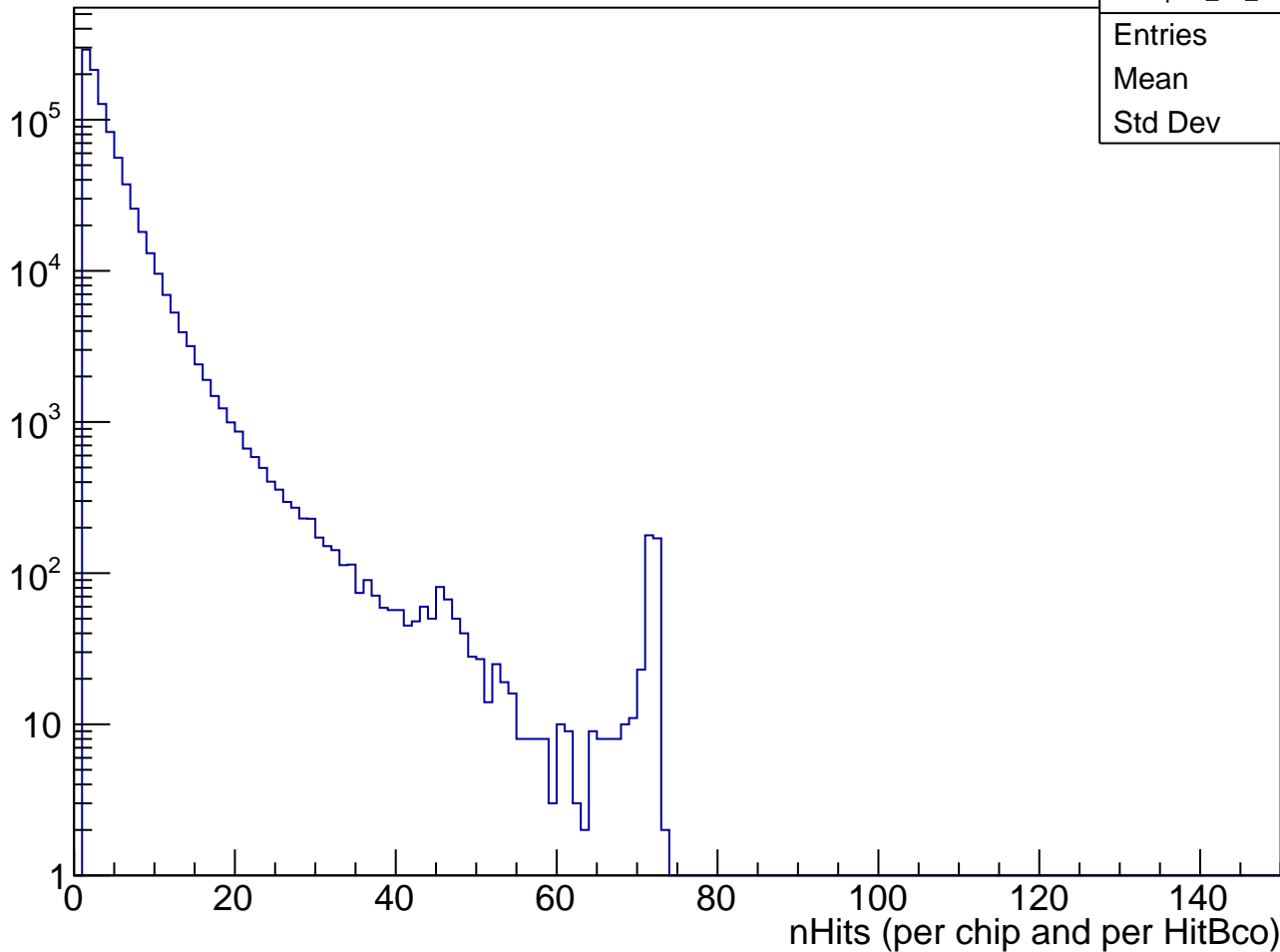
Mean 3.045

Std Dev 3.608

nHits (per chip and per HitBco)

nChipHit_F1_Fch0_Chip3

Entries



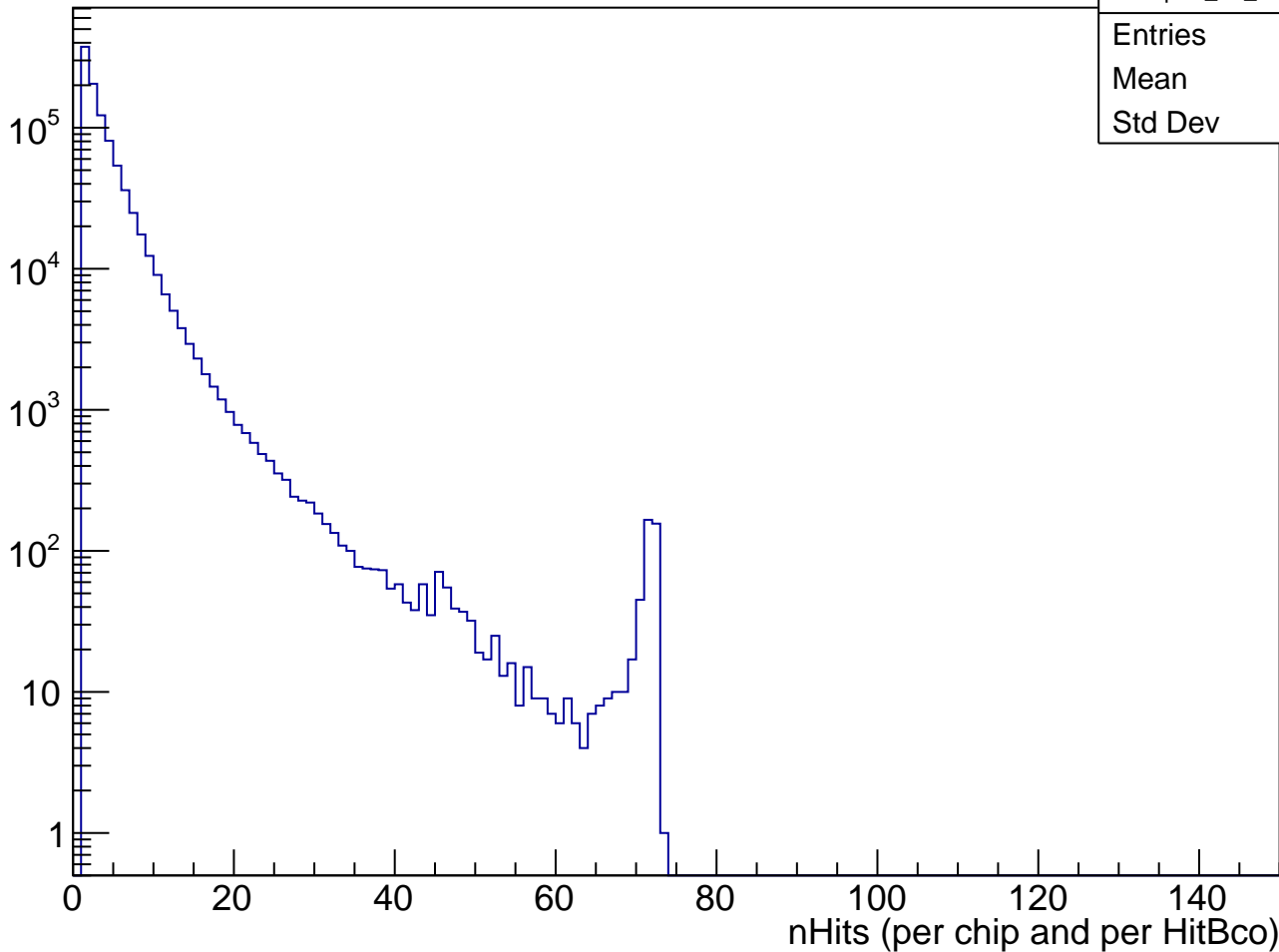
nChipHit_F1_Fch0_Chip3

Entries	908476
Mean	3.348
Std Dev	3.776

nHits (per chip and per HitBco)

nChipHit_F1_Fch0_Chip4

Entries



nChipHit_F1_Fch0_Chip4

Entries 968882

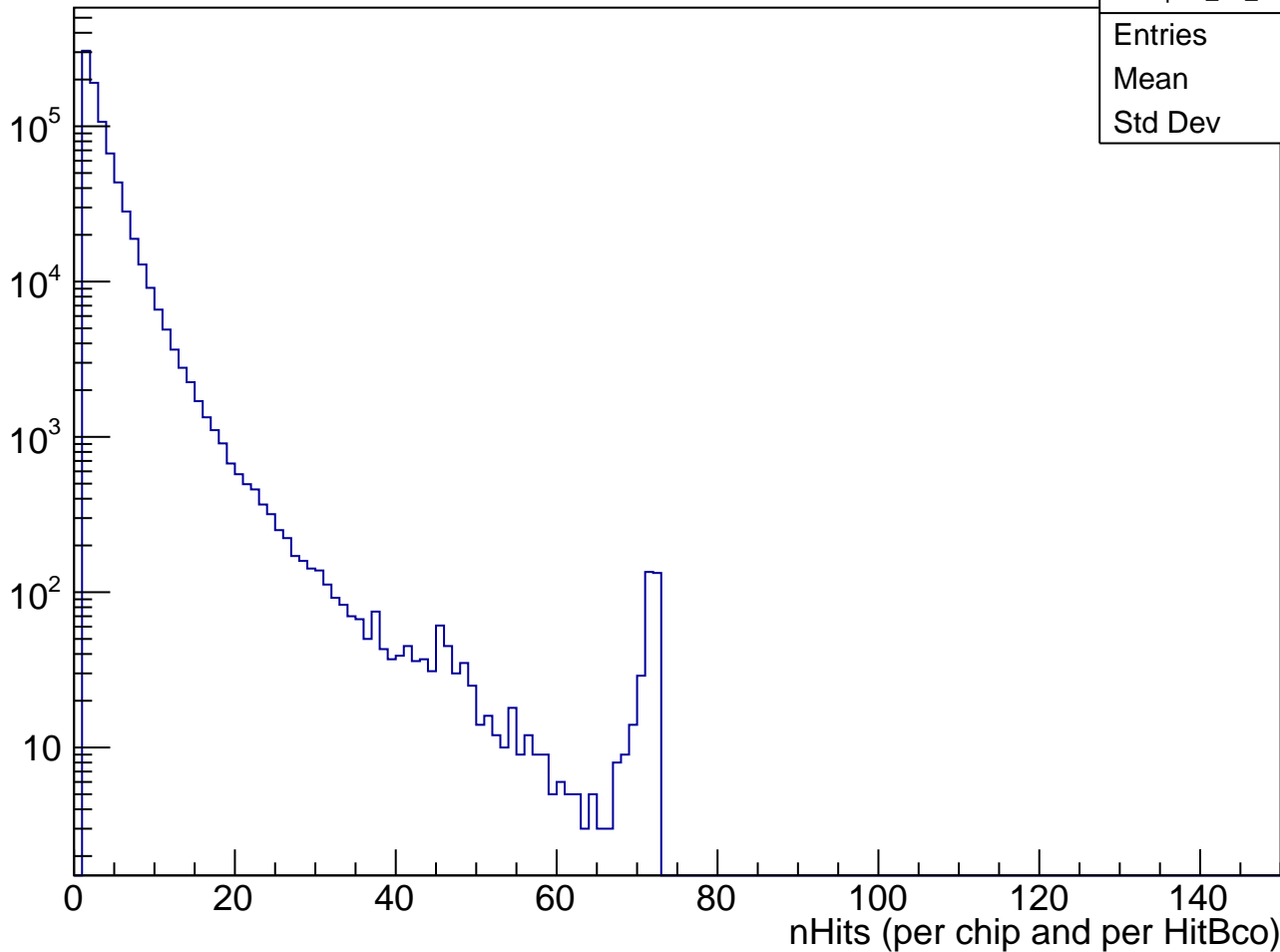
Mean 3.12

Std Dev 3.665

nHits (per chip and per HitBco)

nChipHit_F1_Fch0_Chip5

Entries

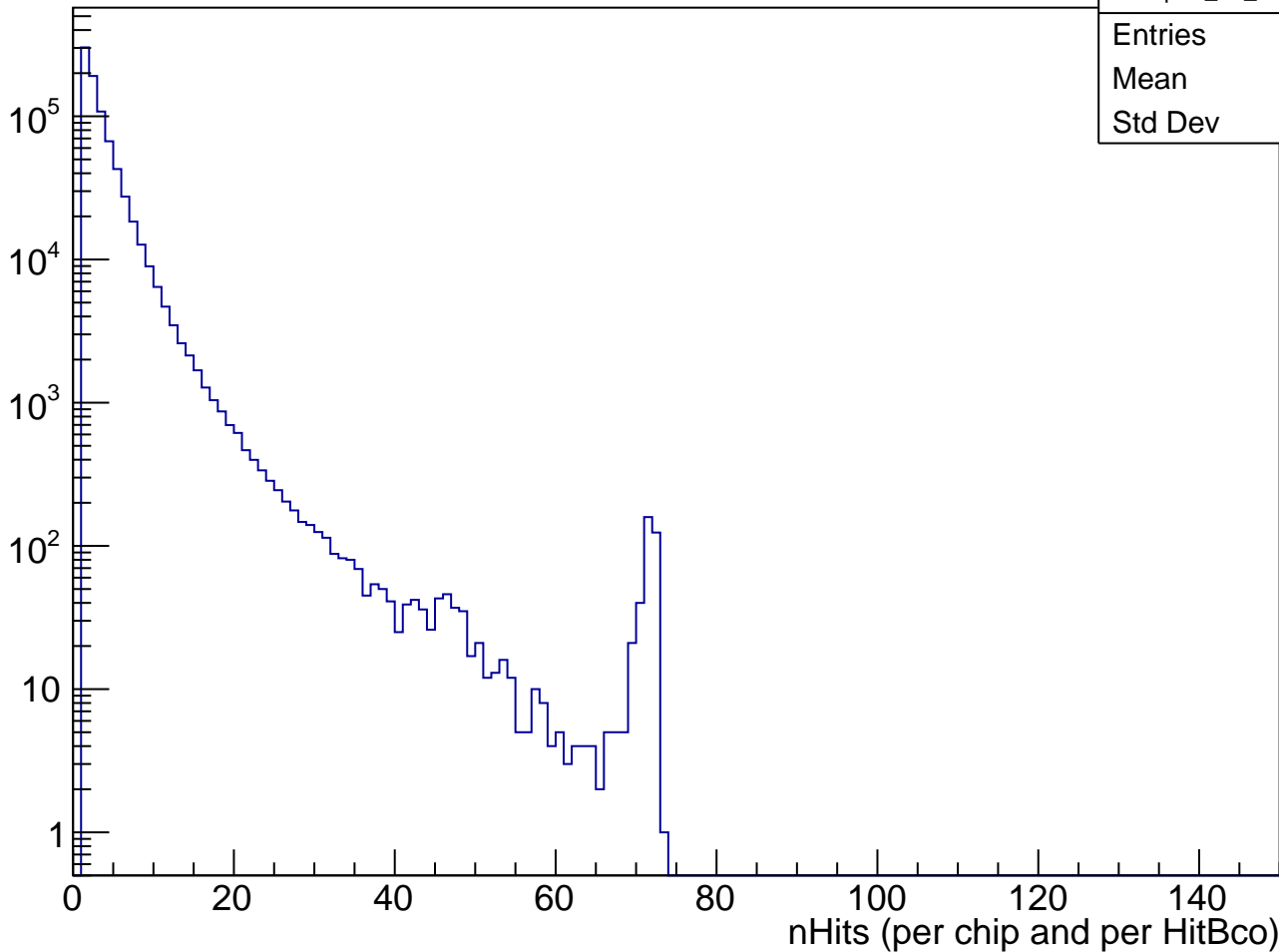


nChipHit_F1_Fch0_Chip5	
Entries	813925
Mean	3.014
Std Dev	3.509

nHits (per chip and per HitBco)

nChipHit_F1_Fch0_Chip6

Entries



nChipHit_F1_Fch0_Chip6

Entries 808552

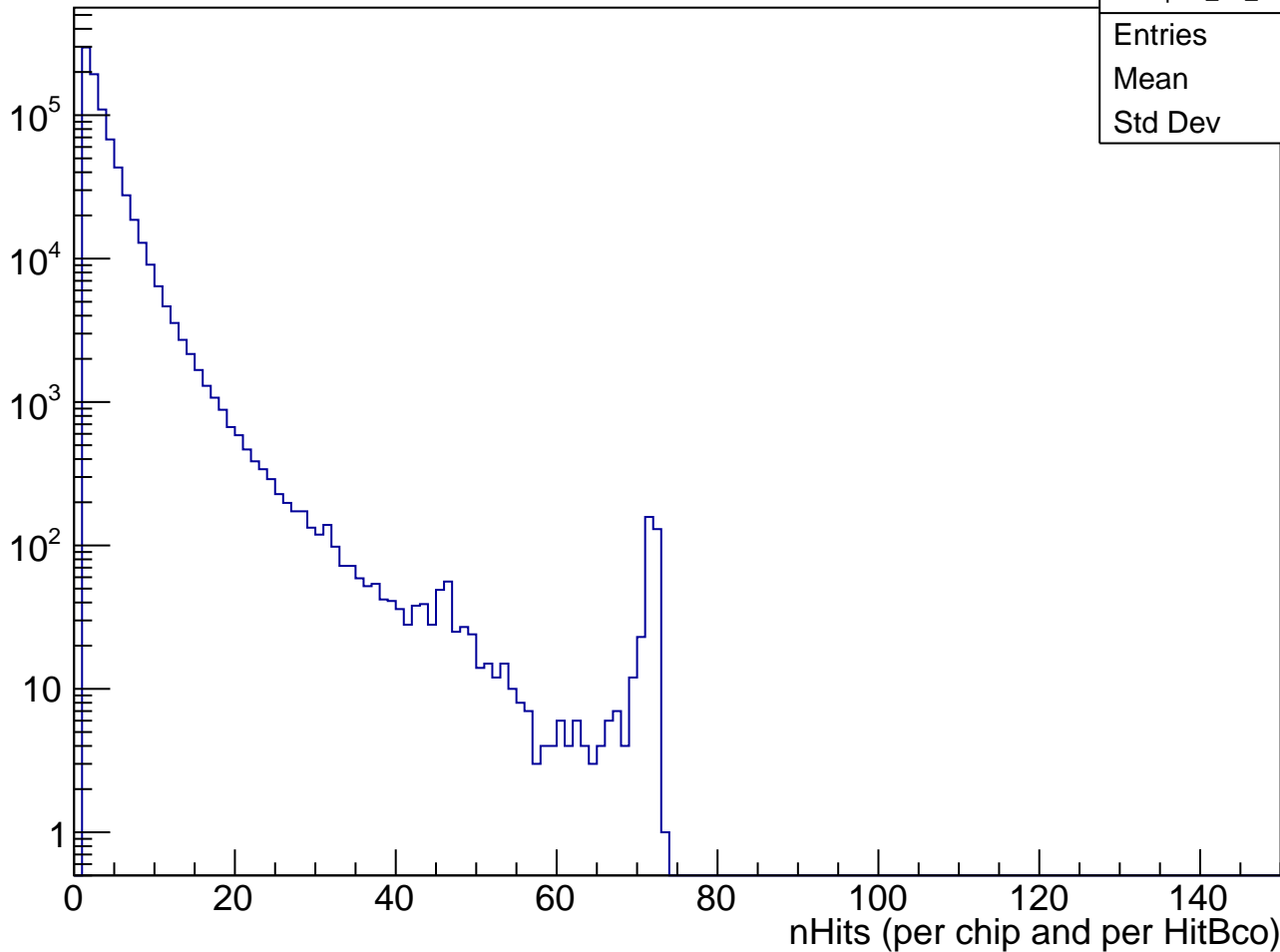
Mean 2.997

Std Dev 3.486

nHits (per chip and per HitBco)

nChipHit_F1_Fch0_Chip7

Entries

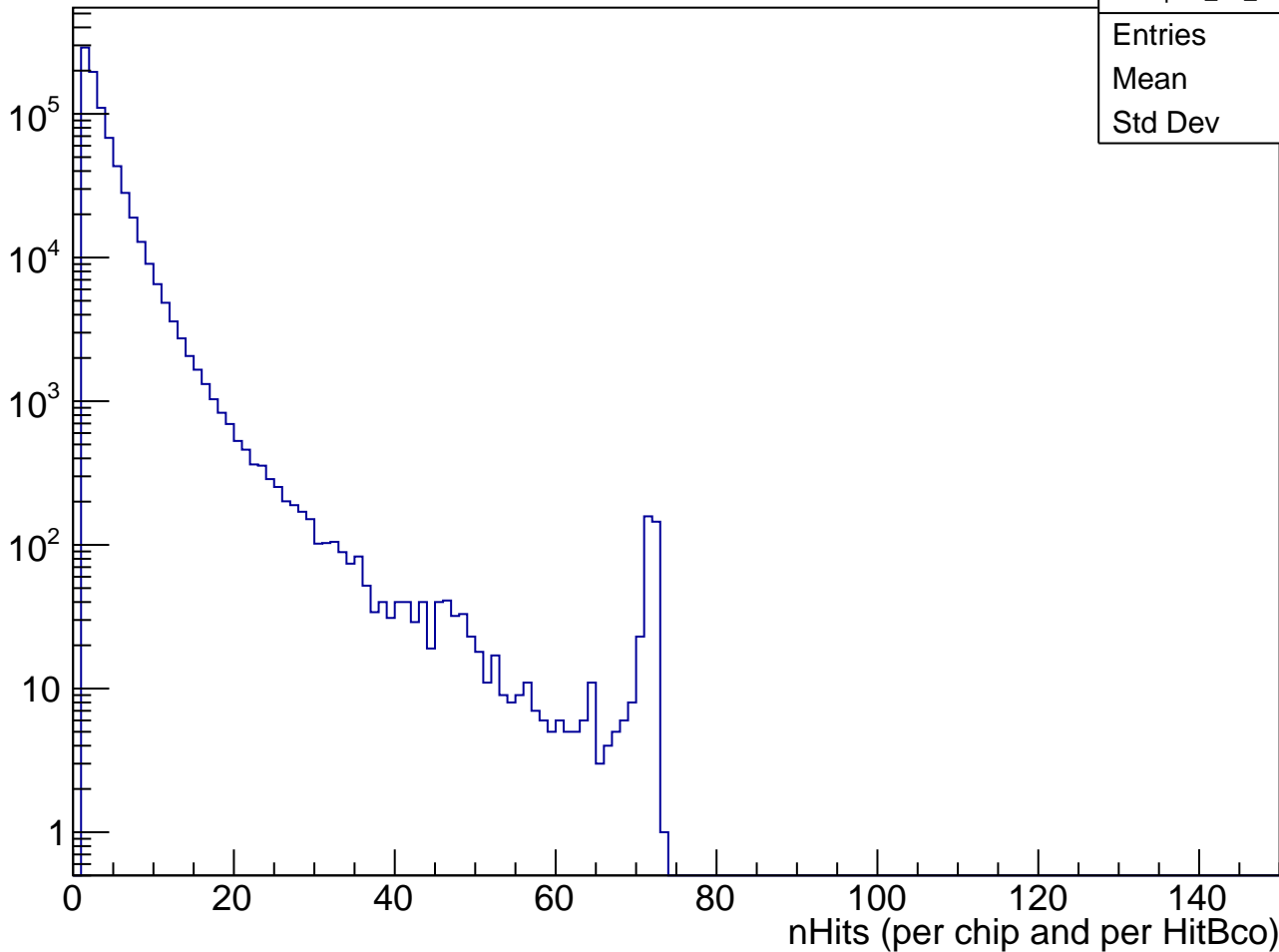


nChipHit_F1_Fch0_Chip7	
Entries	808077
Mean	3.014
Std Dev	3.47

nHits (per chip and per HitBco)

nChipHit_F1_Fch0_Chip8

Entries



nChipHit_F1_Fch0_Chip8

Entries 806065

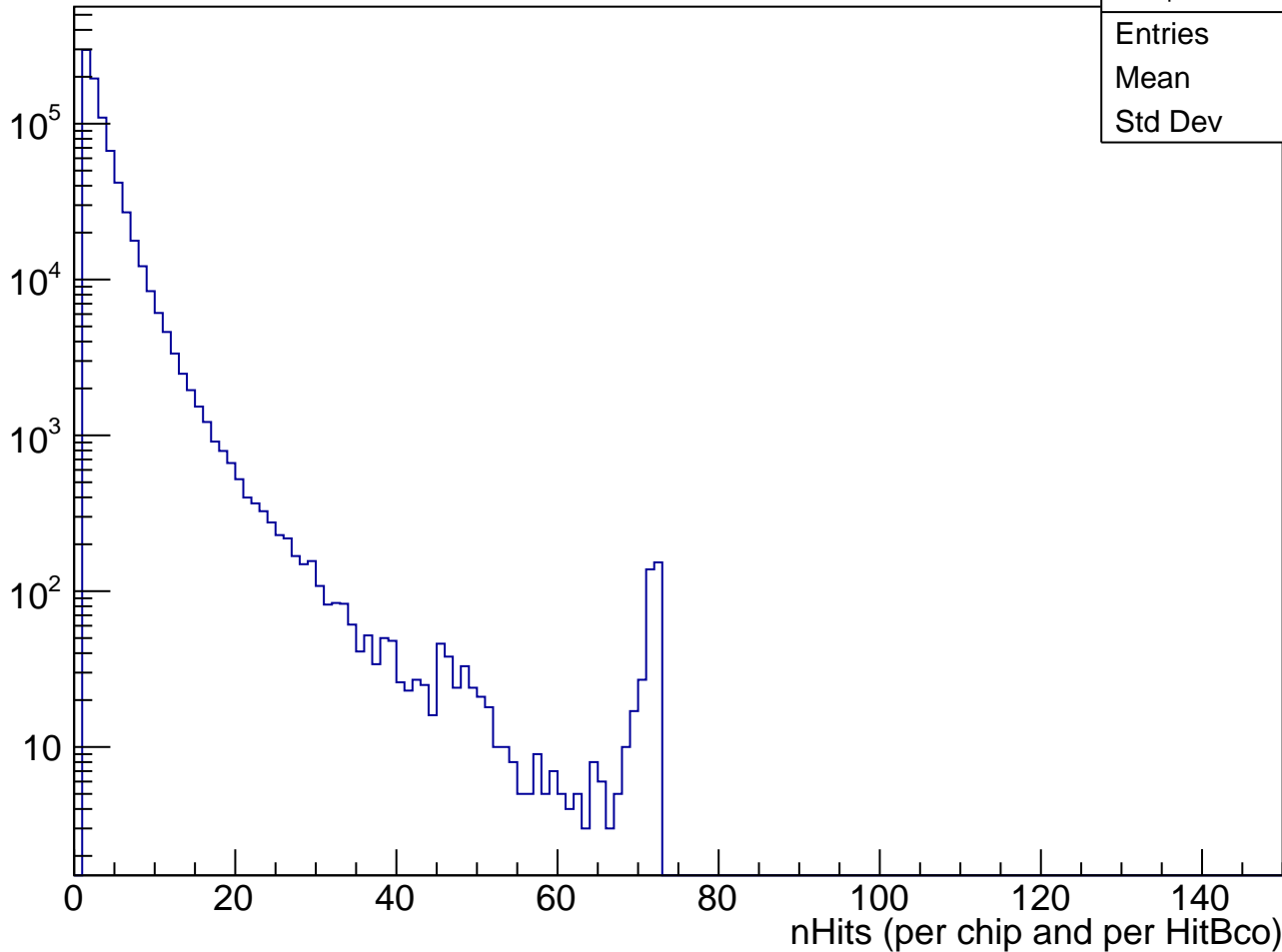
Mean 3.033

Std Dev 3.482

nHits (per chip and per HitBco)

nChipHit_F1_Fch0_Chip9

Entries

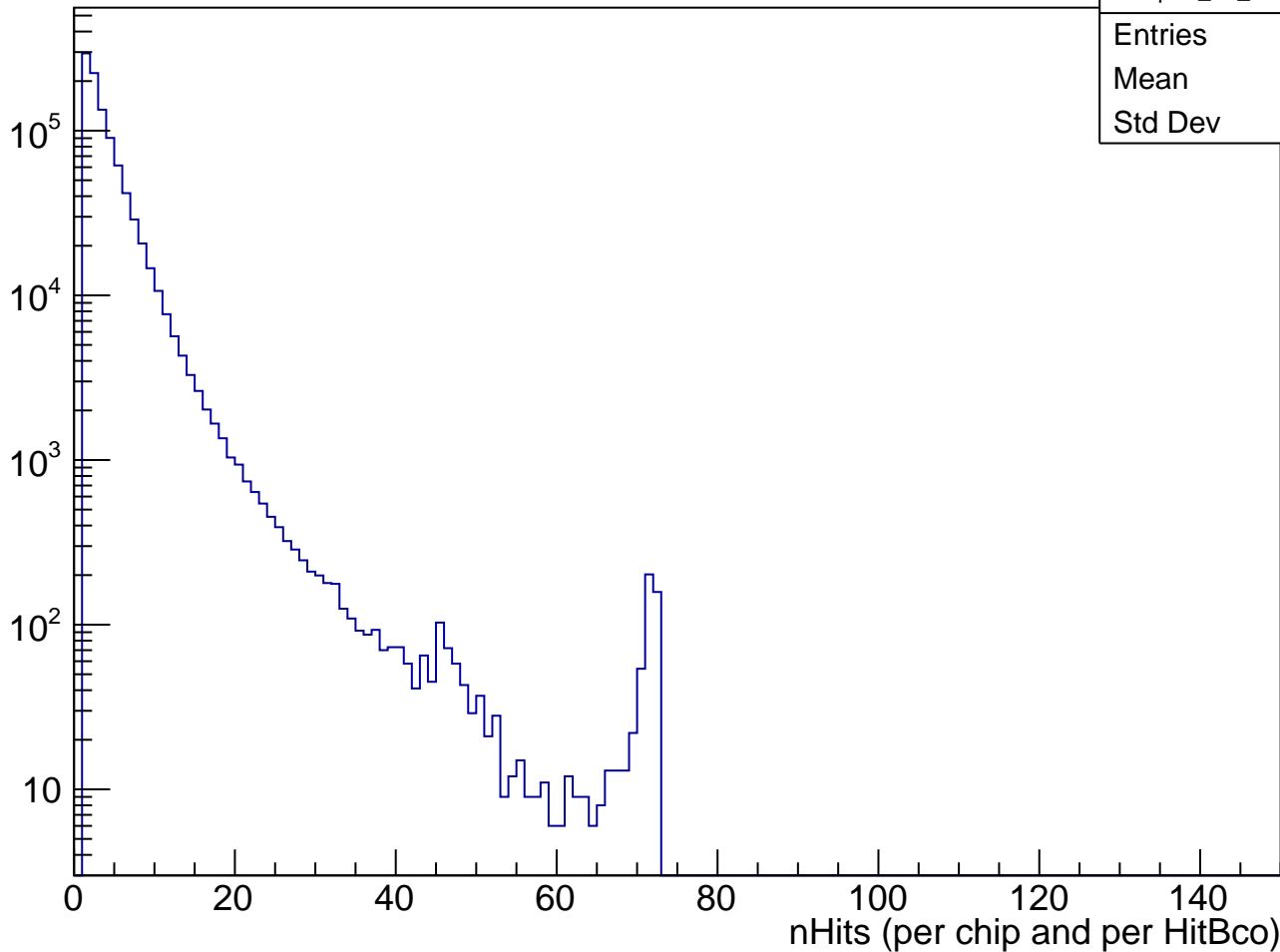


nChipHit_F1_Fch0_Chip9	
Entries	803862
Mean	2.961
Std Dev	3.408

nHits (per chip and per HitBco)

nChipHit_F1_Fch10_Chip0

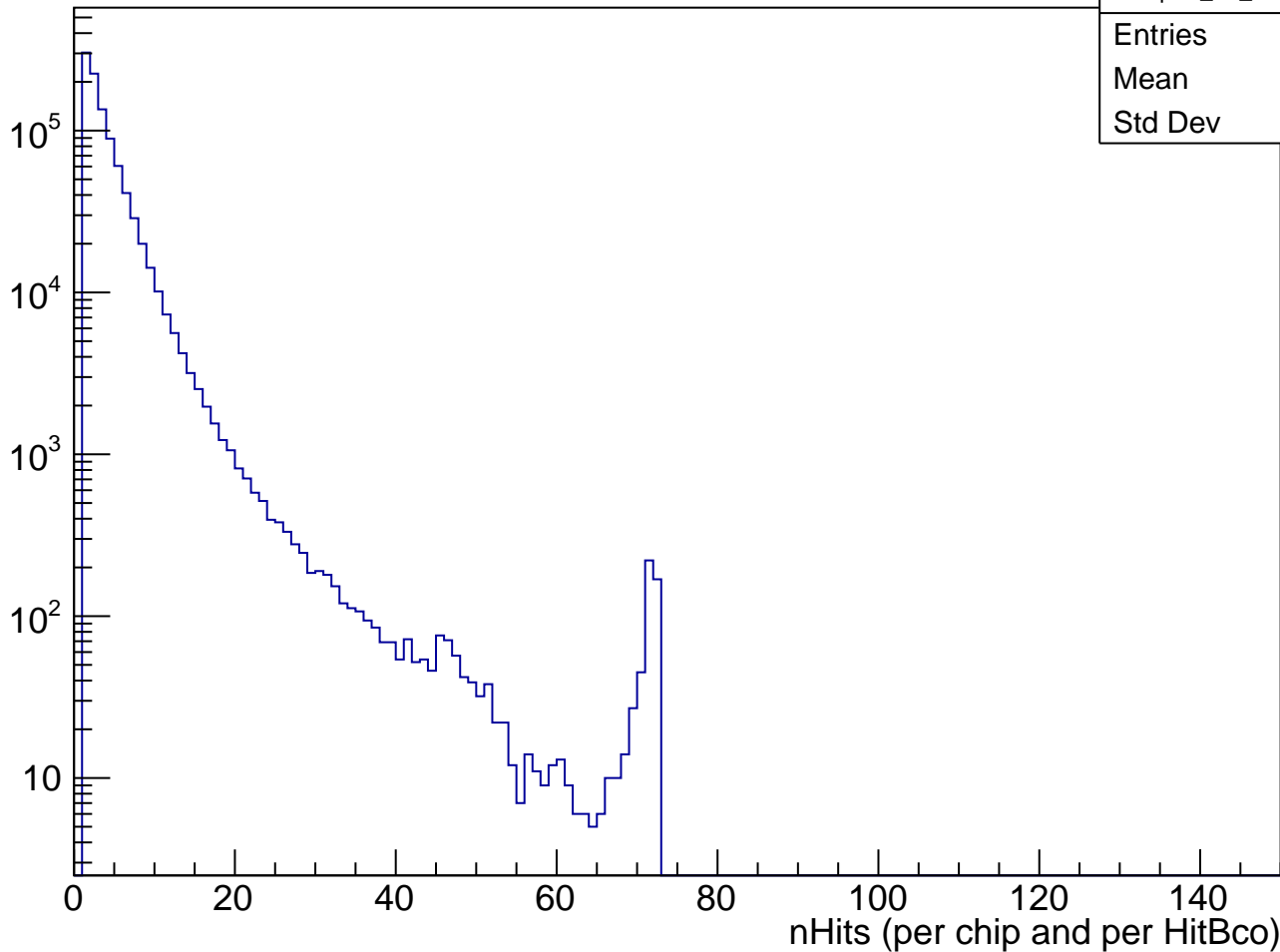
Entries



nChipHit_F1_Fch10_Chip0	
Entries	957876
Mean	3.434
Std Dev	3.853

nChipHit_F1_Fch10_Chip1

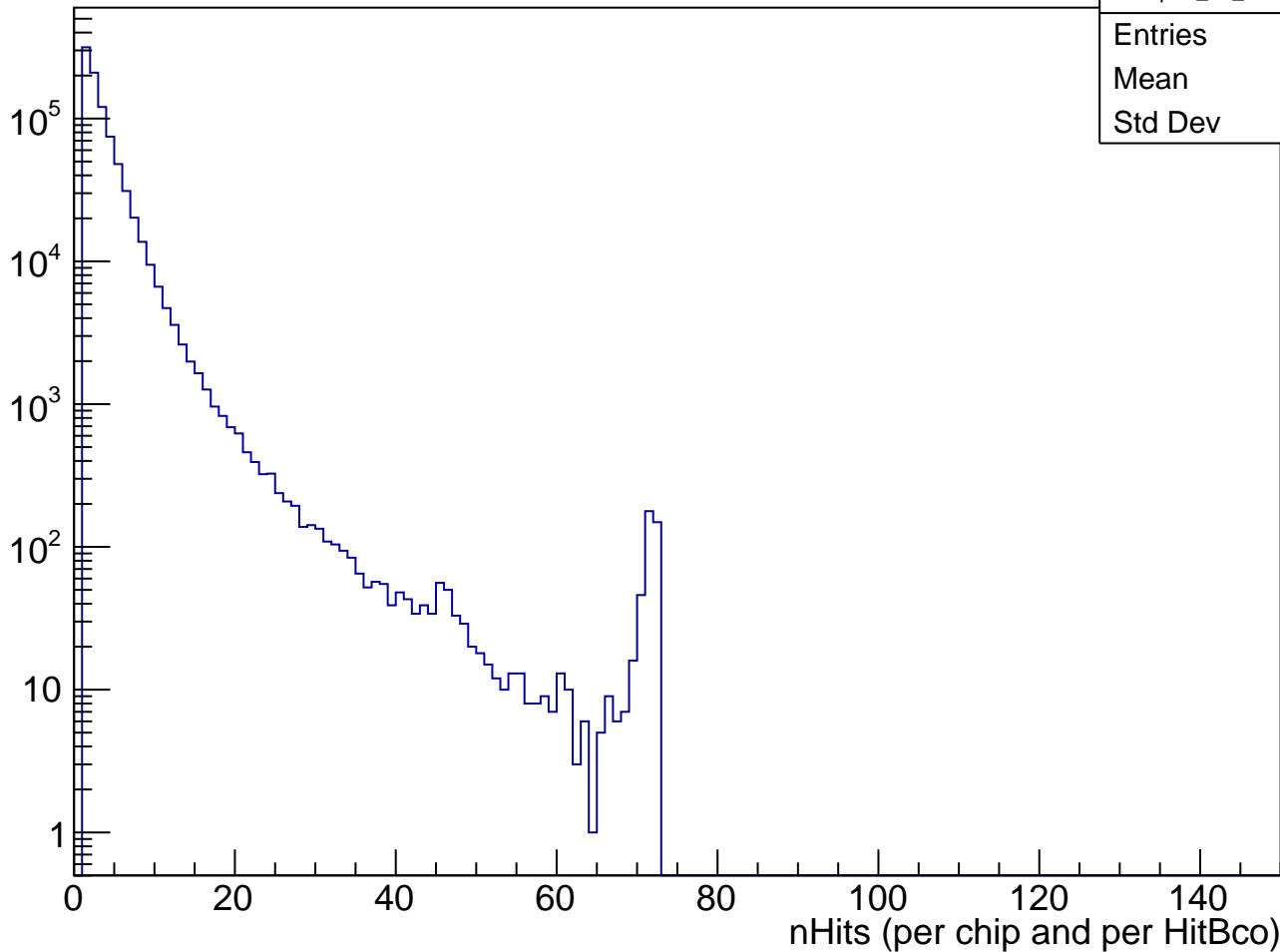
Entries



nChipHit_F1_Fch10_Chip1	
Entries	963128
Mean	3.38
Std Dev	3.821

nChipHit_F1_Fch10_Chip10

Entries



nChipHit_F1_Fch10_Chip10

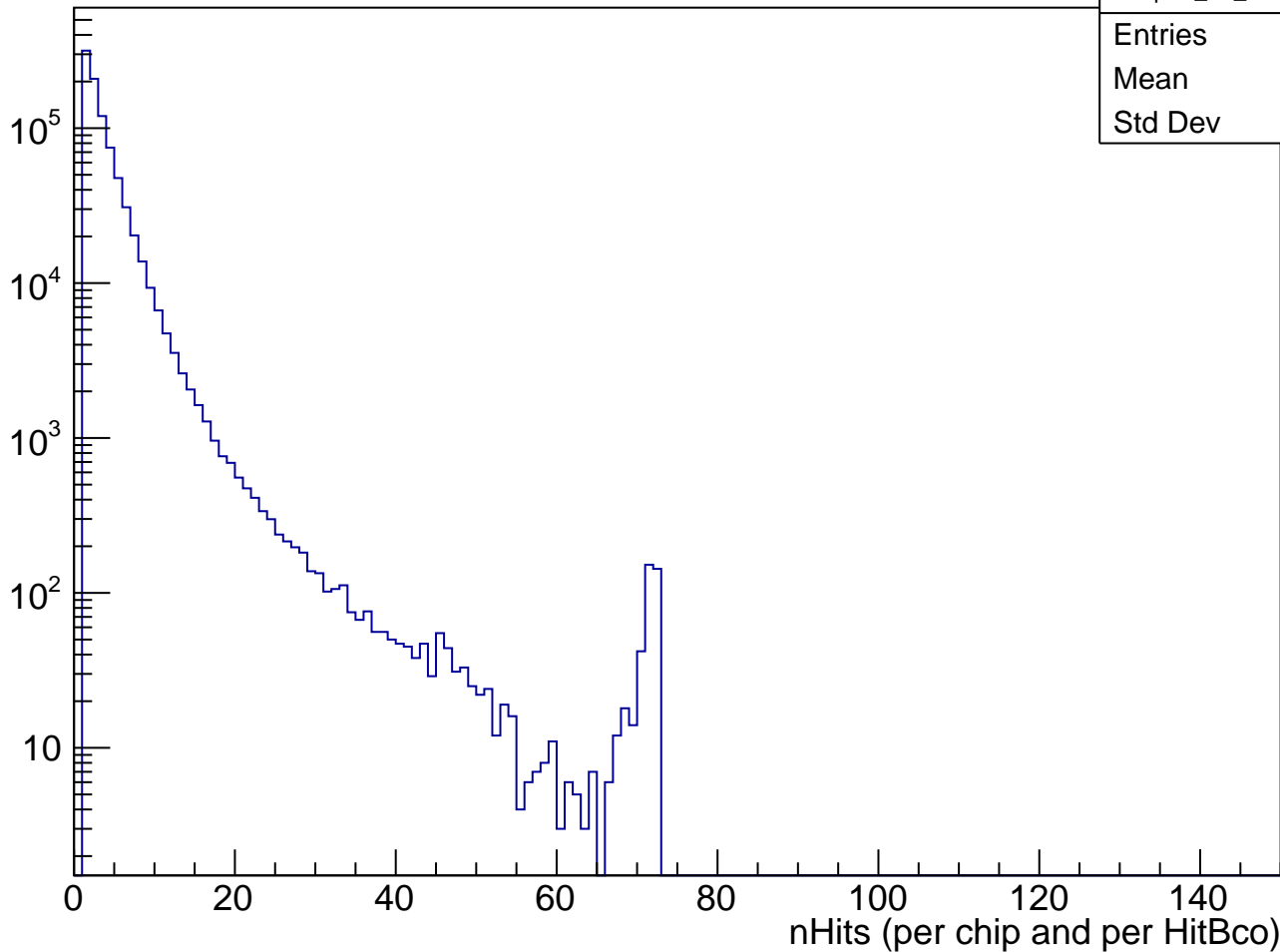
Entries 872273

Mean 3.005

Std Dev 3.457

nChipHit_F1_Fch10_Chip11

Entries

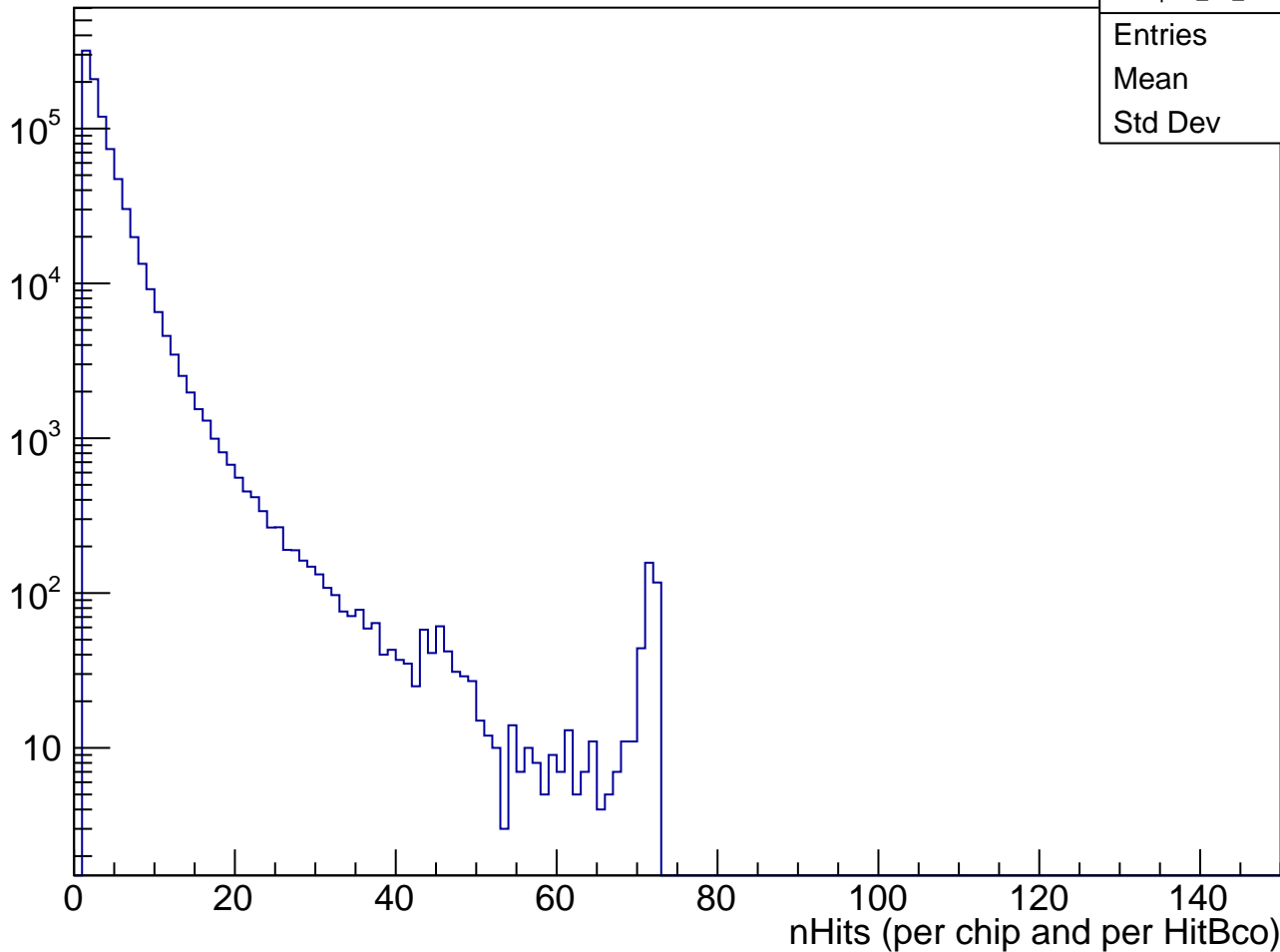


nChipHit_F1_Fch10_Chip11	
Entries	871123
Mean	3.003
Std Dev	3.45

nHits (per chip and per HitBco)

nChipHit_F1_Fch10_Chip12

Entries



nChipHit_F1_Fch10_Chip12

Entries 869115

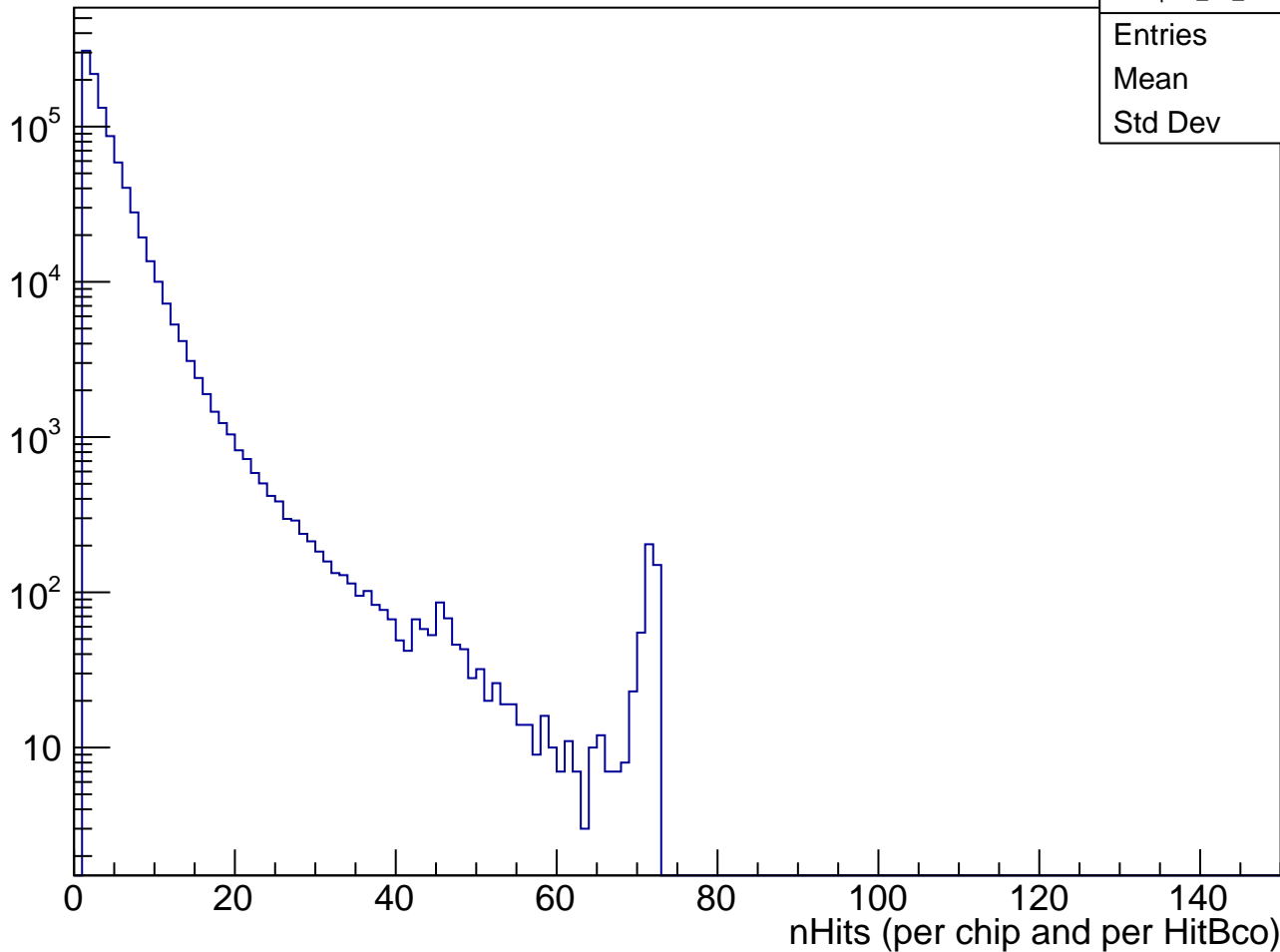
Mean 2.975

Std Dev 3.399

nHits (per chip and per HitBco)

nChipHit_F1_Fch10_Chip13

Entries



nChipHit_F1_Fch10_Chip13

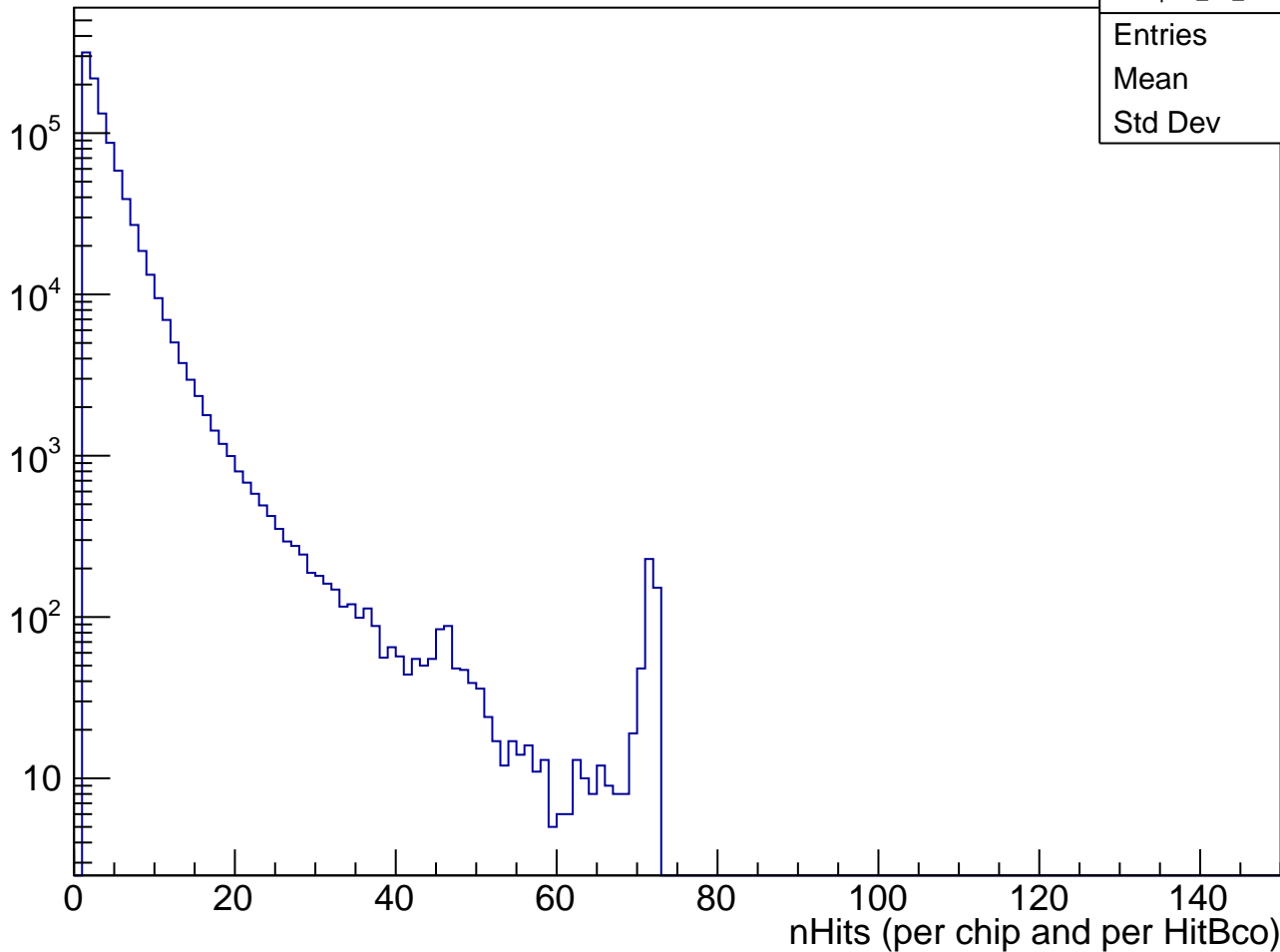
Entries 950248

Mean 3.349

Std Dev 3.799

nChipHit_F1_Fch10_Chip14

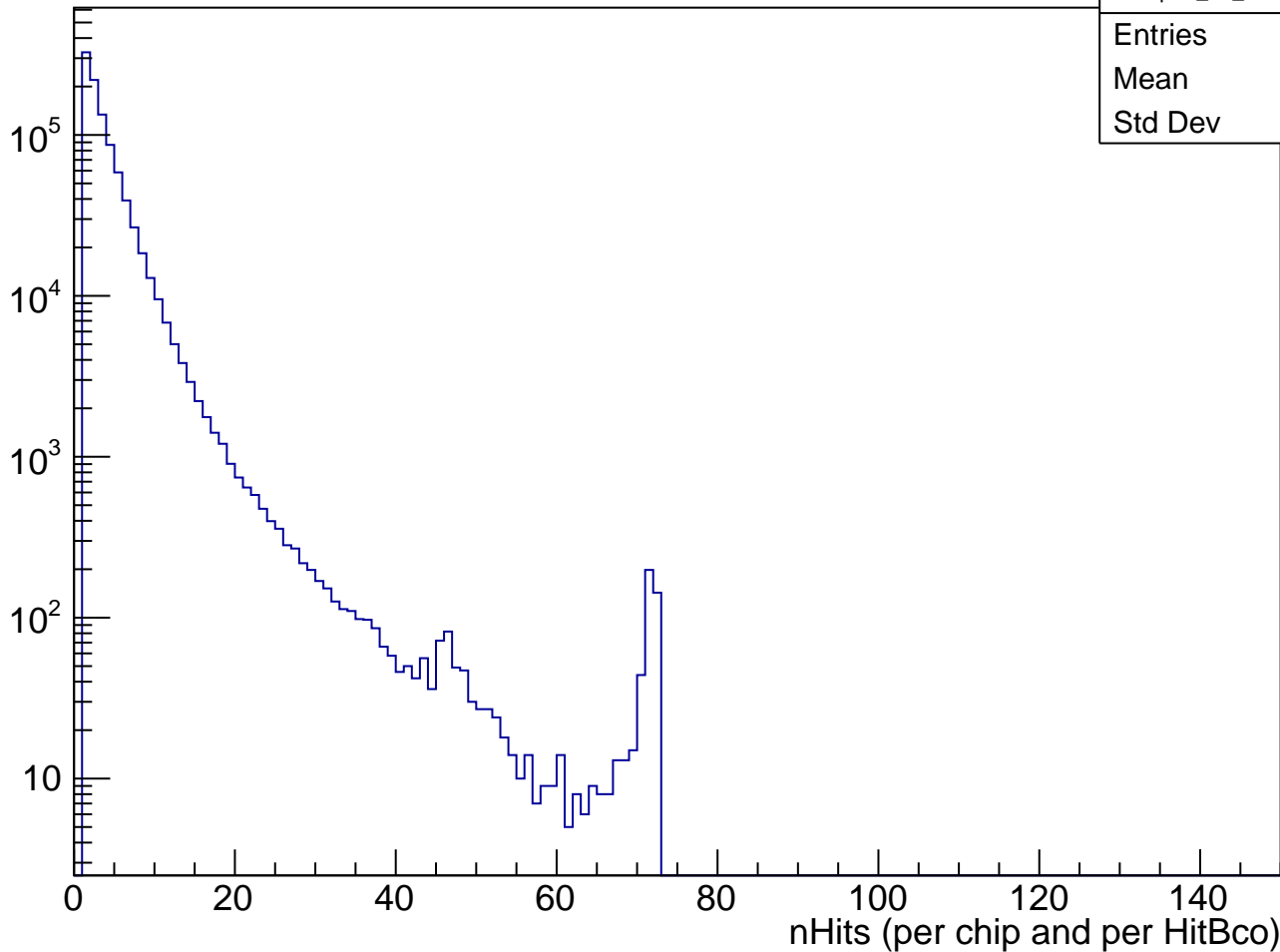
Entries



nChipHit_F1_Fch10_Chip14	
Entries	953025
Mean	3.297
Std Dev	3.778

nChipHit_F1_Fch10_Chip15

Entries



nChipHit_F1_Fch10_Chip15

Entries 964329

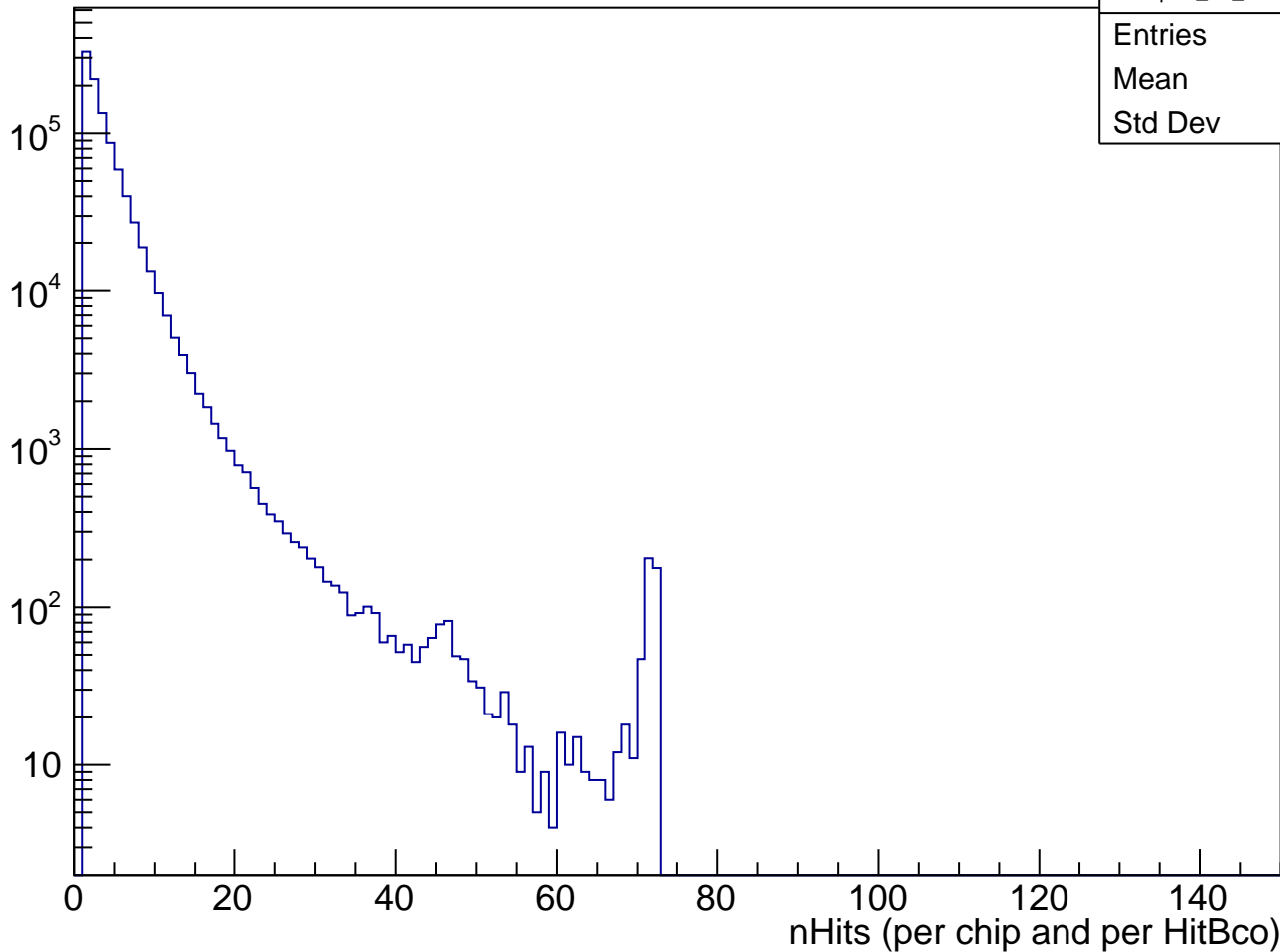
Mean 3.252

Std Dev 3.693

nHits (per chip and per HitBco)

nChipHit_F1_Fch10_Chip16

Entries



nChipHit_F1_Fch10_Chip16

Entries 970507

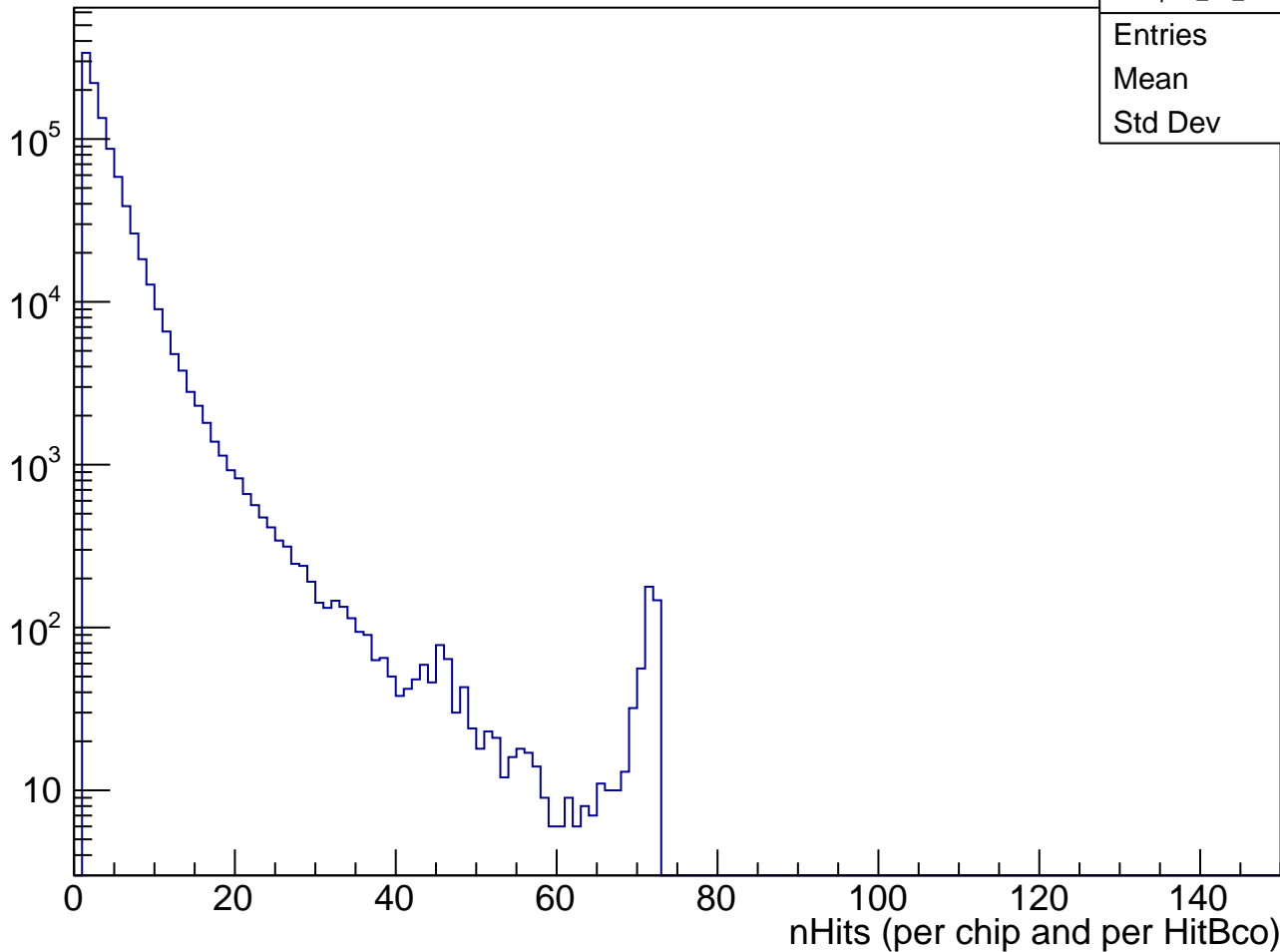
Mean 3.271

Std Dev 3.748

nHits (per chip and per HitBco)

nChipHit_F1_Fch10_Chip17

Entries



nChipHit_F1_Fch10_Chip17

Entries 976288

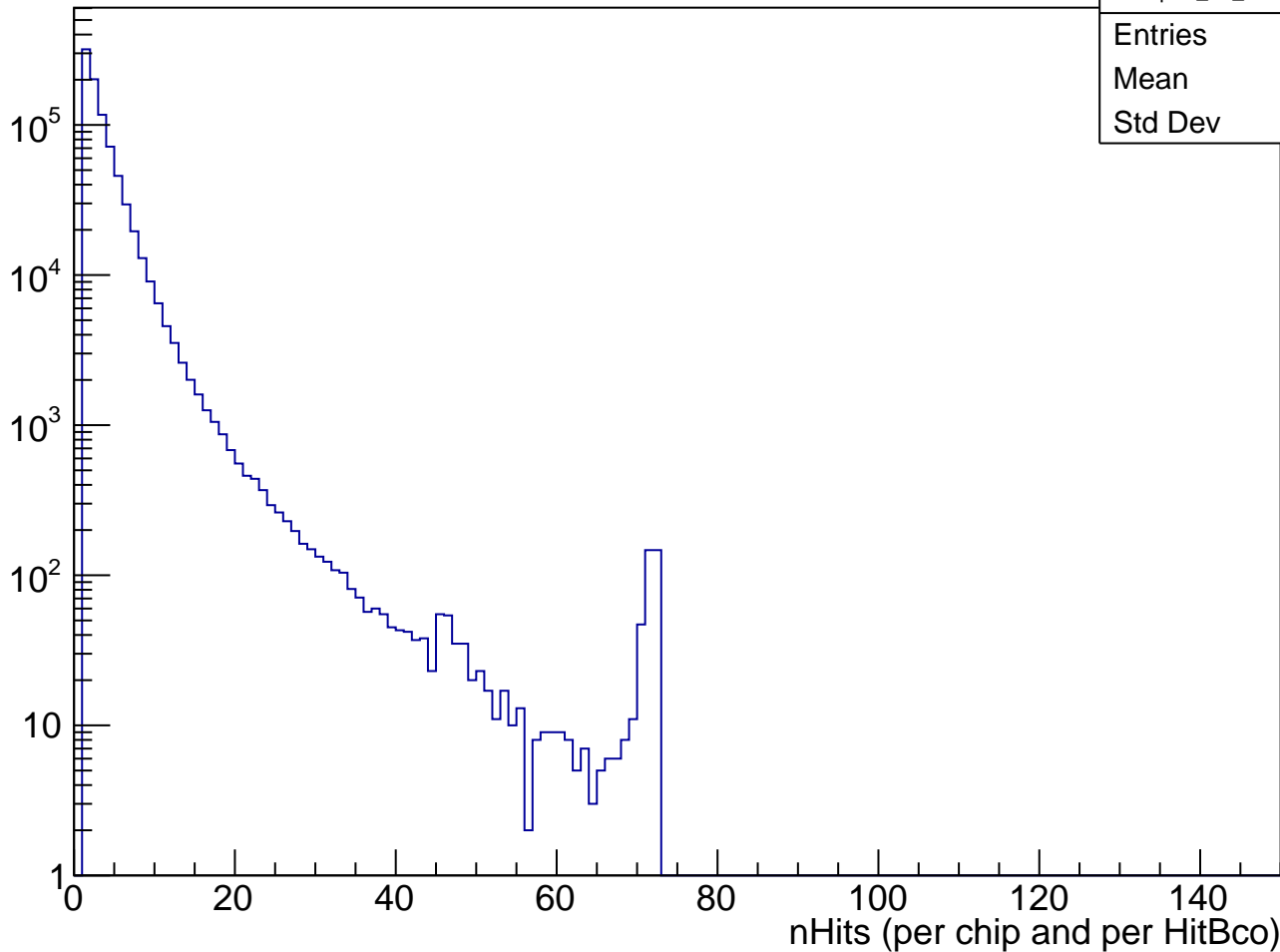
Mean 3.209

Std Dev 3.66

nHits (per chip and per HitBco)

nChipHit_F1_Fch10_Chip18

Entries



nChipHit_F1_Fch10_Chip18

Entries 855388

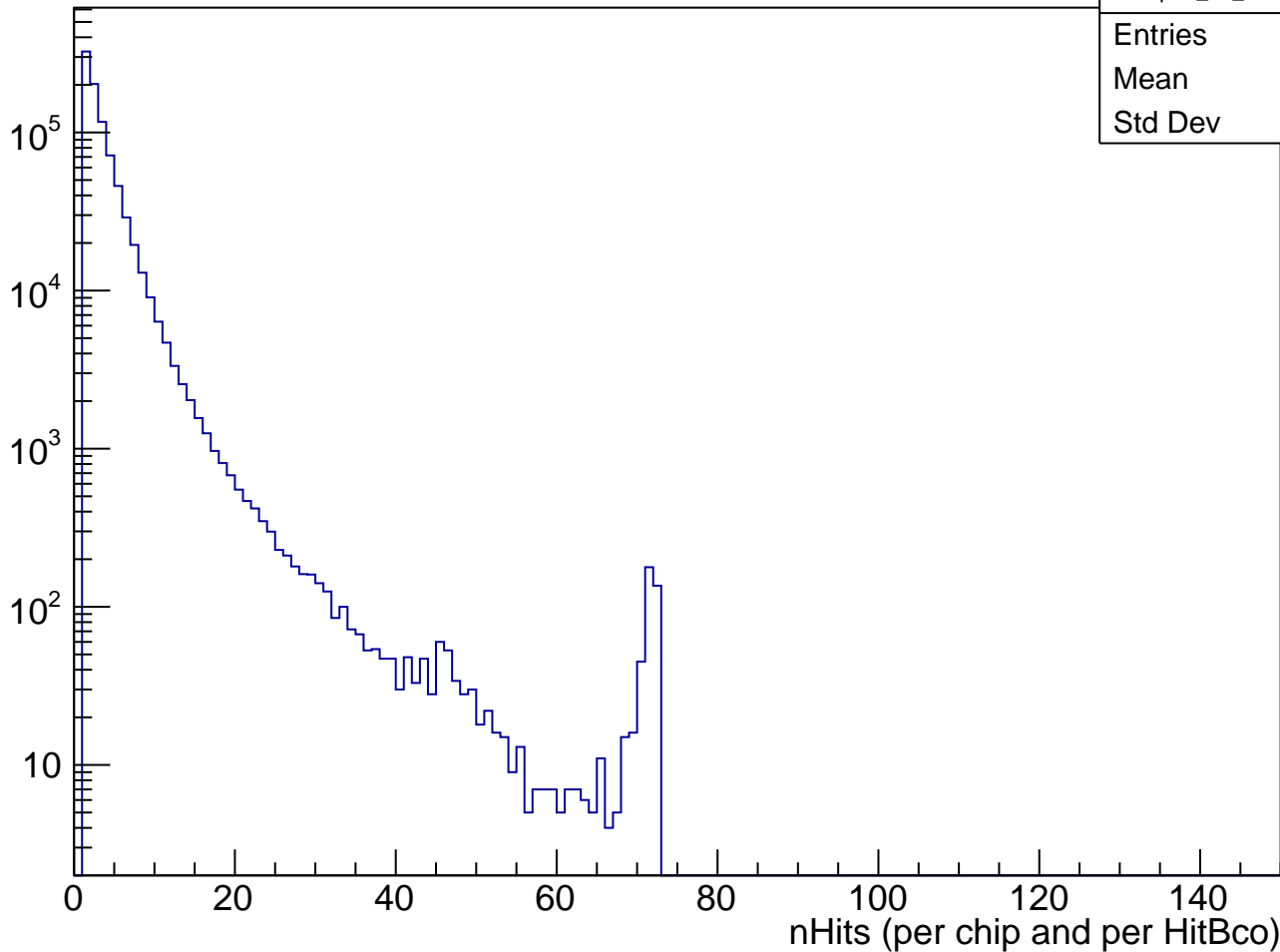
Mean 2.981

Std Dev 3.467

nHits (per chip and per HitBco)

nChipHit_F1_Fch10_Chip19

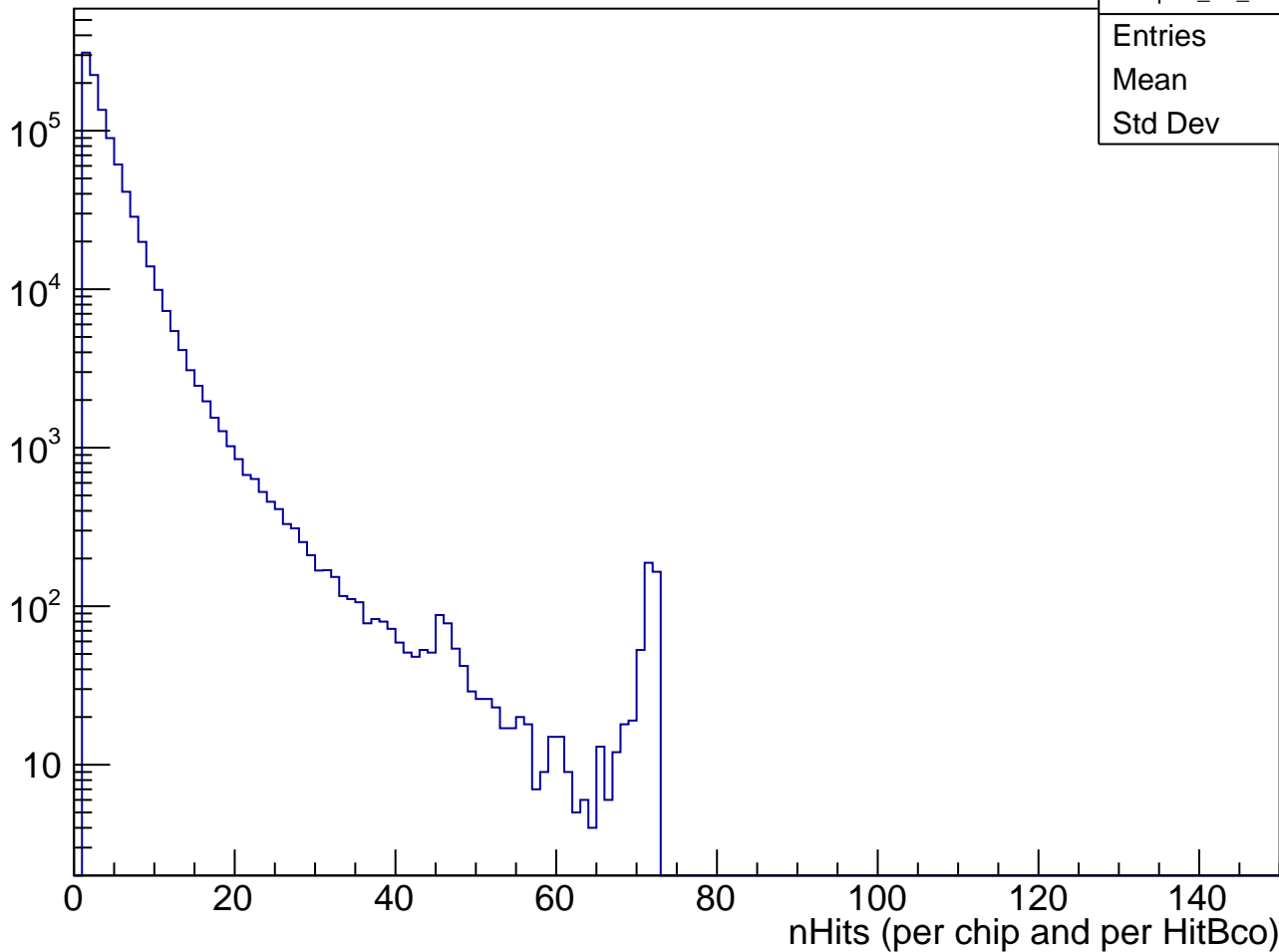
Entries



nChipHit_F1_Fch10_Chip19	
Entries	861299
Mean	2.958
Std Dev	3.461

nChipHit_F1_Fch10_Chip2

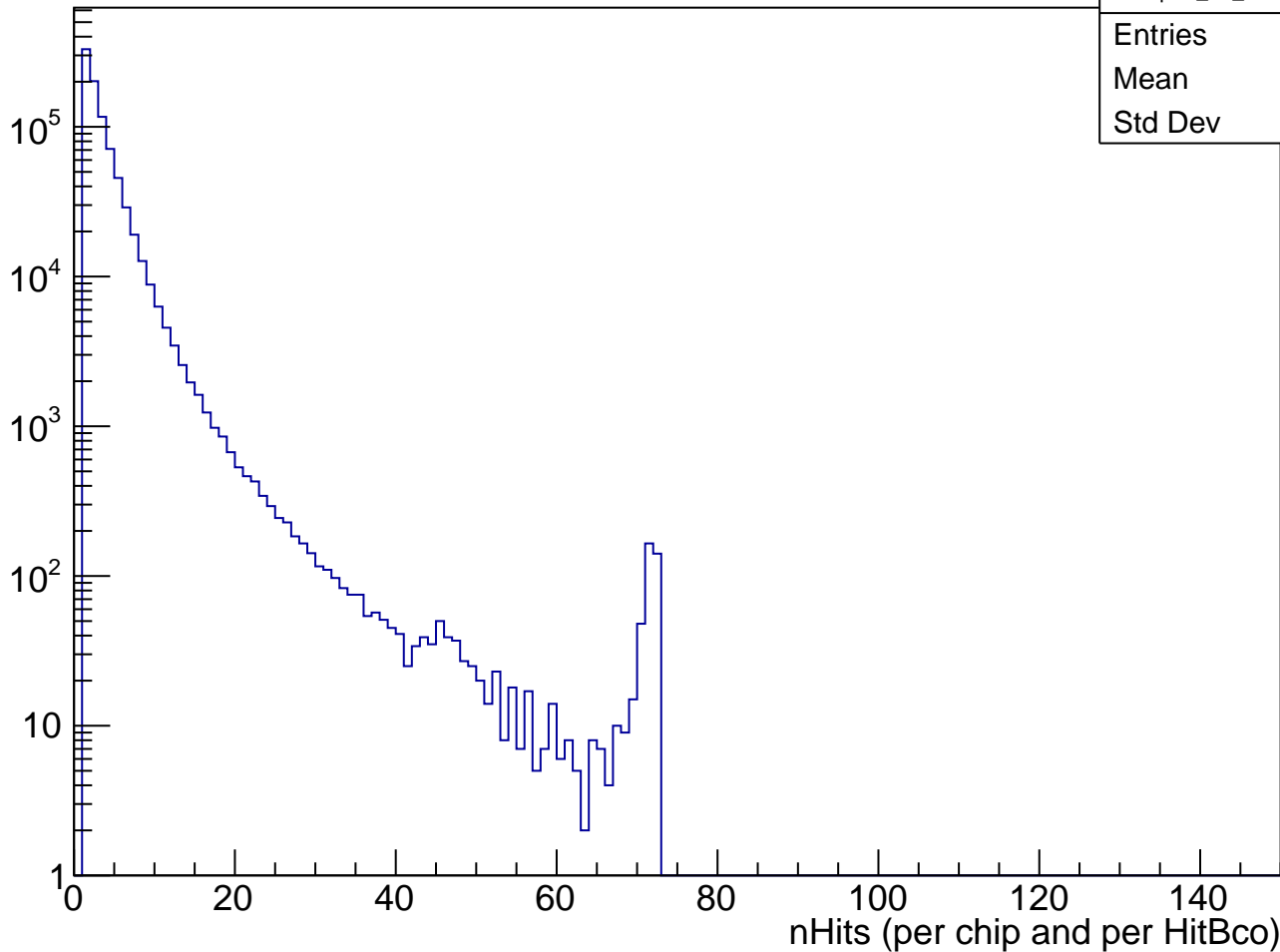
Entries



nChipHit_F1_Fch10_Chip2	
Entries	970919
Mean	3.357
Std Dev	3.793

nChipHit_F1_Fch10_Chip20

Entries

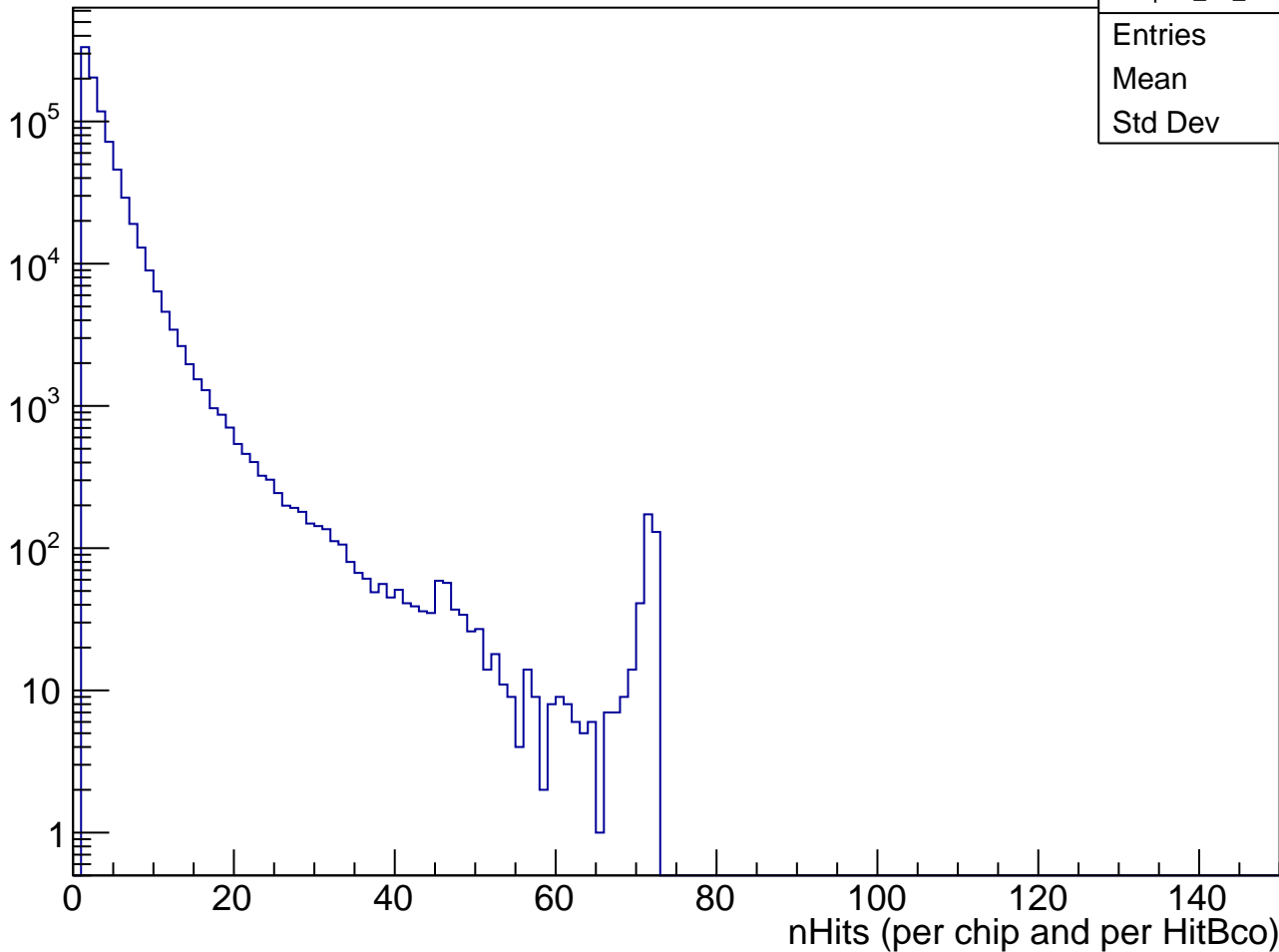


nChipHit_F1_Fch10_Chip20	
Entries	863432
Mean	2.938
Std Dev	3.441

nHits (per chip and per HitBco)

nChipHit_F1_Fch10_Chip21

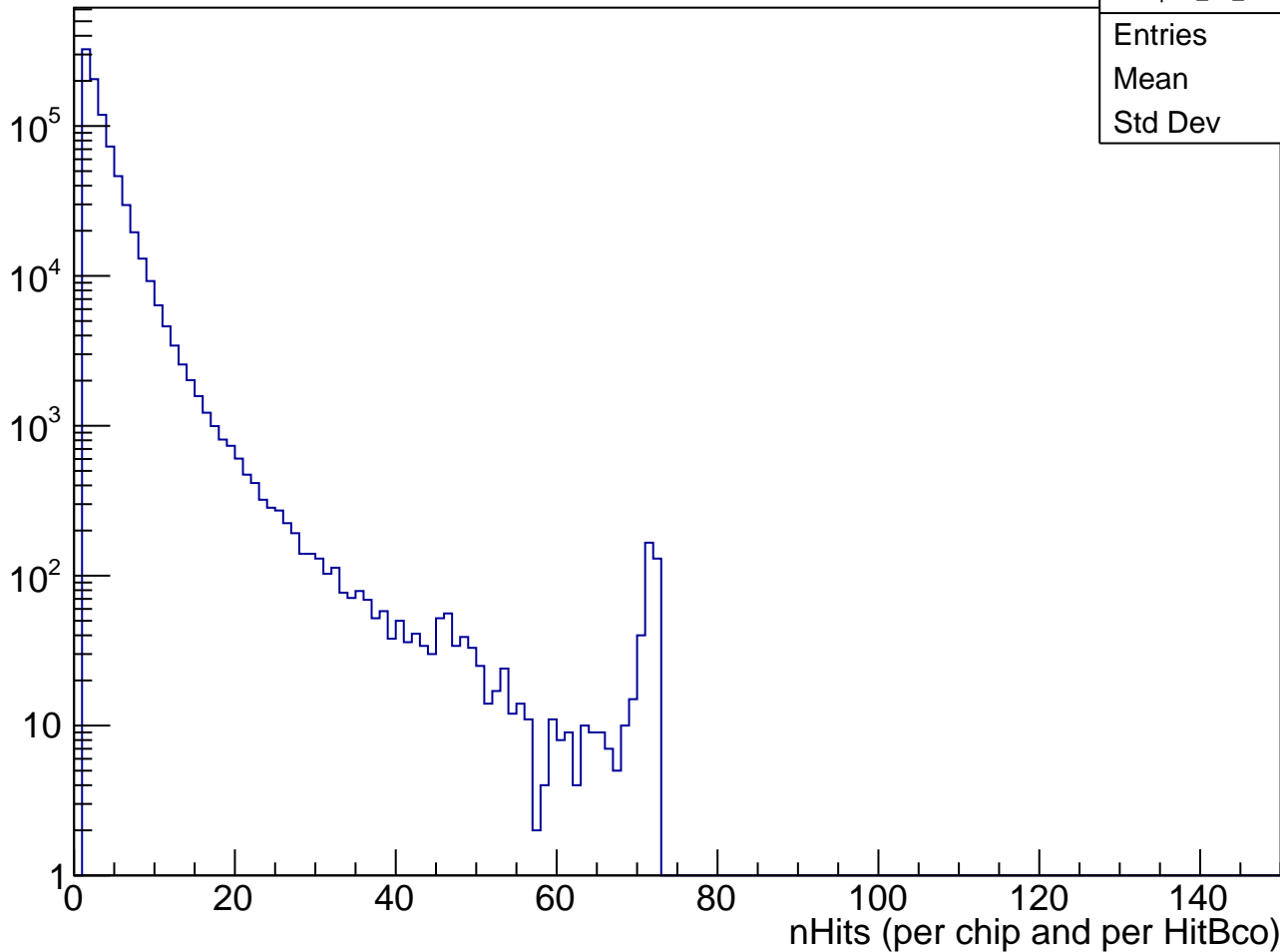
Entries



nChipHit_F1_Fch10_Chip21	
Entries	871410
Mean	2.939
Std Dev	3.443

nChipHit_F1_Fch10_Chip22

Entries



nChipHit_F1_Fch10_Chip22

Entries 869284

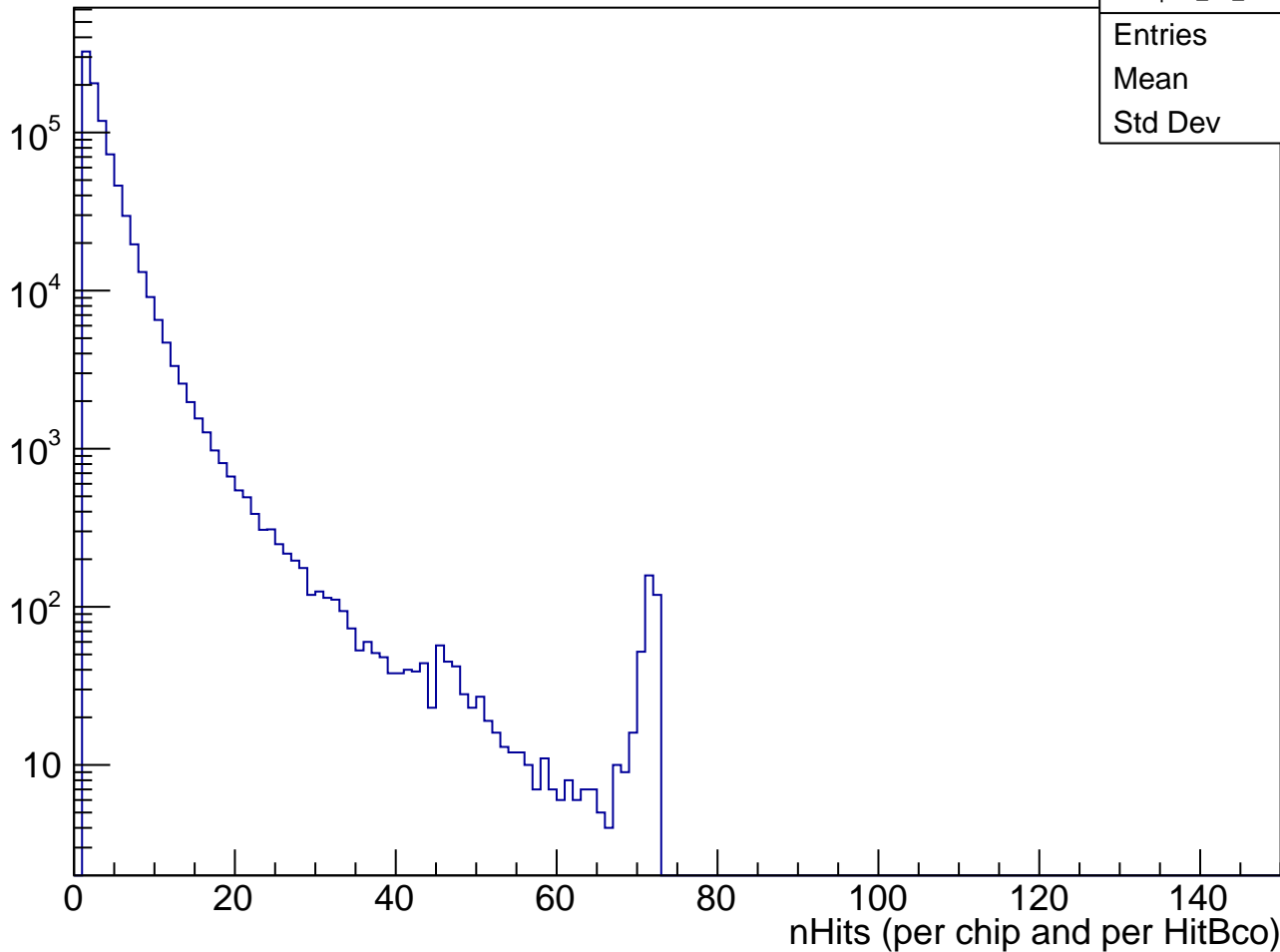
Mean 2.963

Std Dev 3.449

nHits (per chip and per HitBco)

nChipHit_F1_Fch10_Chip23

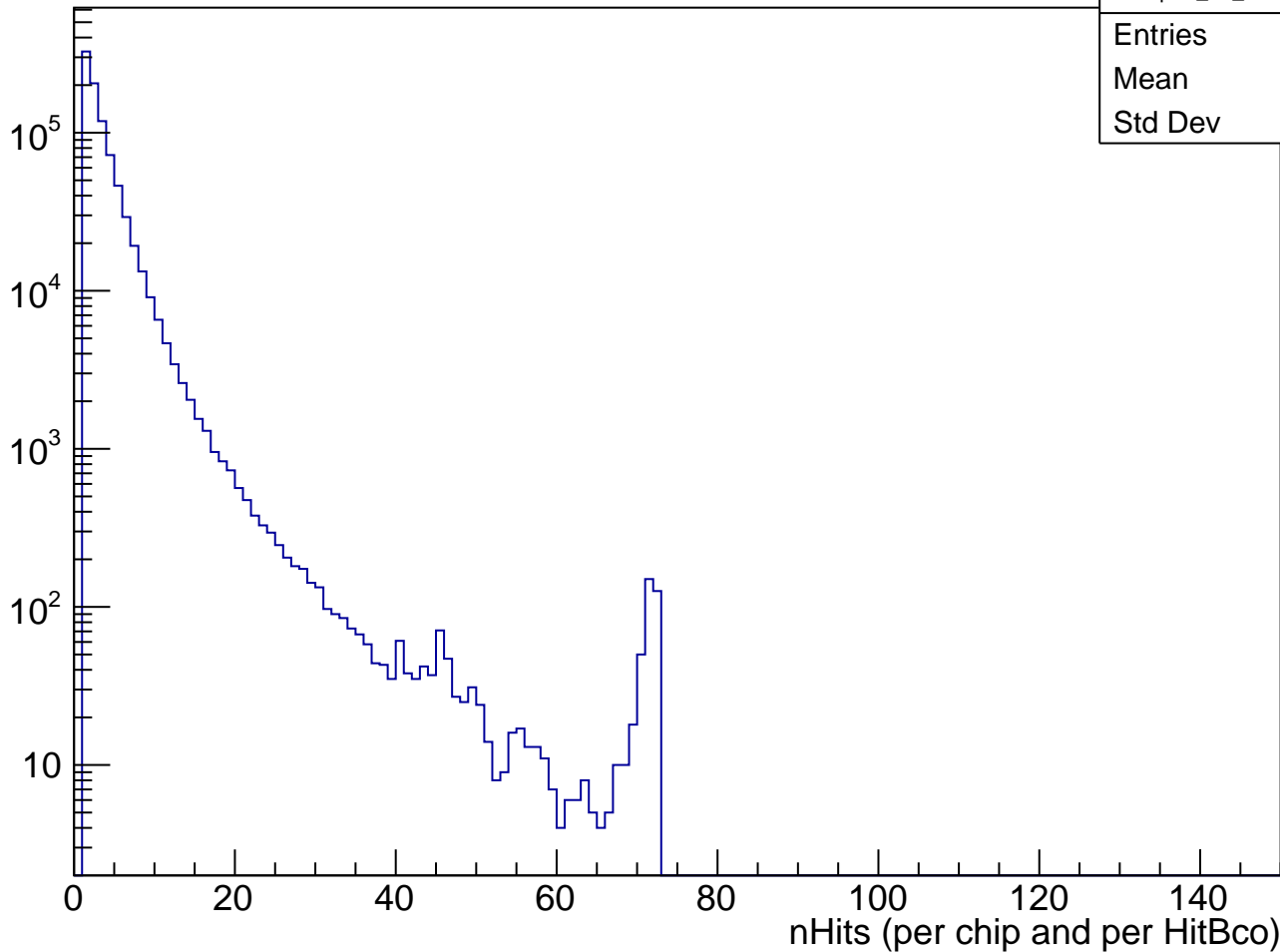
Entries



nChipHit_F1_Fch10_Chip23	
Entries	867845
Mean	2.958
Std Dev	3.422

nChipHit_F1_Fch10_Chip24

Entries



nChipHit_F1_Fch10_Chip24

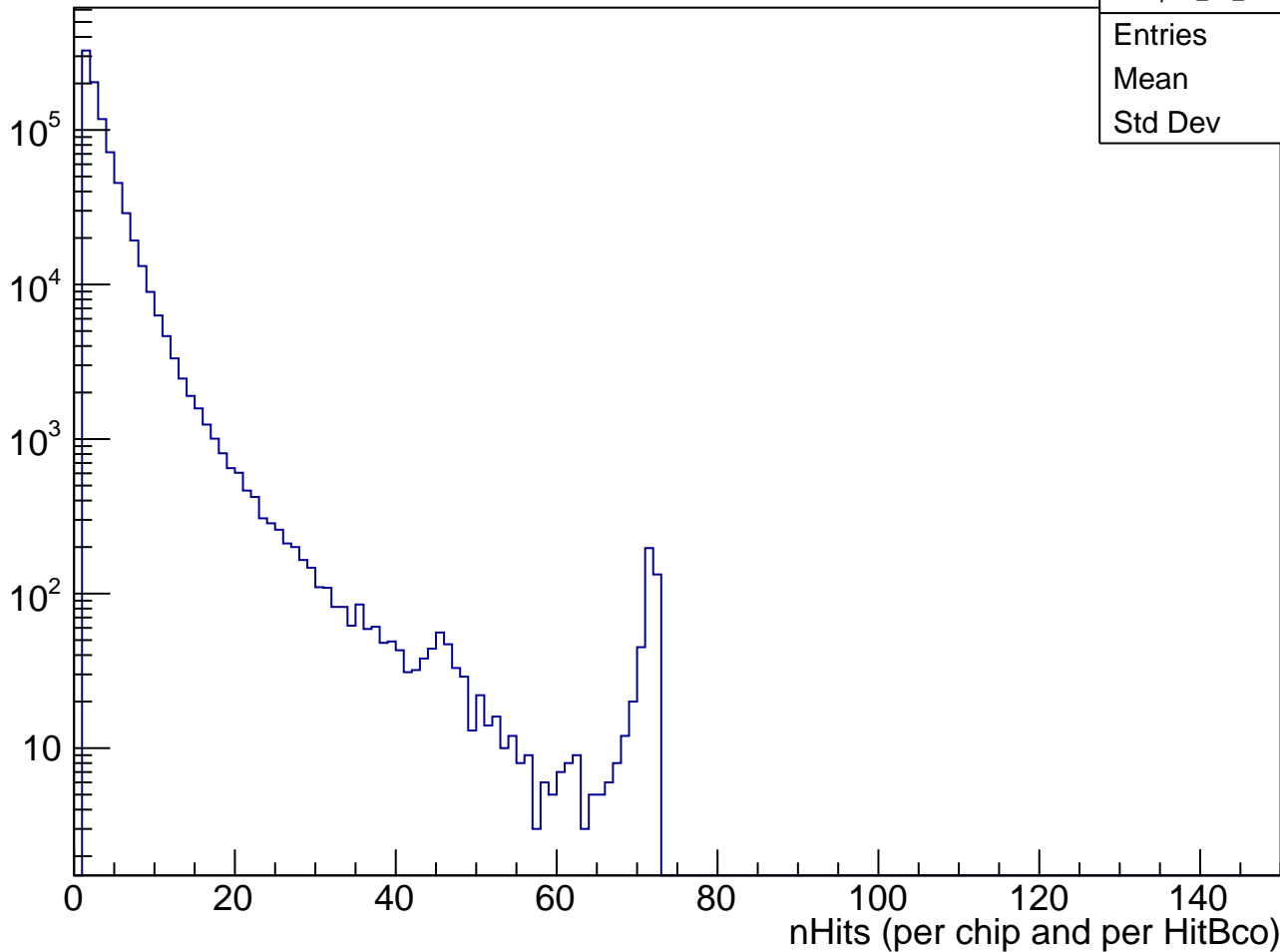
Entries 868615

Mean 2.955

Std Dev 3.426

nChipHit_F1_Fch10_Chip25

Entries



nChipHit_F1_Fch10_Chip25

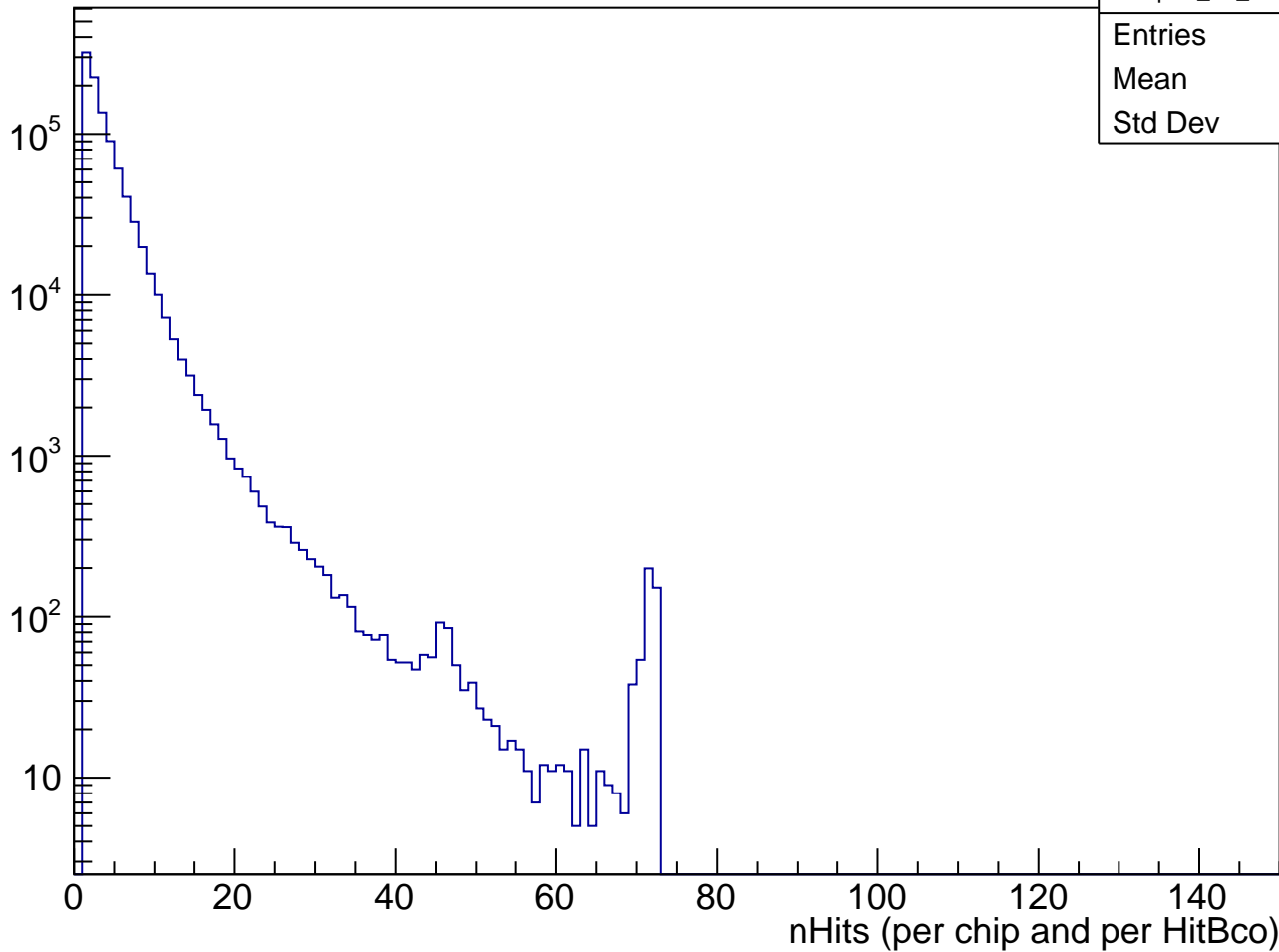
Entries 864170

Mean 2.946

Std Dev 3.447

nChipHit_F1_Fch10_Chip3

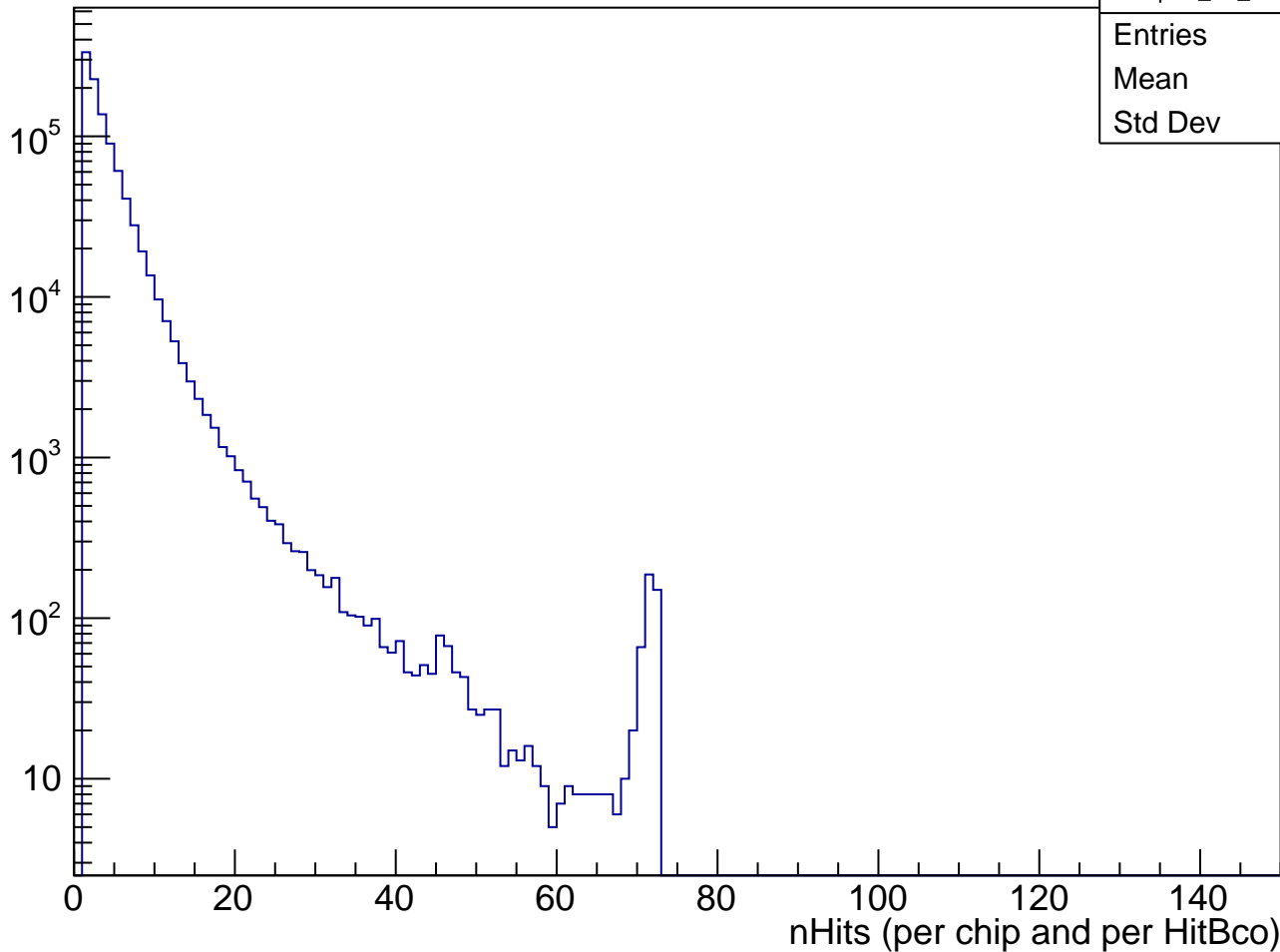
Entries



nChipHit_F1_Fch10_Chip3	
Entries	980719
Mean	3.318
Std Dev	3.765

nChipHit_F1_Fch10_Chip4

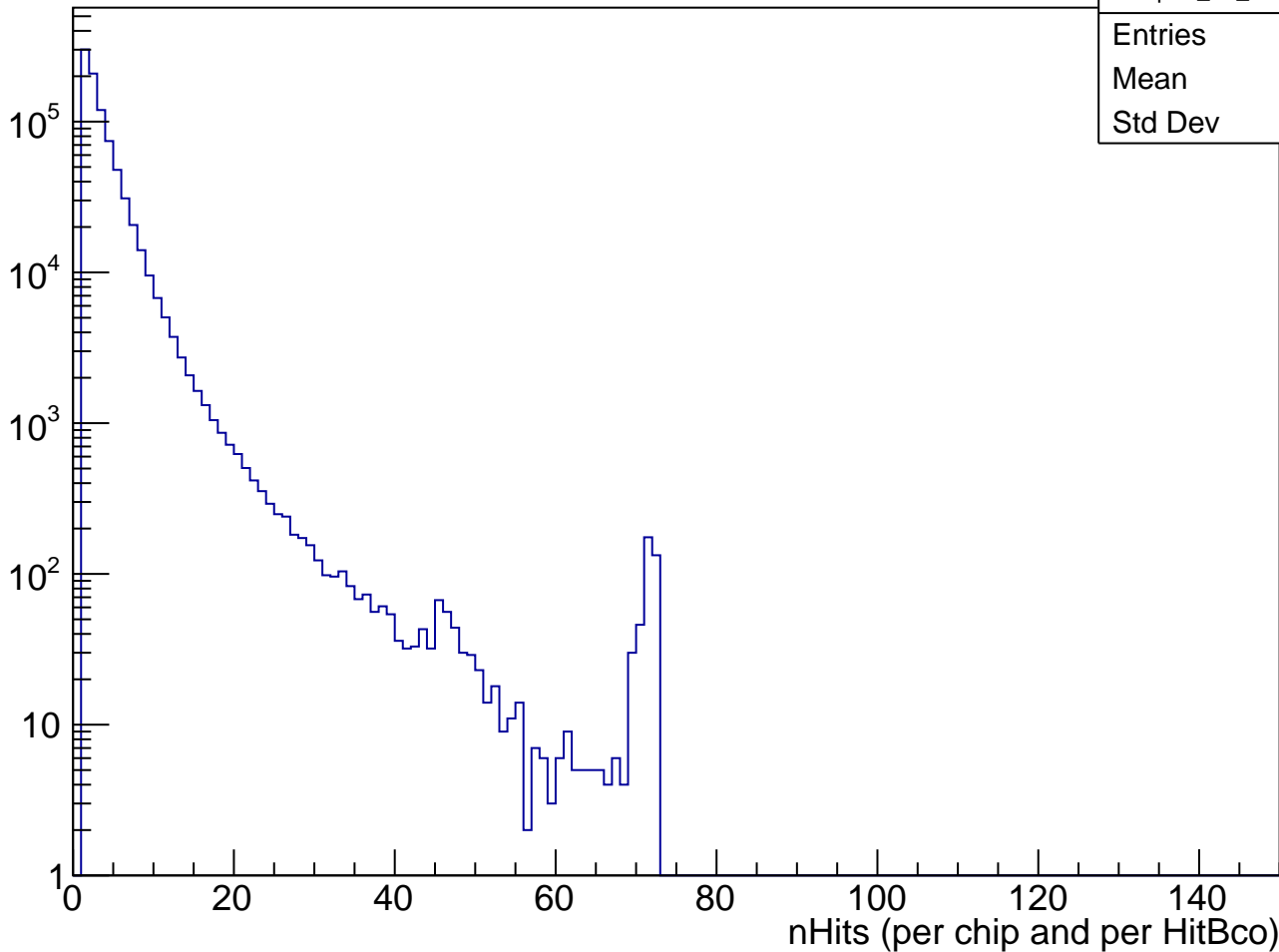
Entries



nChipHit_F1_Fch10_Chip4	
Entries	993859
Mean	3.268
Std Dev	3.701

nChipHit_F1_Fch10_Chip5

Entries

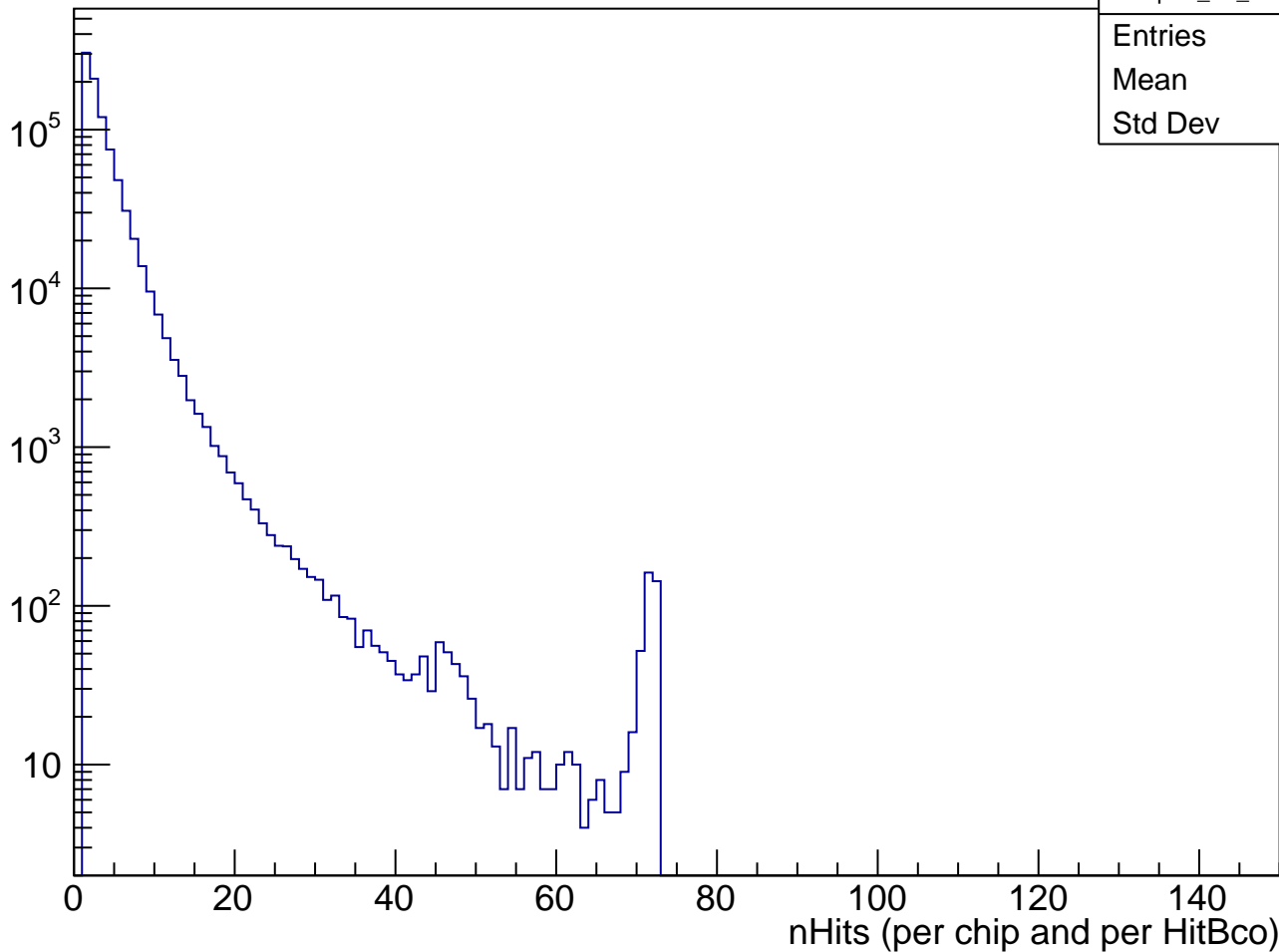


nChipHit_F1_Fch10_Chip5	
Entries	856874
Mean	3.059
Std Dev	3.506

nHits (per chip and per HitBco)

nChipHit_F1_Fch10_Chip6

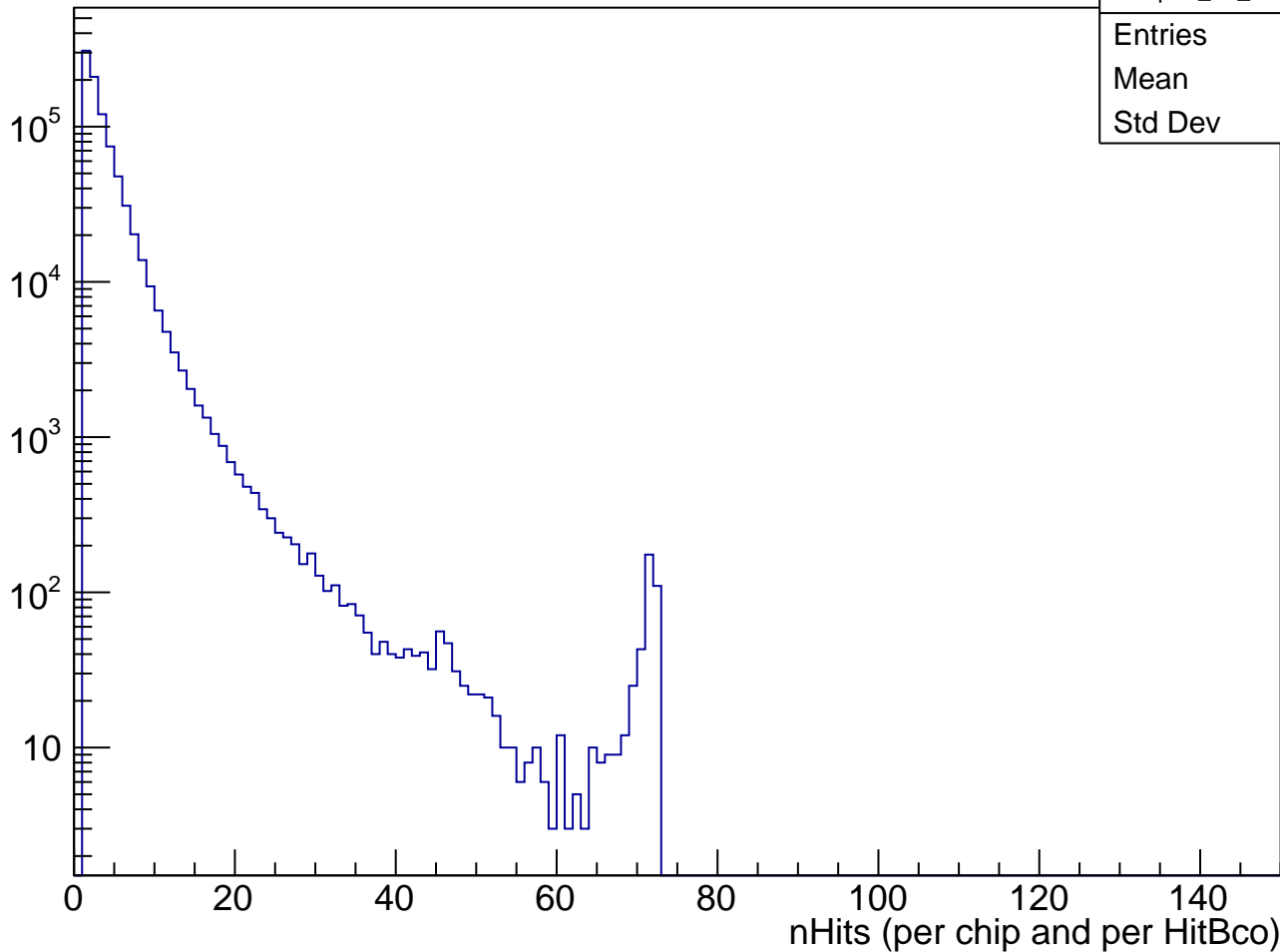
Entries



nChipHit_F1_Fch10_Chip6	
Entries	862717
Mean	3.041
Std Dev	3.49

nChipHit_F1_Fch10_Chip7

Entries

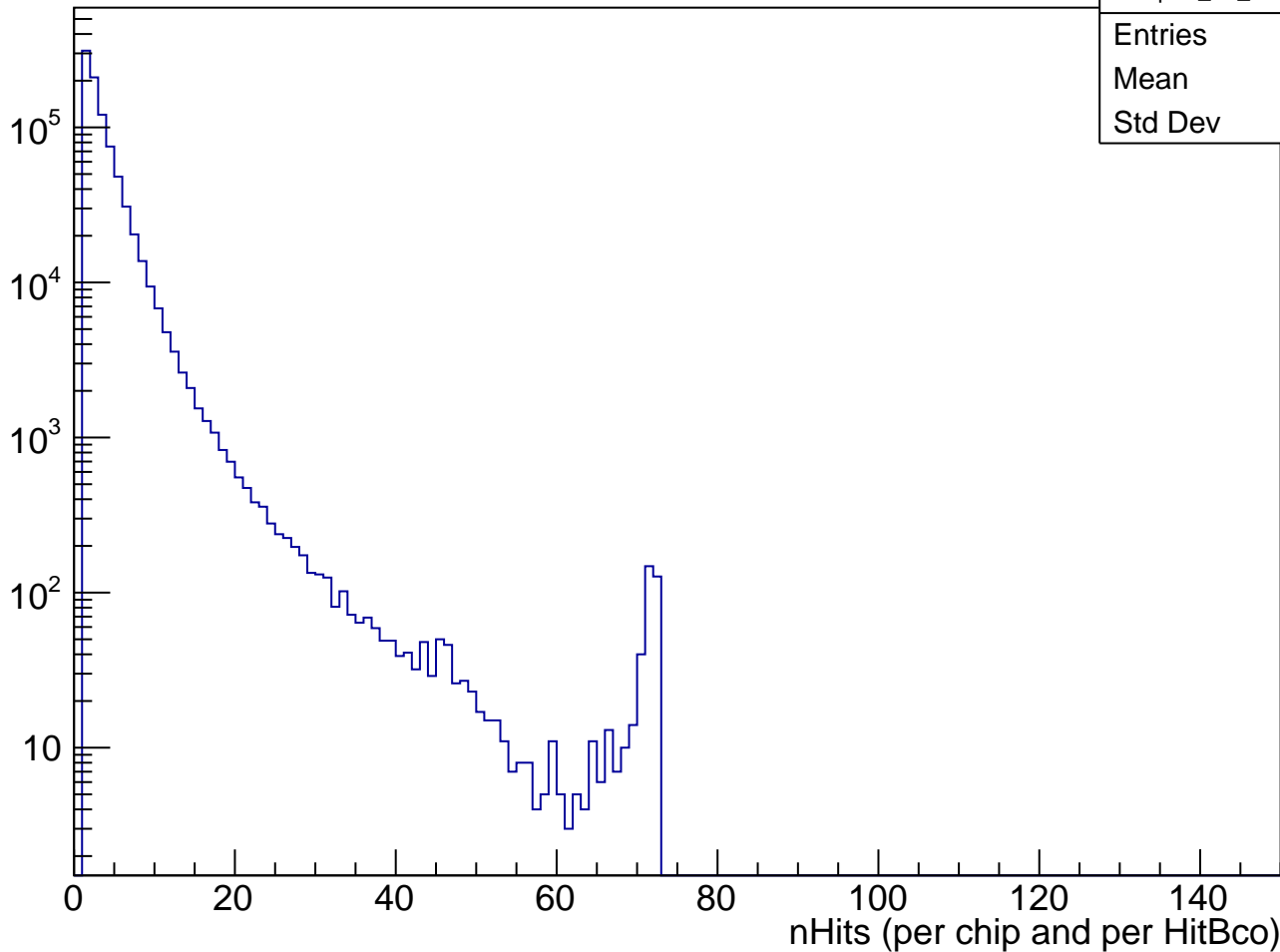


nChipHit_F1_Fch10_Chip7	
Entries	864697
Mean	3.023
Std Dev	3.452

nHits (per chip and per HitBco)

nChipHit_F1_Fch10_Chip8

Entries

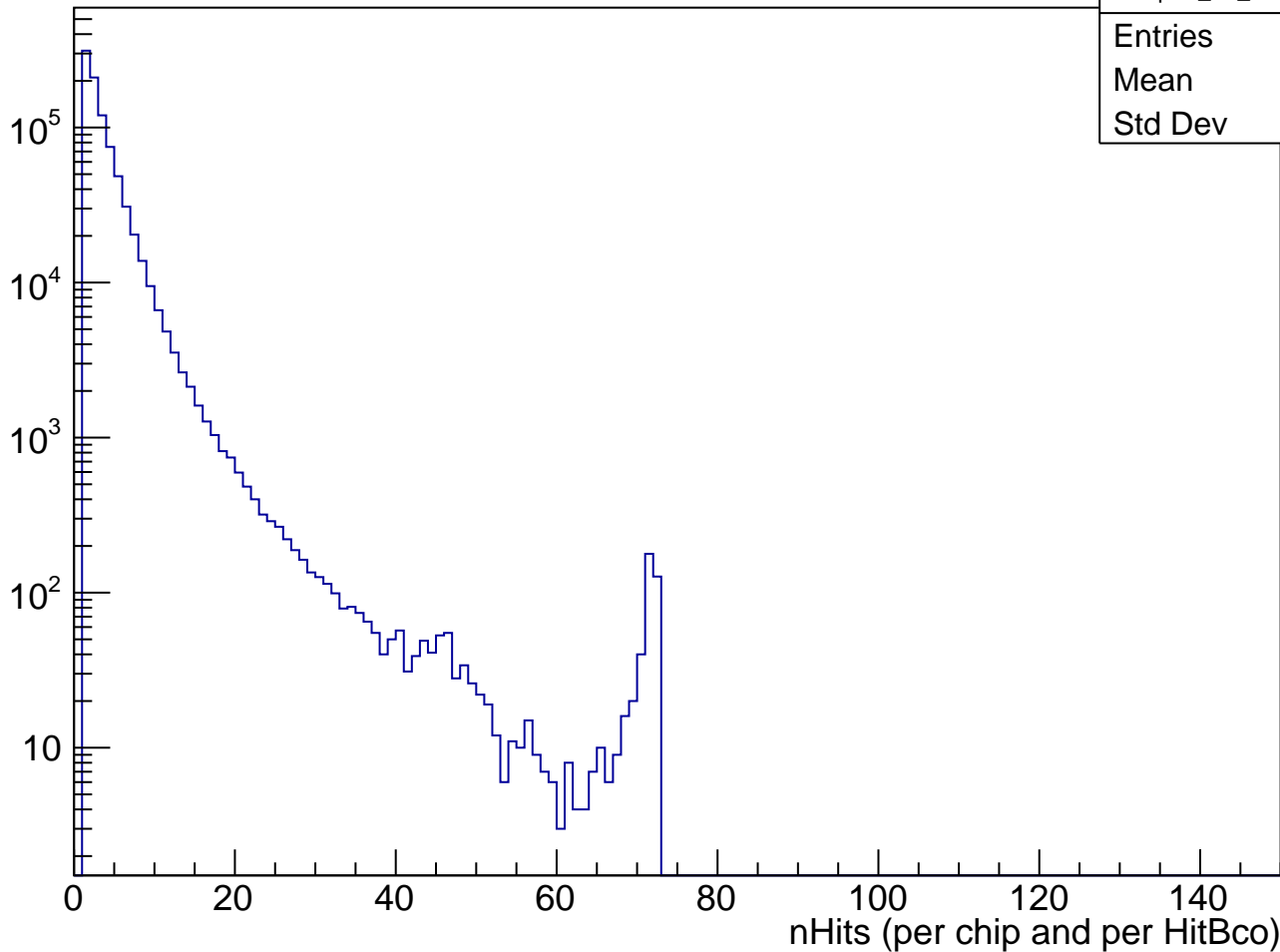


nChipHit_F1_Fch10_Chip8	
Entries	870689
Mean	3.011
Std Dev	3.412

nHits (per chip and per HitBco)

nChipHit_F1_Fch10_Chip9

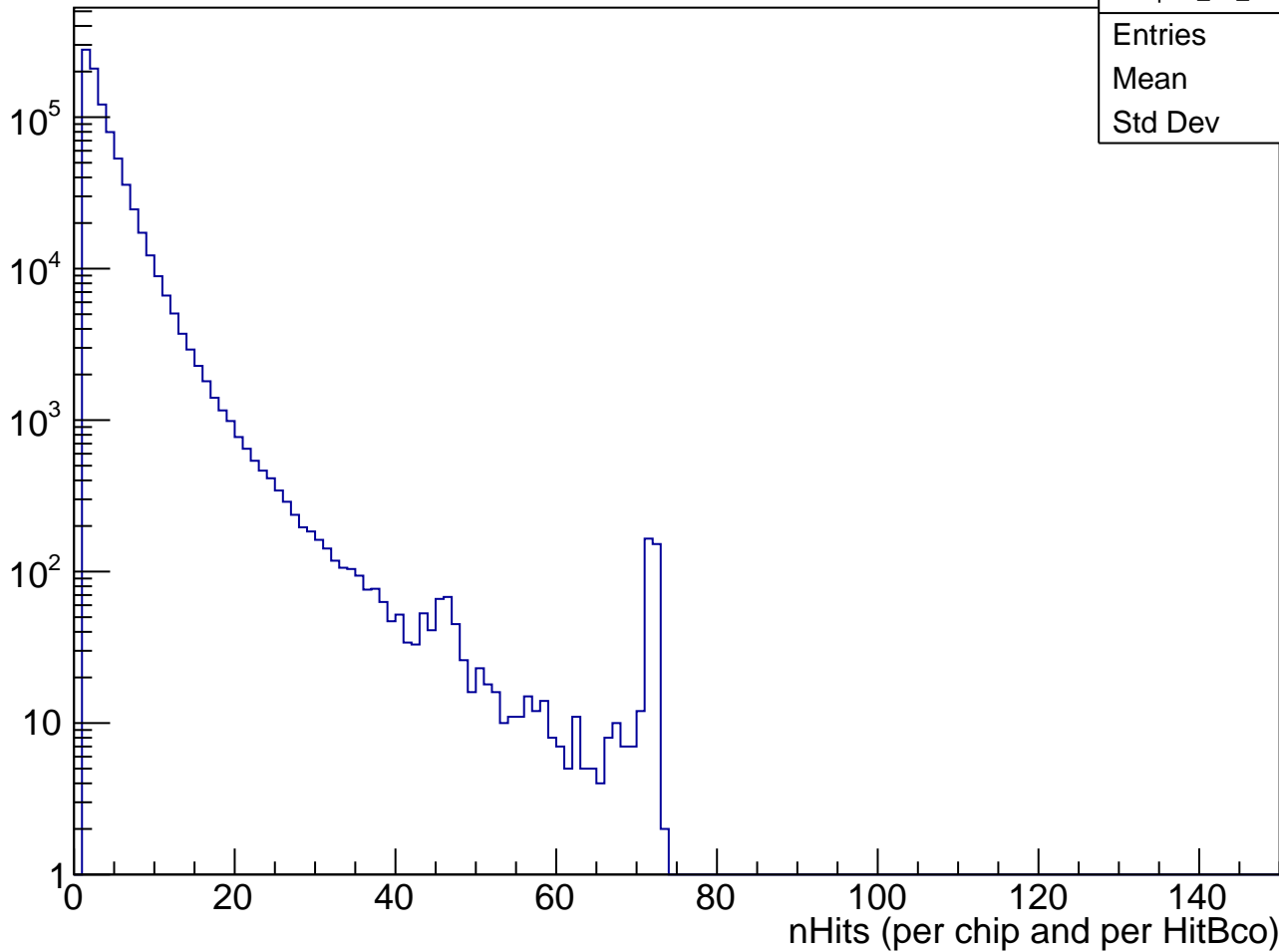
Entries



nChipHit_F1_Fch10_Chip9	
Entries	870861
Mean	3.017
Std Dev	3.465

nChipHit_F1_Fch11_Chip0

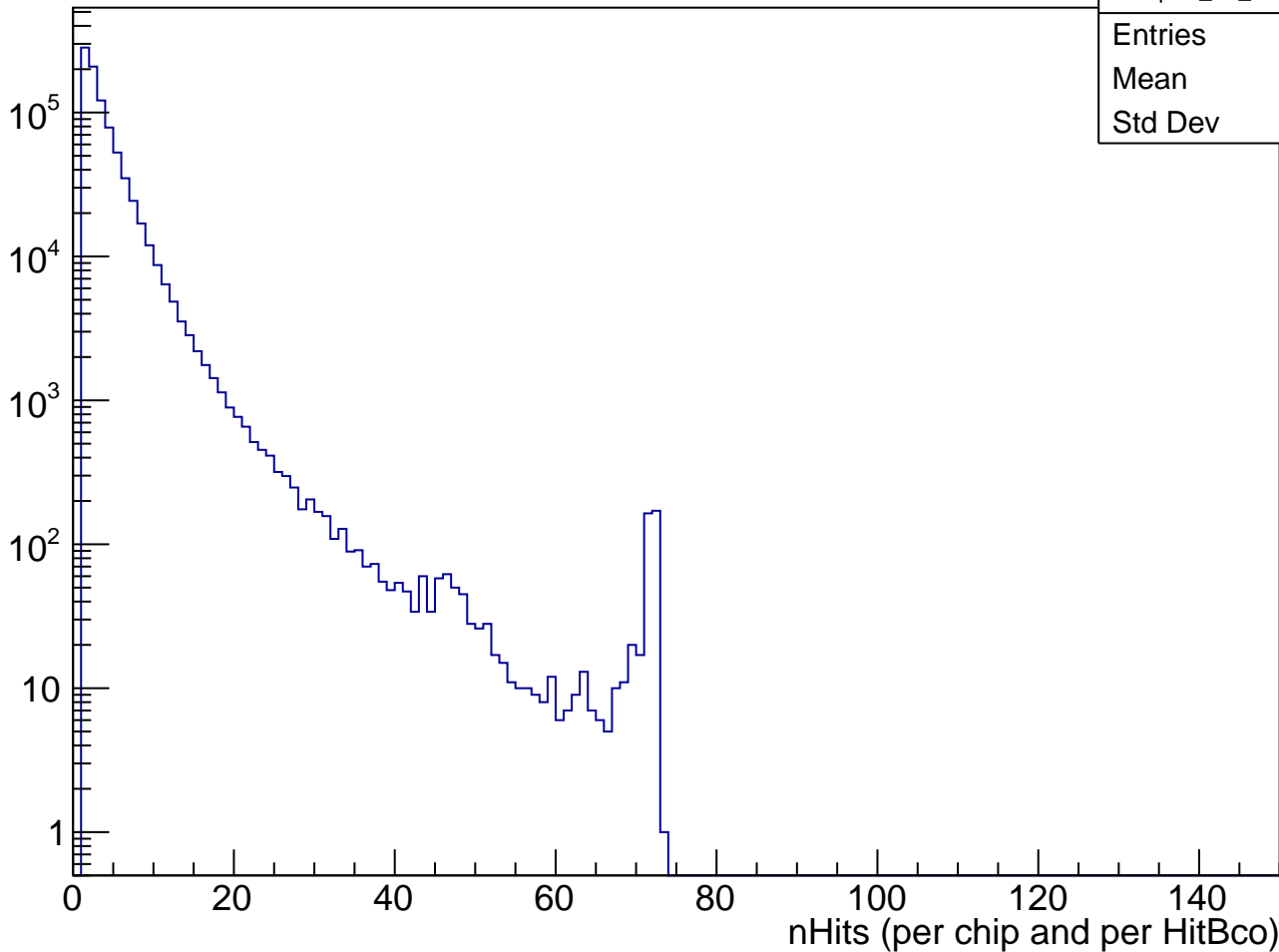
Entries



nChipHit_F1_Fch11_Chip0	
Entries	873149
Mean	3.324
Std Dev	3.718

nChipHit_F1_Fch11_Chip1

Entries

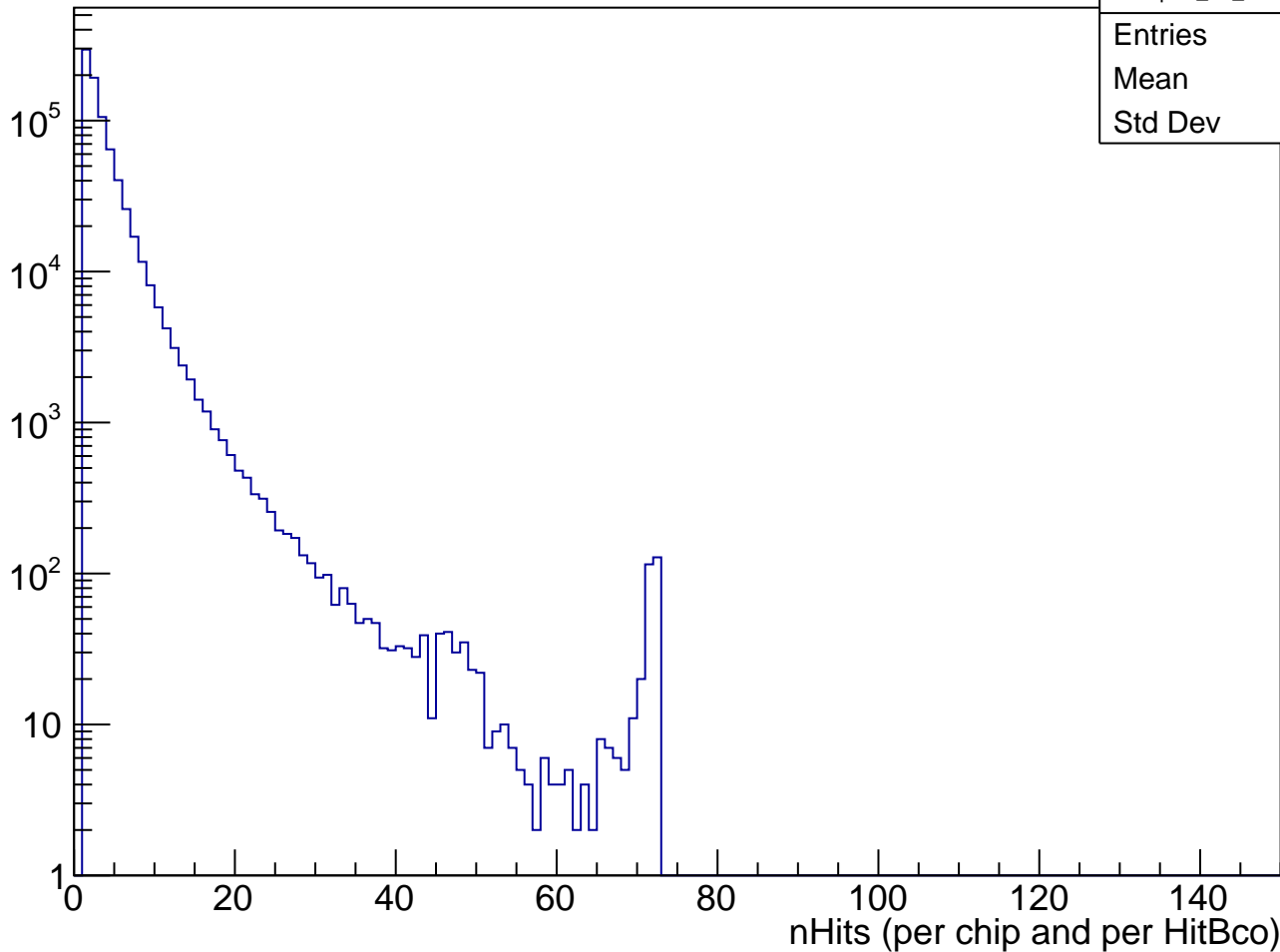


nChipHit_F1_Fch11_Chip1	
Entries	871812
Mean	3.299
Std Dev	3.744

nHits (per chip and per HitBco)

nChipHit_F1_Fch11_Chip10

Entries



nChipHit_F1_Fch11_Chip10

Entries 787175

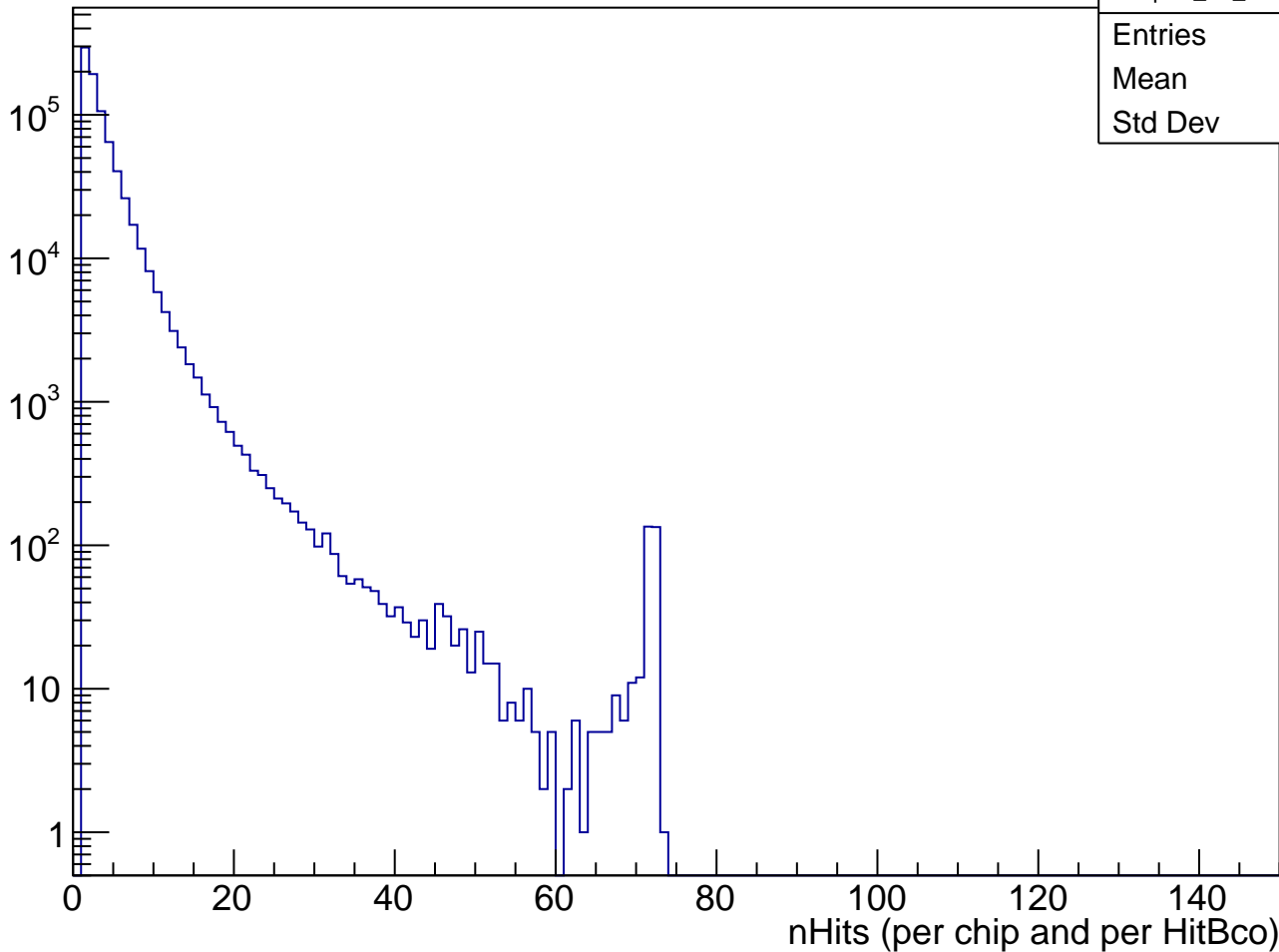
Mean 2.92

Std Dev 3.324

nHits (per chip and per HitBco)

nChipHit_F1_Fch11_Chip11

Entries

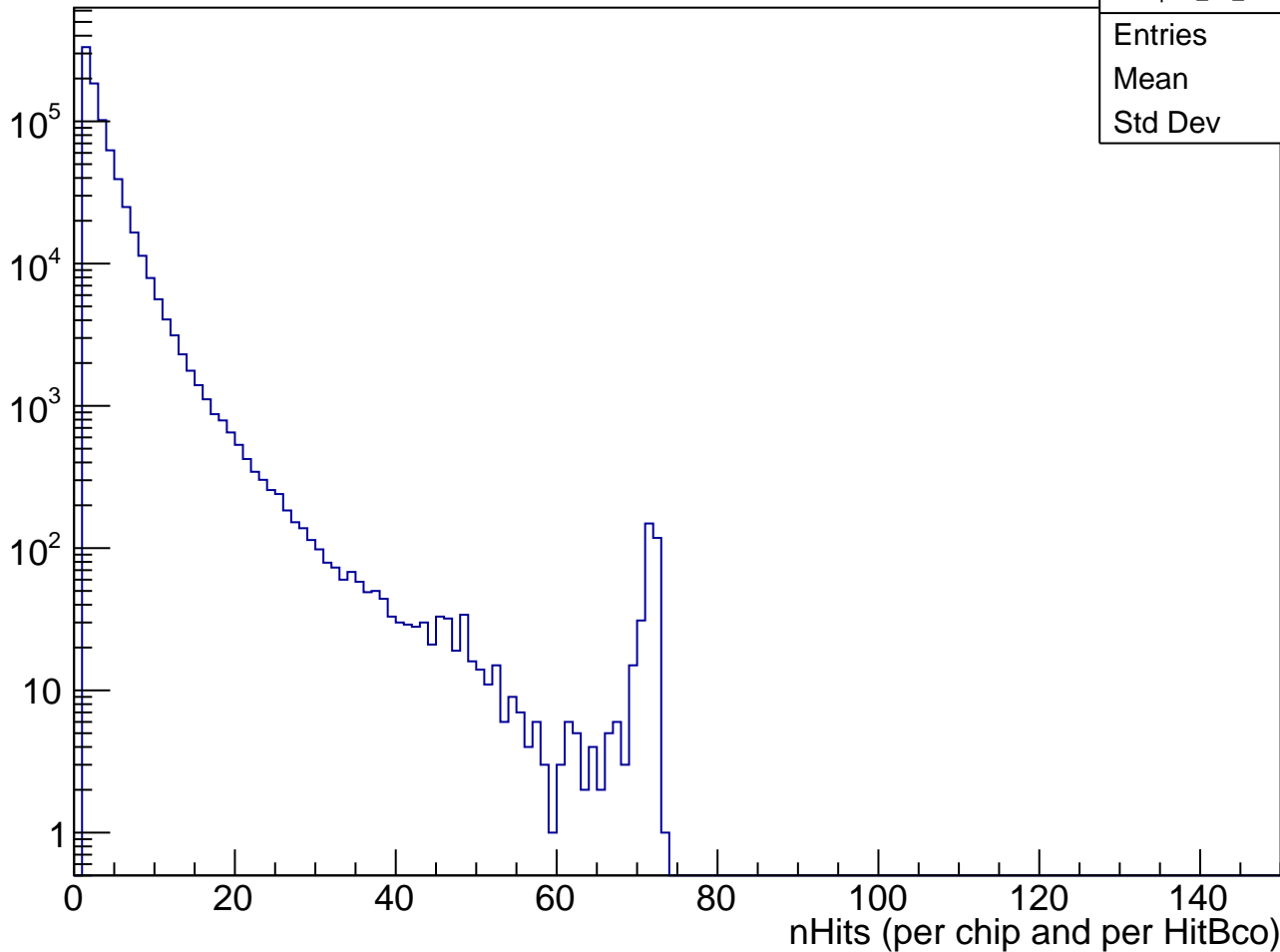


nChipHit_F1_Fch11_Chip11	
Entries	787898
Mean	2.927
Std Dev	3.342

nHits (per chip and per HitBco)

nChipHit_F1_Fch11_Chip12

Entries



nChipHit_F1_Fch11_Chip12

Entries 807462

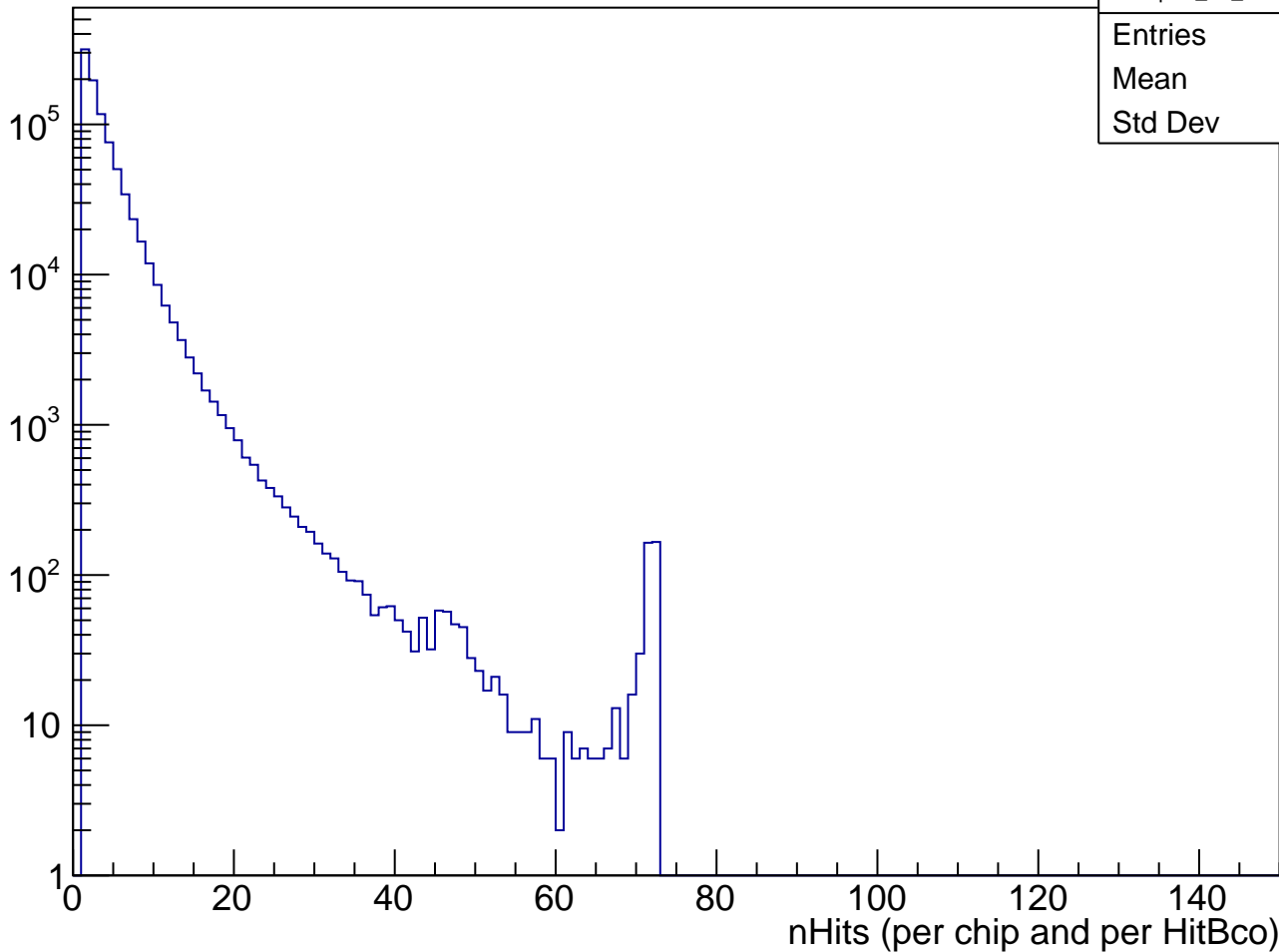
Mean 2.822

Std Dev 3.31

nHits (per chip and per HitBco)

nChipHit_F1_Fch11_Chip13

Entries

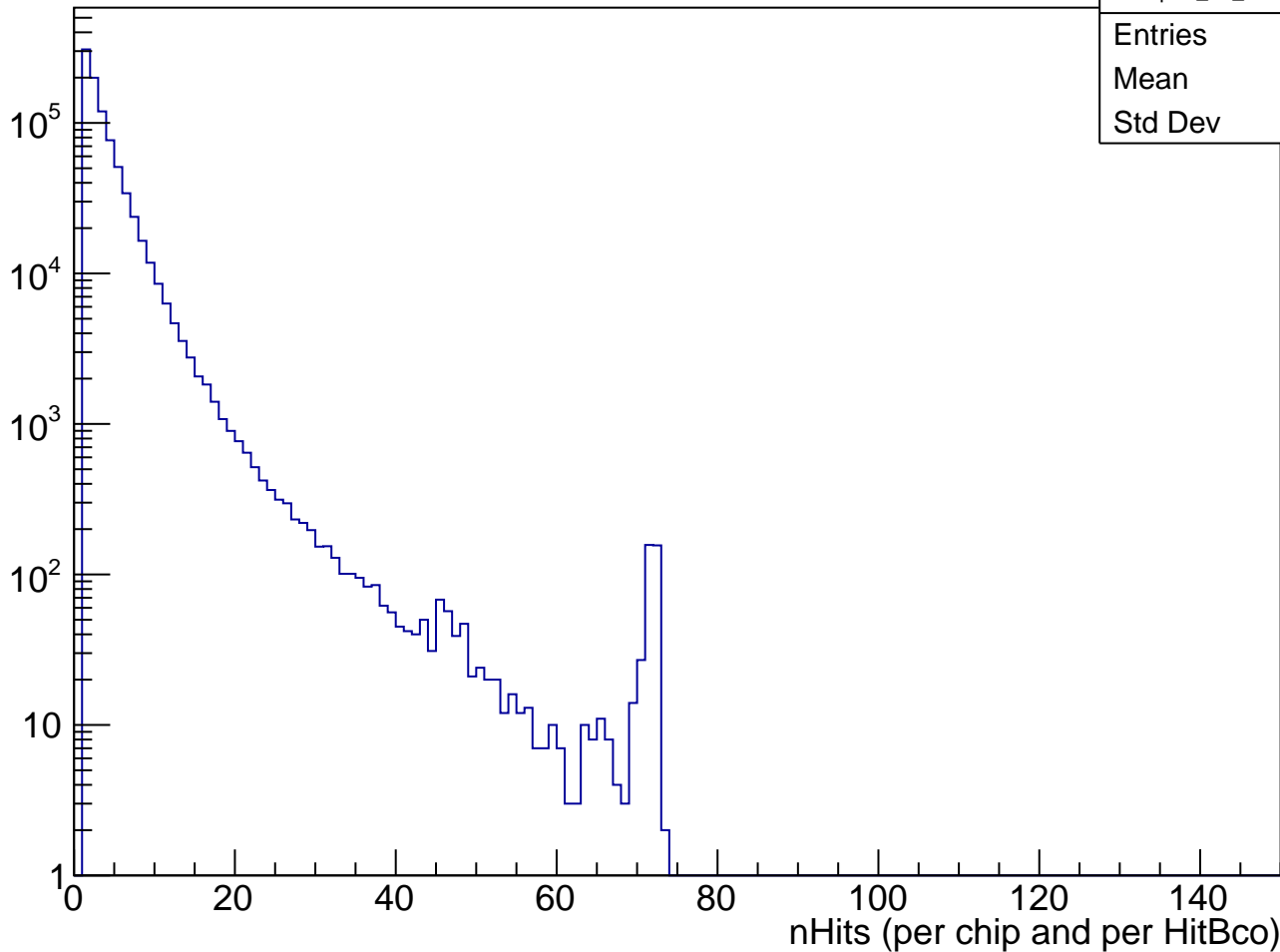


nChipHit_F1_Fch11_Chip13	
Entries	881384
Mean	3.21
Std Dev	3.717

nHits (per chip and per HitBco)

nChipHit_F1_Fch11_Chip14

Entries



nChipHit_F1_Fch11_Chip14

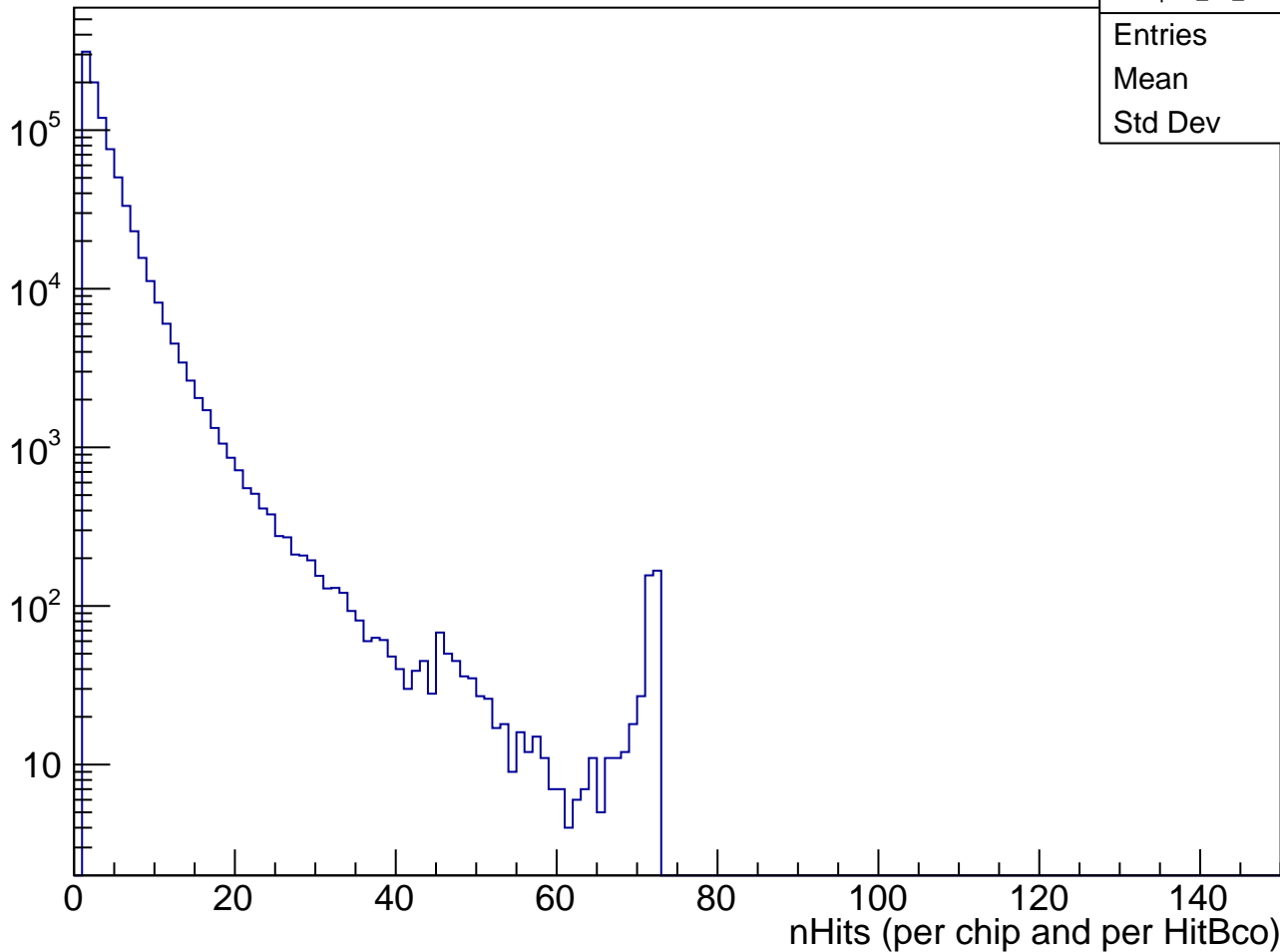
Entries 879830

Mean 3.222

Std Dev 3.7

nChipHit_F1_Fch11_Chip15

Entries



nChipHit_F1_Fch11_Chip15

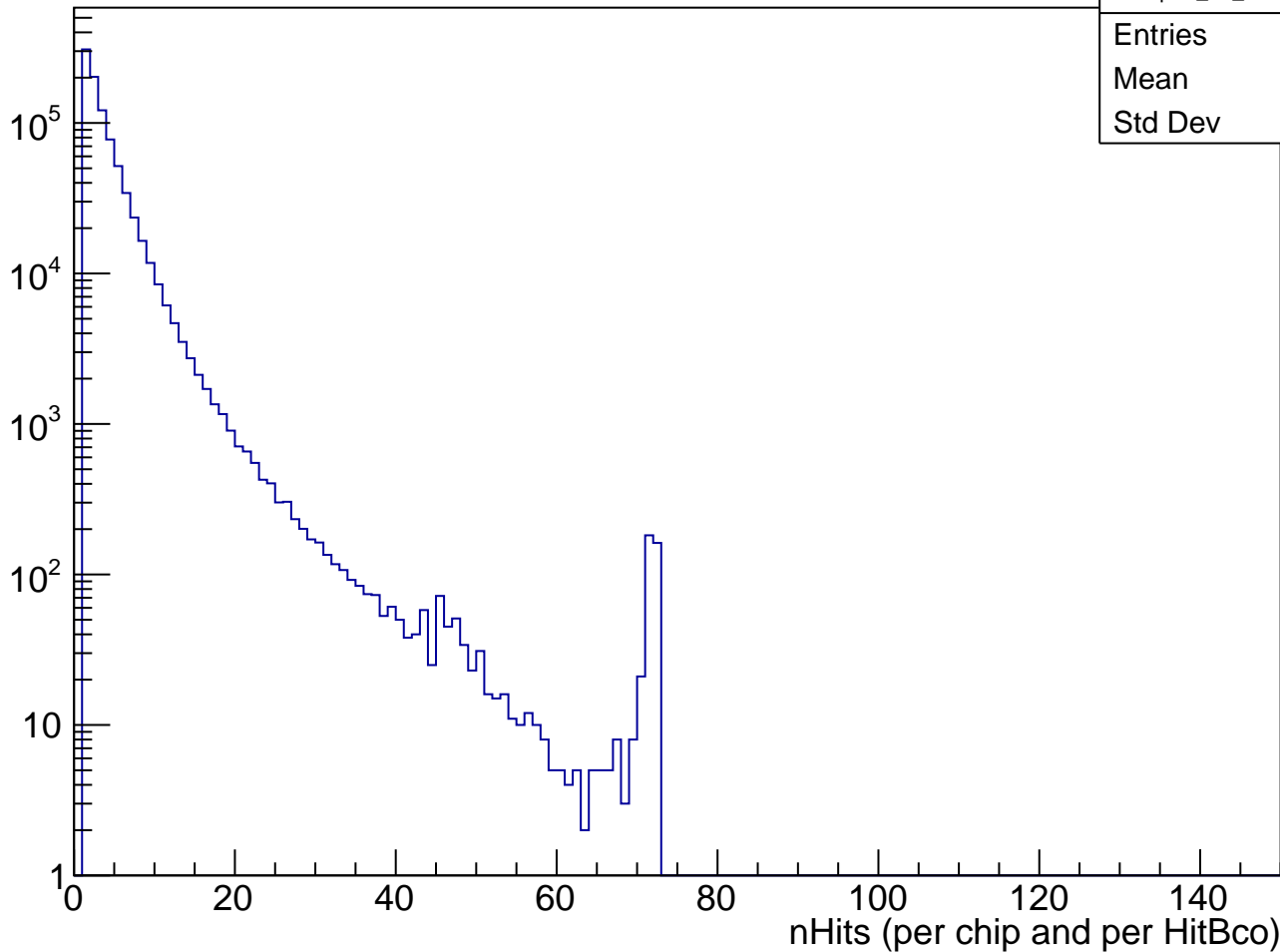
Entries 878916

Mean 3.174

Std Dev 3.671

nChipHit_F1_Fch11_Chip16

Entries



nChipHit_F1_Fch11_Chip16

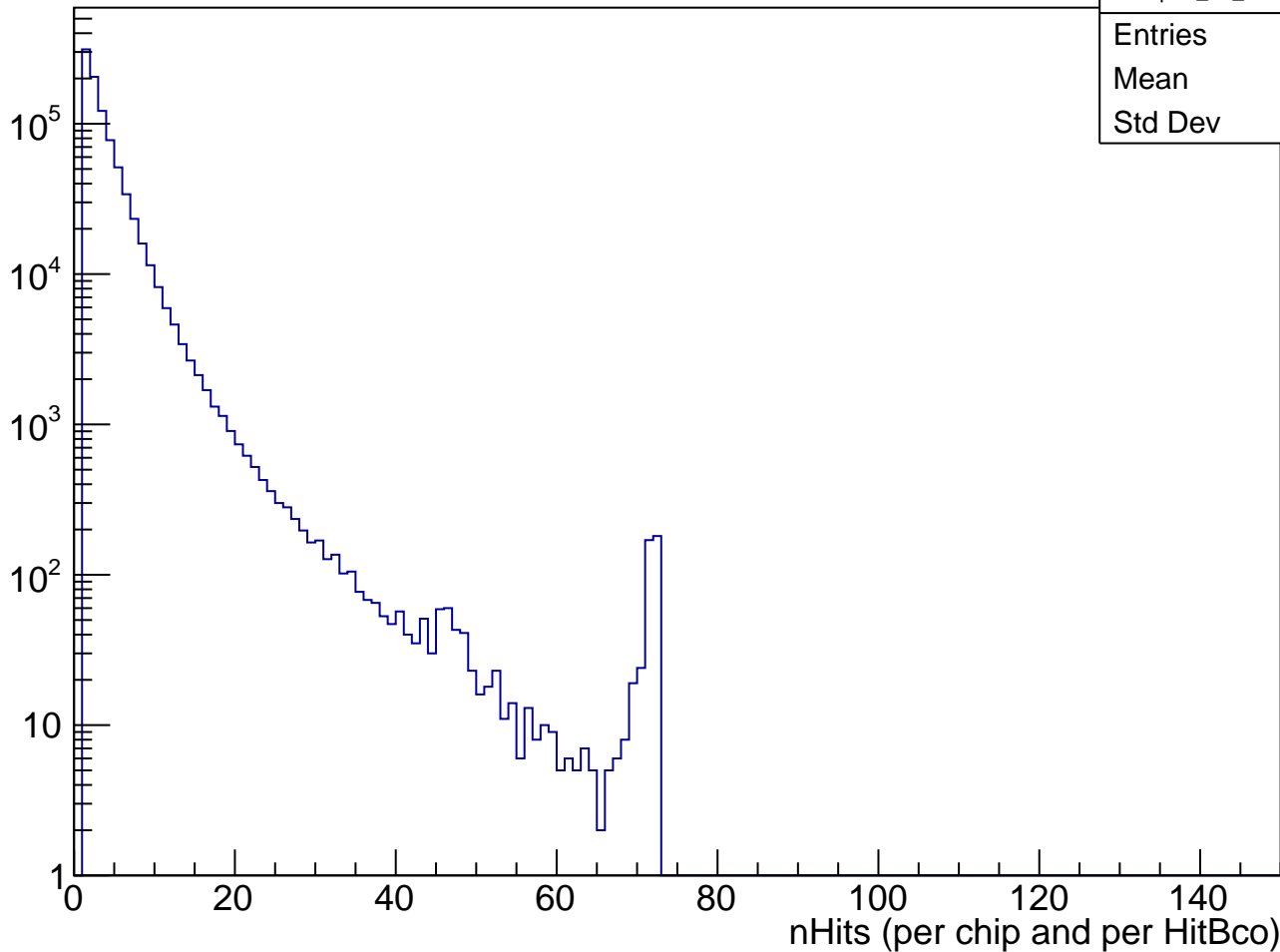
Entries 886146

Mean 3.212

Std Dev 3.673

nChipHit_F1_Fch11_Chip17

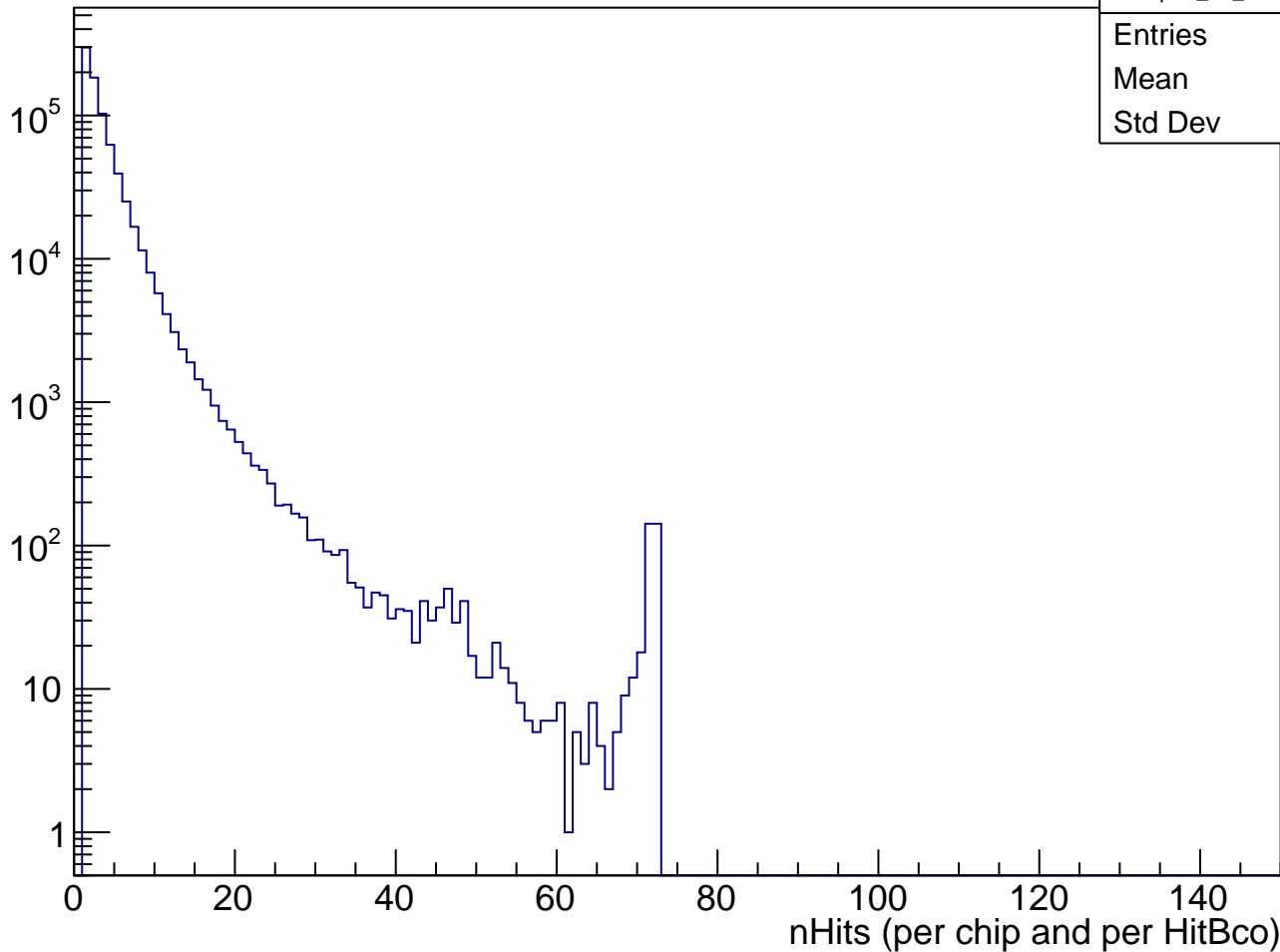
Entries



nChipHit_F1_Fch11_Chip17	
Entries	890642
Mean	3.182
Std Dev	3.661

nChipHit_F1_Fch11_Chip18

Entries



nChipHit_F1_Fch11_Chip18

Entries 774239

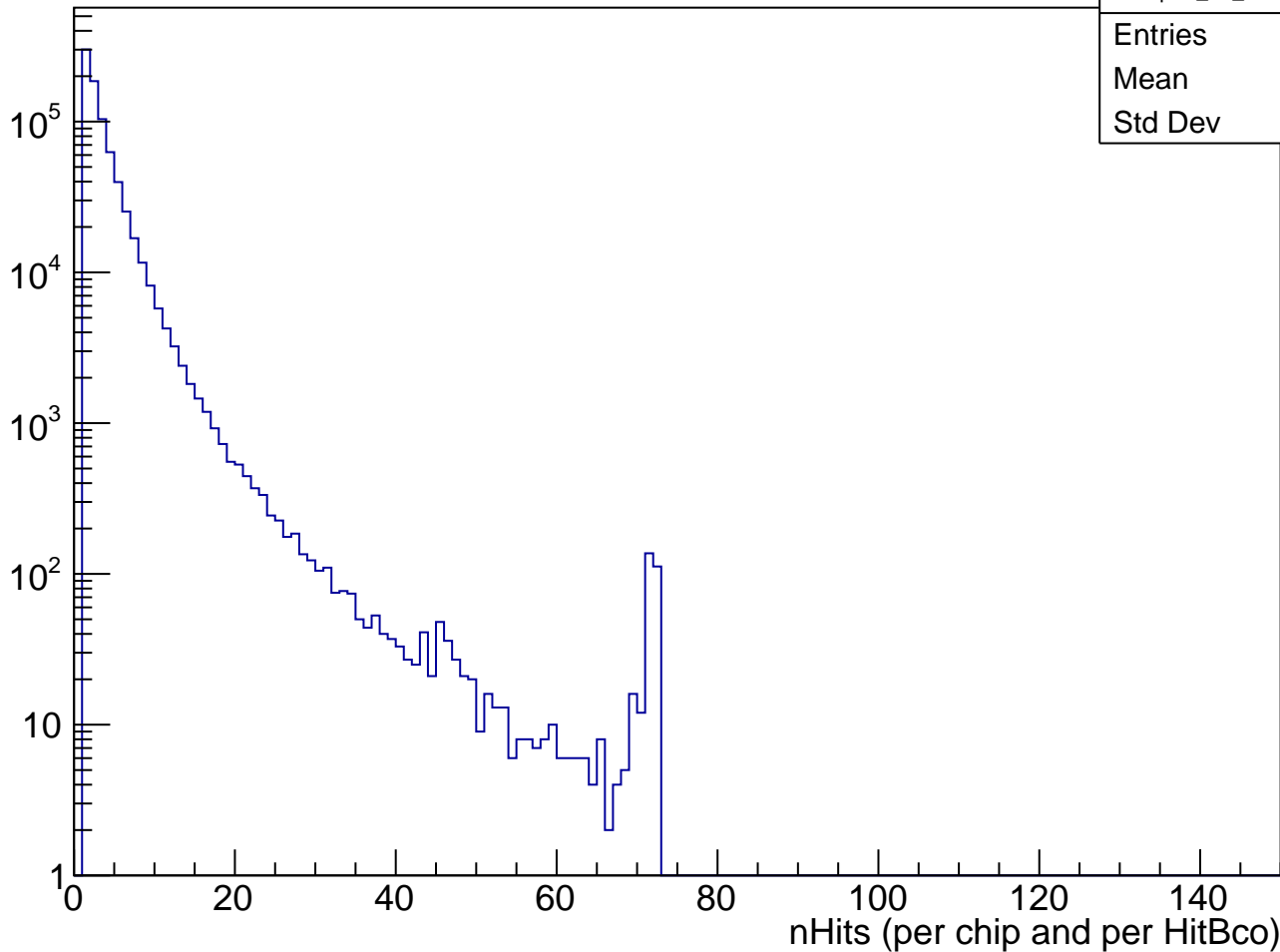
Mean 2.923

Std Dev 3.422

nHits (per chip and per HitBco)

nChipHit_F1_Fch11_Chip19

Entries

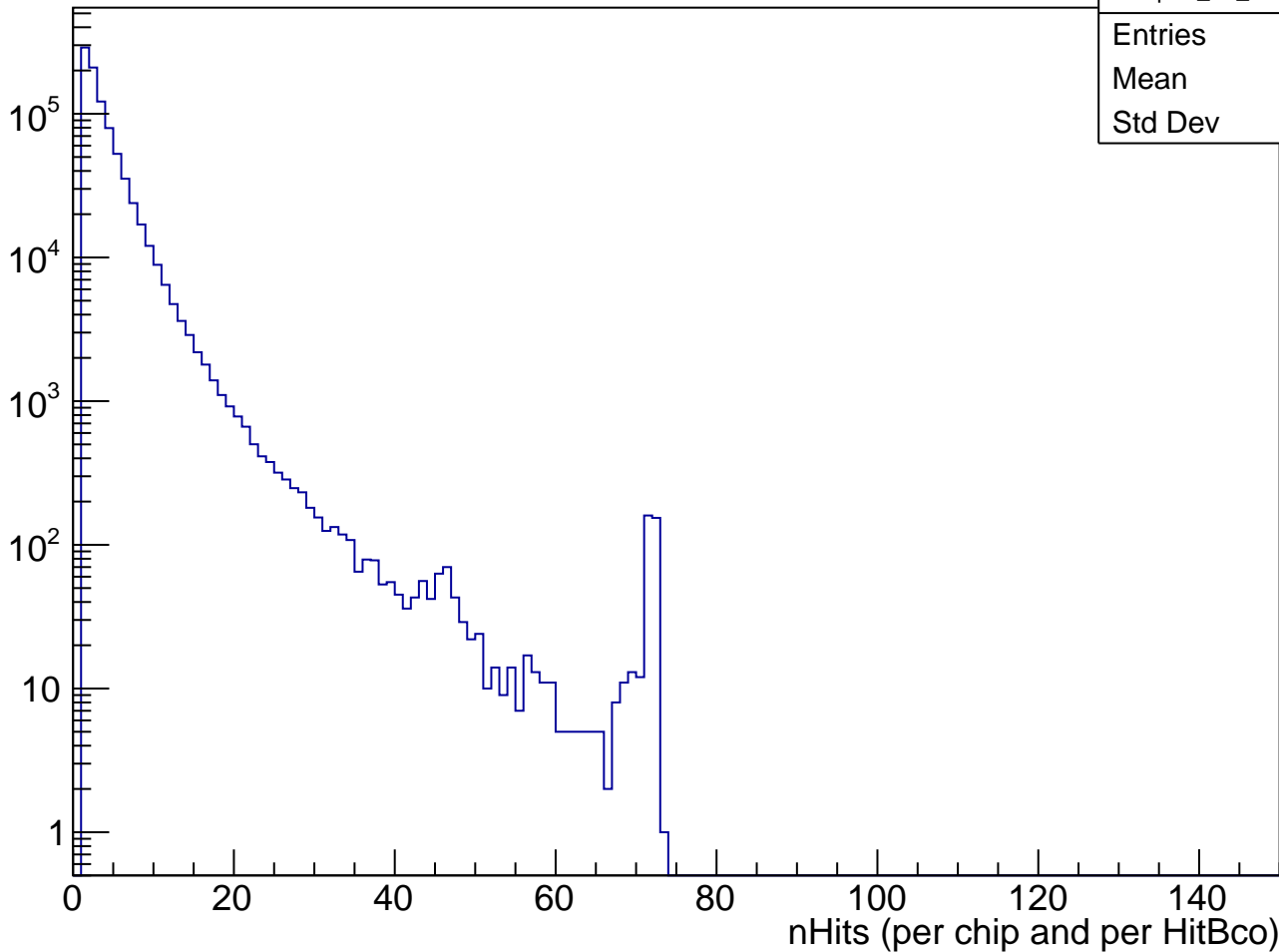


nChipHit_F1_Fch11_Chip19	
Entries	780826
Mean	2.915
Std Dev	3.369

nHits (per chip and per HitBco)

nChipHit_F1_Fch11_Chip2

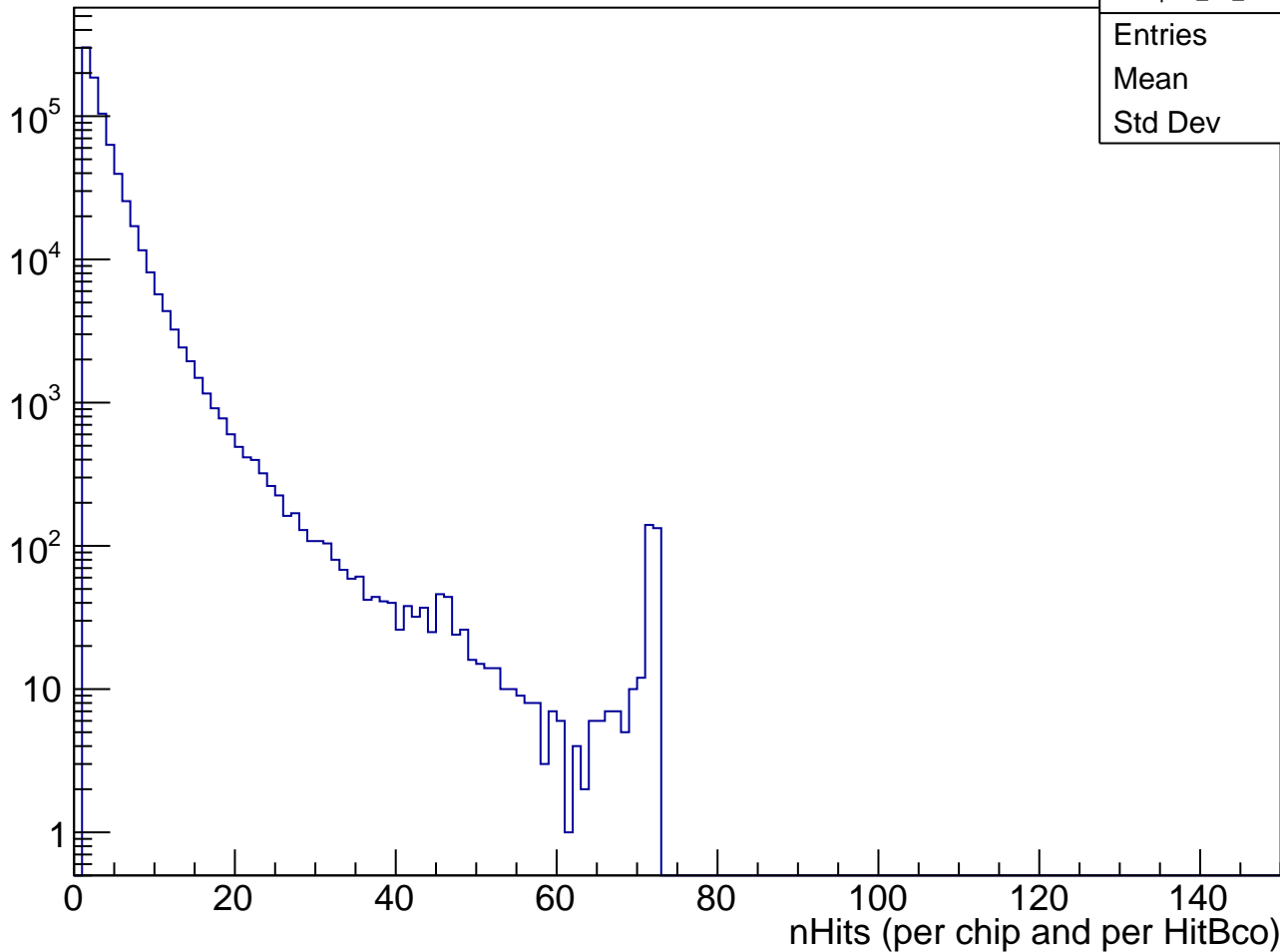
Entries



nChipHit_F1_Fch11_Chip2	
Entries	880755
Mean	3.275
Std Dev	3.681

nChipHit_F1_Fch11_Chip20

Entries

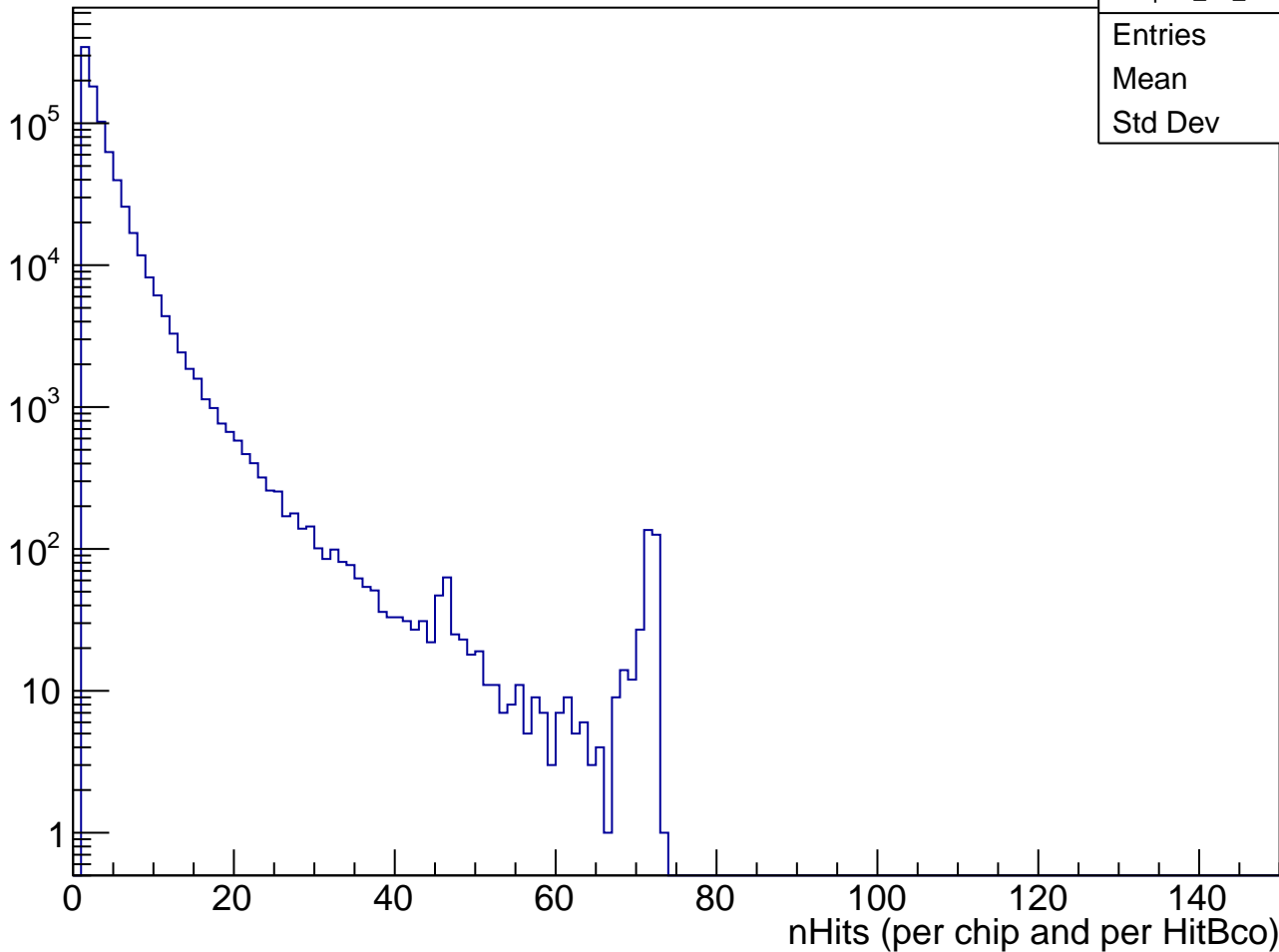


nChipHit_F1_Fch11_Chip20	
Entries	783764
Mean	2.917
Std Dev	3.381

nHits (per chip and per HitBco)

nChipHit_F1_Fch11_Chip21

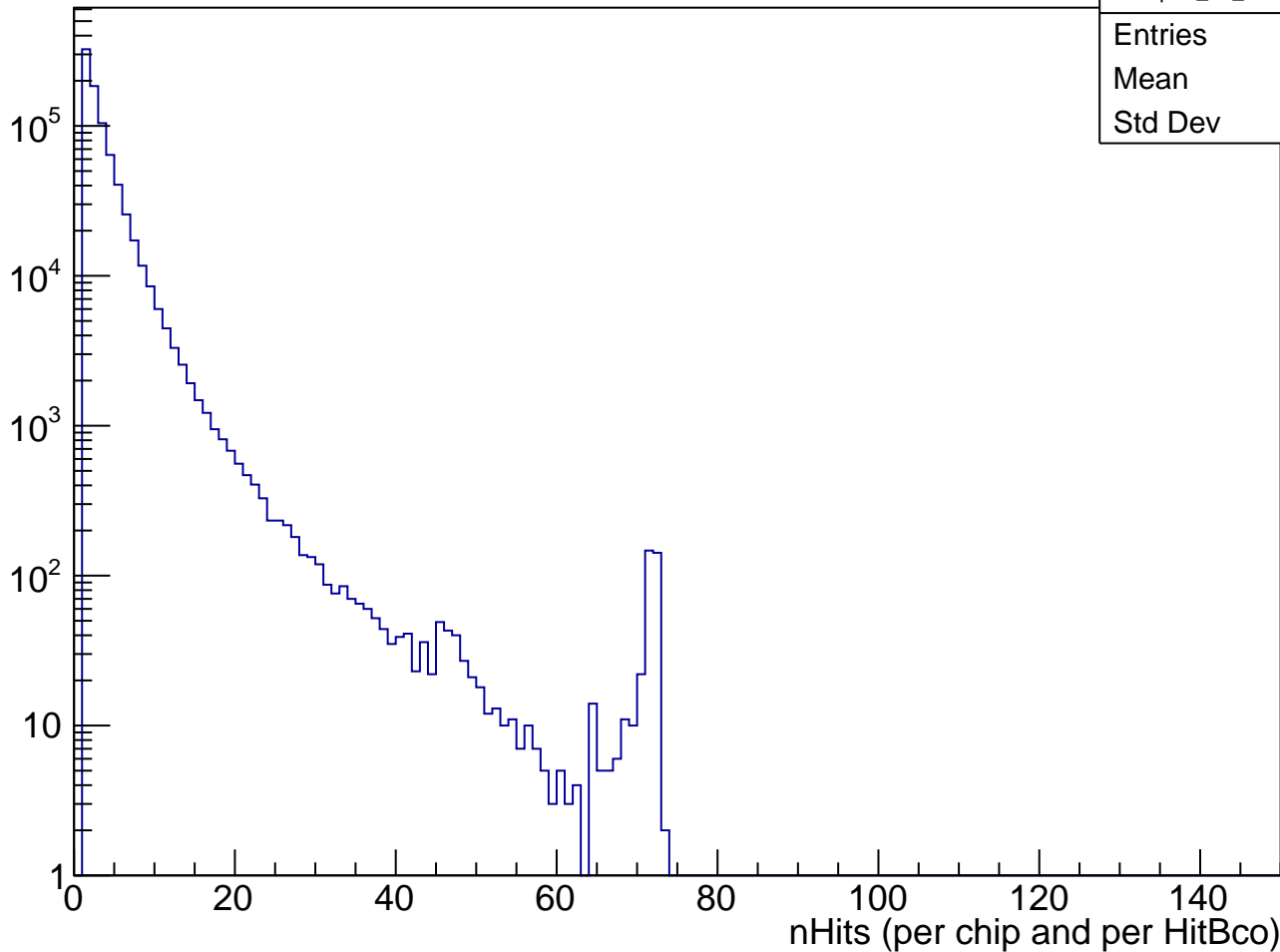
Entries



nChipHit_F1_Fch11_Chip21	
Entries	821289
Mean	2.838
Std Dev	3.378

nChipHit_F1_Fch11_Chip22

Entries



nChipHit_F1_Fch11_Chip22

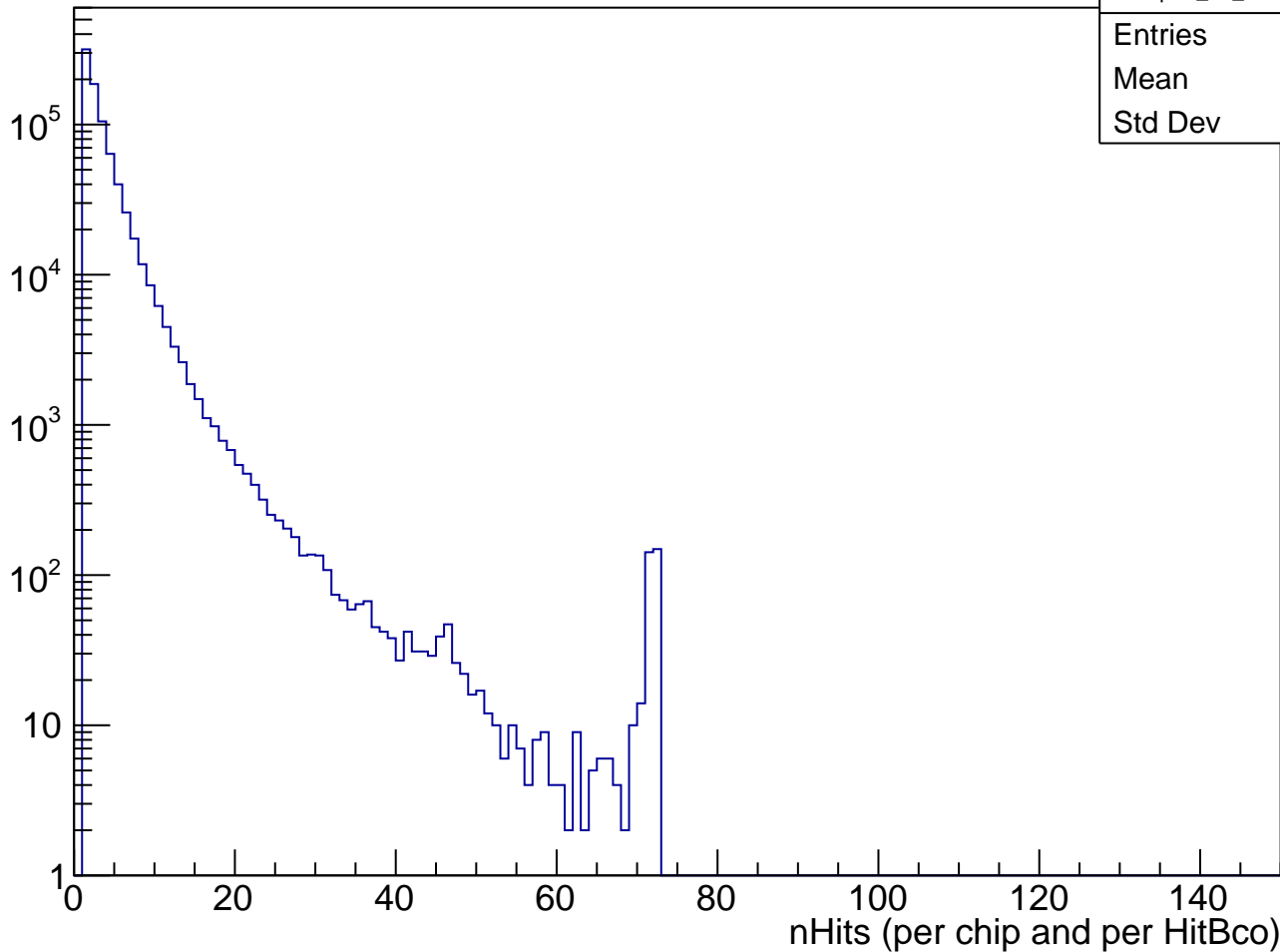
Entries 808677

Mean 2.895

Std Dev 3.427

nChipHit_F1_Fch11_Chip23

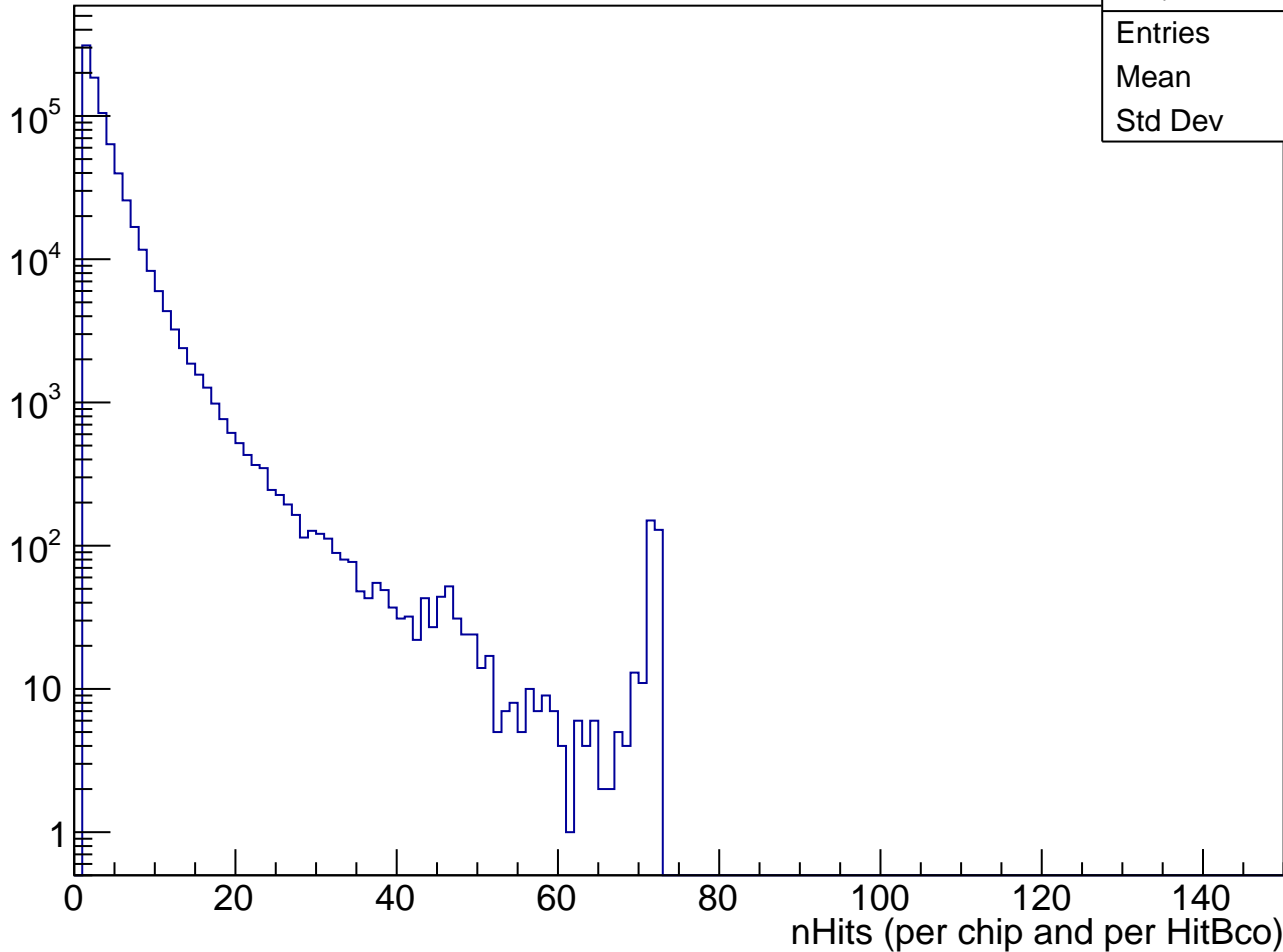
Entries



nChipHit_F1_Fch11_Chip23	
Entries	803196
Mean	2.908
Std Dev	3.401

nChipHit_F1_Fch11_Chip24

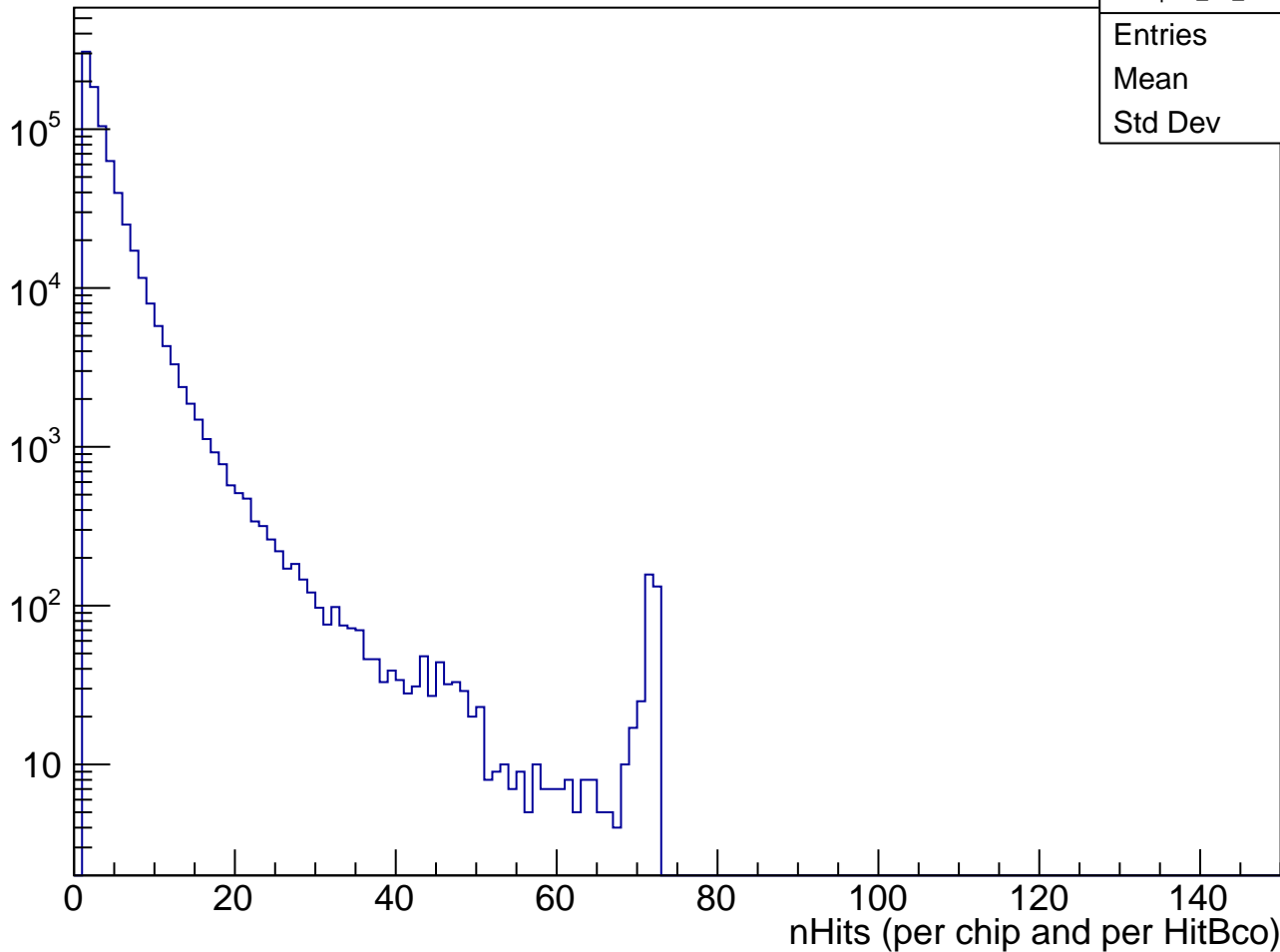
Entries



nChipHit_F1_Fch11_Chip24	
Entries	793878
Mean	2.908
Std Dev	3.392

nChipHit_F1_Fch11_Chip25

Entries



nChipHit_F1_Fch11_Chip25

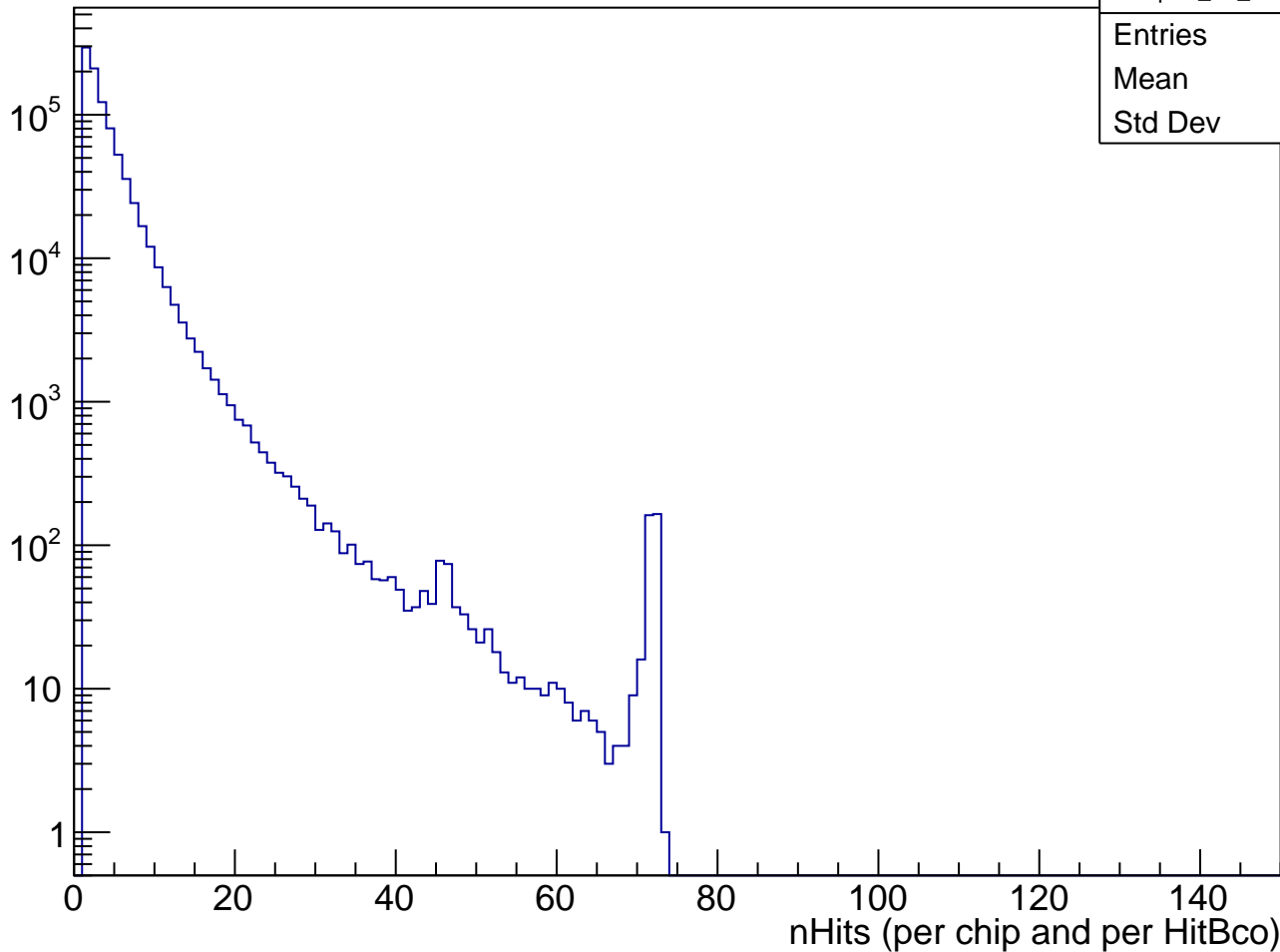
Entries 787554

Mean 2.908

Std Dev 3.422

nChipHit_F1_Fch11_Chip3

Entries

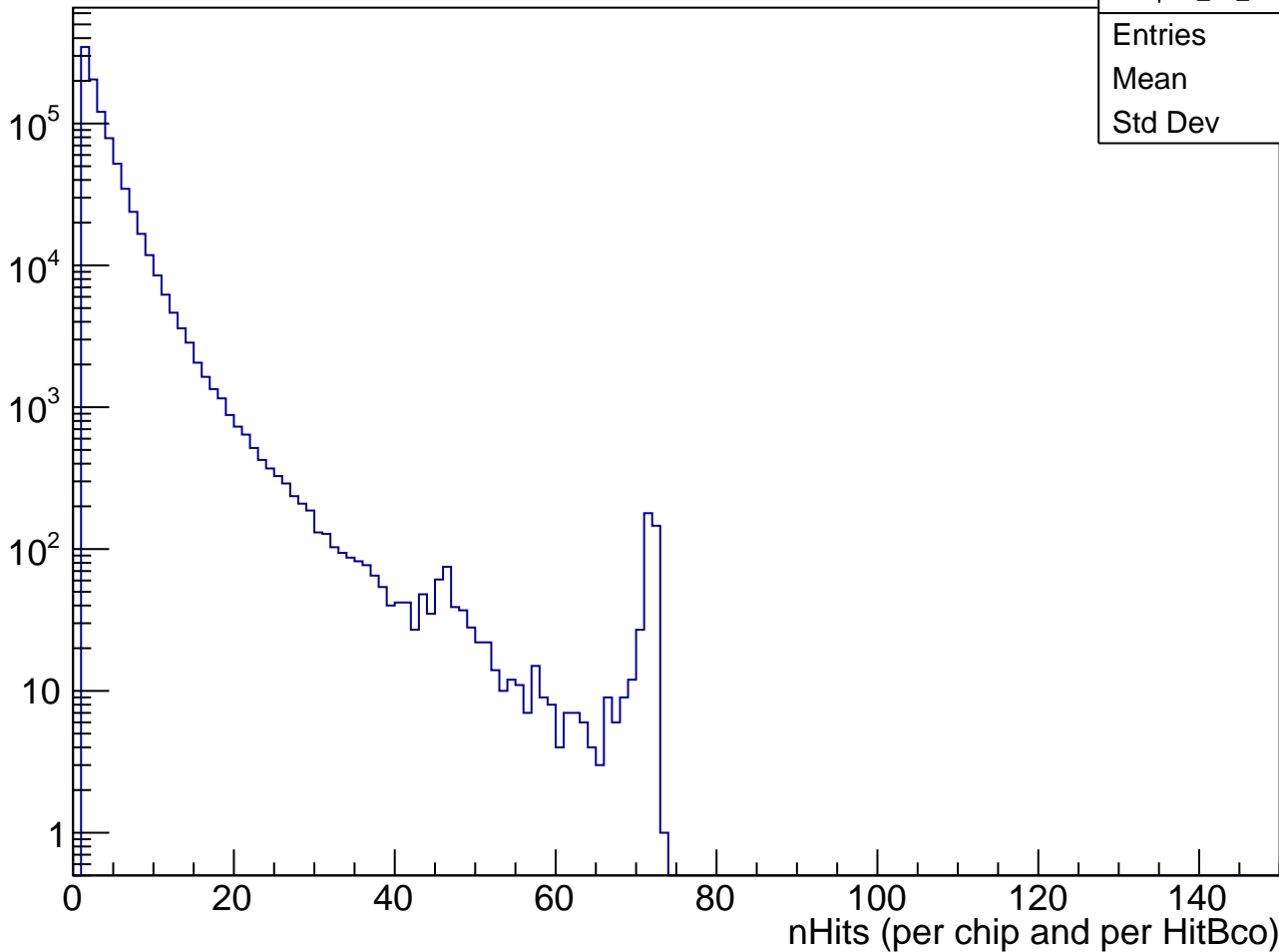


nChipHit_F1_Fch11_Chip3	
Entries	888013
Mean	3.26
Std Dev	3.677

nHits (per chip and per HitBco)

nChipHit_F1_Fch11_Chip4

Entries

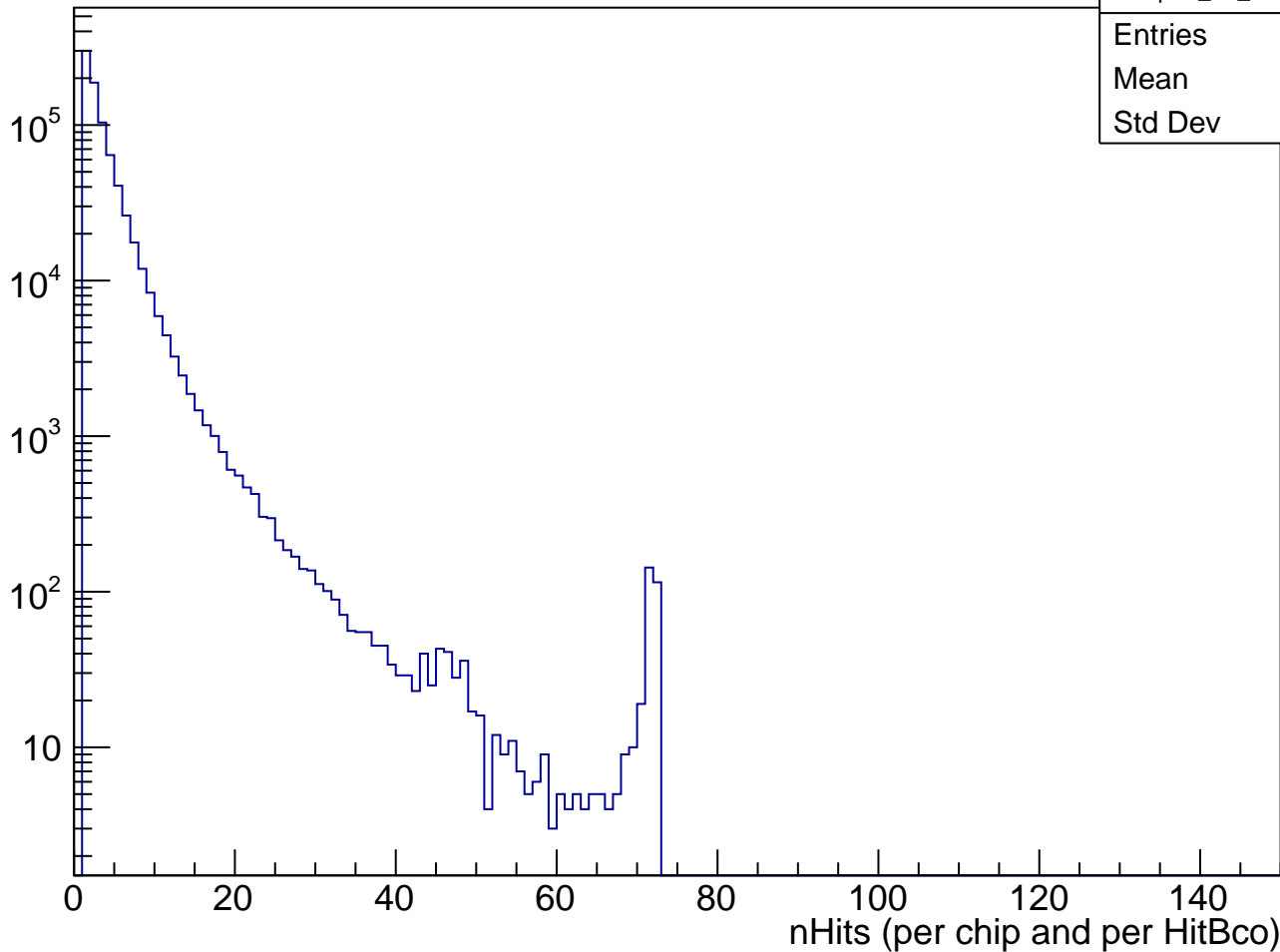


nChipHit_F1_Fch11_Chip4	
Entries	928264
Mean	3.121
Std Dev	3.605

nHits (per chip and per HitBco)

nChipHit_F1_Fch11_Chip5

Entries

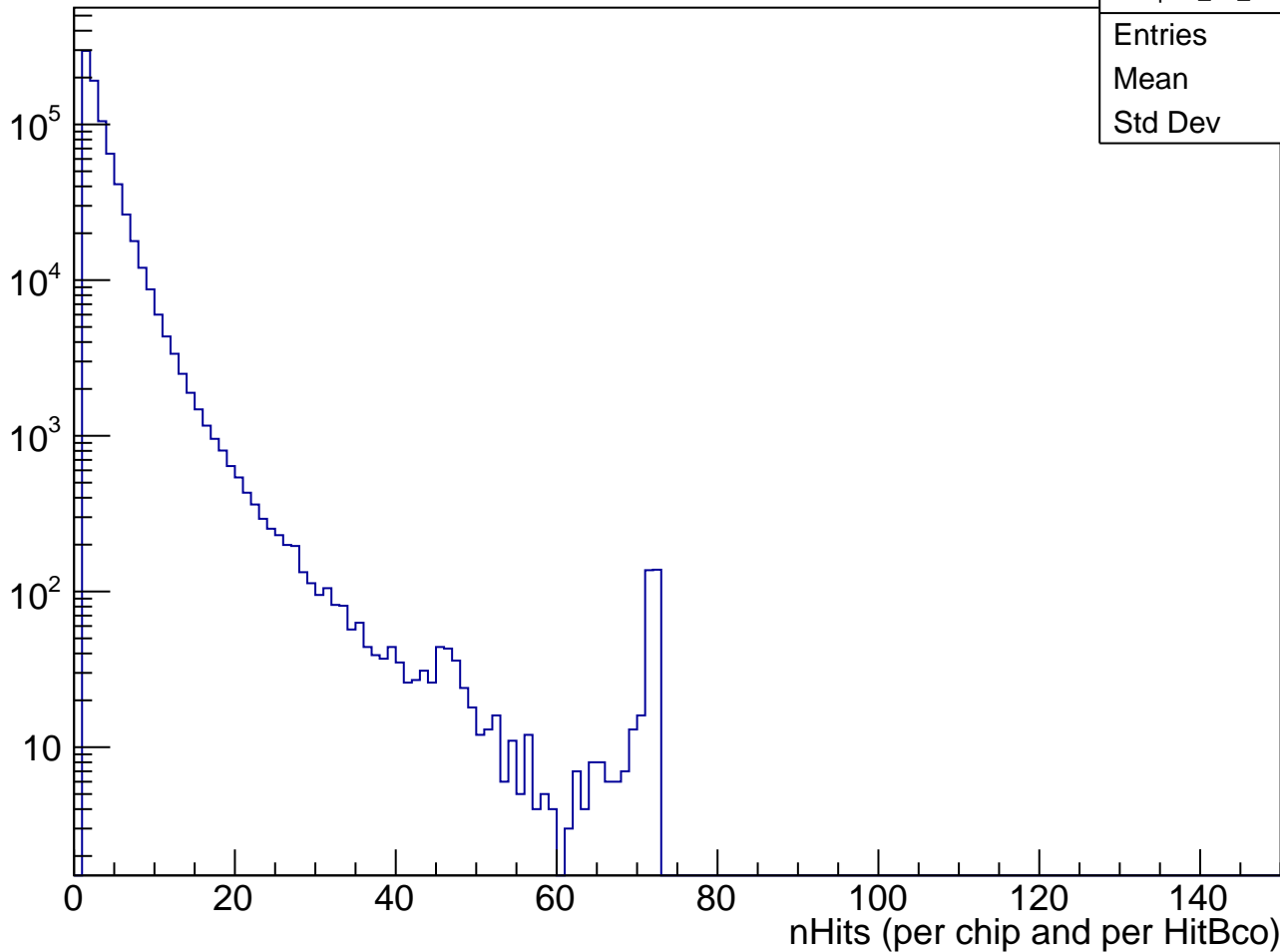


nChipHit_F1_Fch11_Chip5	
Entries	787184
Mean	2.942
Std Dev	3.391

nHits (per chip and per HitBco)

nChipHit_F1_Fch11_Chip6

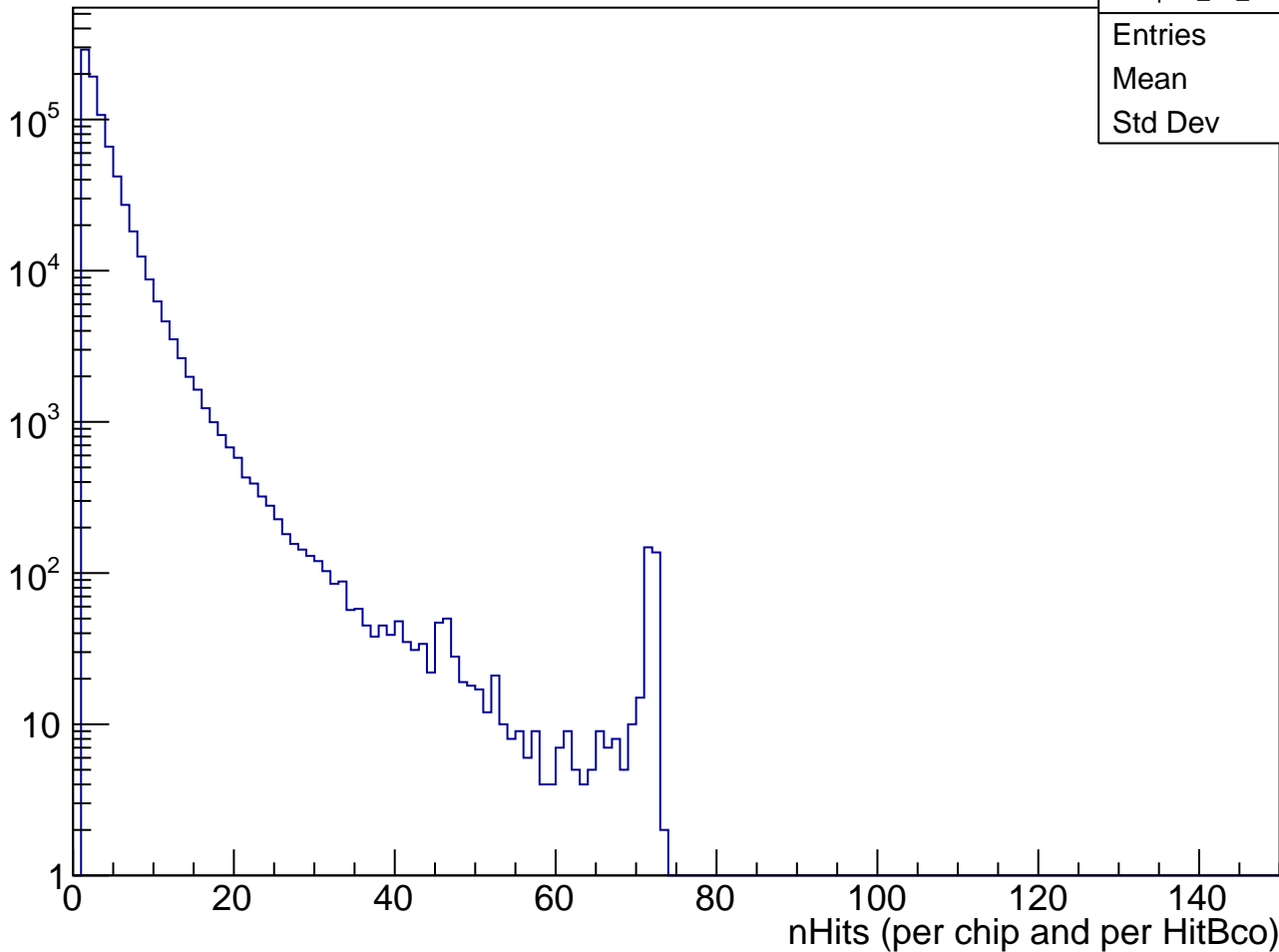
Entries



nChipHit_F1_Fch11_Chip6	
Entries	790990
Mean	2.954
Std Dev	3.396

nChipHit_F1_Fch11_Chip7

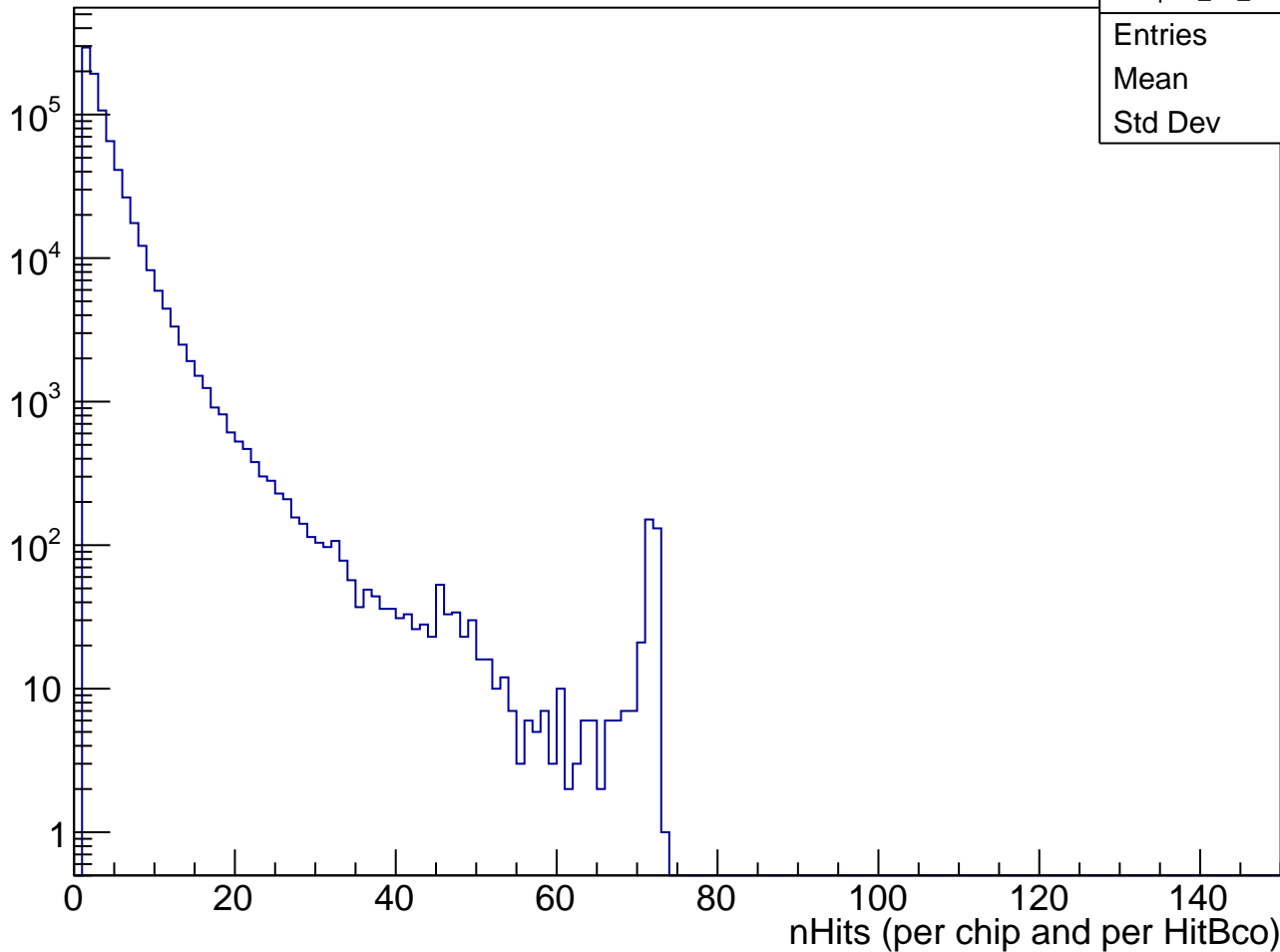
Entries



nChipHit_F1_Fch11_Chip7	
Entries	792679
Mean	3.002
Std Dev	3.449

nChipHit_F1_Fch11_Chip8

Entries

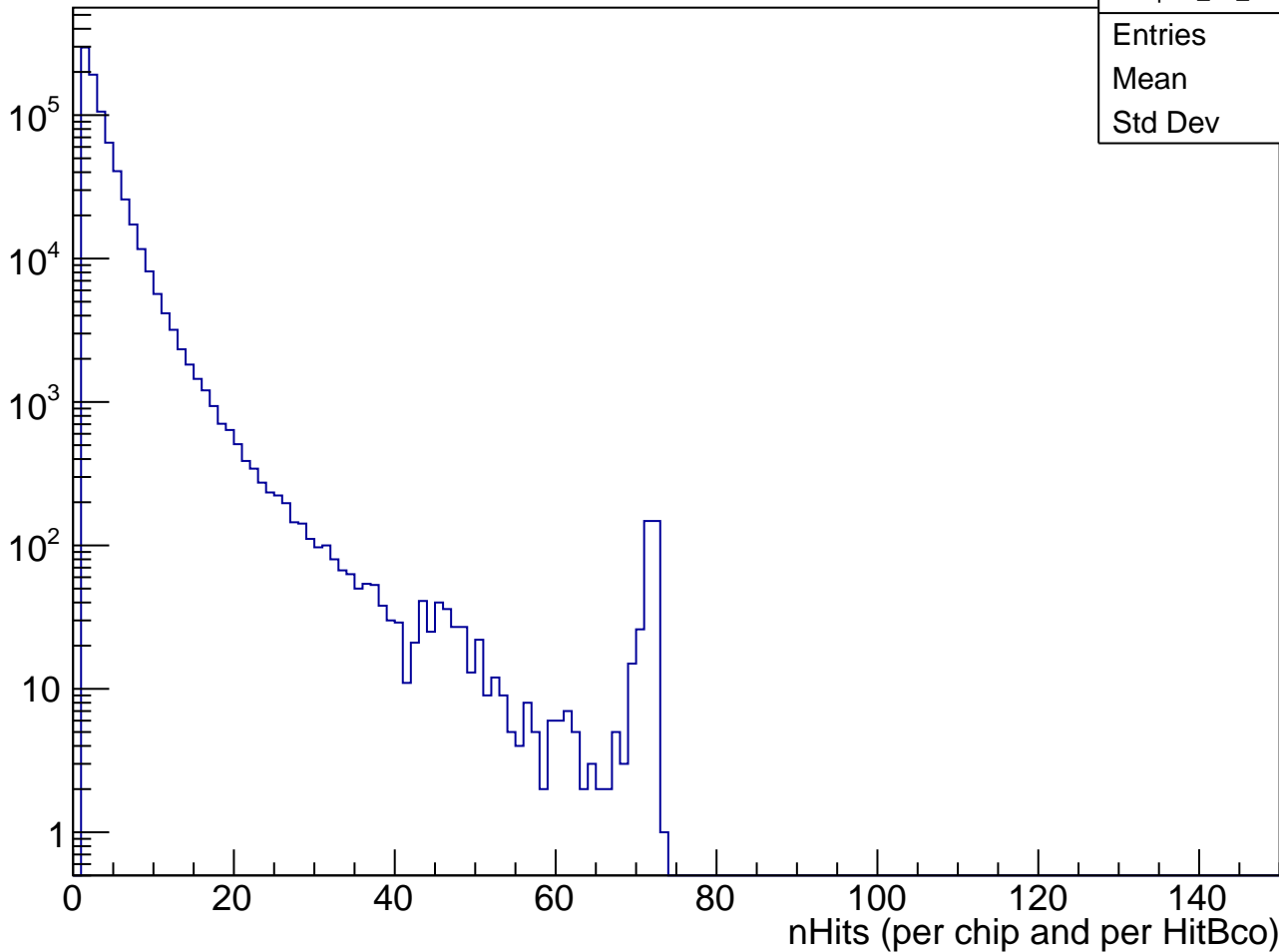


nChipHit_F1_Fch11_Chip8	
Entries	790802
Mean	2.96
Std Dev	3.399

nHits (per chip and per HitBco)

nChipHit_F1_Fch11_Chip9

Entries

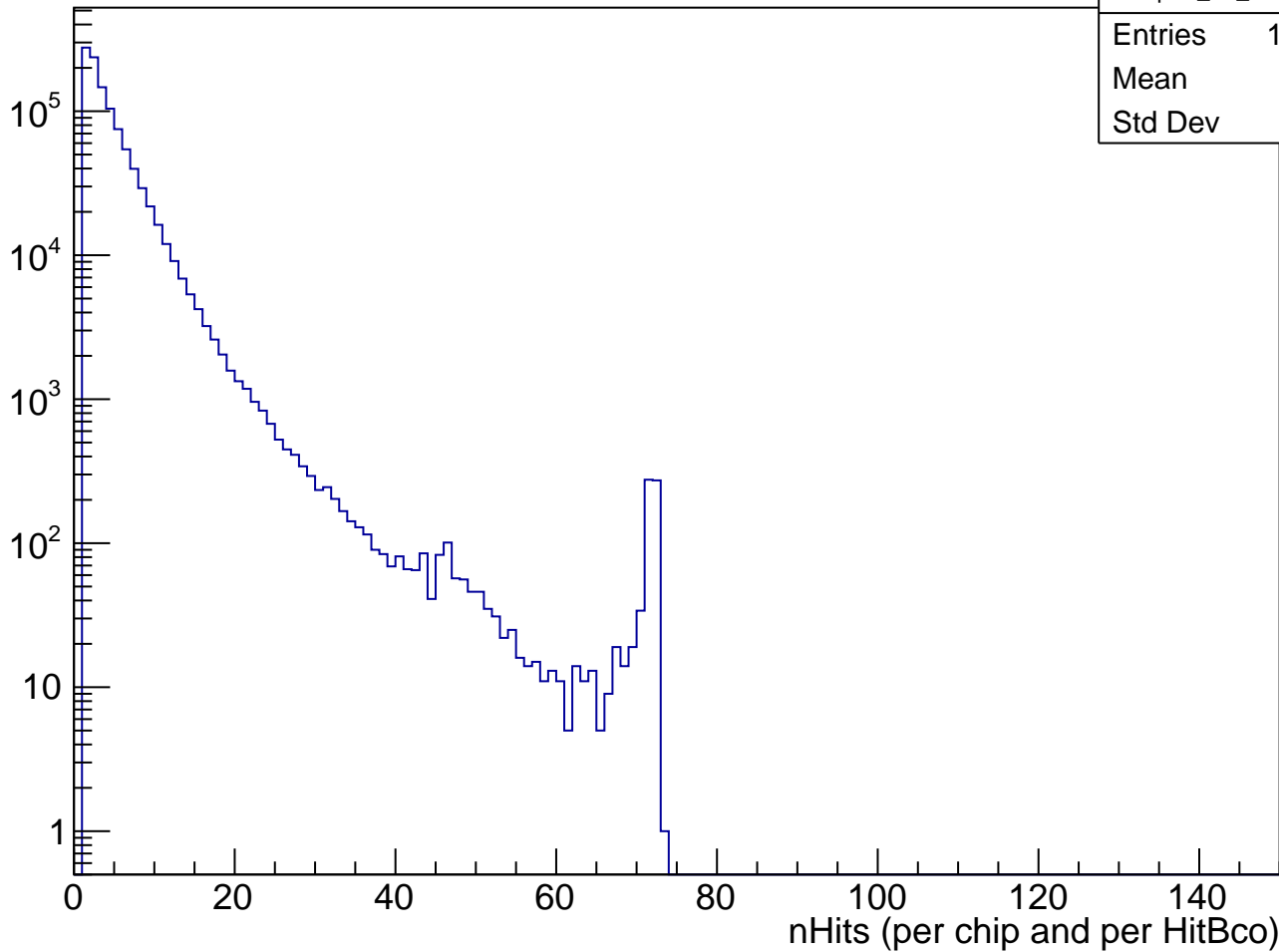


nChipHit_F1_Fch11_Chip9	
Entries	787588
Mean	2.922
Std Dev	3.362

nHits (per chip and per HitBco)

nChipHit_F1_Fch12_Chip0

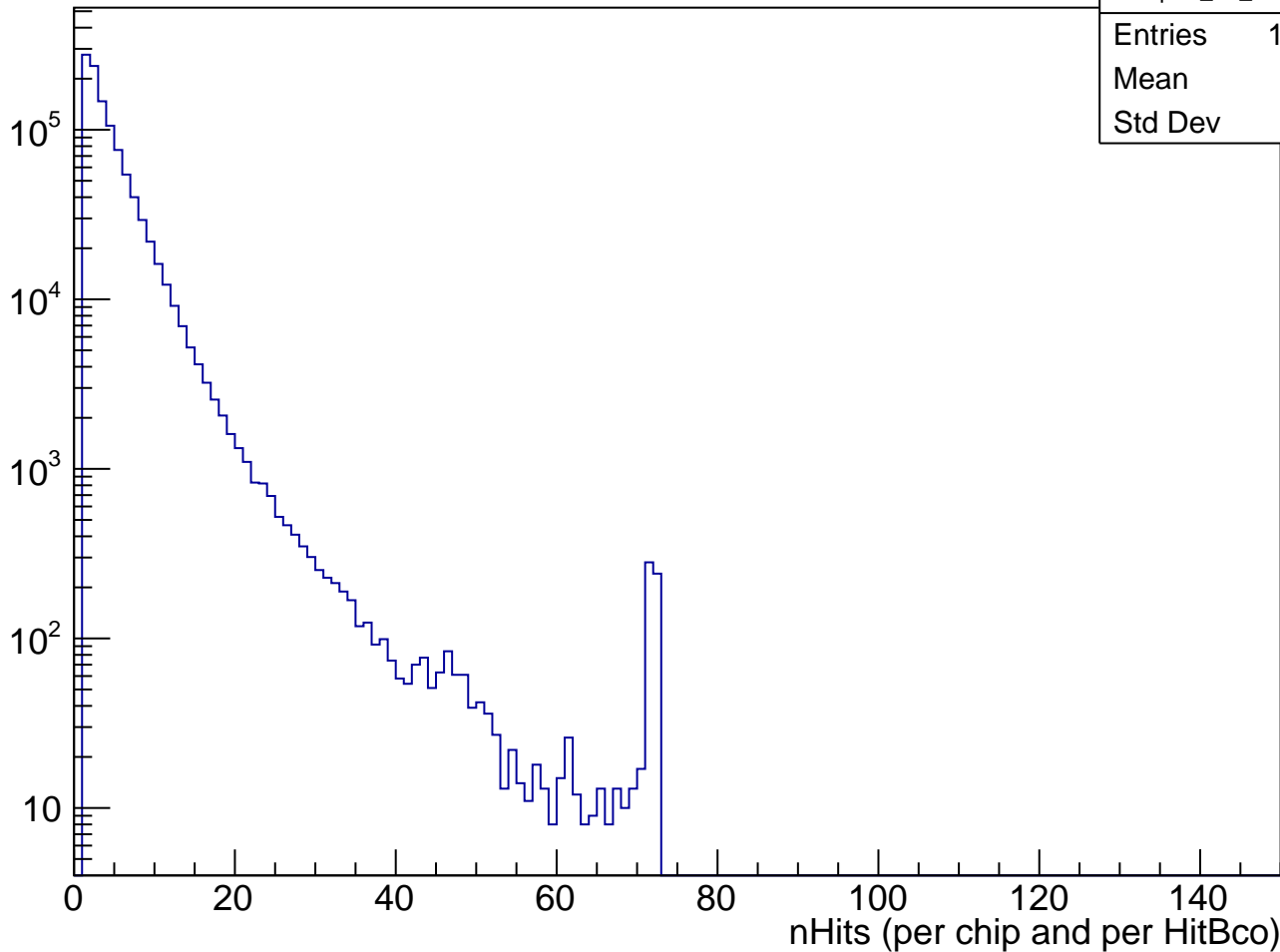
Entries



nChipHit_F1_Fch12_Chip0	
Entries	1057798
Mean	3.889
Std Dev	4.228

nChipHit_F1_Fch12_Chip1

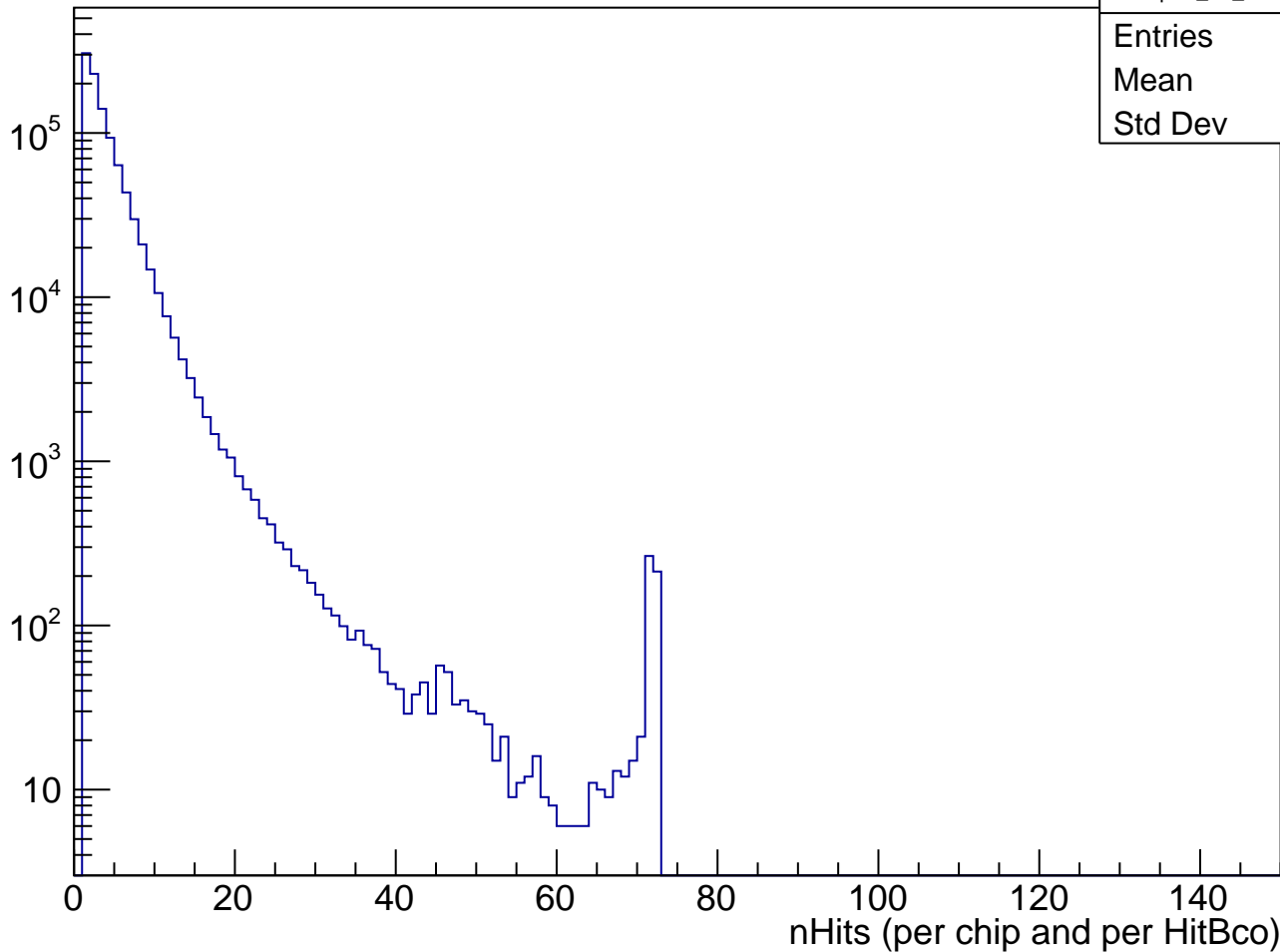
Entries



nChipHit_F1_Fch12_Chip1	
Entries	1062347
Mean	3.881
Std Dev	4.184

nChipHit_F1_Fch12_Chip10

Entries



nChipHit_F1_Fch12_Chip10

Entries 987551

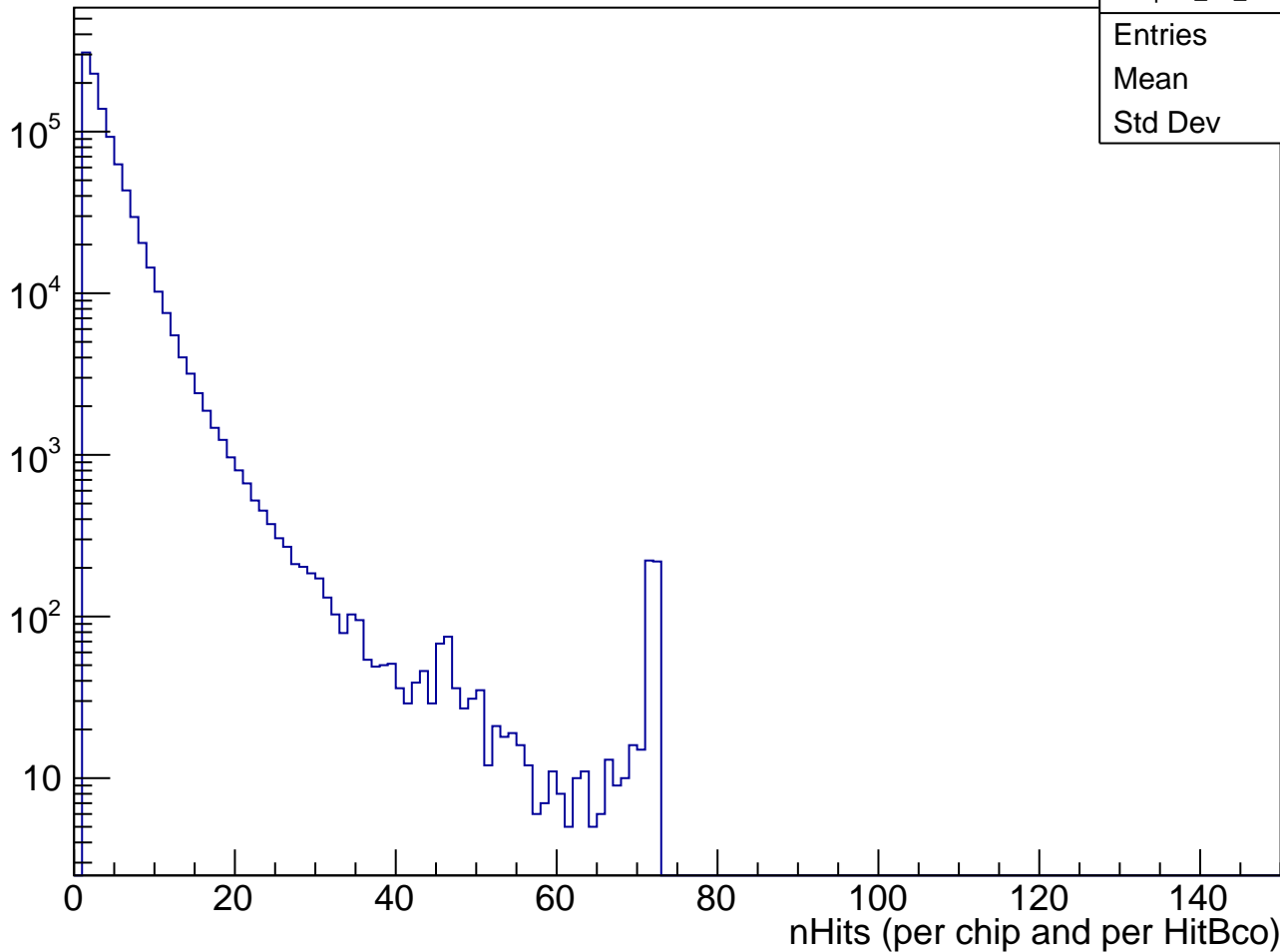
Mean 3.375

Std Dev 3.725

nHits (per chip and per HitBco)

nChipHit_F1_Fch12_Chip11

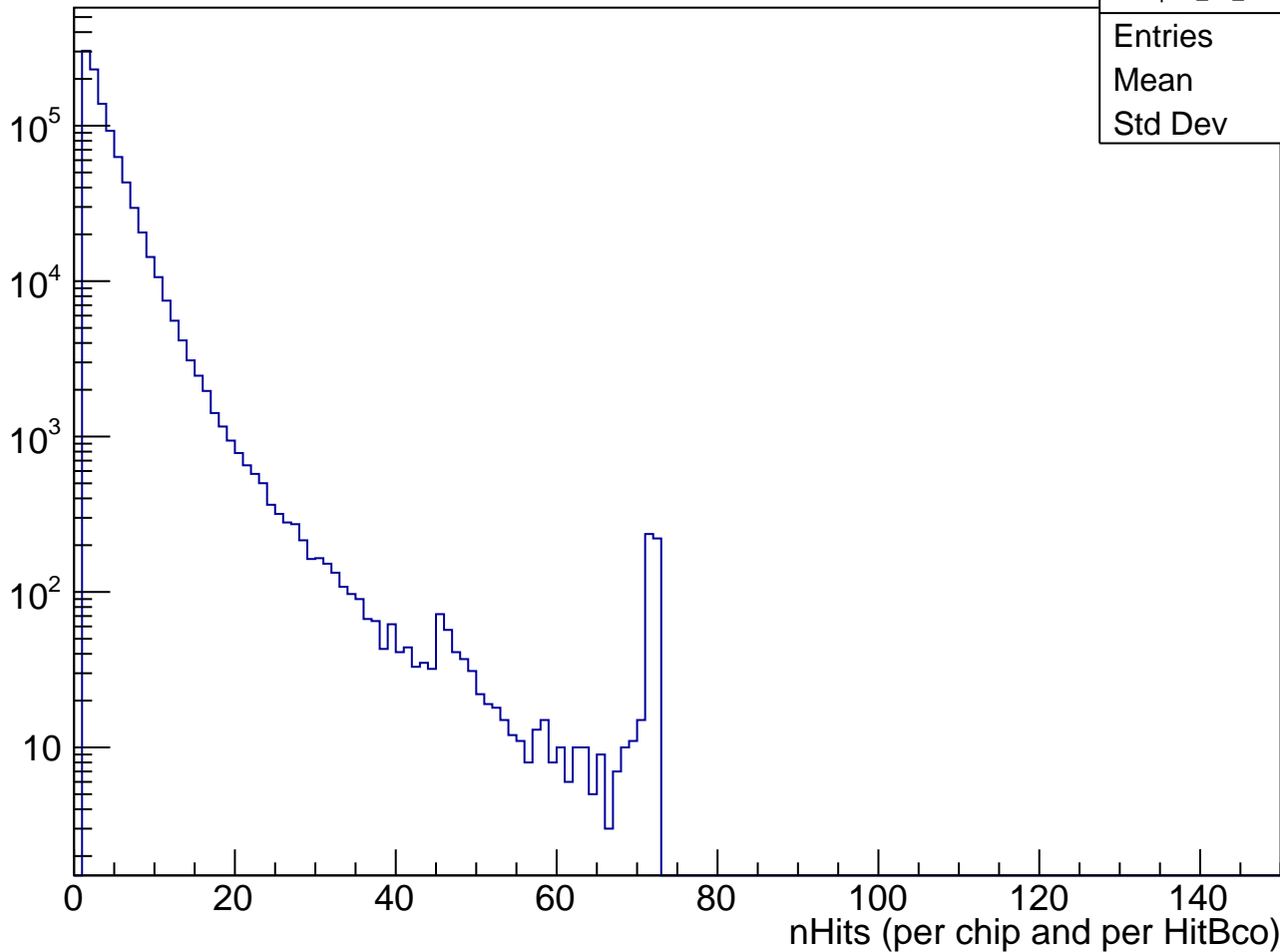
Entries



nChipHit_F1_Fch12_Chip11	
Entries	983058
Mean	3.35
Std Dev	3.686

nChipHit_F1_Fch12_Chip12

Entries



nChipHit_F1_Fch12_Chip12

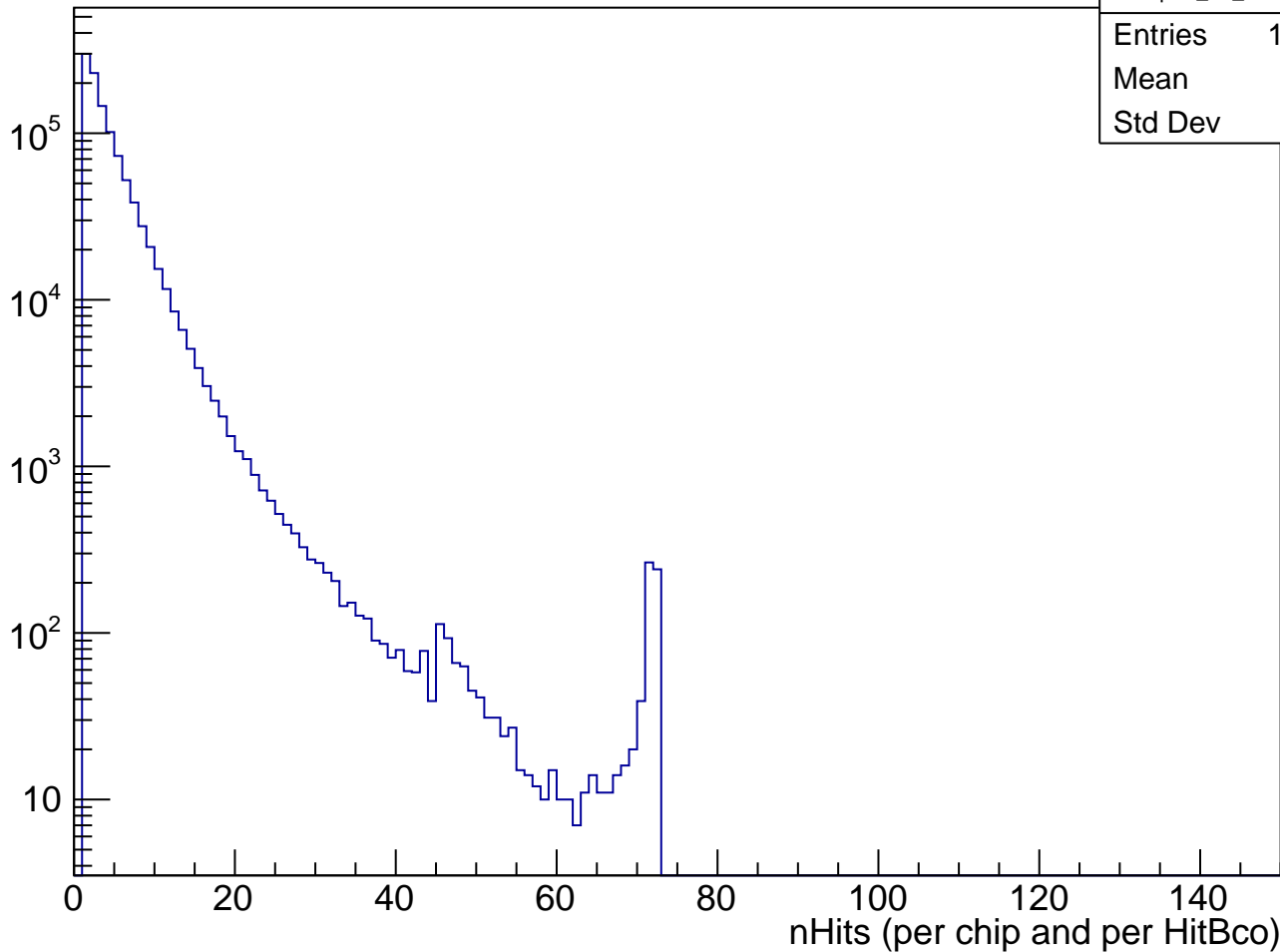
Entries 980281

Mean 3.367

Std Dev 3.71

nChipHit_F1_Fch12_Chip13

Entries



nChipHit_F1_Fch12_Chip13

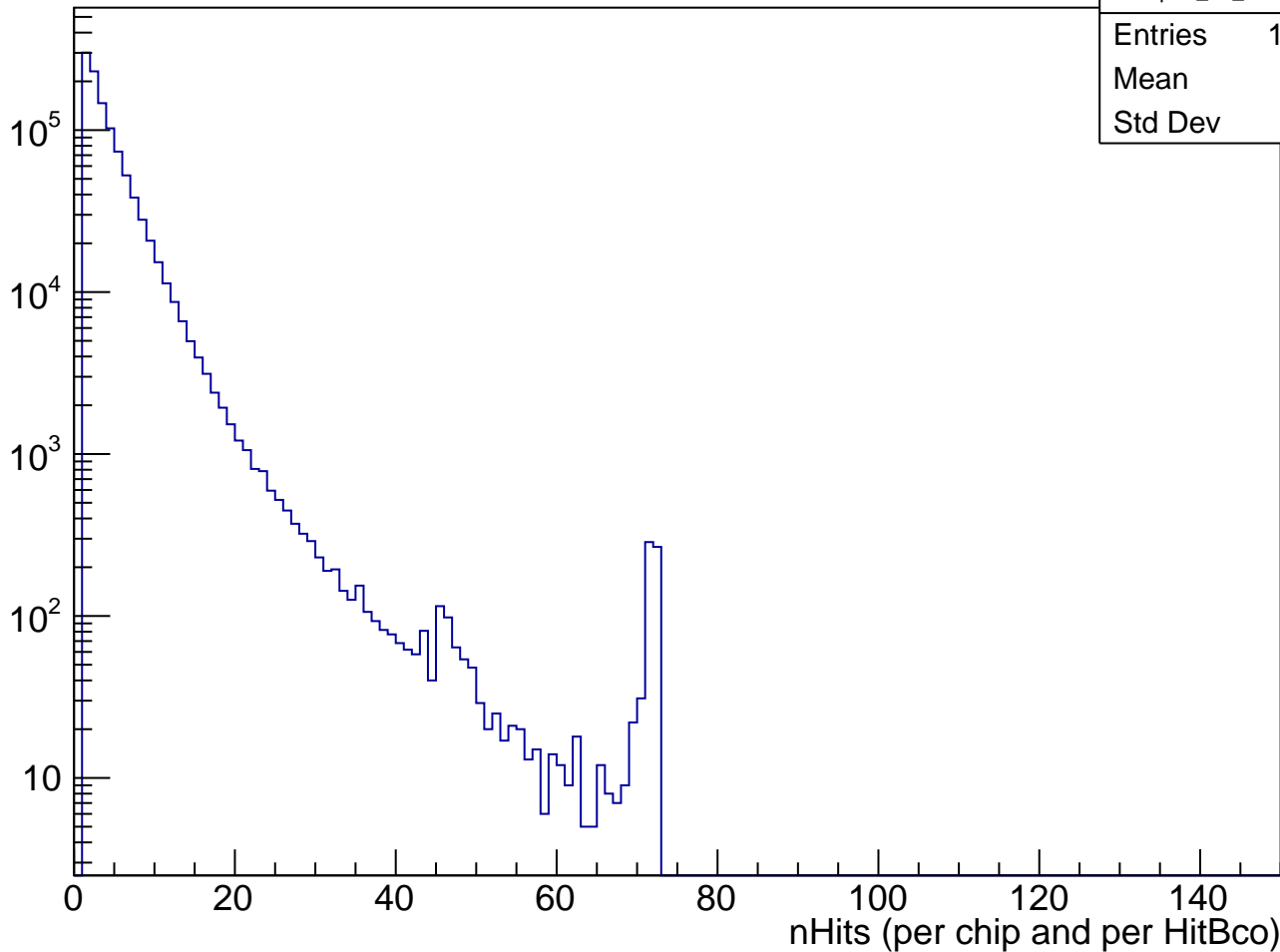
Entries 1058517

Mean 3.782

Std Dev 4.172

nChipHit_F1_Fch12_Chip14

Entries



nChipHit_F1_Fch12_Chip14

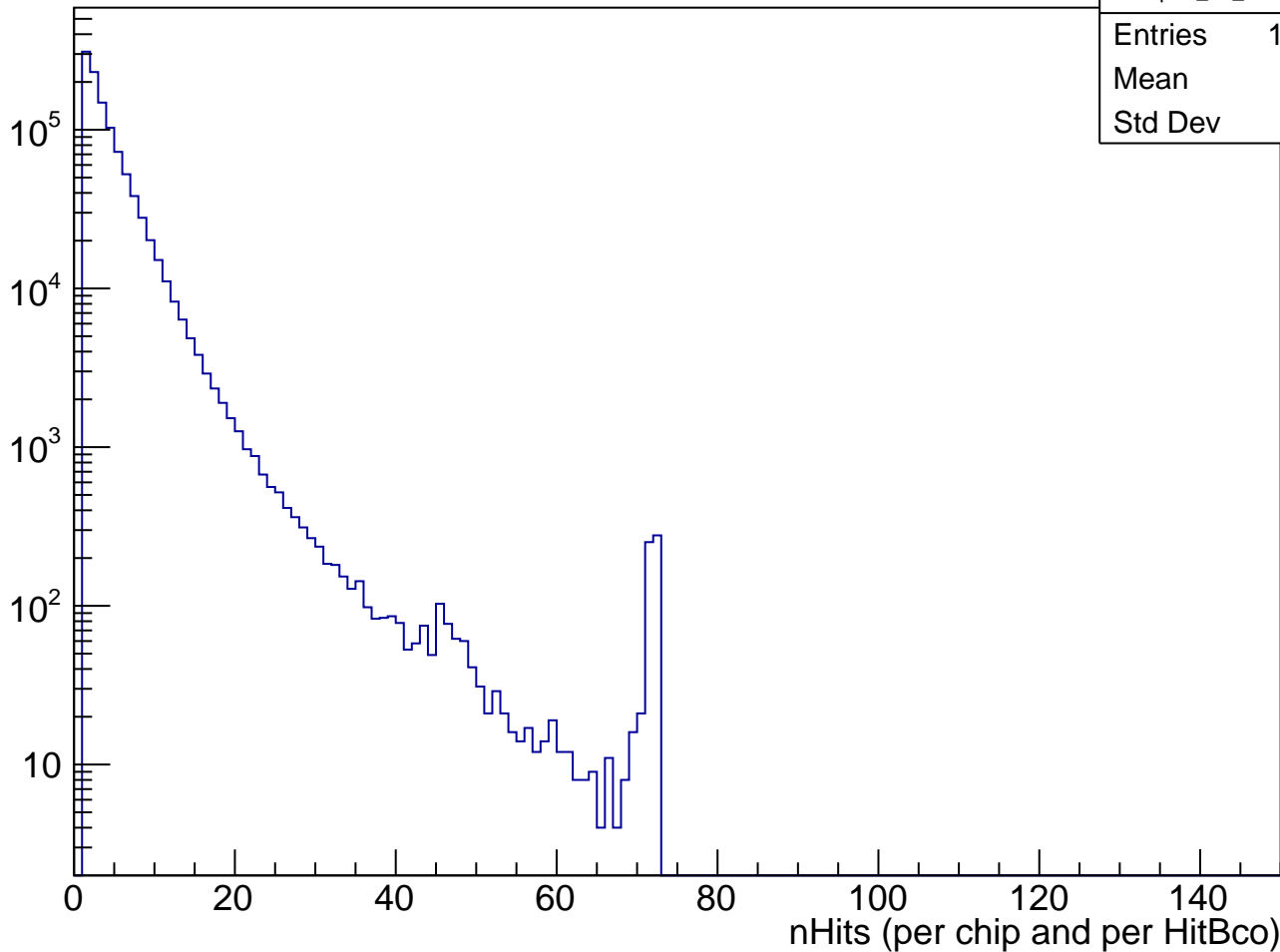
Entries 1063272

Mean 3.771

Std Dev 4.151

nChipHit_F1_Fch12_Chip15

Entries



nChipHit_F1_Fch12_Chip15

Entries 1071217

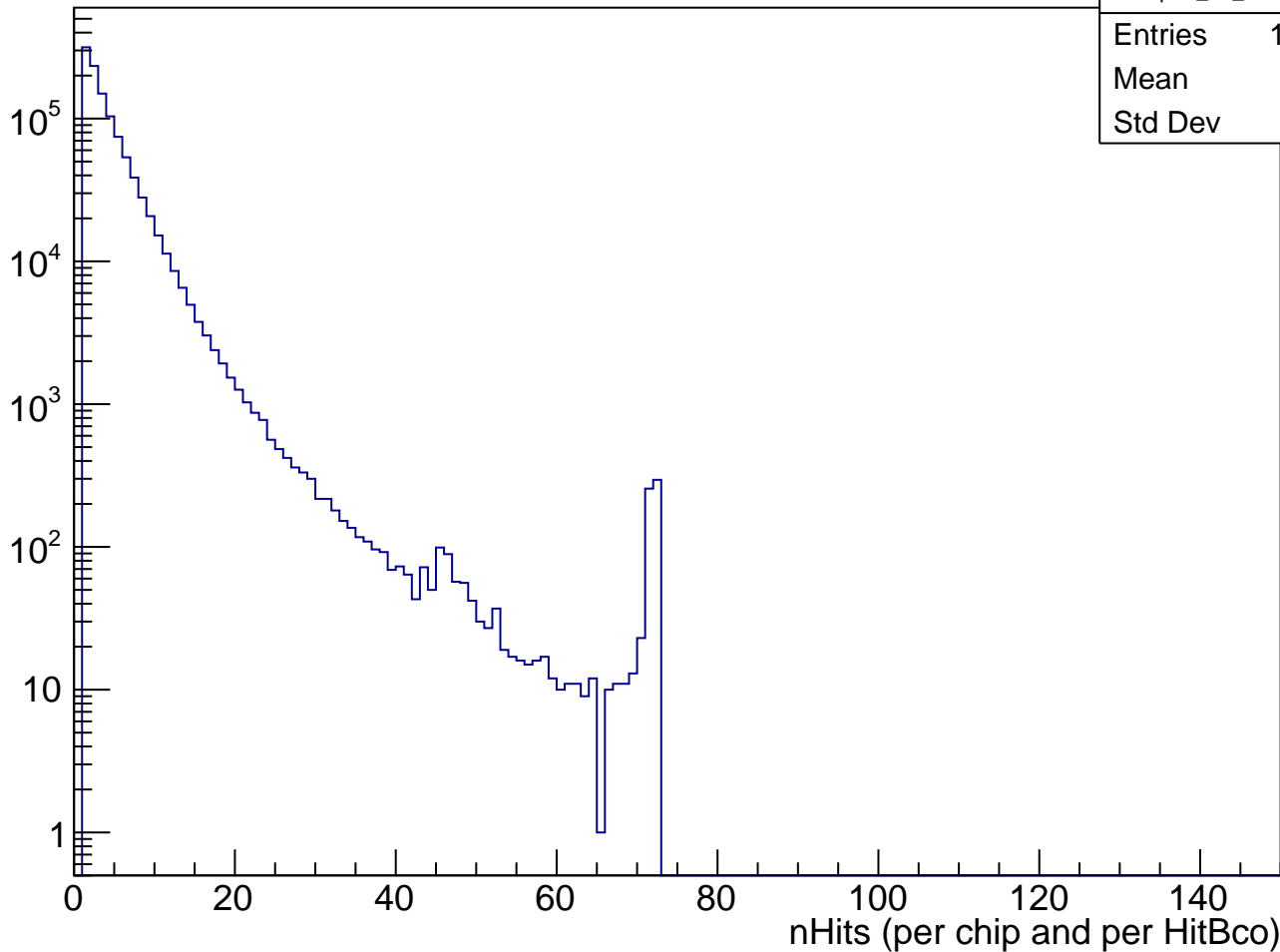
Mean 3.718

Std Dev 4.088

nHits (per chip and per HitBco)

nChipHit_F1_Fch12_Chip16

Entries



nChipHit_F1_Fch12_Chip16

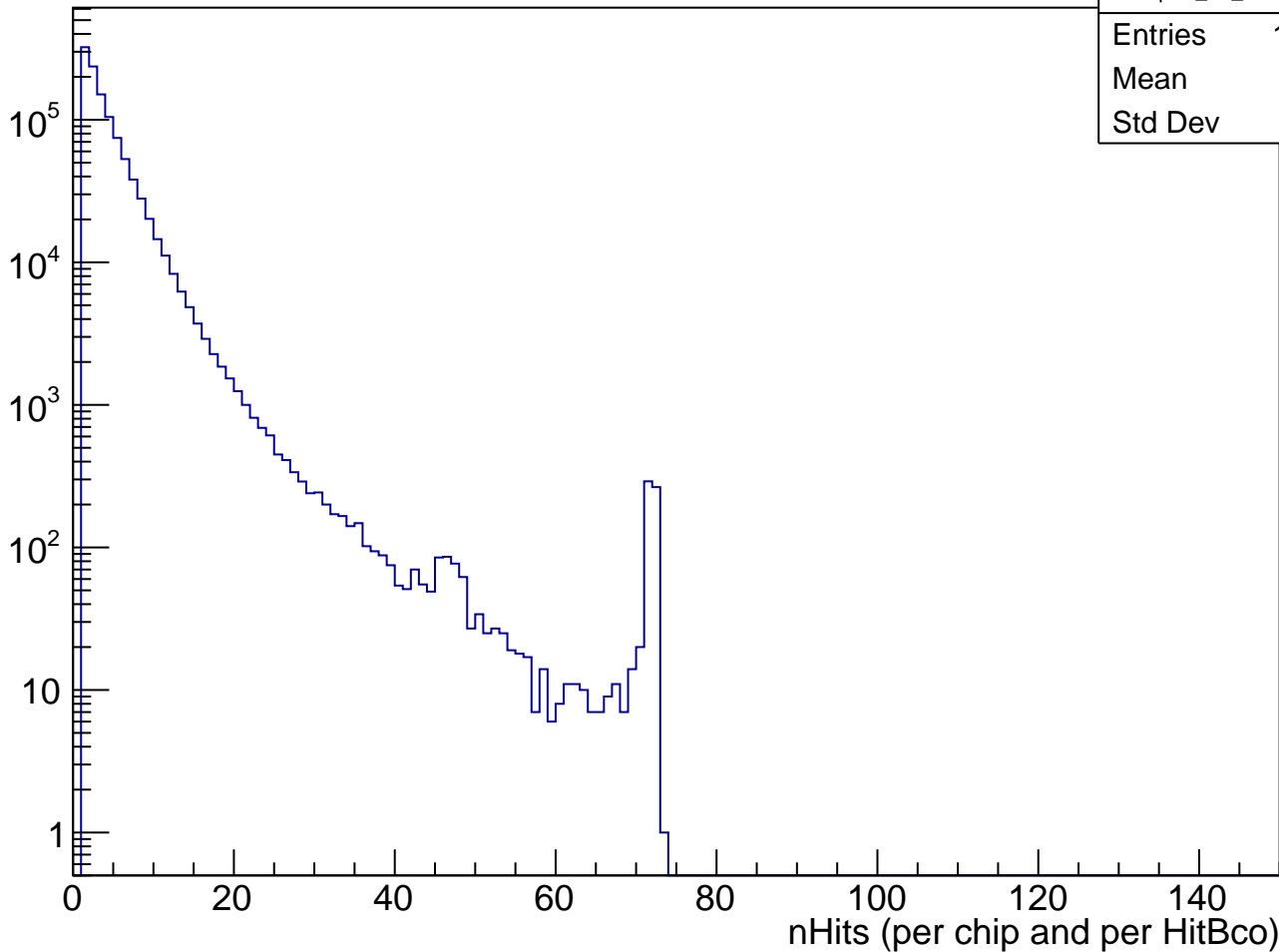
Entries 1086500

Mean 3.724

Std Dev 4.103

nChipHit_F1_Fch12_Chip17

Entries



nChipHit_F1_Fch12_Chip17

Entries 1094831

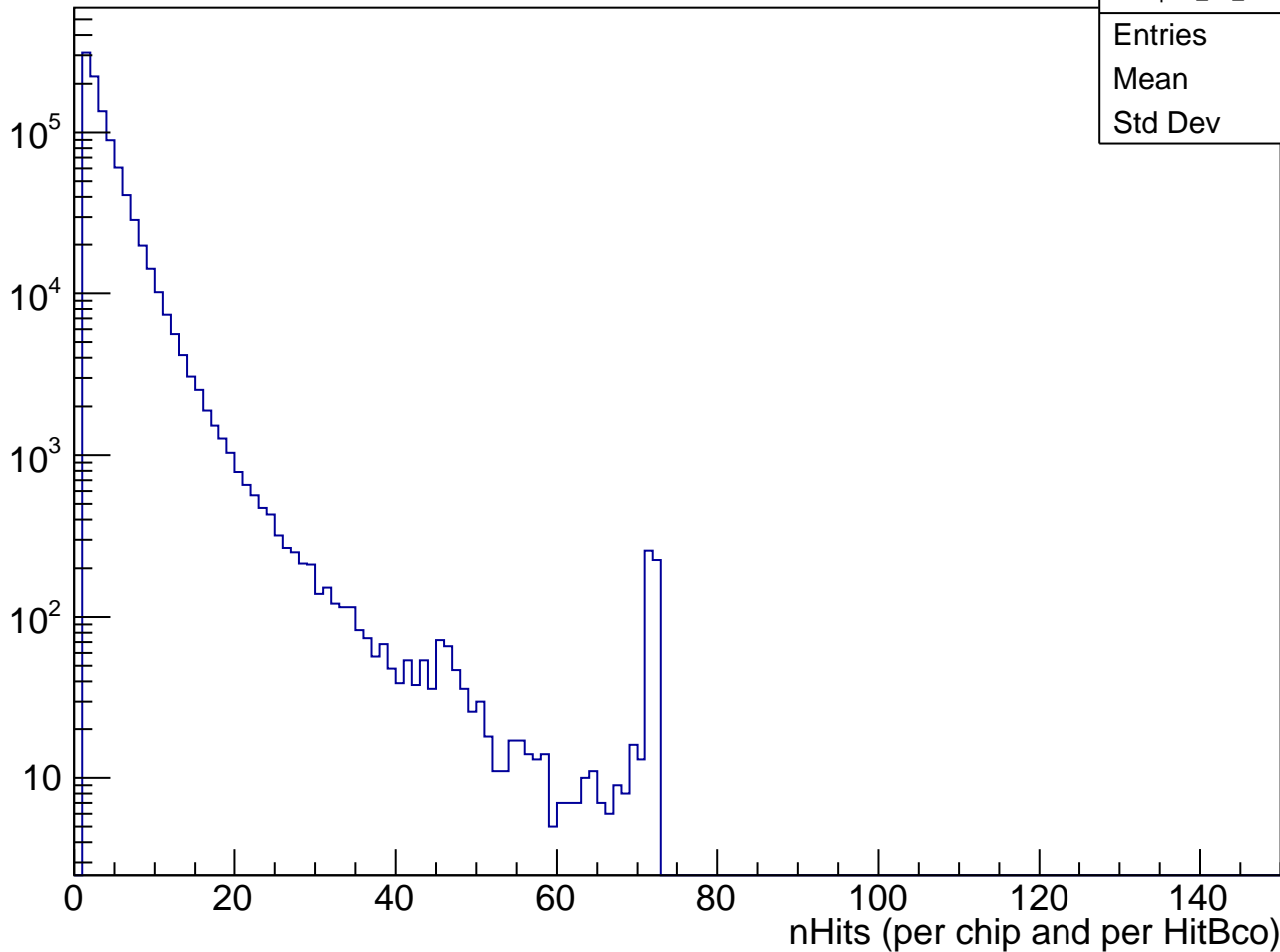
Mean 3.674

Std Dev 4.053

nHits (per chip and per HitBco)

nChipHit_F1_Fch12_Chip18

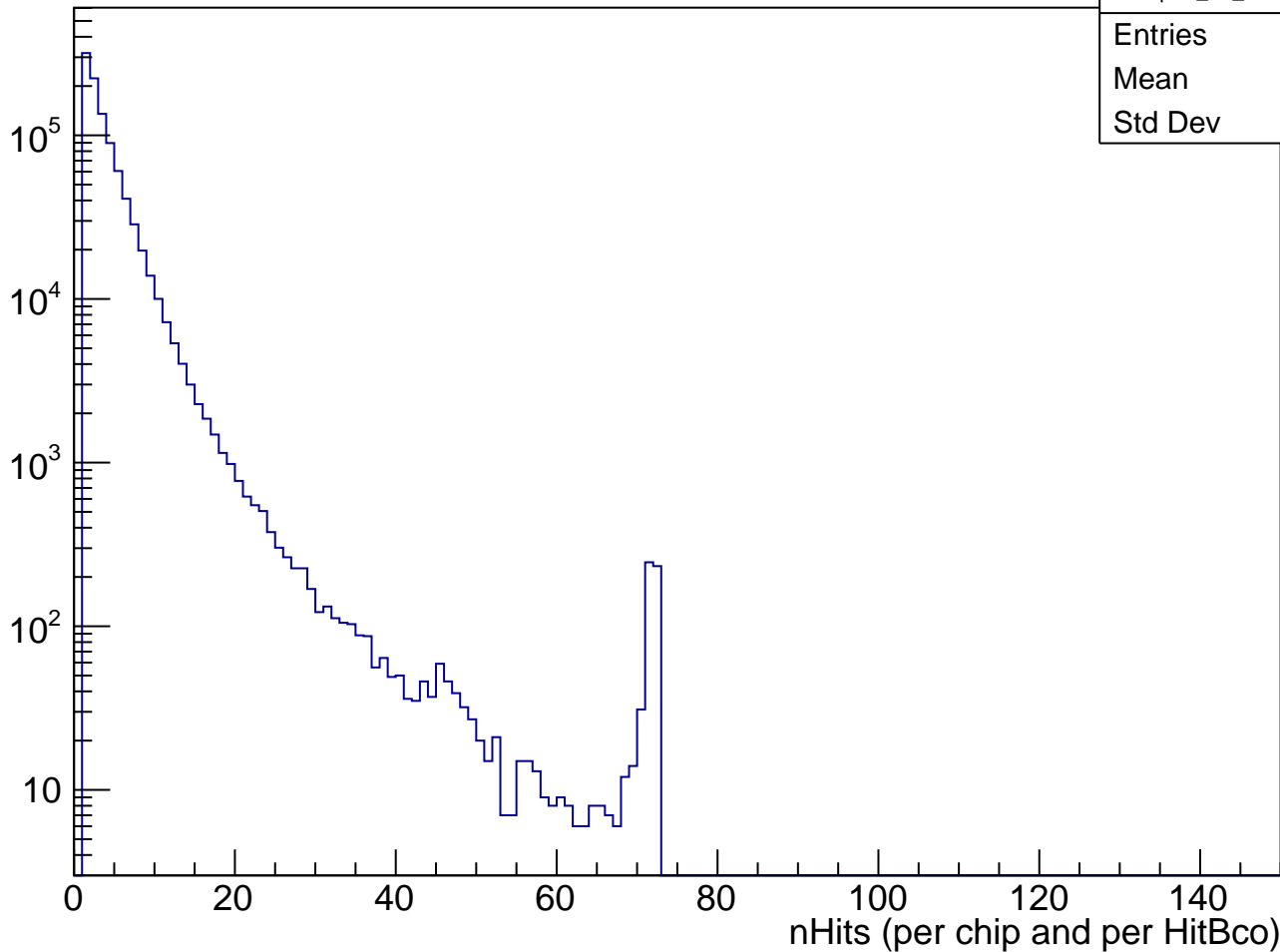
Entries



nChipHit_F1_Fch12_Chip18	
Entries	968083
Mean	3.347
Std Dev	3.77

nChipHit_F1_Fch12_Chip19

Entries



nChipHit_F1_Fch12_Chip19

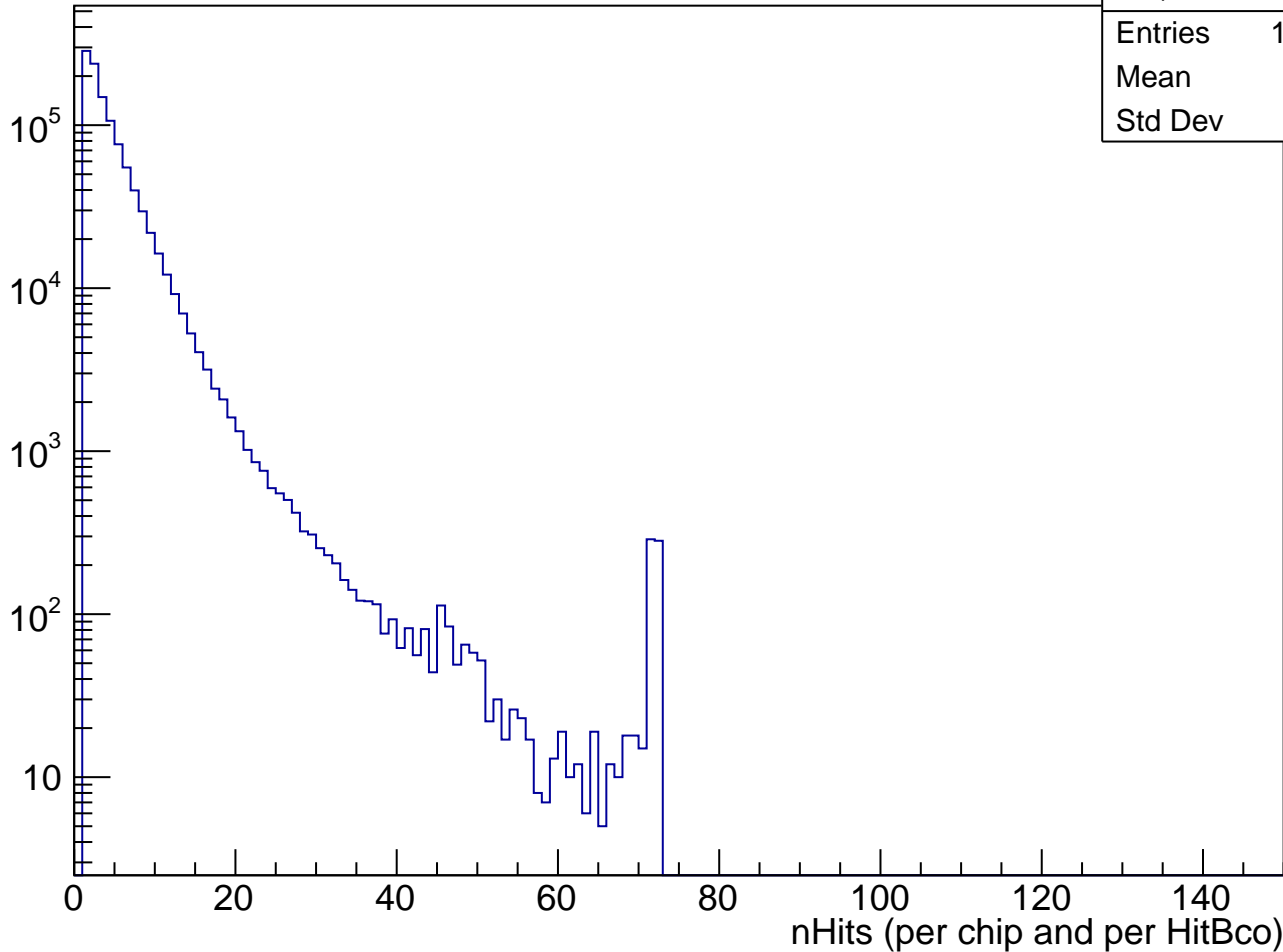
Entries 973260

Mean 3.306

Std Dev 3.714

nChipHit_F1_Fch12_Chip2

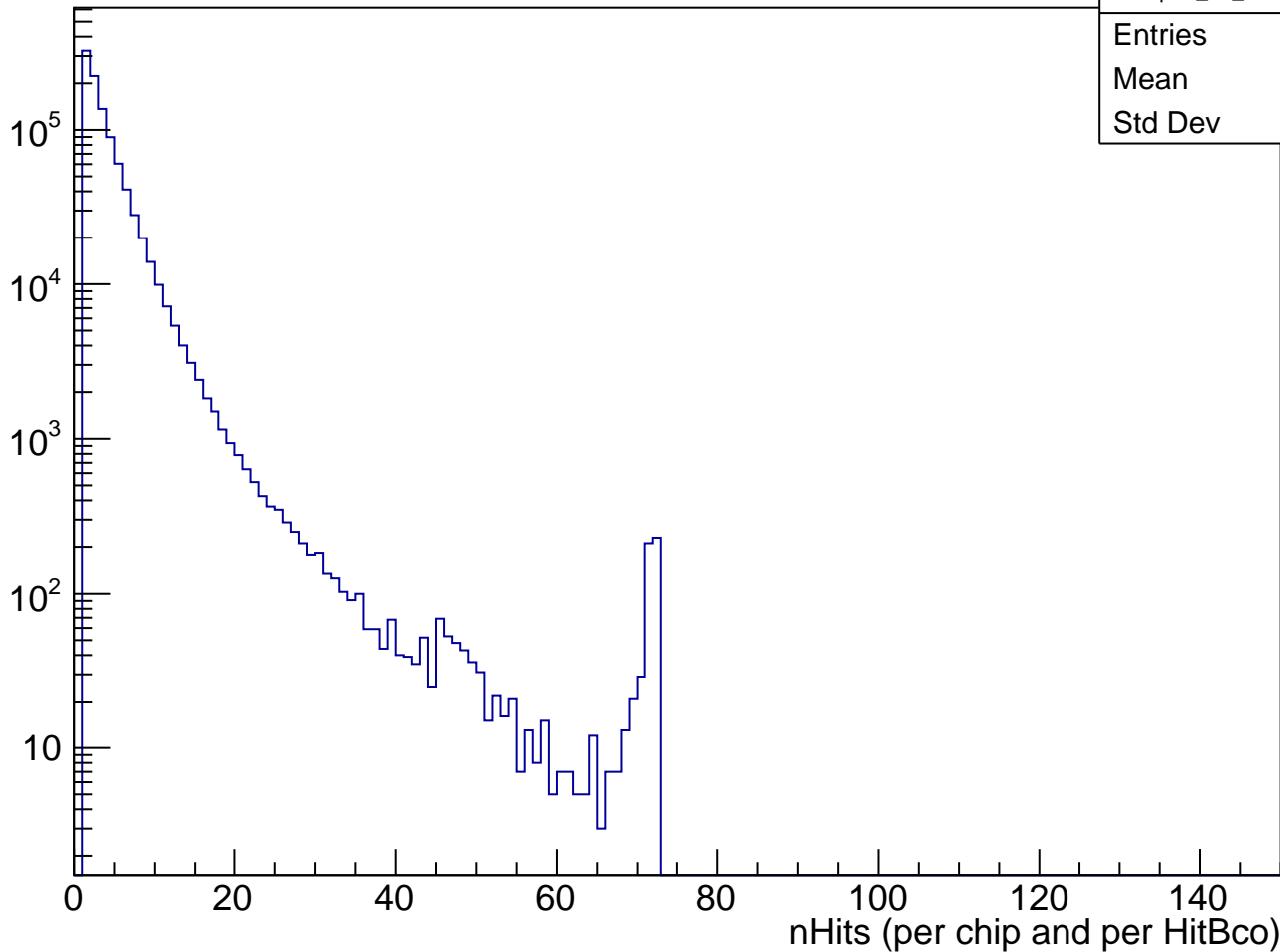
Entries



nChipHit_F1_Fch12_Chip2	
Entries	1074402
Mean	3.86
Std Dev	4.199

nChipHit_F1_Fch12_Chip20

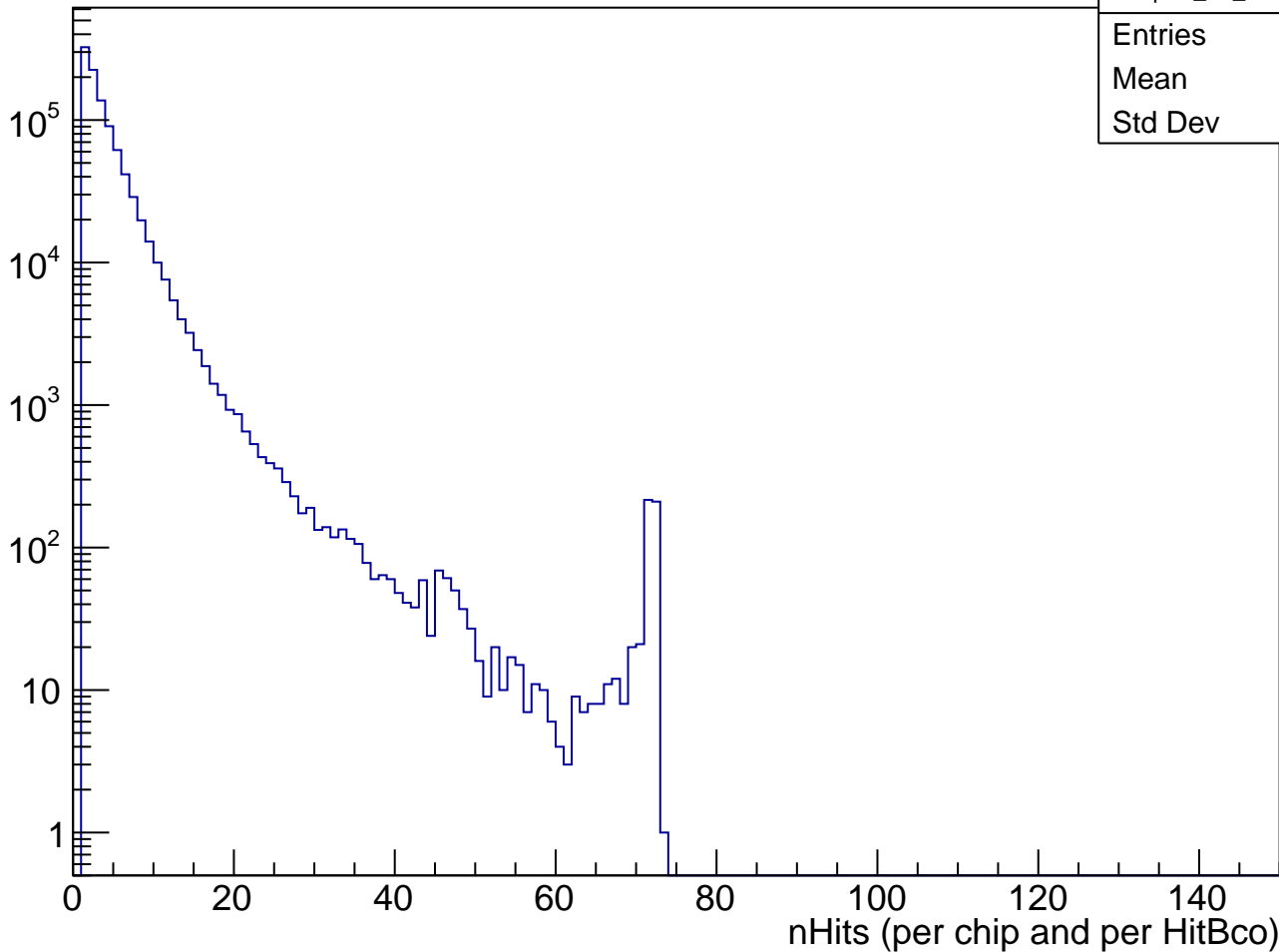
Entries



nChipHit_F1_Fch12_Chip20	
Entries	981349
Mean	3.291
Std Dev	3.703

nChipHit_F1_Fch12_Chip21

Entries

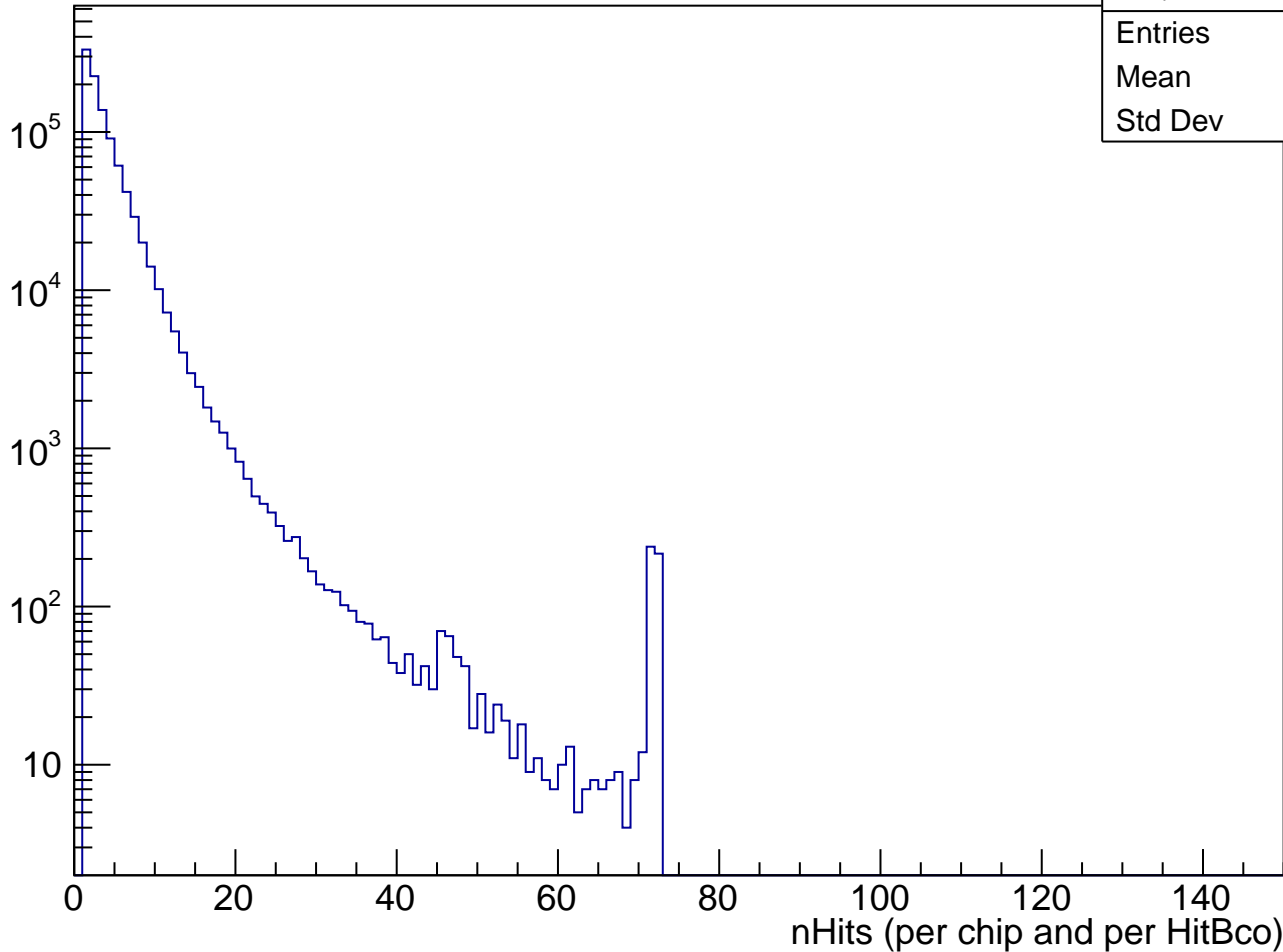


nChipHit_F1_Fch12_Chip21	
Entries	987556
Mean	3.302
Std Dev	3.691

nHits (per chip and per HitBco)

nChipHit_F1_Fch12_Chip22

Entries



nChipHit_F1_Fch12_Chip22

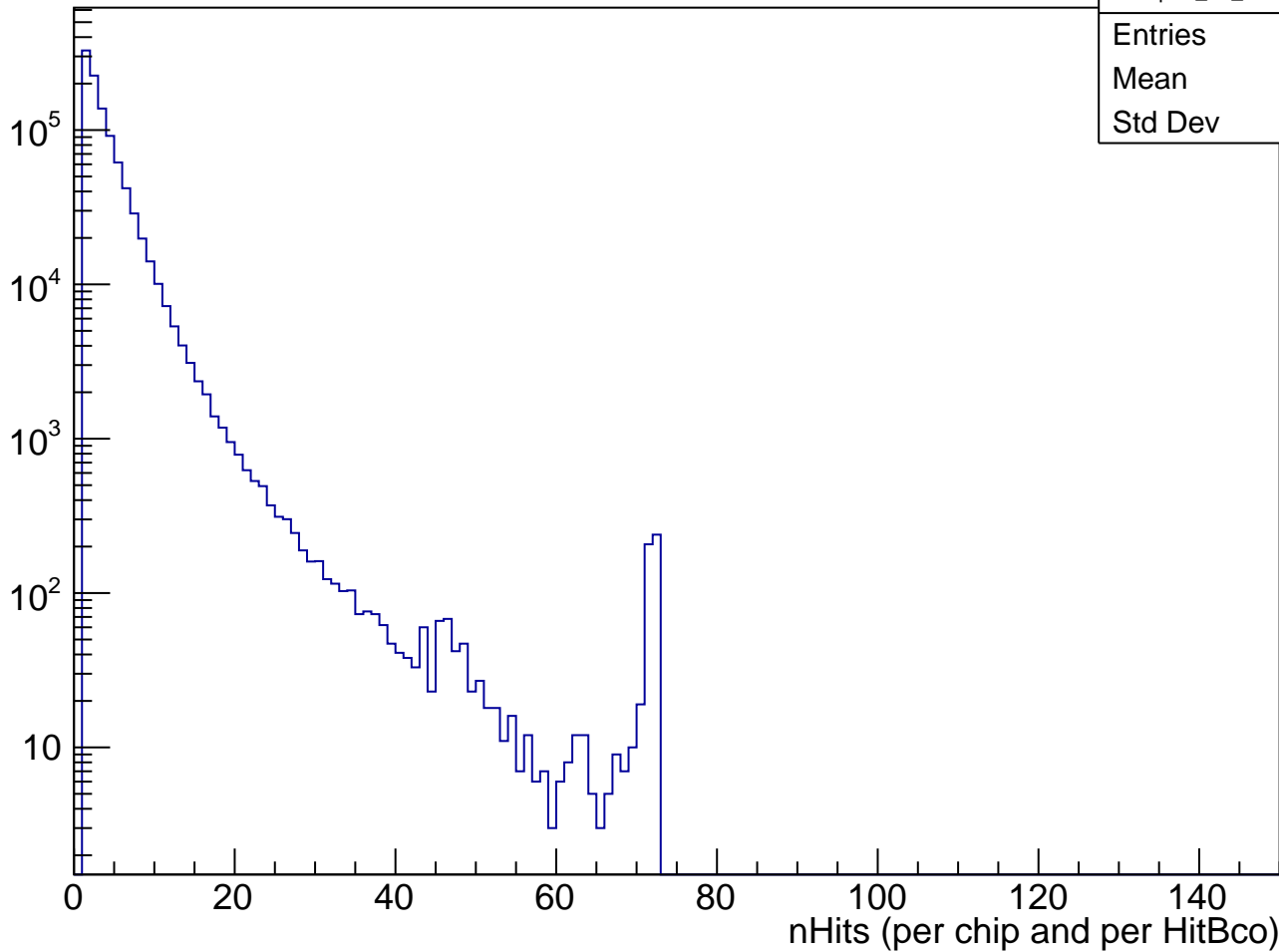
Entries 996509

Mean 3.283

Std Dev 3.675

nChipHit_F1_Fch12_Chip23

Entries



nChipHit_F1_Fch12_Chip23

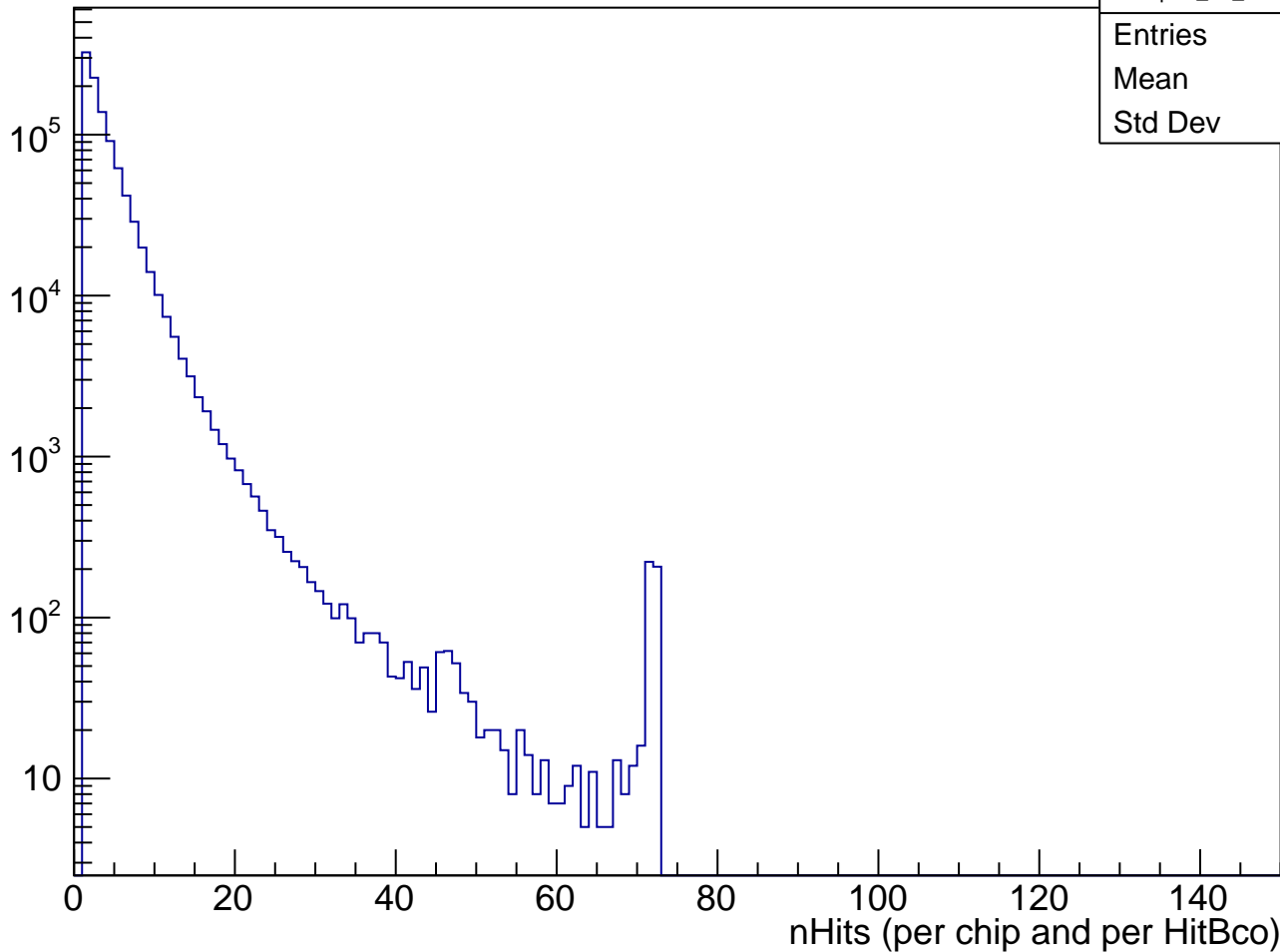
Entries 992742

Mean 3.288

Std Dev 3.666

nChipHit_F1_Fch12_Chip24

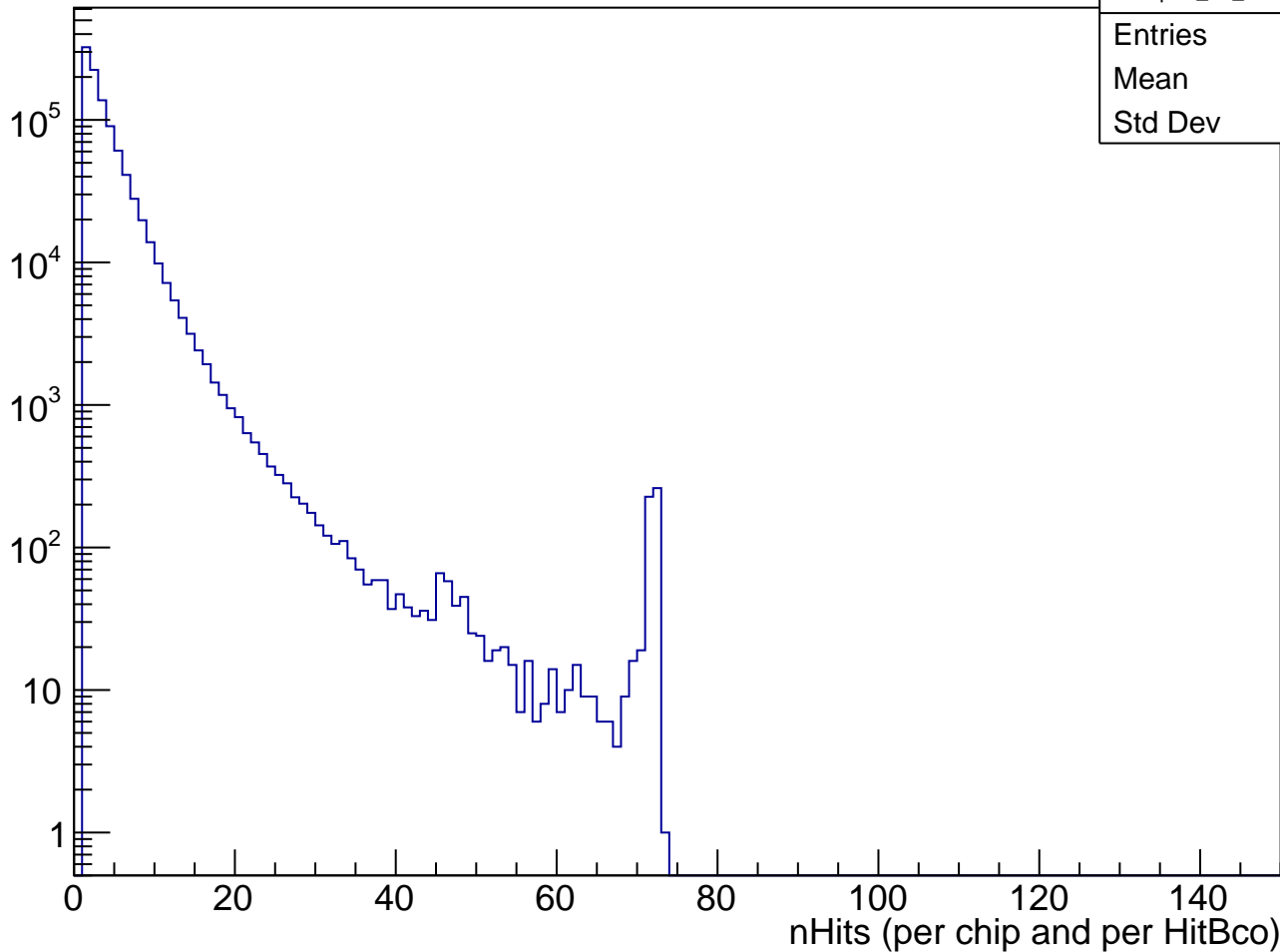
Entries



nChipHit_F1_Fch12_Chip24	
Entries	990325
Mean	3.3
Std Dev	3.674

nChipHit_F1_Fch12_Chip25

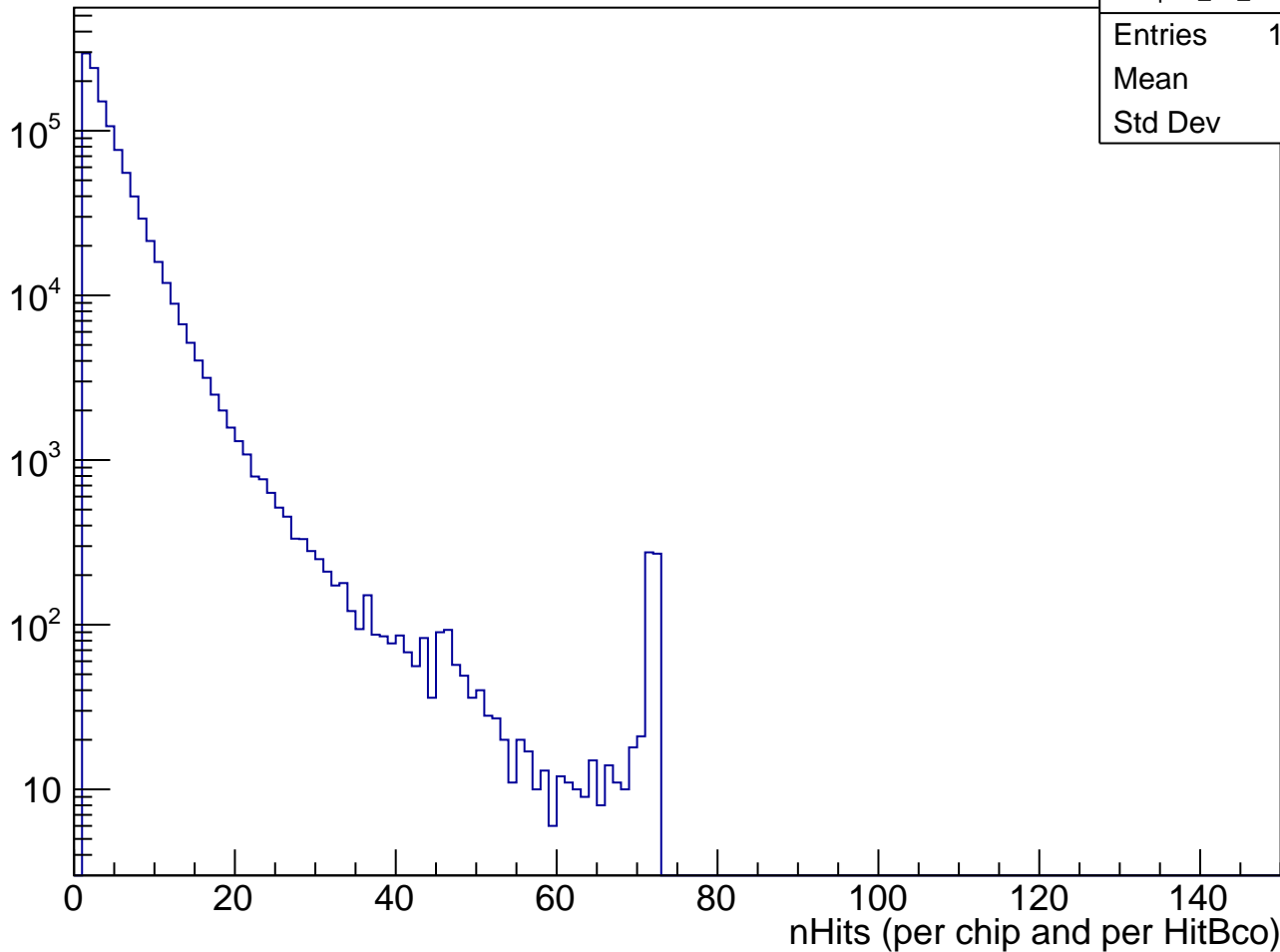
Entries



nChipHit_F1_Fch12_Chip25	
Entries	983056
Mean	3.292
Std Dev	3.704

nChipHit_F1_Fch12_Chip3

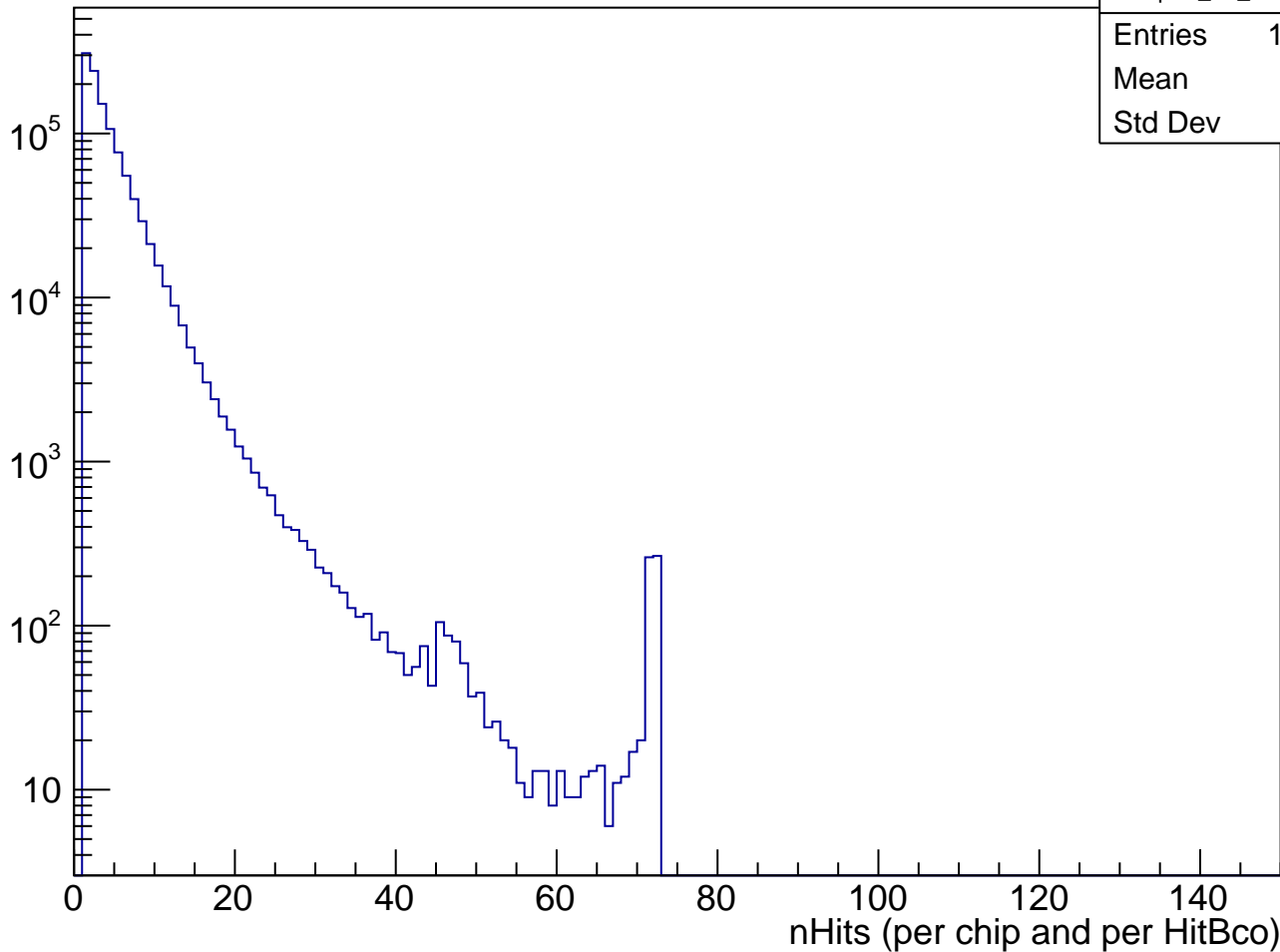
Entries



nChipHit_F1_Fch12_Chip3	
Entries	1086307
Mean	3.806
Std Dev	4.125

nChipHit_F1_Fch12_Chip4

Entries

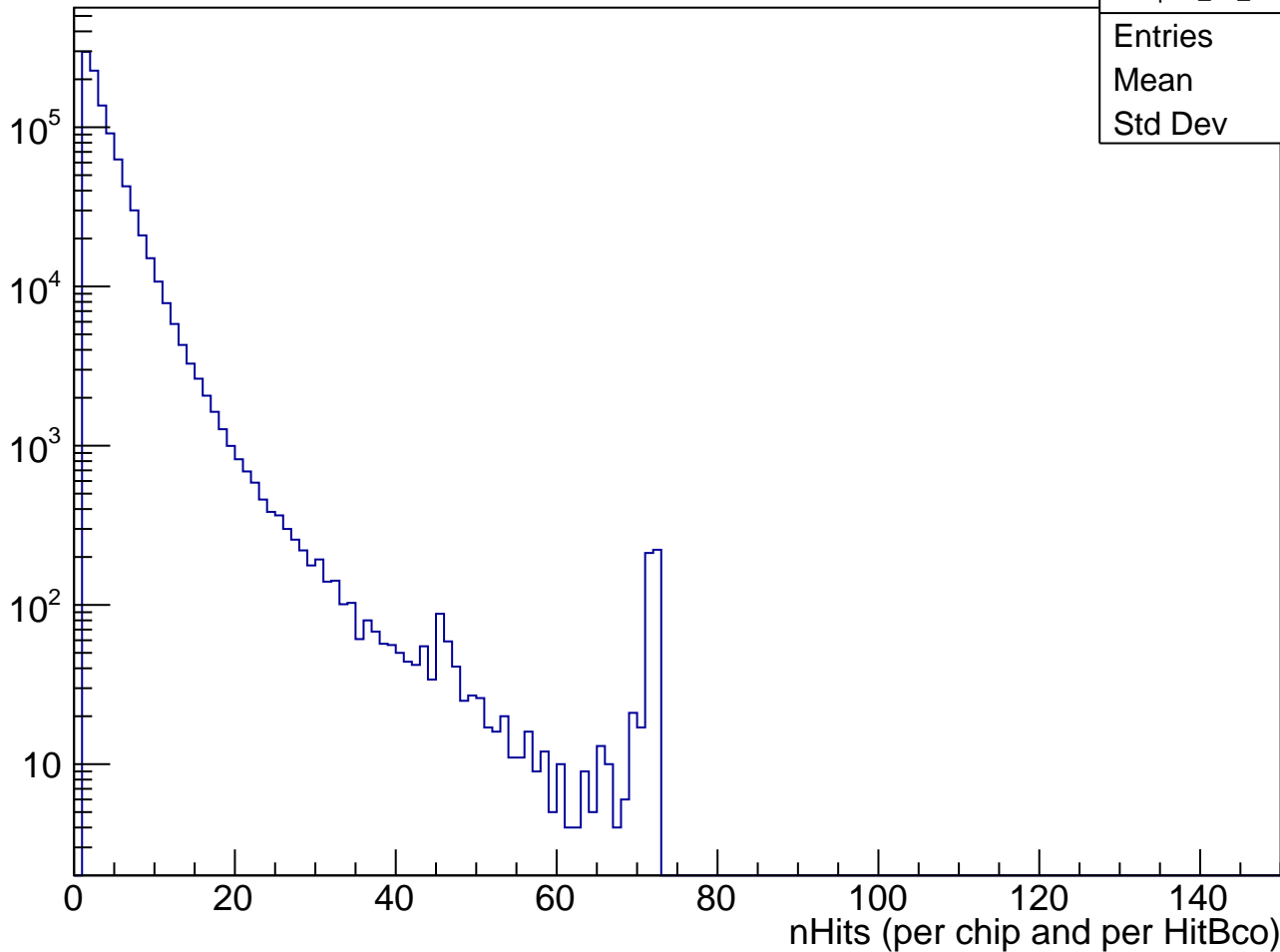


nChipHit_F1_Fch12_Chip4	
Entries	1100015
Mean	3.754
Std Dev	4.078

nHits (per chip and per HitBco)

nChipHit_F1_Fch12_Chip5

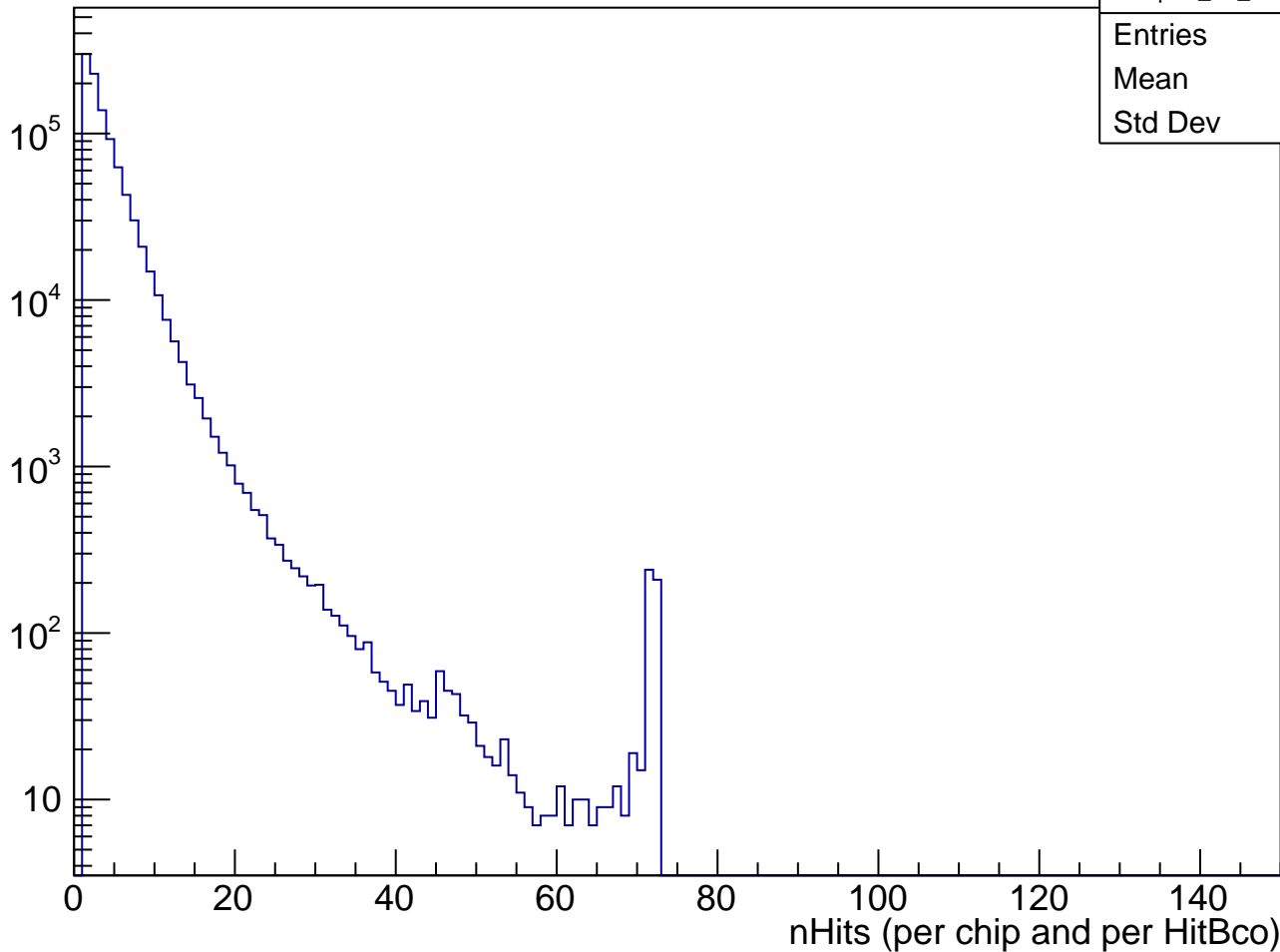
Entries



nChipHit_F1_Fch12_Chip5	
Entries	970981
Mean	3.415
Std Dev	3.761

nChipHit_F1_Fch12_Chip6

Entries

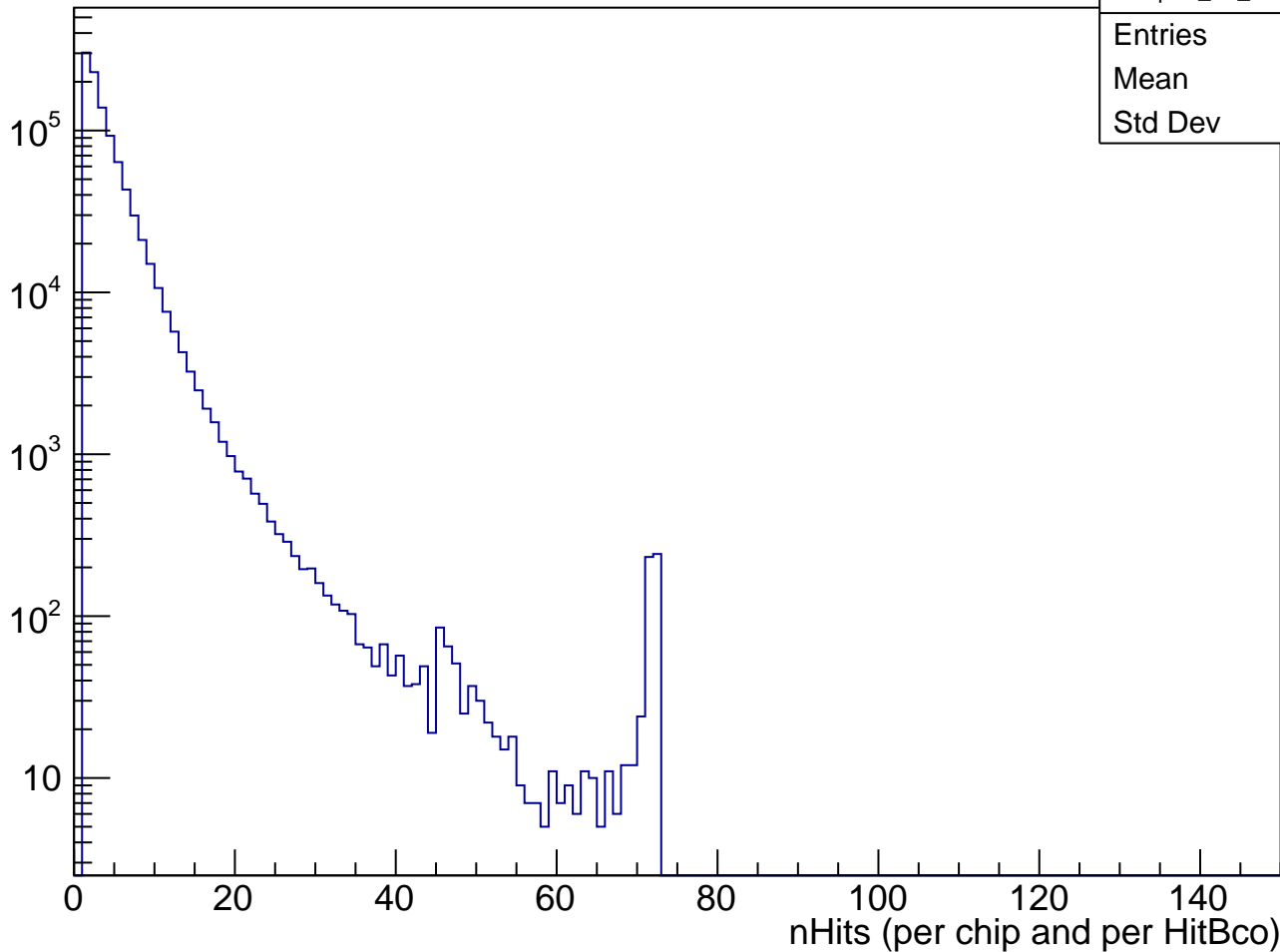


nChipHit_F1_Fch12_Chip6	
Entries	977385
Mean	3.389
Std Dev	3.729

nHits (per chip and per HitBco)

nChipHit_F1_Fch12_Chip7

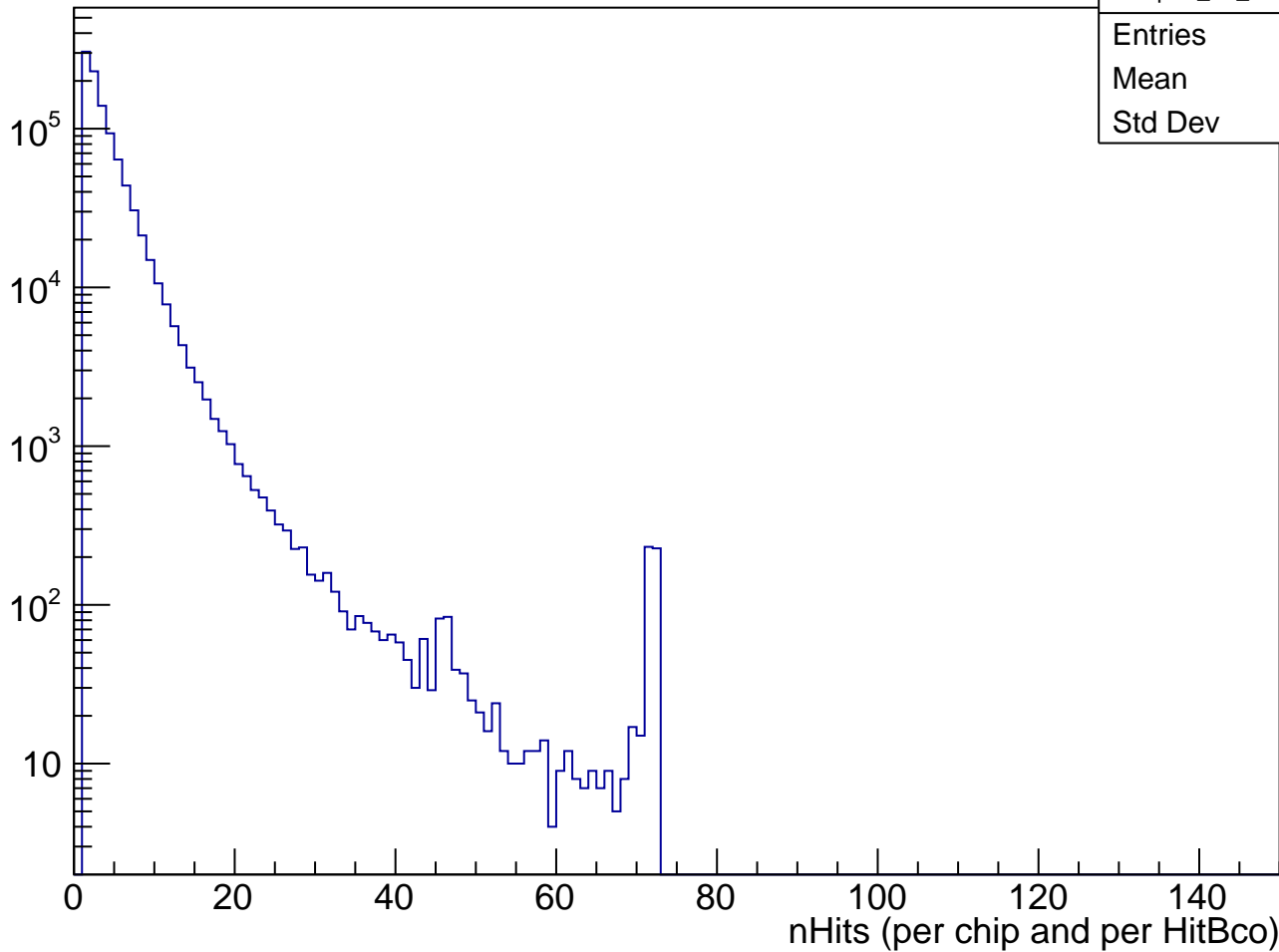
Entries



nChipHit_F1_Fch12_Chip7	
Entries	982546
Mean	3.389
Std Dev	3.744

nChipHit_F1_Fch12_Chip8

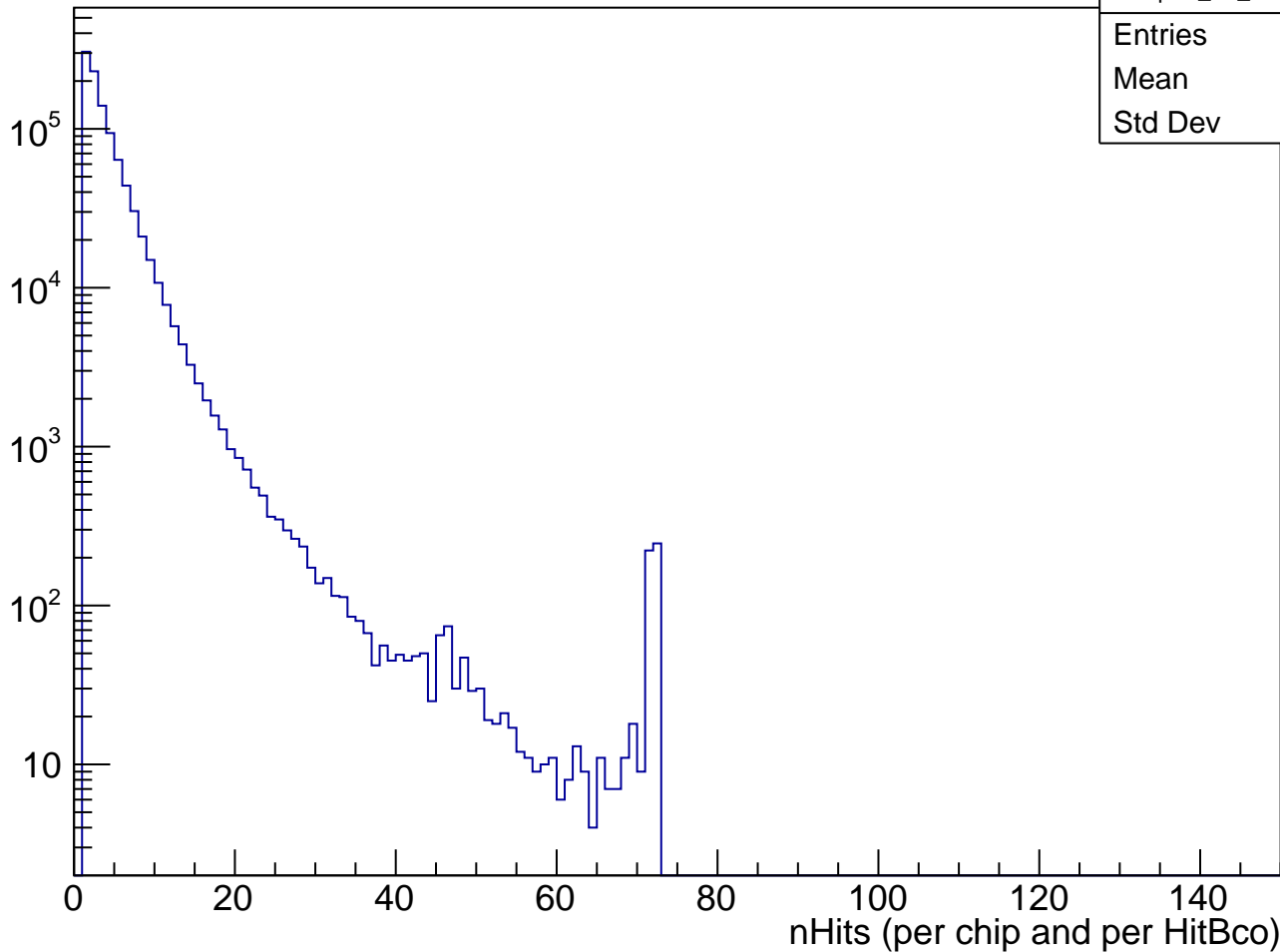
Entries



nChipHit_F1_Fch12_Chip8	
Entries	988878
Mean	3.387
Std Dev	3.728

nChipHit_F1_Fch12_Chip9

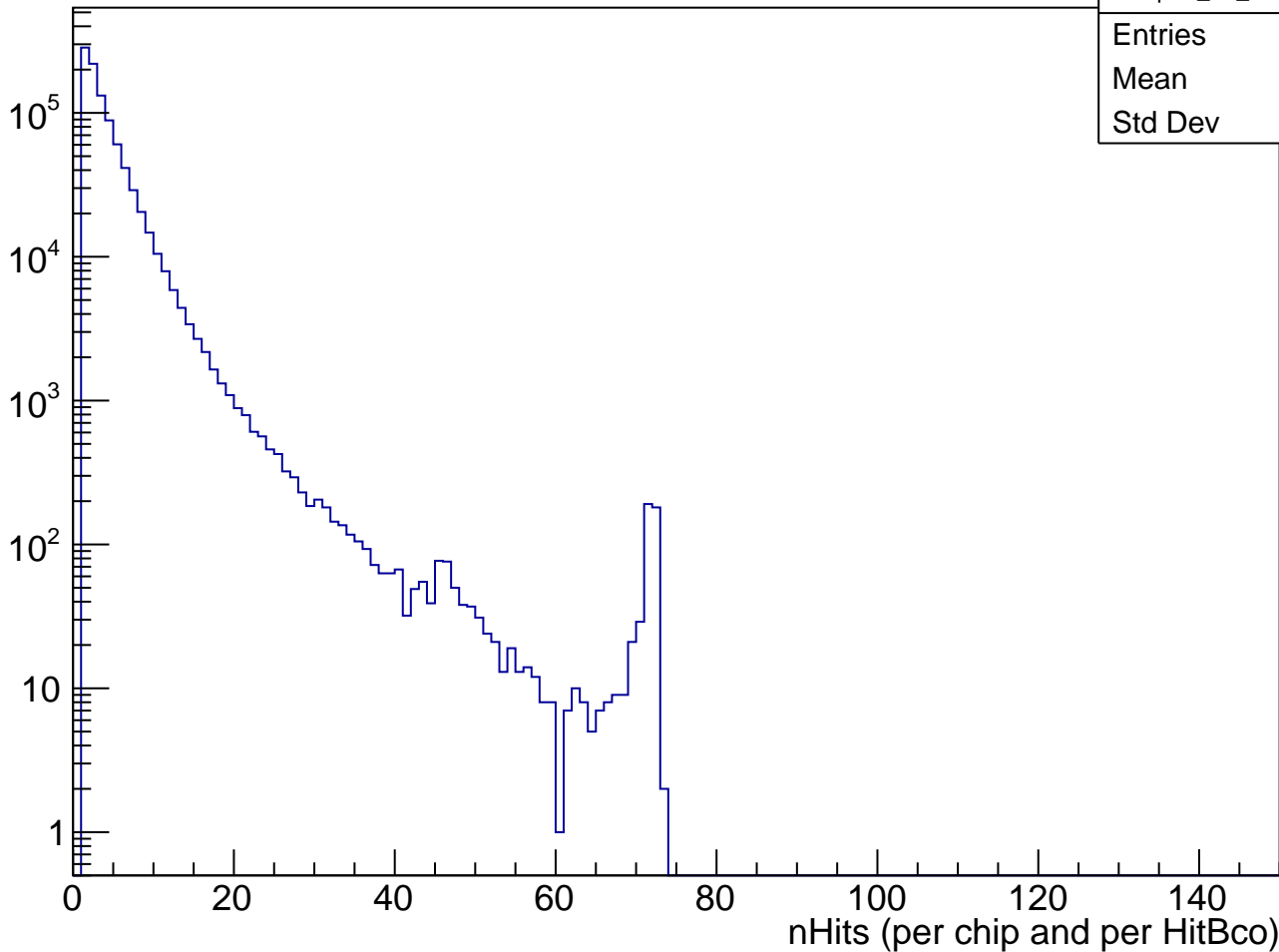
Entries



nChipHit_F1_Fch12_Chip9	
Entries	990773
Mean	3.392
Std Dev	3.739

nChipHit_F1_Fch13_Chip0

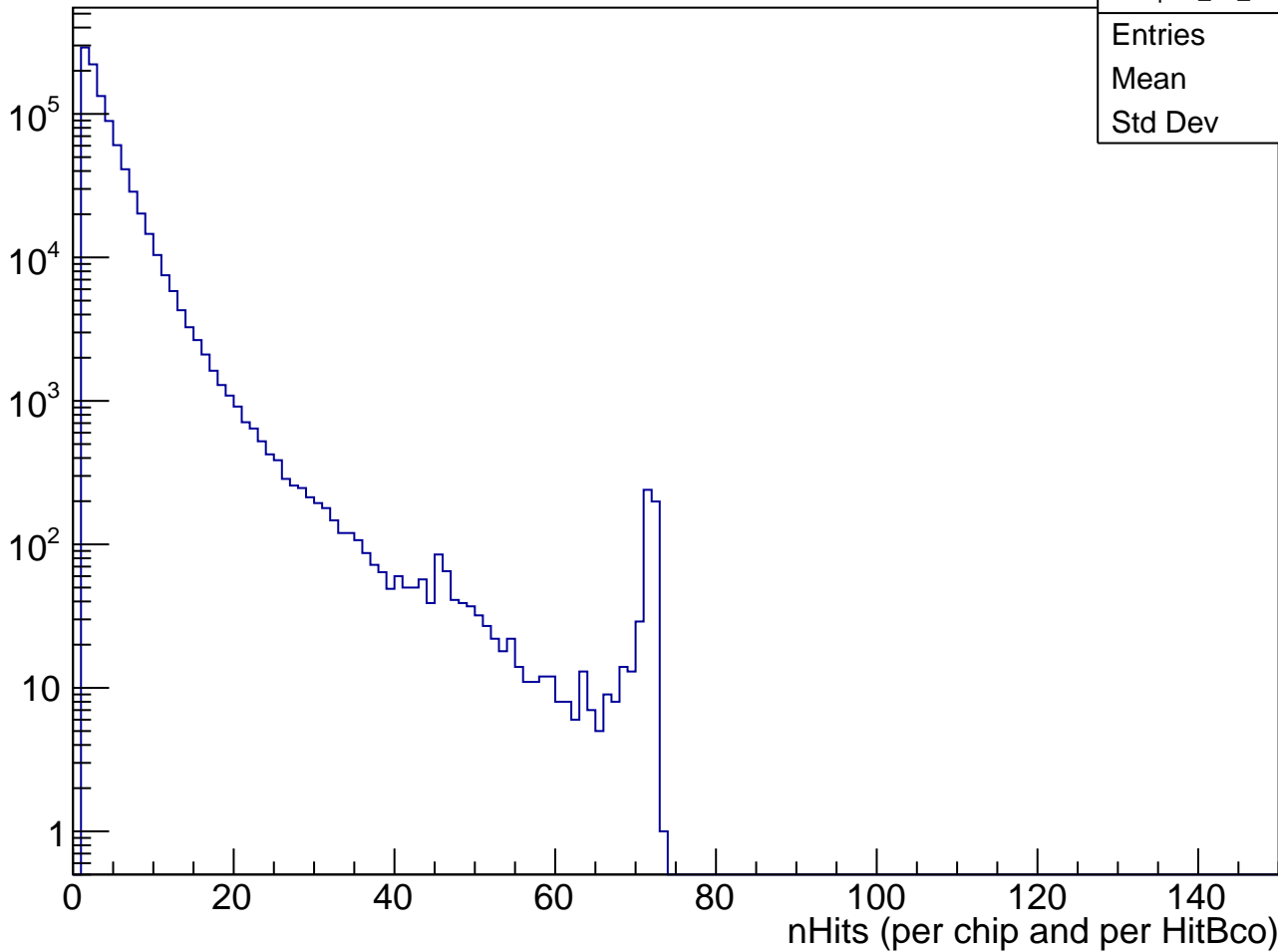
Entries



nChipHit_F1_Fch13_Chip0	
Entries	939050
Mean	3.467
Std Dev	3.856

nChipHit_F1_Fch13_Chip1

Entries

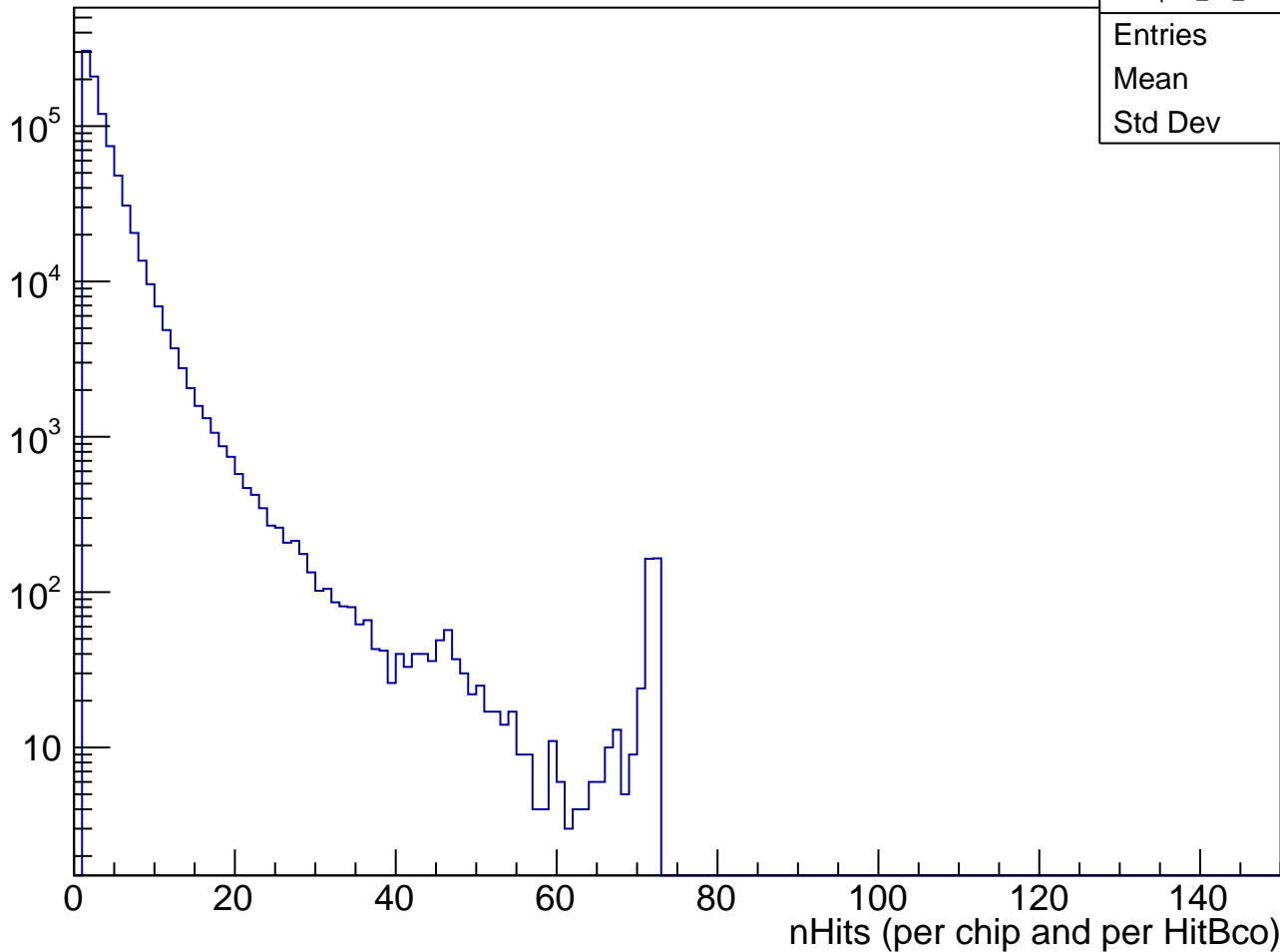


nChipHit_F1_Fch13_Chip1	
Entries	947035
Mean	3.436
Std Dev	3.868

nHits (per chip and per HitBco)

nChipHit_F1_Fch13_Chip10

Entries



nChipHit_F1_Fch13_Chip10

Entries 861518

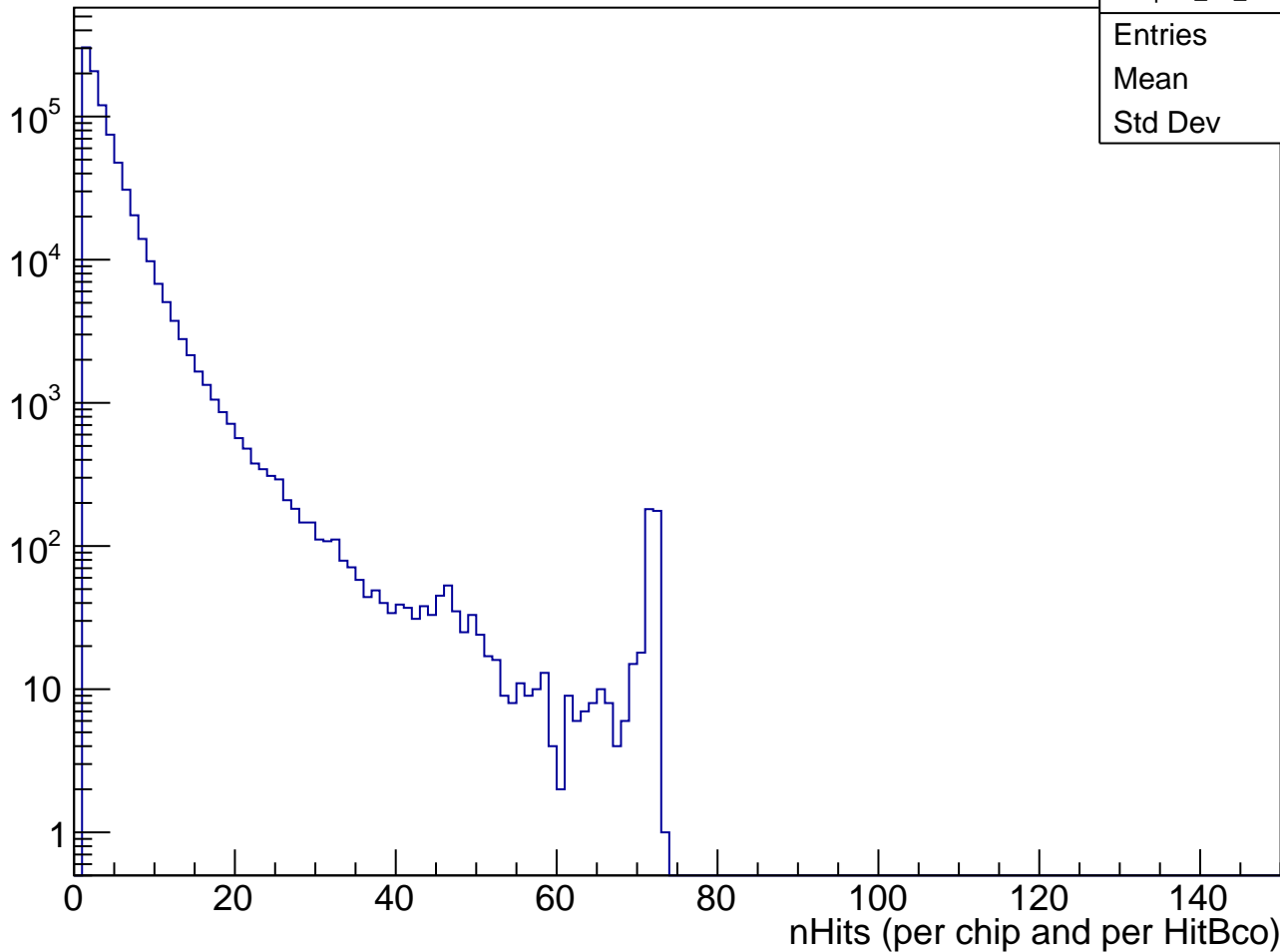
Mean 3.037

Std Dev 3.463

nHits (per chip and per HitBco)

nChipHit_F1_Fch13_Chip11

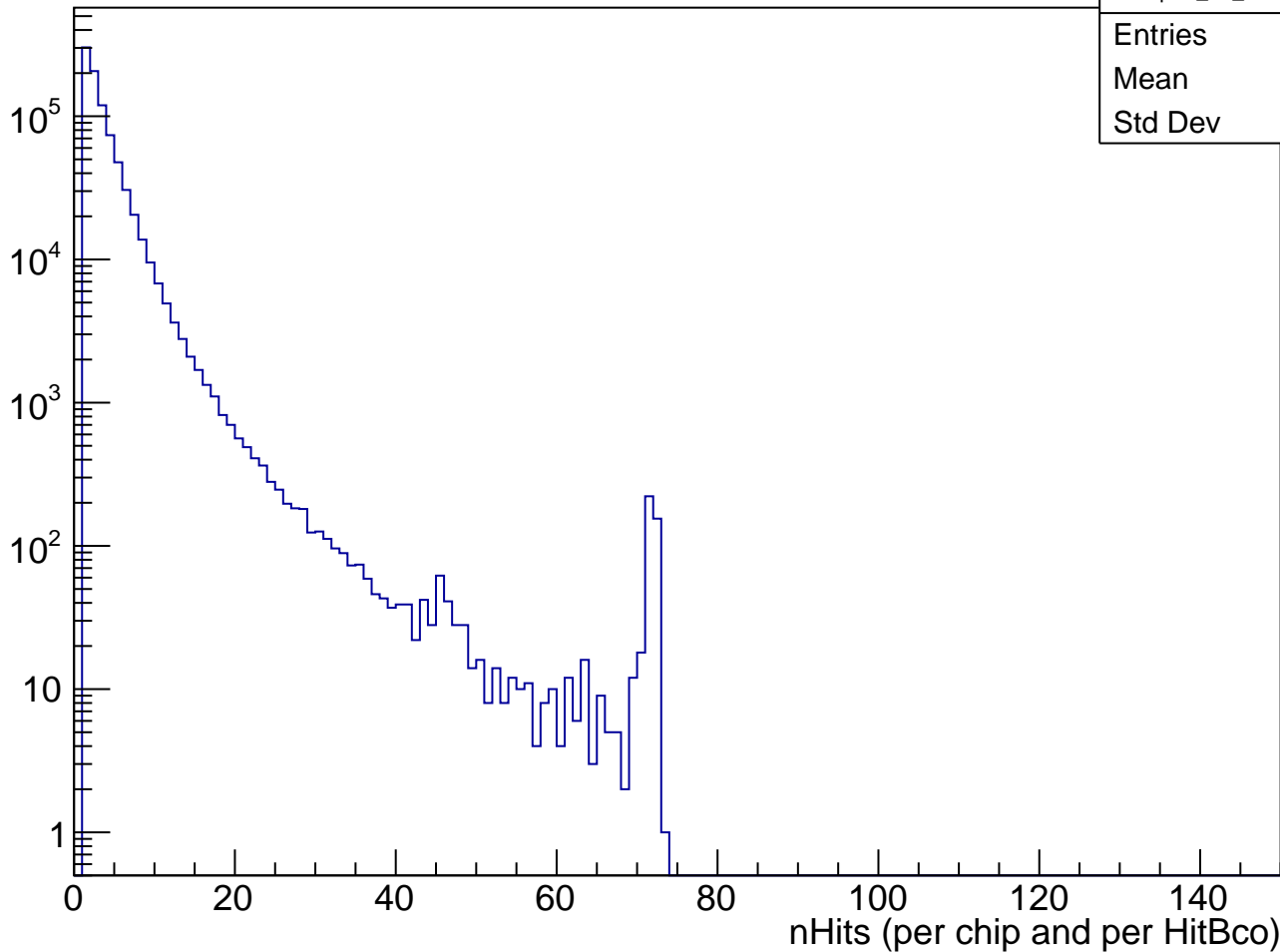
Entries



nChipHit_F1_Fch13_Chip11	
Entries	859444
Mean	3.048
Std Dev	3.489

nChipHit_F1_Fch13_Chip12

Entries



nChipHit_F1_Fch13_Chip12

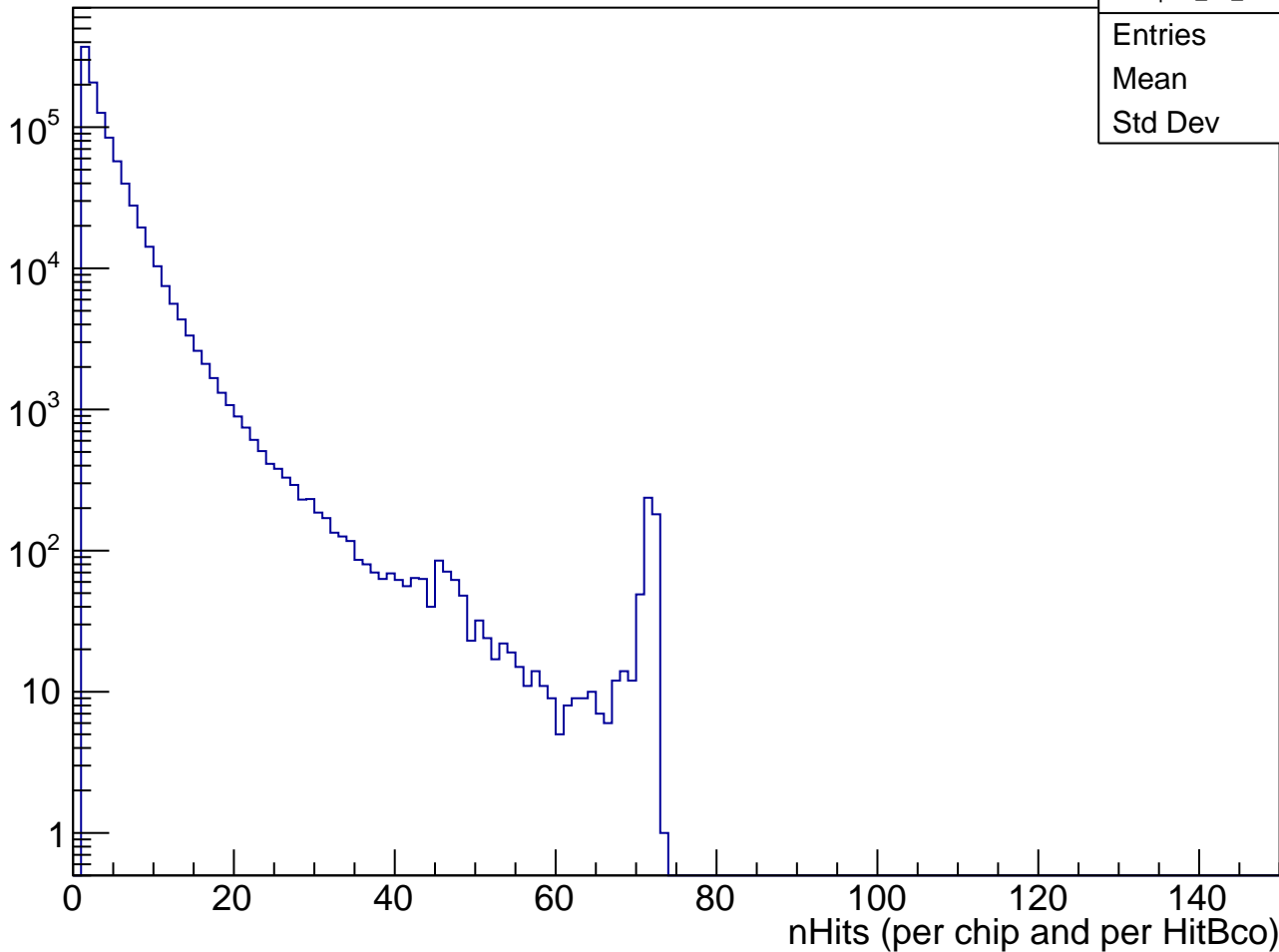
Entries 854586

Mean 3.046

Std Dev 3.494

nChipHit_F1_Fch13_Chip13

Entries



nChipHit_F1_Fch13_Chip13

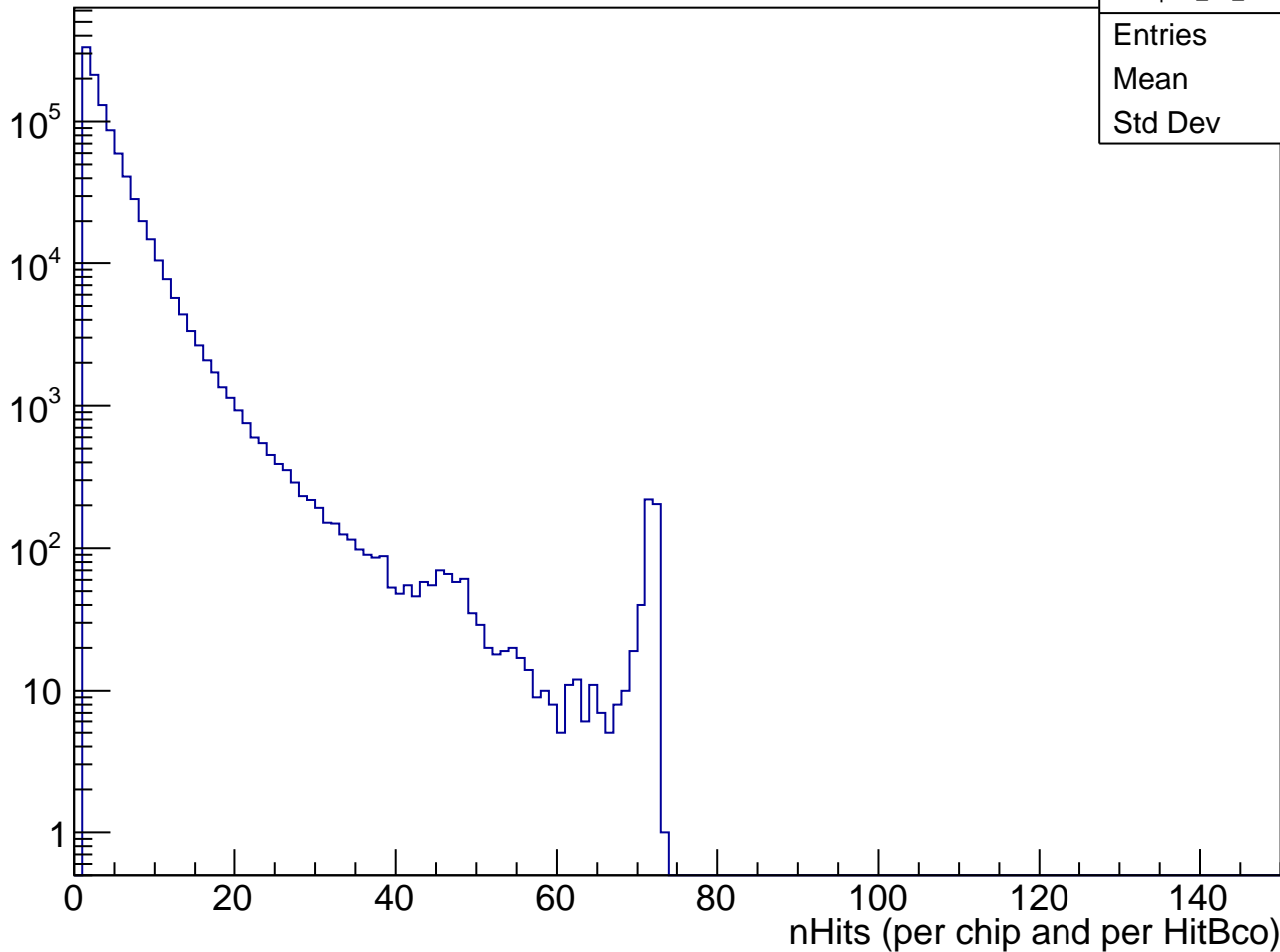
Entries 994055

Mean 3.244

Std Dev 3.829

nChipHit_F1_Fch13_Chip14

Entries



nChipHit_F1_Fch13_Chip14

Entries 973544

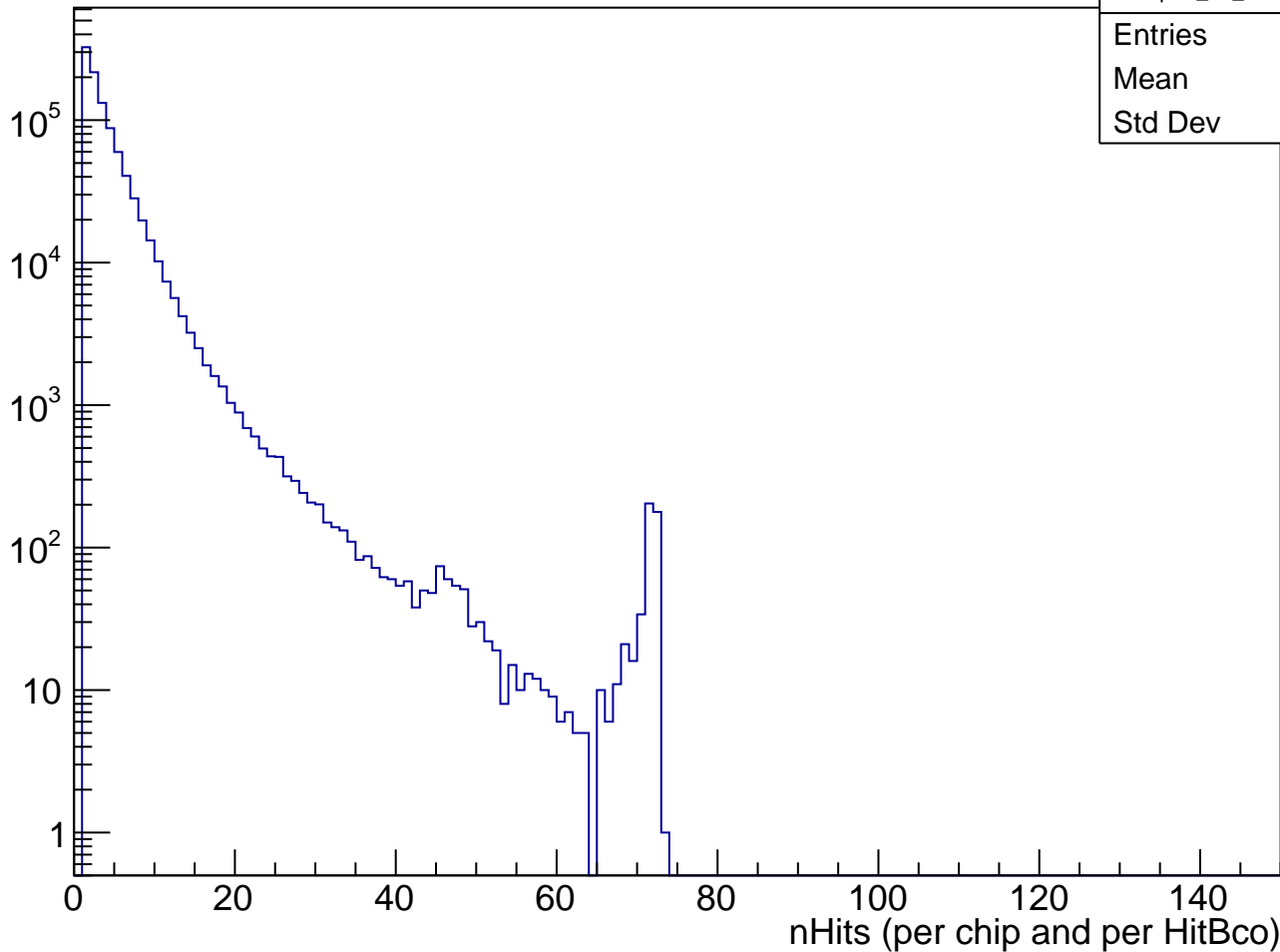
Mean 3.352

Std Dev 3.865

nHits (per chip and per HitBco)

nChipHit_F1_Fch13_Chip15

Entries



nChipHit_F1_Fch13_Chip15

Entries 970422

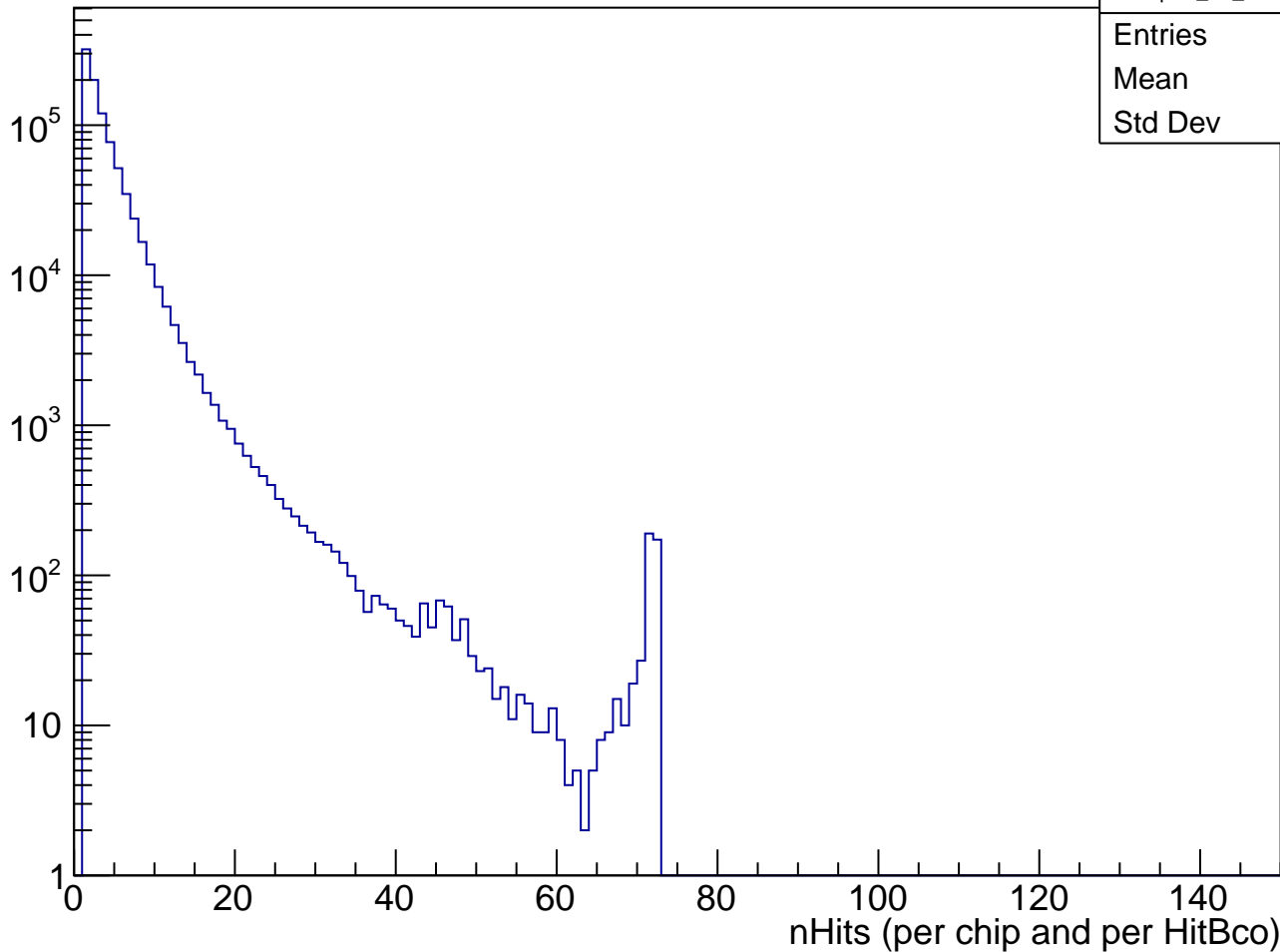
Mean 3.332

Std Dev 3.781

nHits (per chip and per HitBco)

nChipHit_F1_Fch13_Chip16

Entries



nChipHit_F1_Fch13_Chip16

Entries 894933

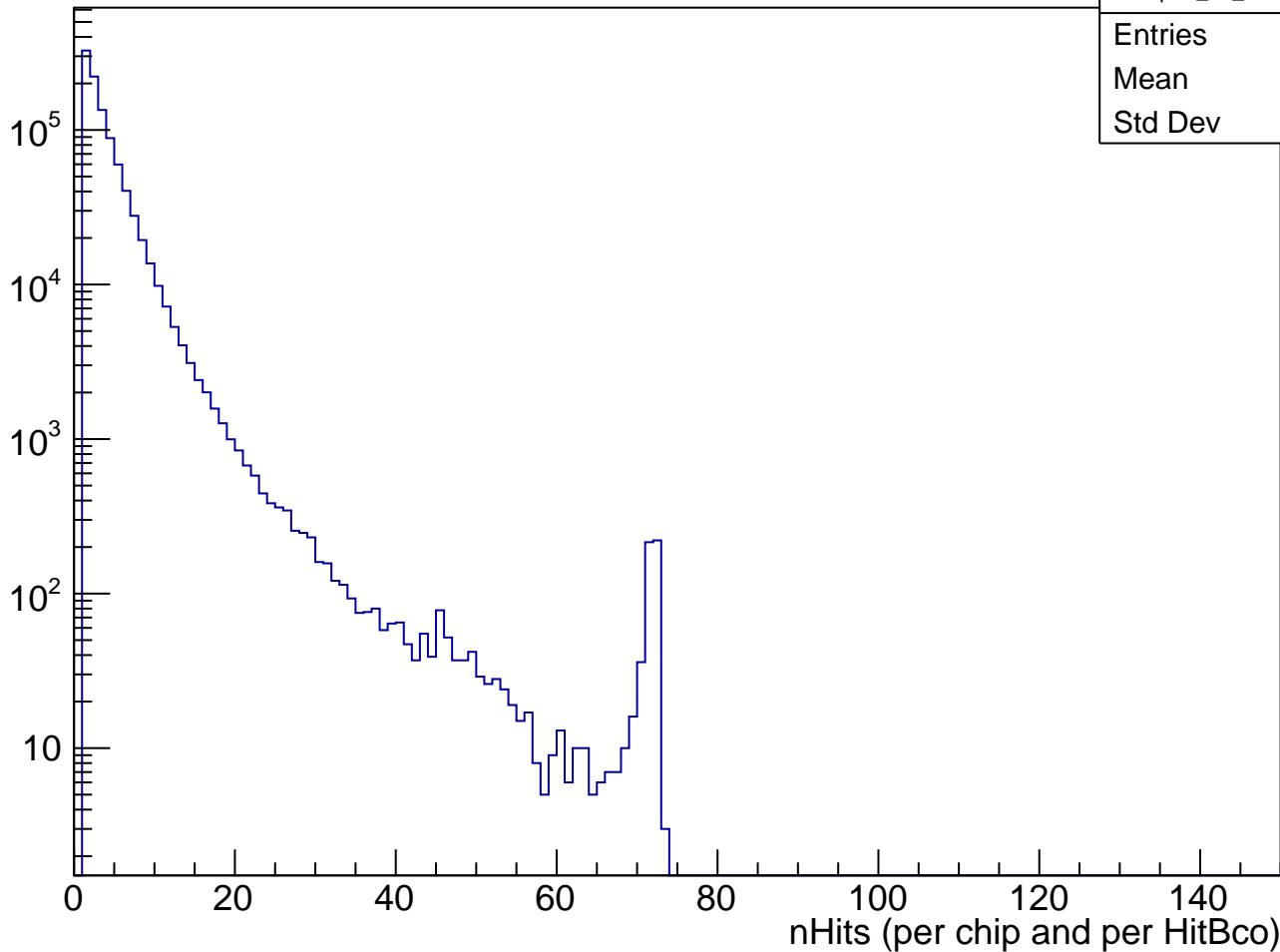
Mean 3.199

Std Dev 3.737

nHits (per chip and per HitBco)

nChipHit_F1_Fch13_Chip17

Entries



nChipHit_F1_Fch13_Chip17

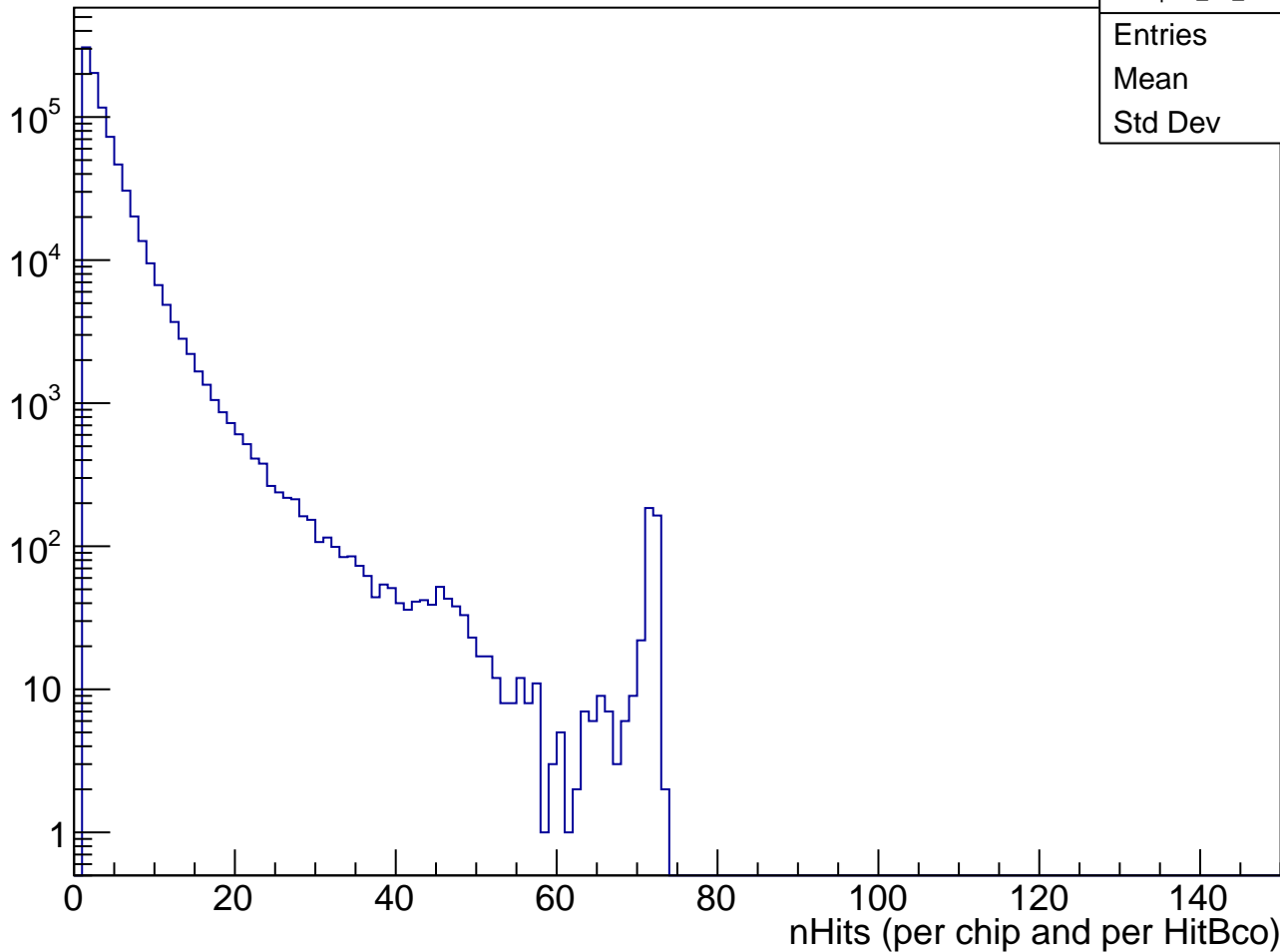
Entries 976975

Mean 3.299

Std Dev 3.768

nChipHit_F1_Fch13_Chip18

Entries



nChipHit_F1_Fch13_Chip18

Entries 850534

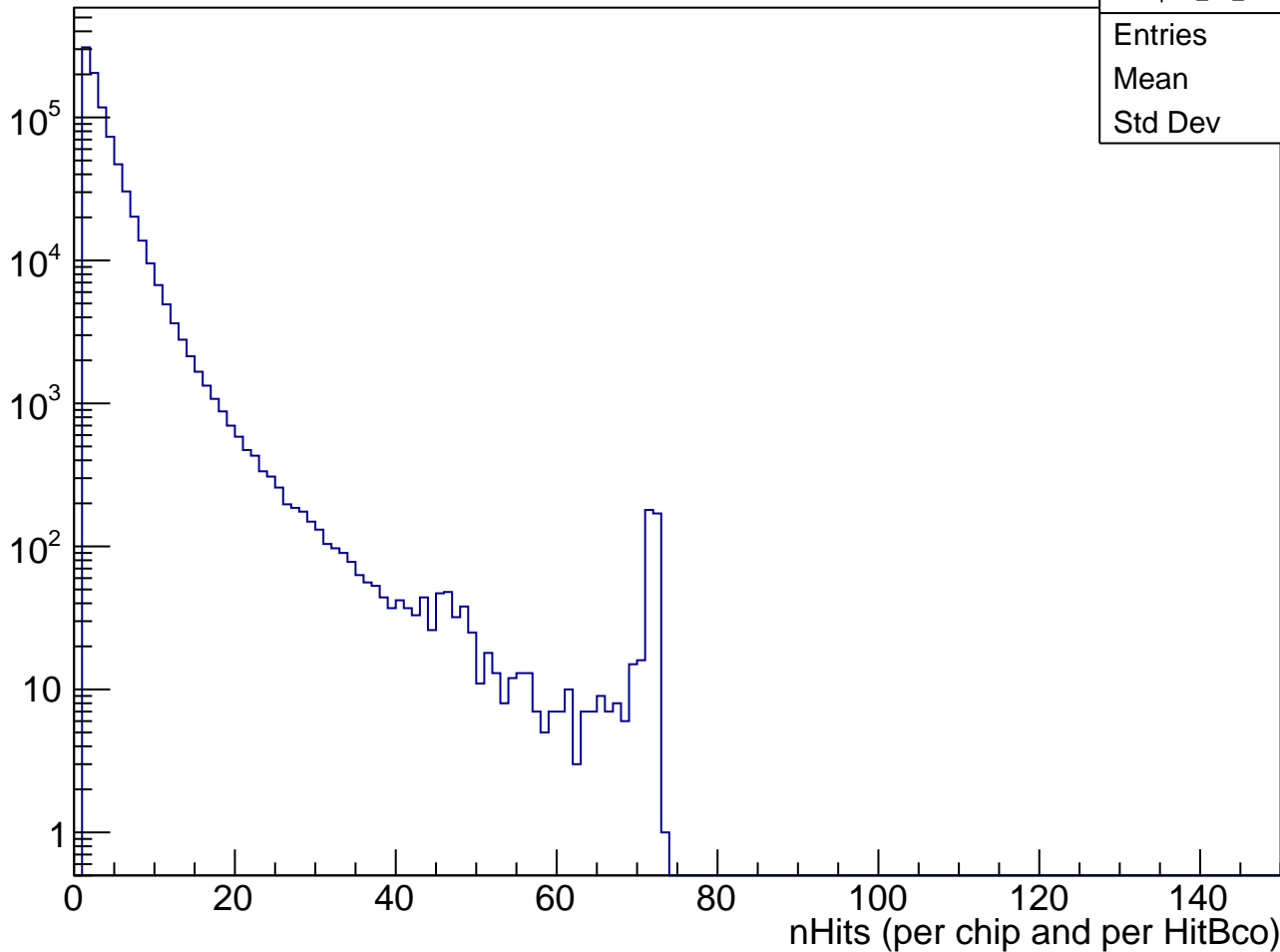
Mean 3.037

Std Dev 3.501

nHits (per chip and per HitBco)

nChipHit_F1_Fch13_Chip19

Entries



nChipHit_F1_Fch13_Chip19

Entries 855958

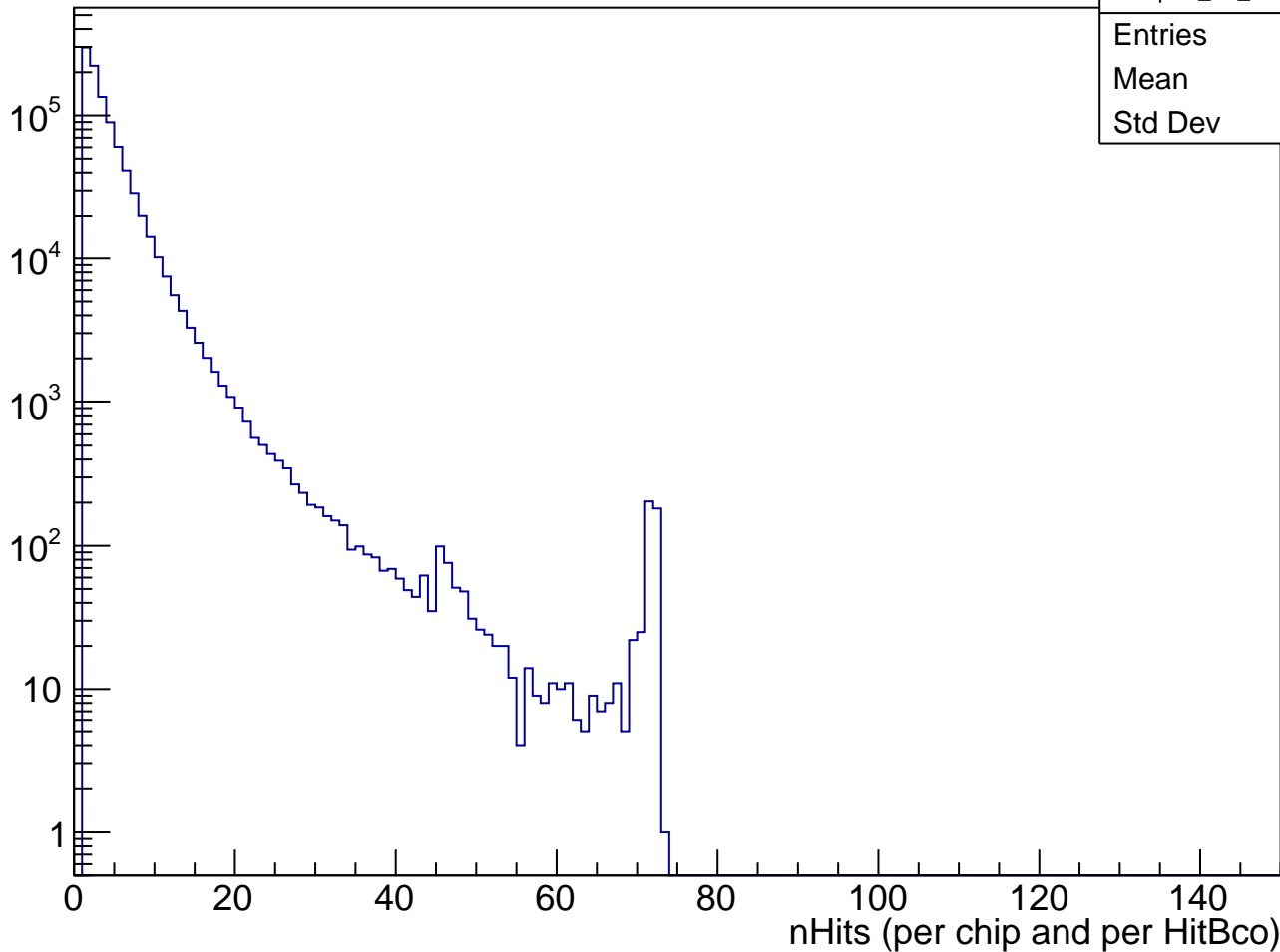
Mean 3.031

Std Dev 3.494

nHits (per chip and per HitBco)

nChipHit_F1_Fch13_Chip2

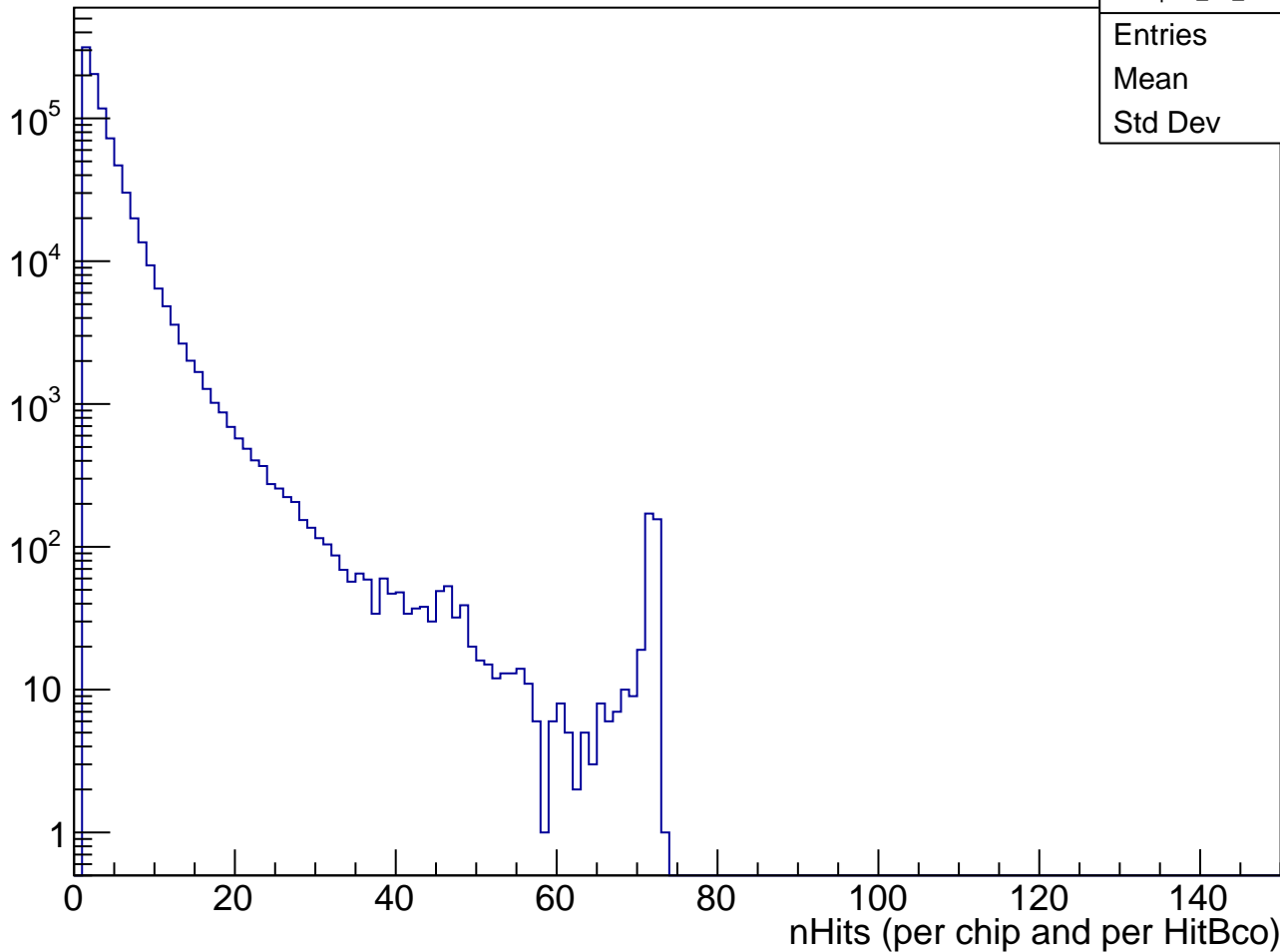
Entries



nChipHit_F1_Fch13_Chip2	
Entries	954815
Mean	3.406
Std Dev	3.812

nChipHit_F1_Fch13_Chip20

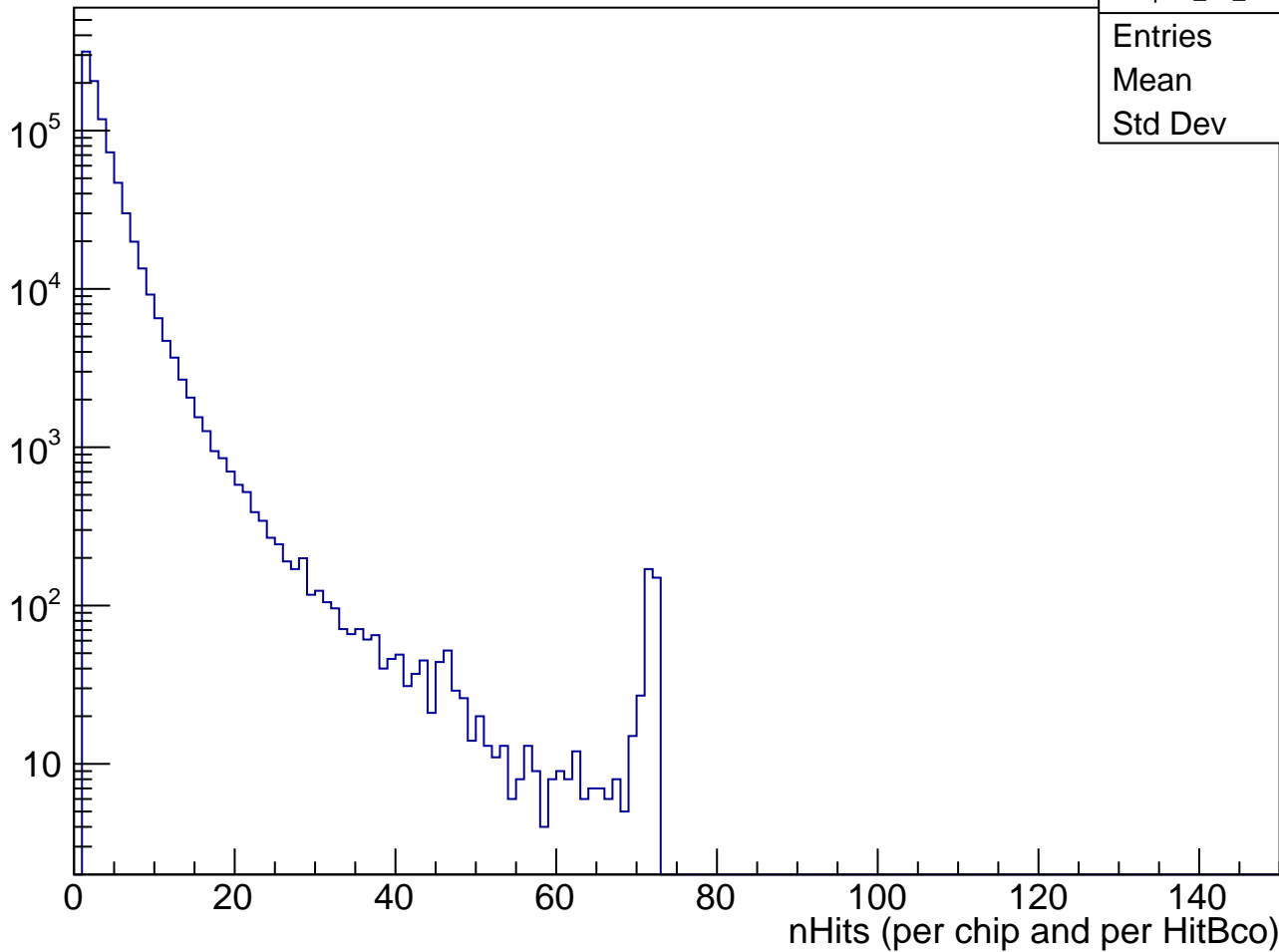
Entries



nChipHit_F1_Fch13_Chip20	
Entries	858308
Mean	2.999
Std Dev	3.444

nChipHit_F1_Fch13_Chip21

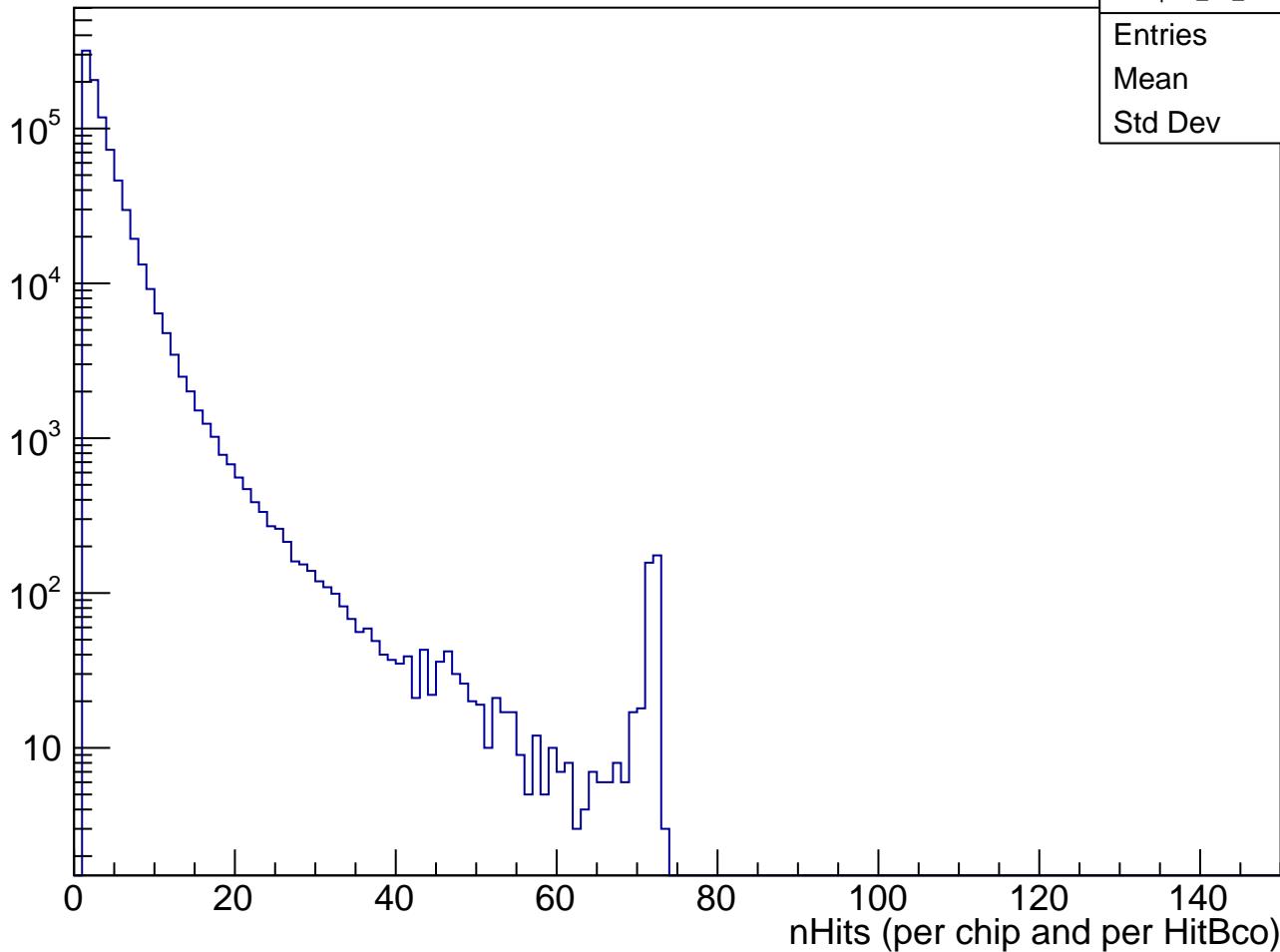
Entries



nChipHit_F1_Fch13_Chip21	
Entries	860951
Mean	2.991
Std Dev	3.436

nChipHit_F1_Fch13_Chip22

Entries



nChipHit_F1_Fch13_Chip22

Entries 861370

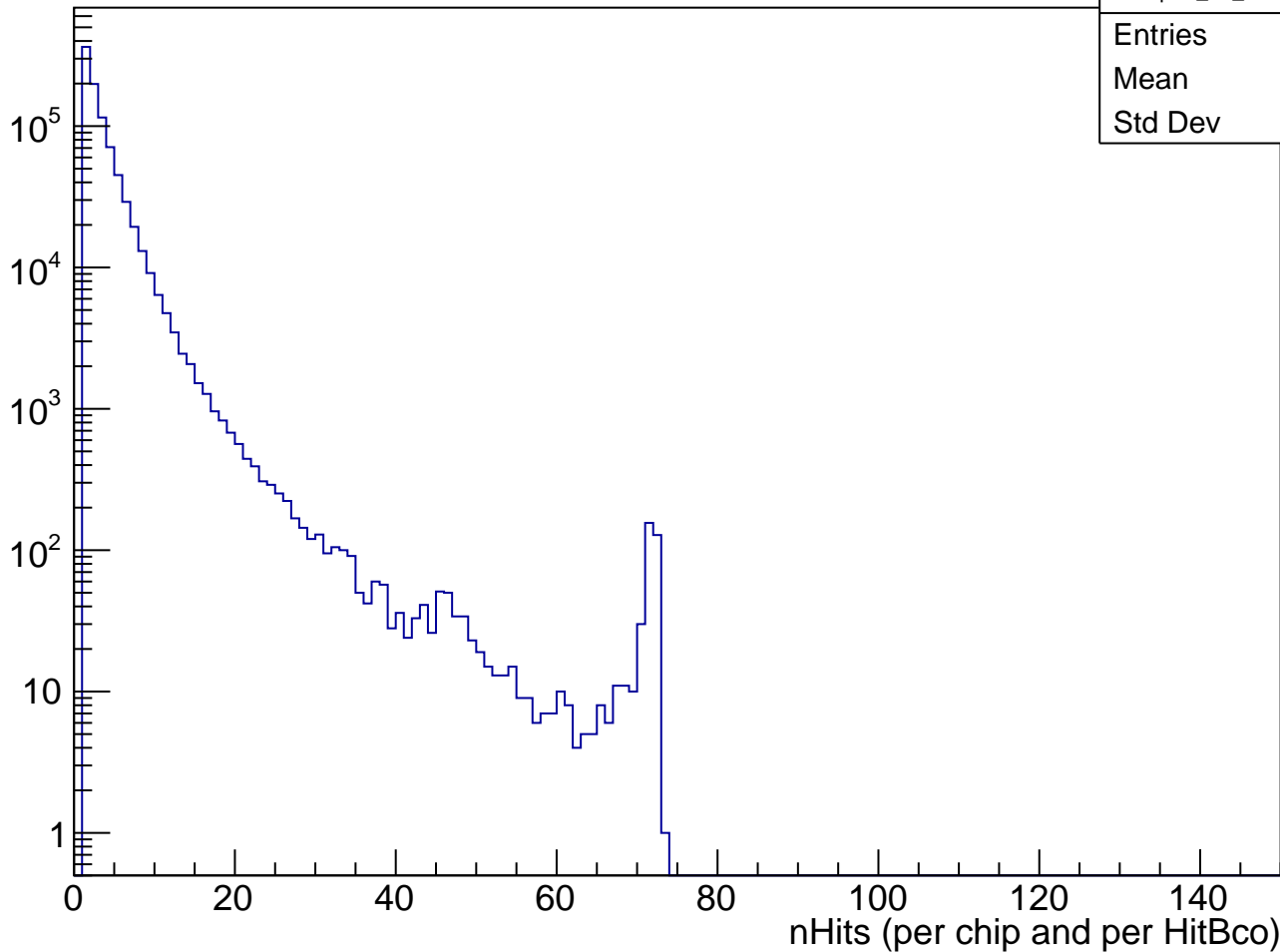
Mean 2.968

Std Dev 3.412

nHits (per chip and per HitBco)

nChipHit_F1_Fch13_Chip23

Entries



nChipHit_F1_Fch13_Chip23

Entries 892793

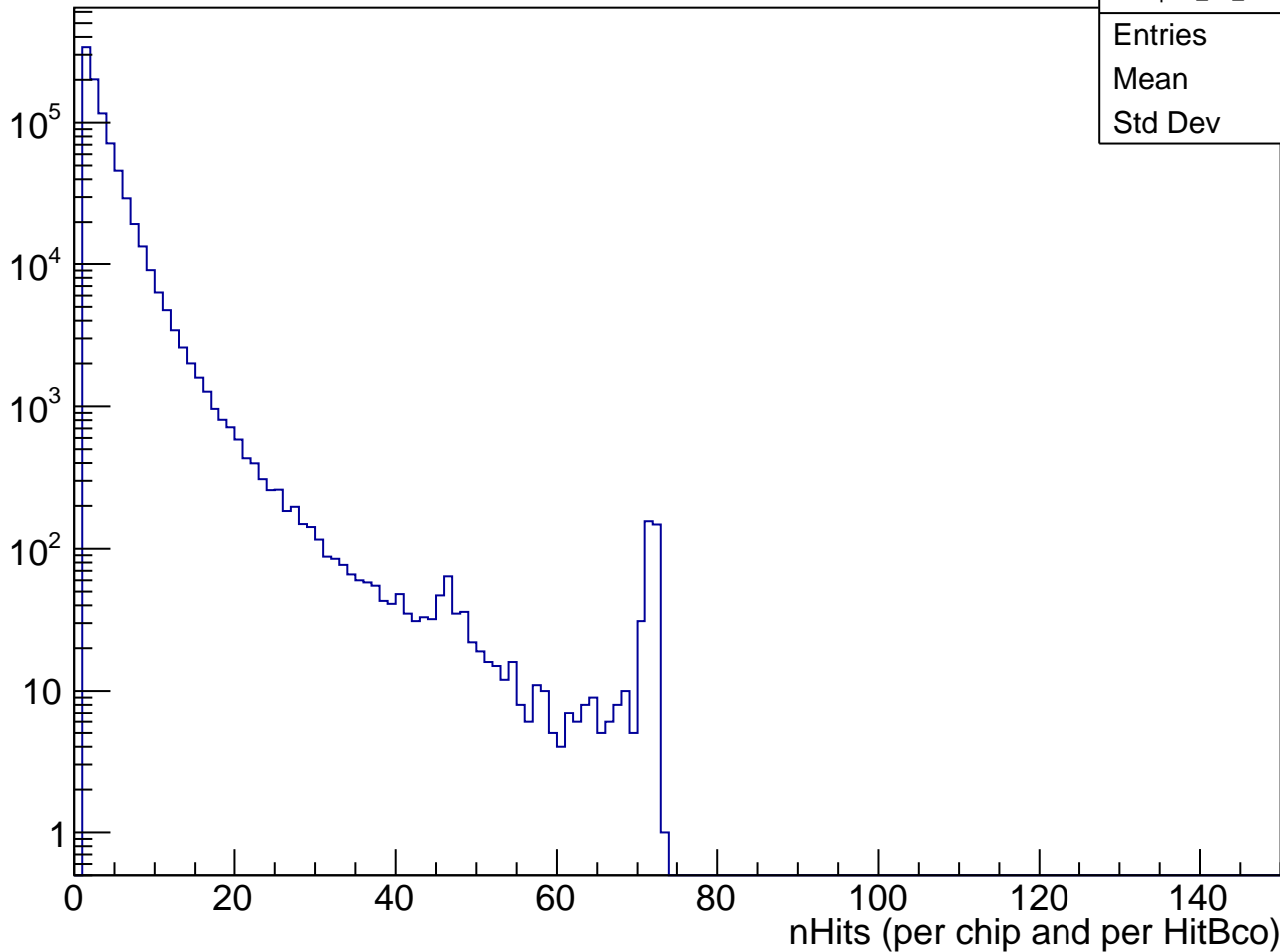
Mean 2.868

Std Dev 3.359

nHits (per chip and per HitBco)

nChipHit_F1_Fch13_Chip24

Entries



nChipHit_F1_Fch13_Chip24

Entries 874218

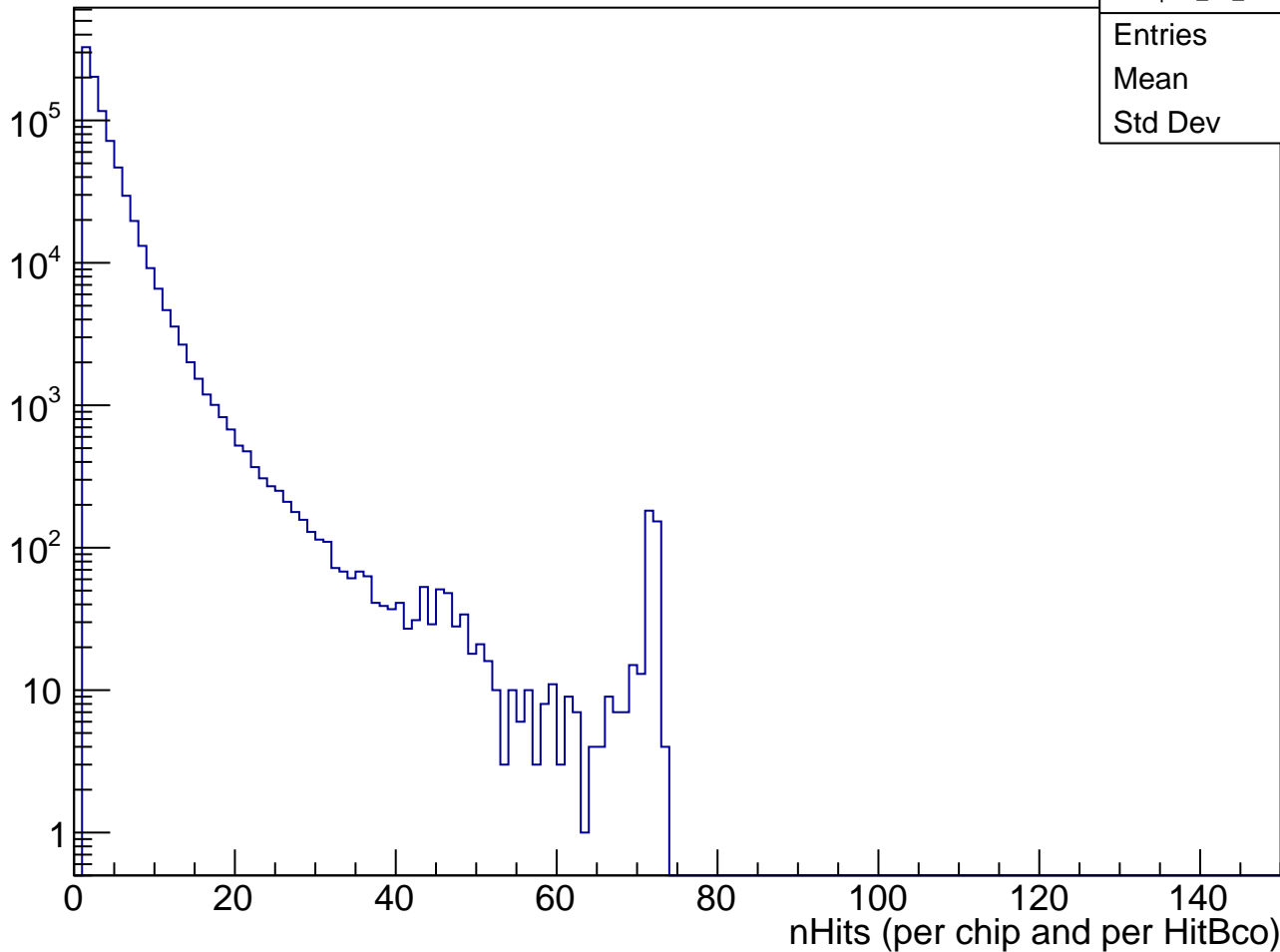
Mean 2.923

Std Dev 3.397

nHits (per chip and per HitBco)

nChipHit_F1_Fch13_Chip25

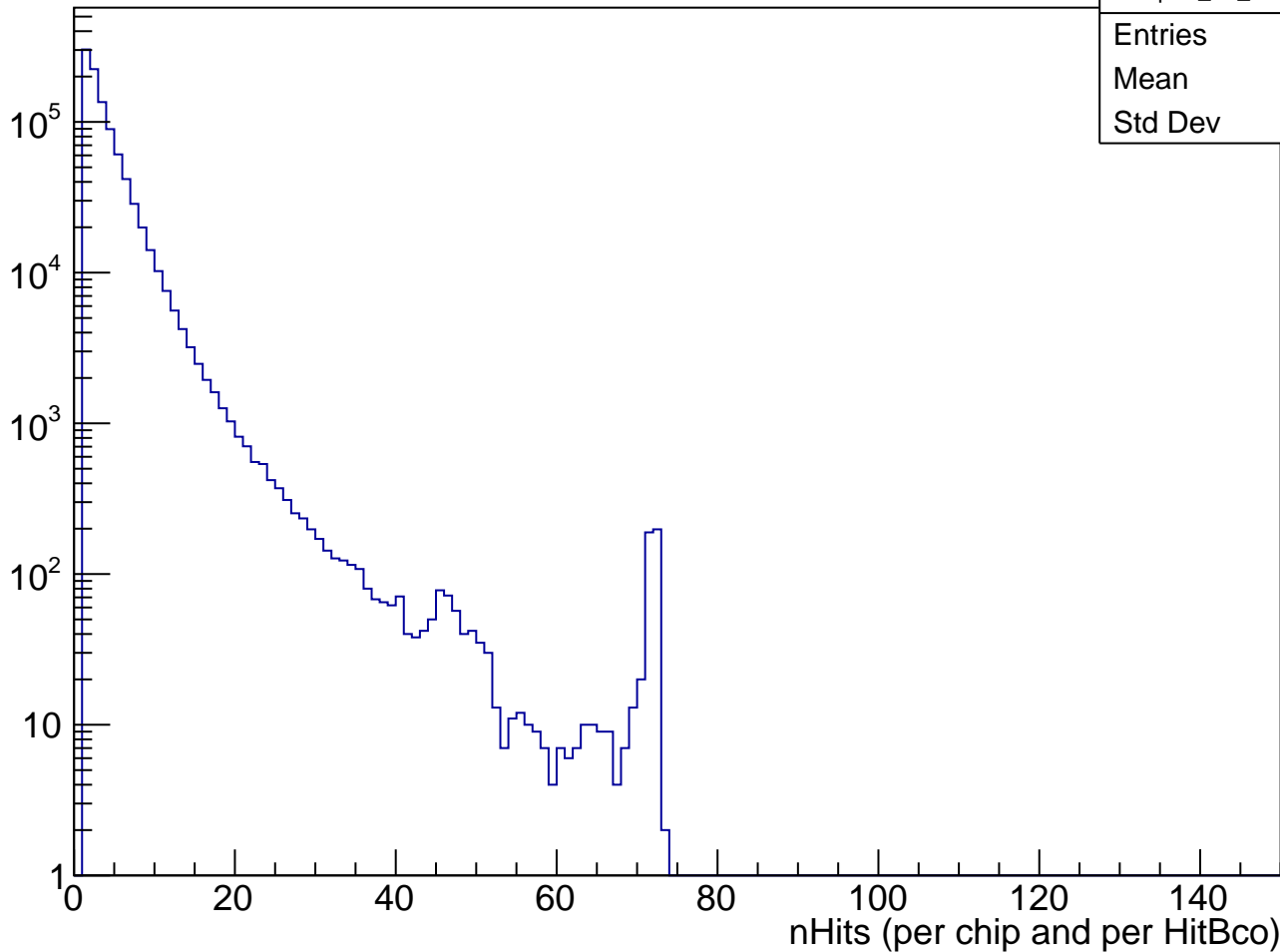
Entries



nChipHit_F1_Fch13_Chip25	
Entries	864568
Mean	2.953
Std Dev	3.402

nChipHit_F1_Fch13_Chip3

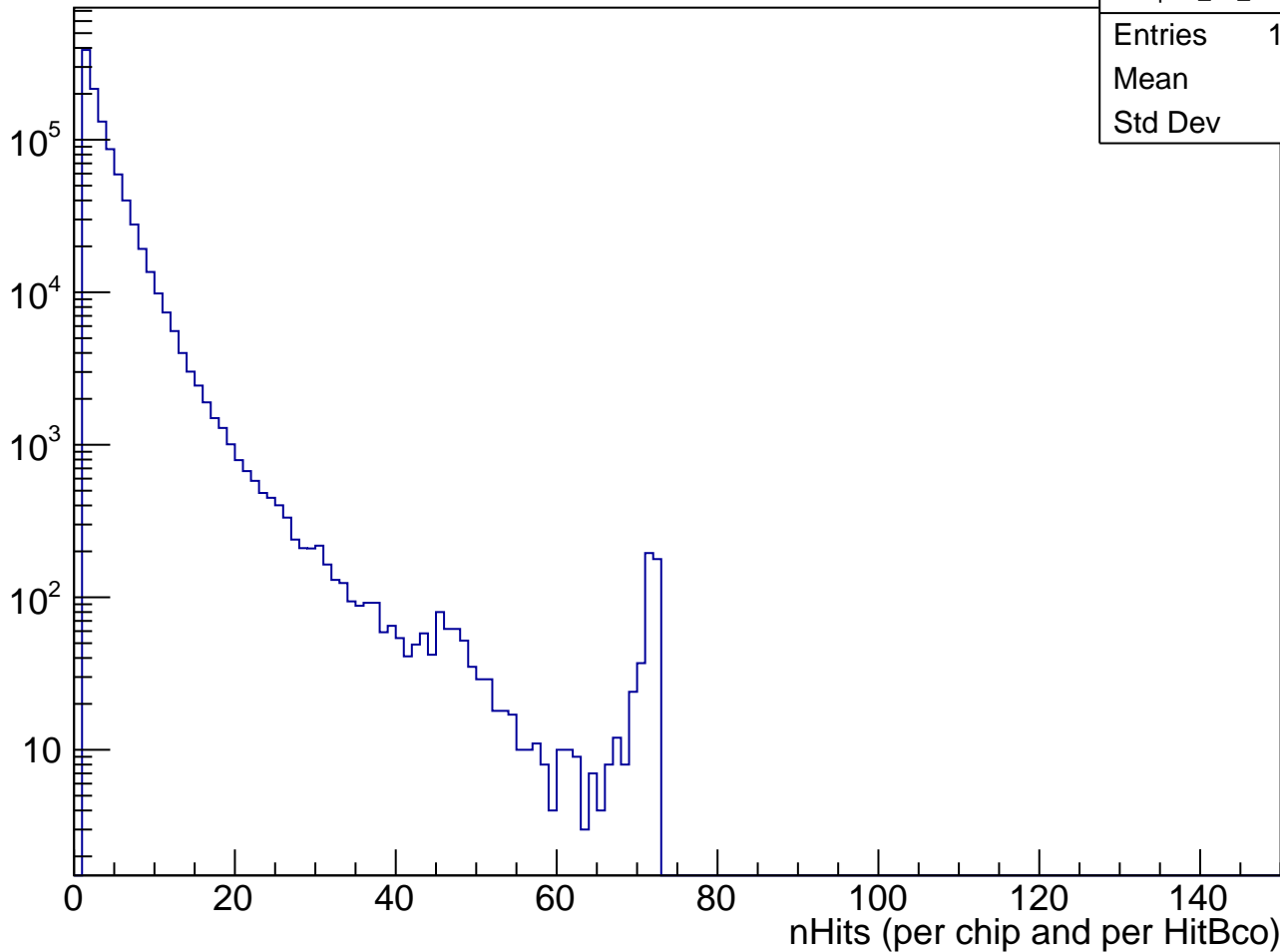
Entries



nChipHit_F1_Fch13_Chip3	
Entries	962626
Mean	3.377
Std Dev	3.757

nChipHit_F1_Fch13_Chip4

Entries

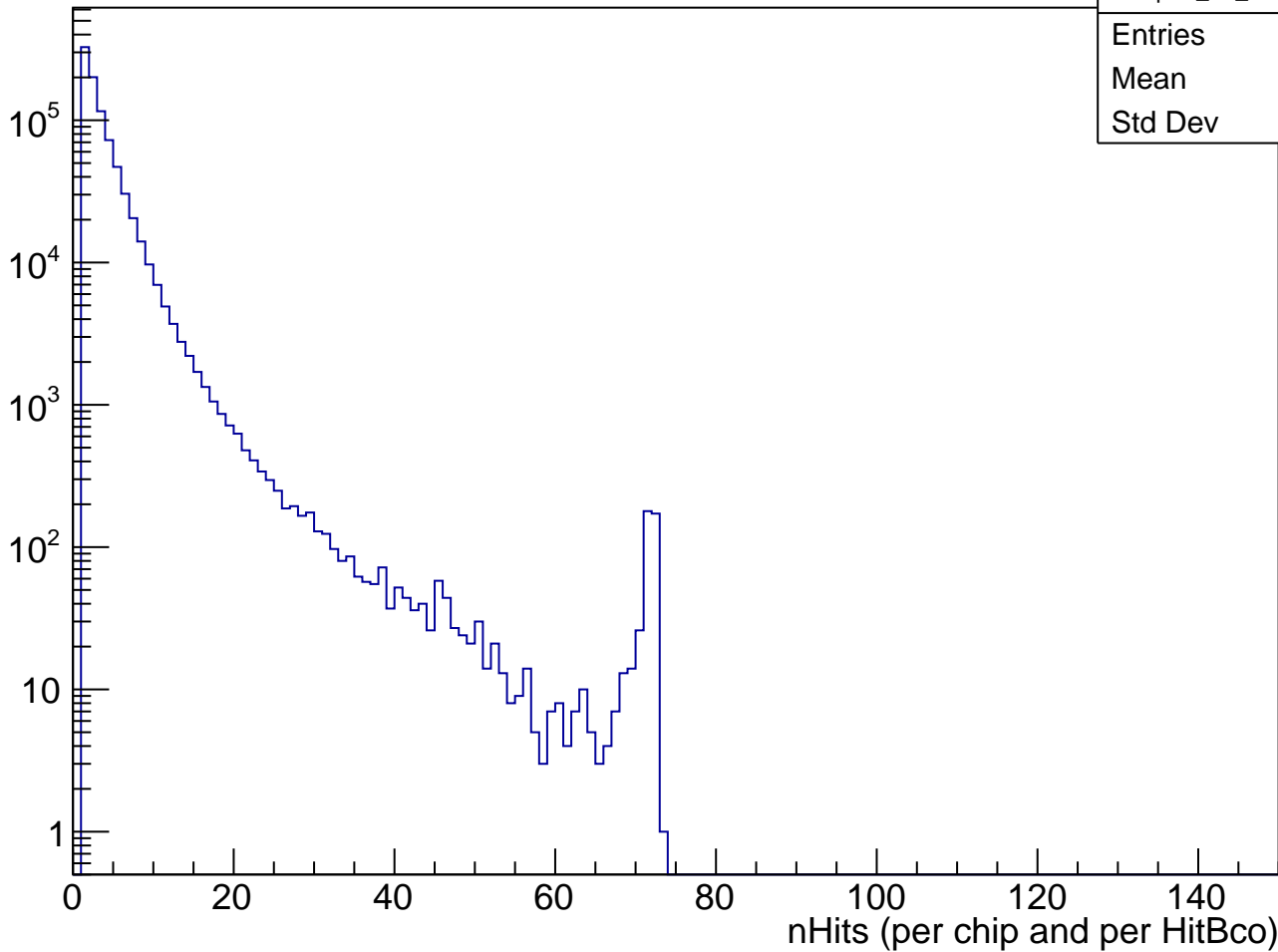


nChipHit_F1_Fch13_Chip4	
Entries	1026158
Mean	3.167
Std Dev	3.693

nHits (per chip and per HitBco)

nChipHit_F1_Fch13_Chip5

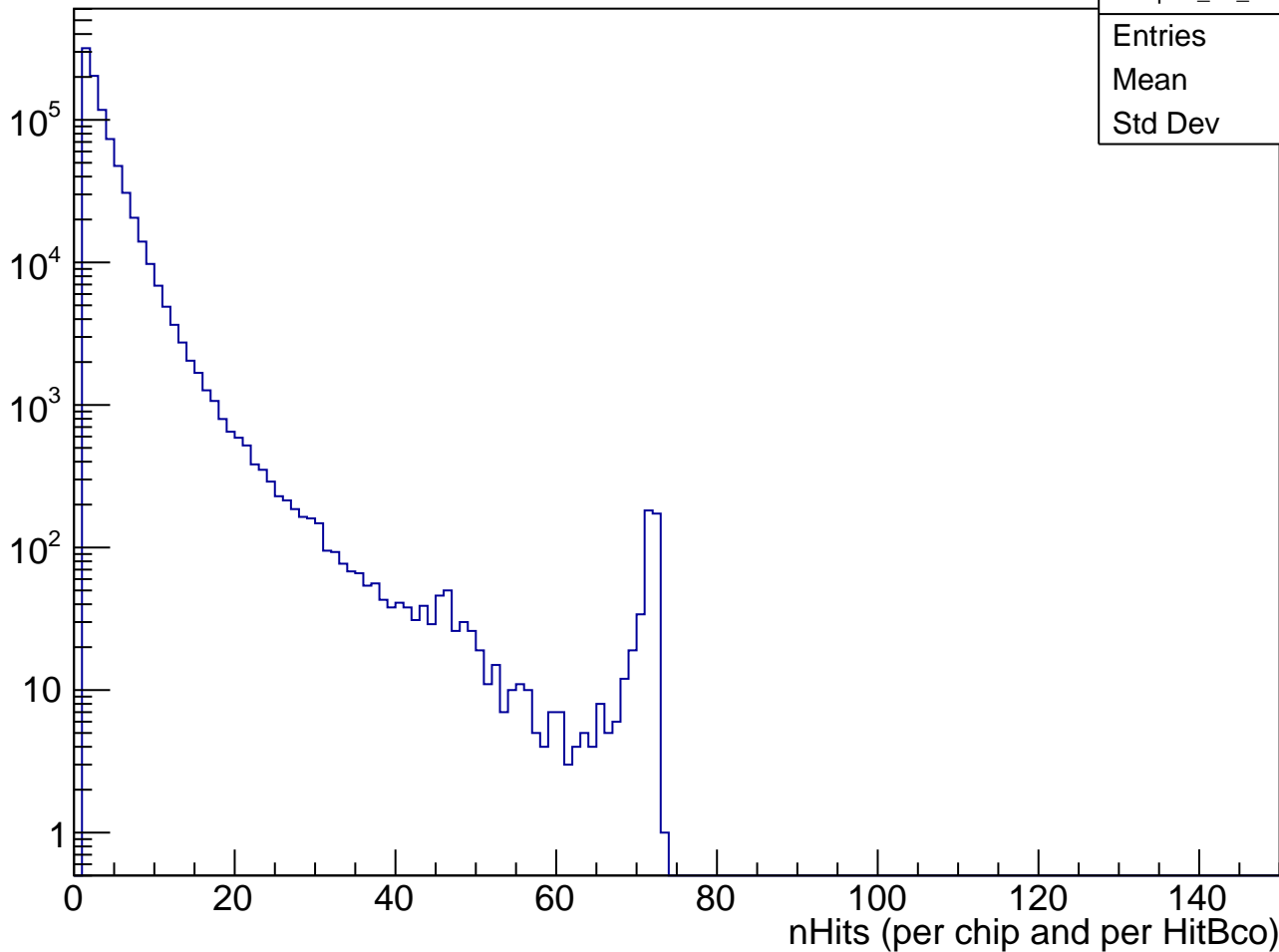
Entries



nChipHit_F1_Fch13_Chip5	
Entries	868140
Mean	3.005
Std Dev	3.507

nChipHit_F1_Fch13_Chip6

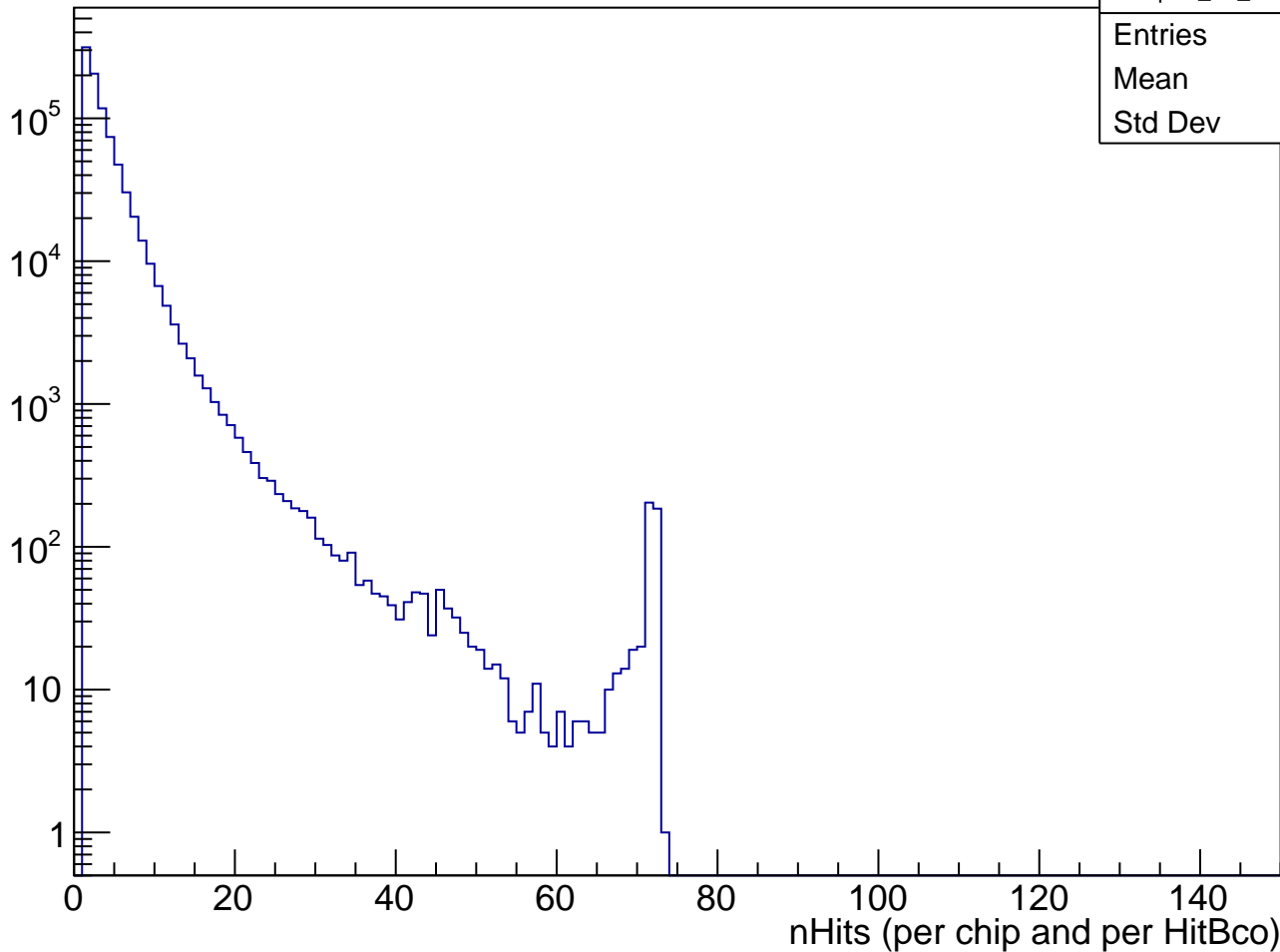
Entries



nChipHit_F1_Fch13_Chip6	
Entries	865687
Mean	3.013
Std Dev	3.478

nChipHit_F1_Fch13_Chip7

Entries

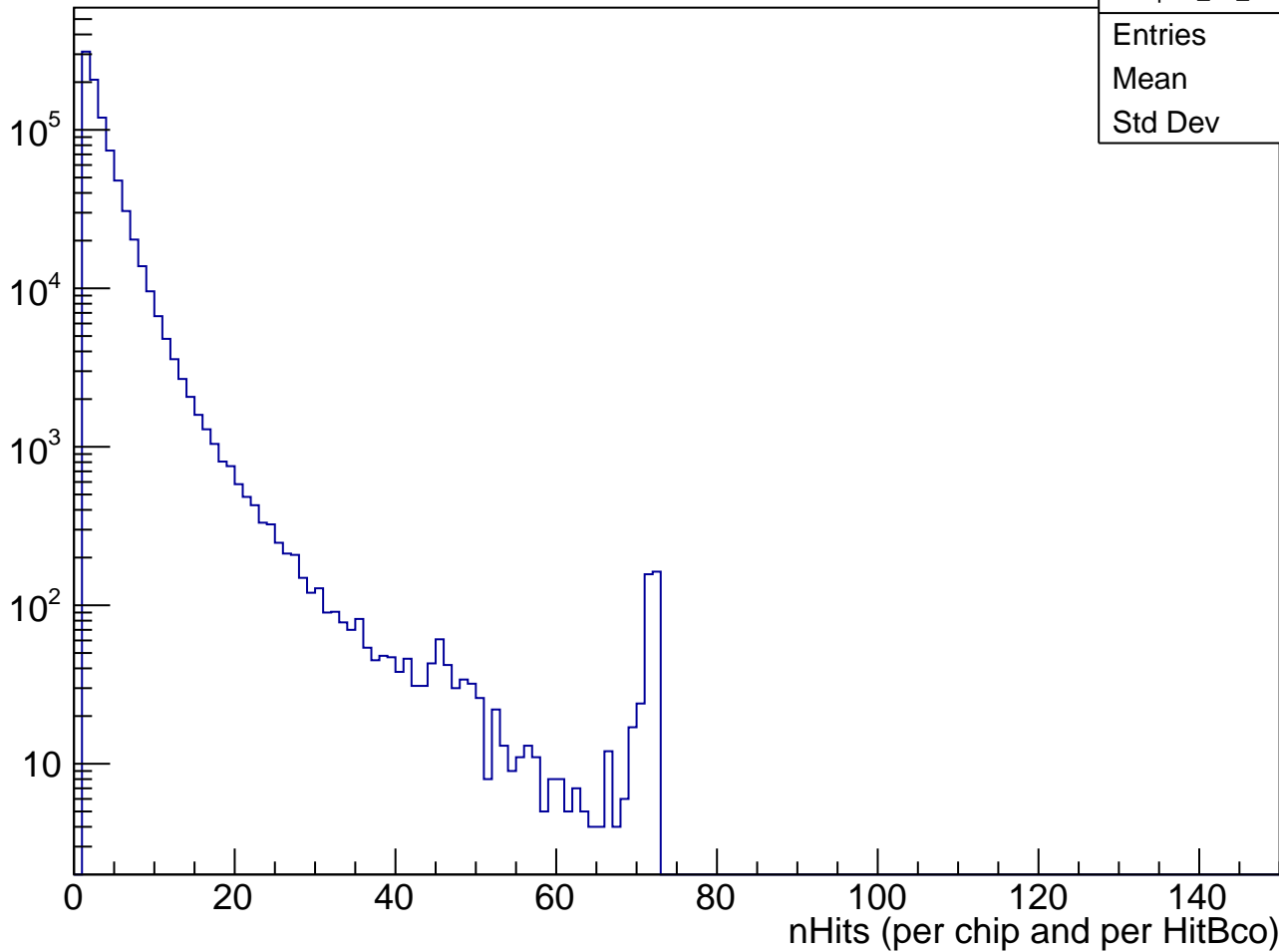


nChipHit_F1_Fch13_Chip7	
Entries	863309
Mean	3.014
Std Dev	3.494

nHits (per chip and per HitBco)

nChipHit_F1_Fch13_Chip8

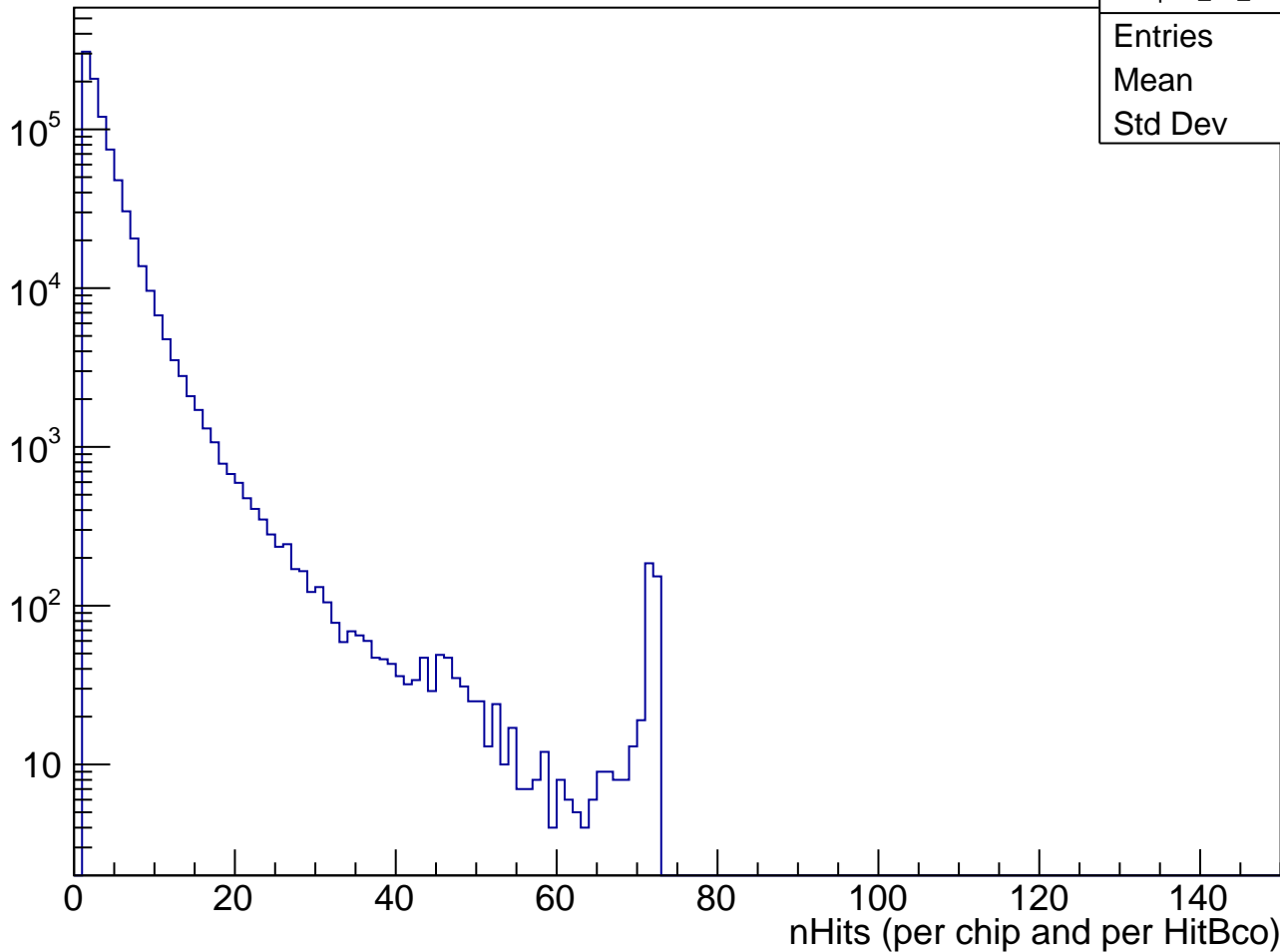
Entries



nChipHit_F1_Fch13_Chip8	
Entries	864619
Mean	3.018
Std Dev	3.457

nChipHit_F1_Fch13_Chip9

Entries

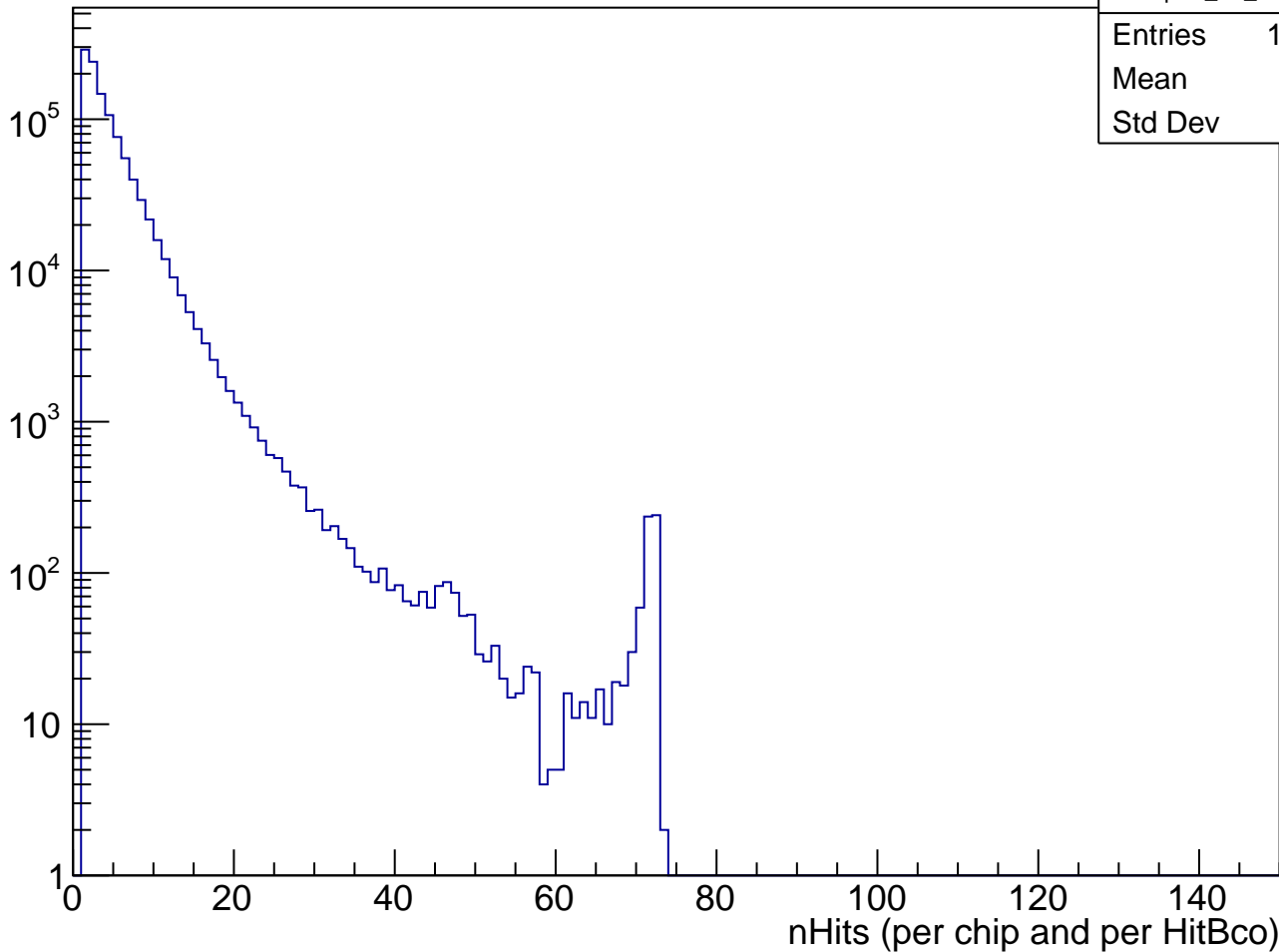


nChipHit_F1_Fch13_Chip9	
Entries	863551
Mean	3.025
Std Dev	3.456

nHits (per chip and per HitBco)

nChipHit_F1_Fch1_Chip0

Entries



nChipHit_F1_Fch1_Chip0

Entries 1076877

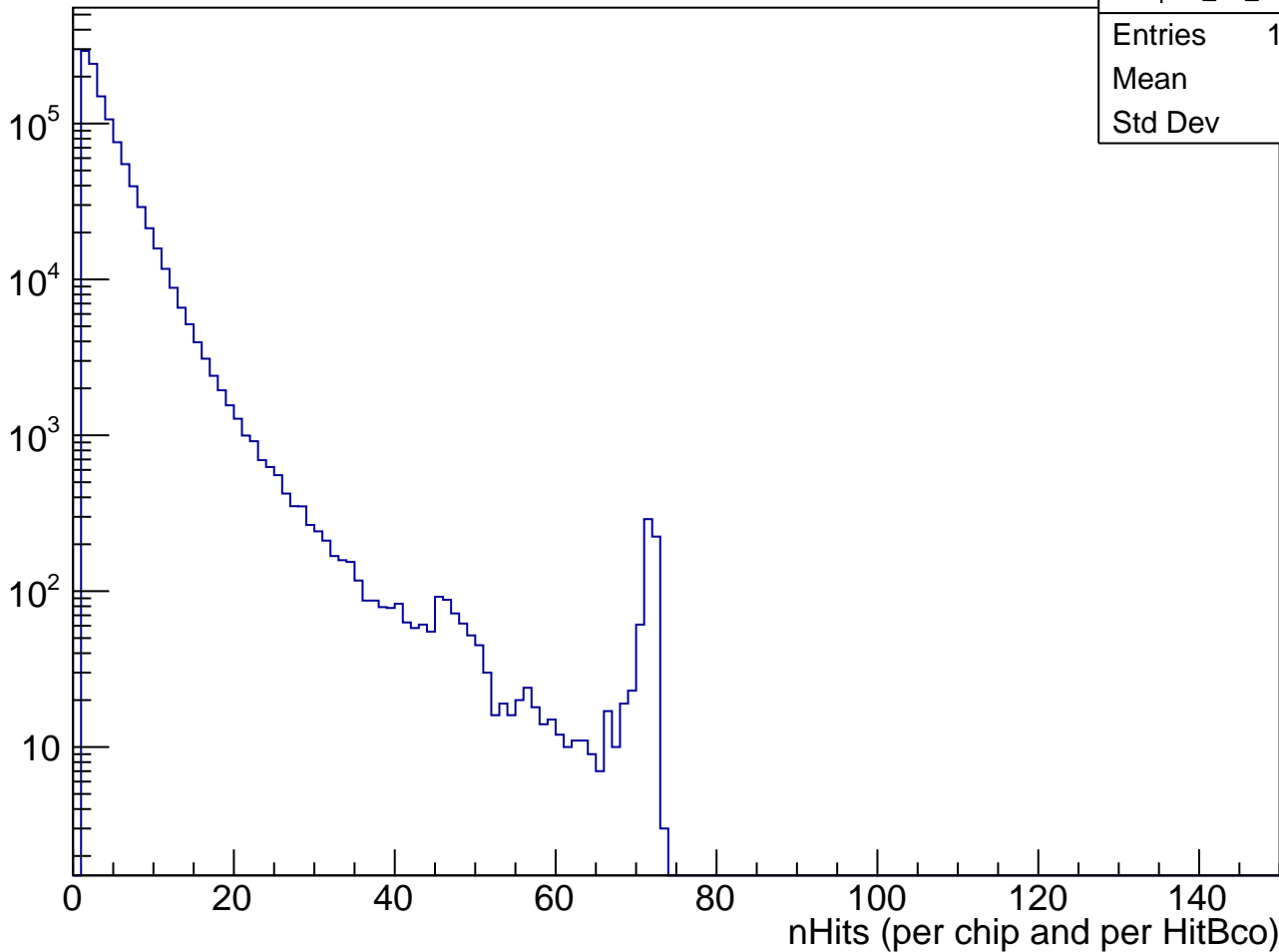
Mean 3.841

Std Dev 4.169

nHits (per chip and per HitBco)

nChipHit_F1_Fch1_Chip1

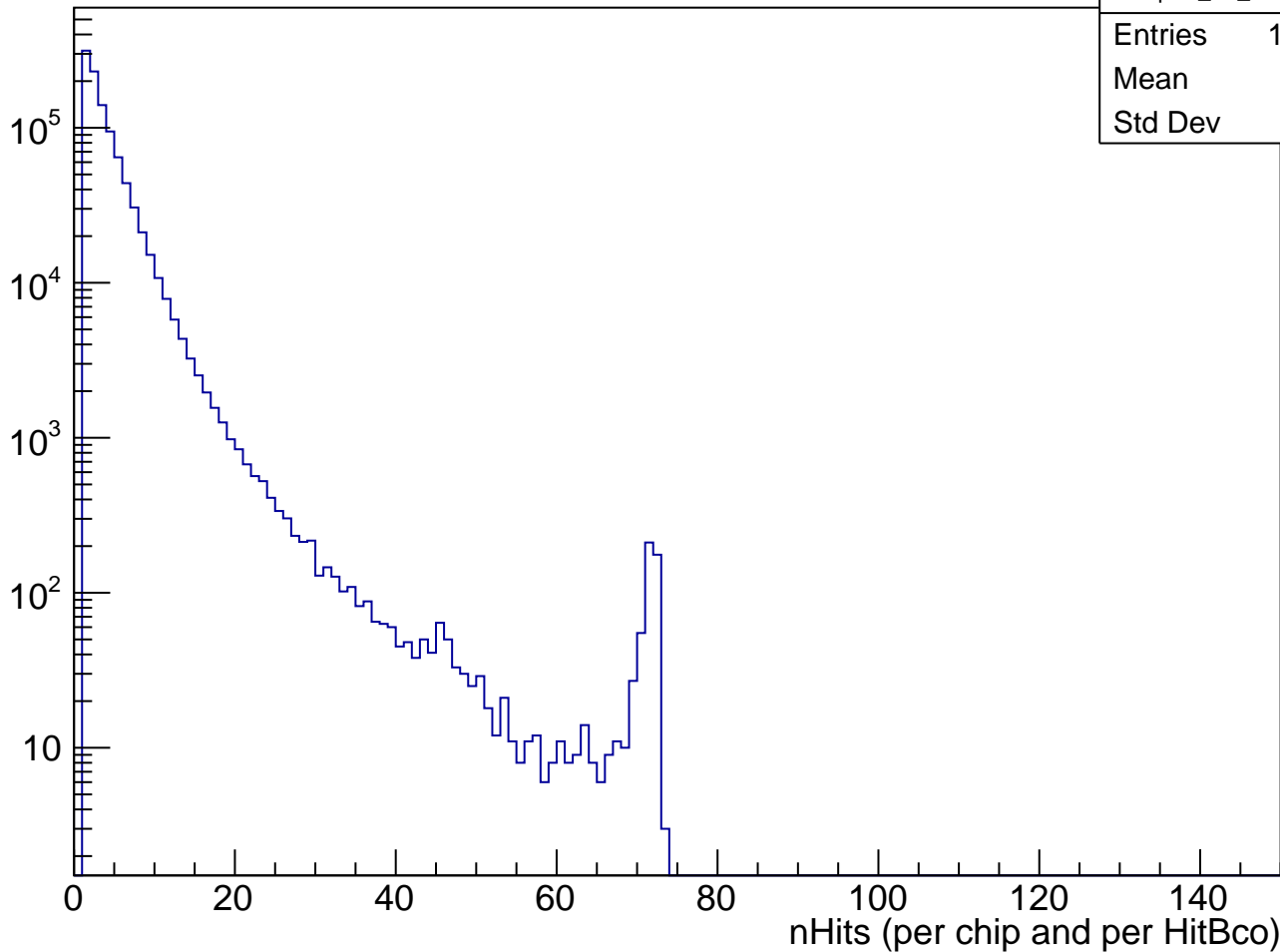
Entries



nChipHit_F1_Fch1_Chip1	
Entries	1081172
Mean	3.804
Std Dev	4.143

nChipHit_F1_Fch1_Chip10

Entries

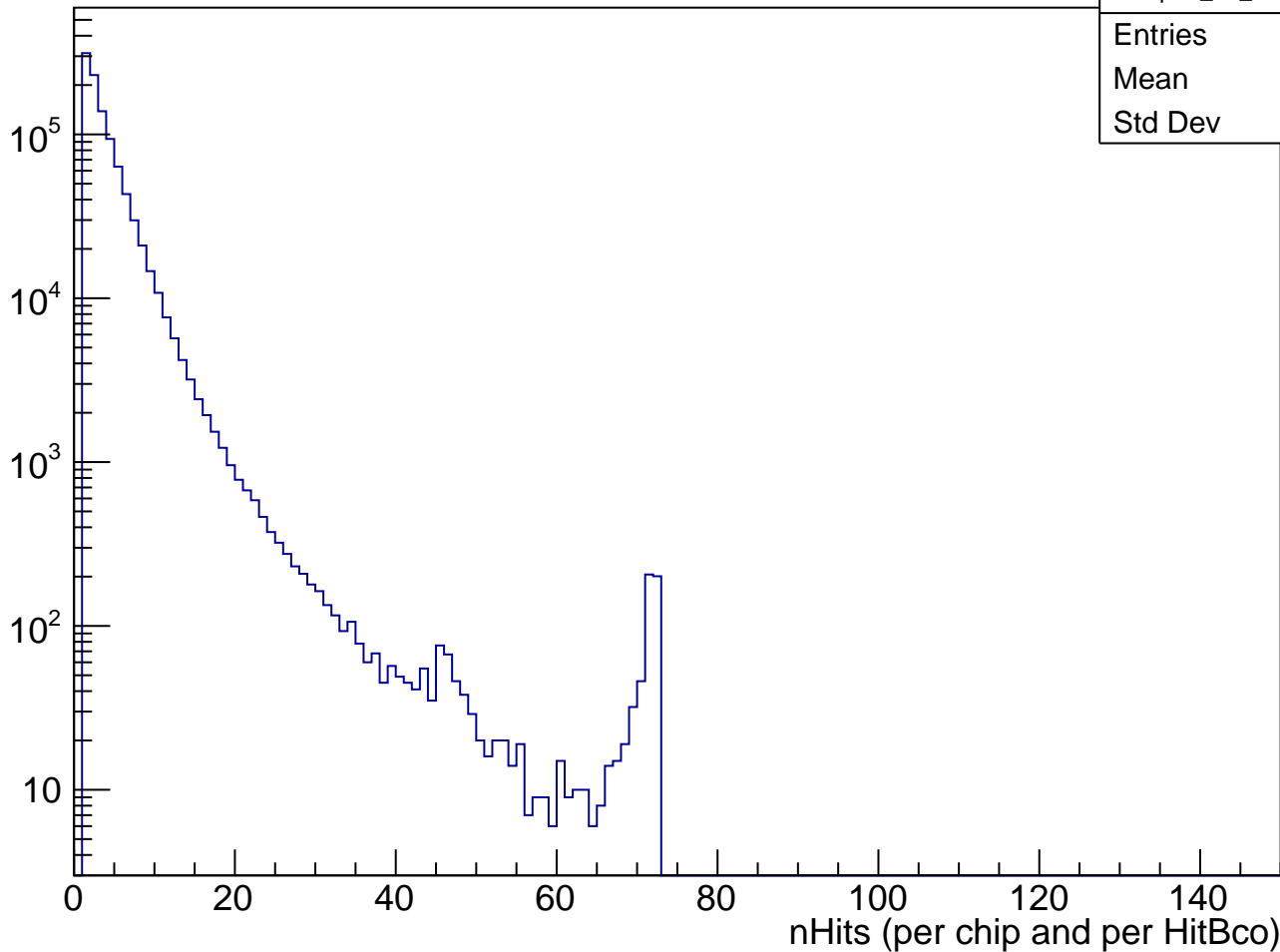


nChipHit_F1_Fch1_Chip10	
Entries	1002129
Mean	3.374
Std Dev	3.713

nHits (per chip and per HitBco)

nChipHit_F1_Fch1_Chip11

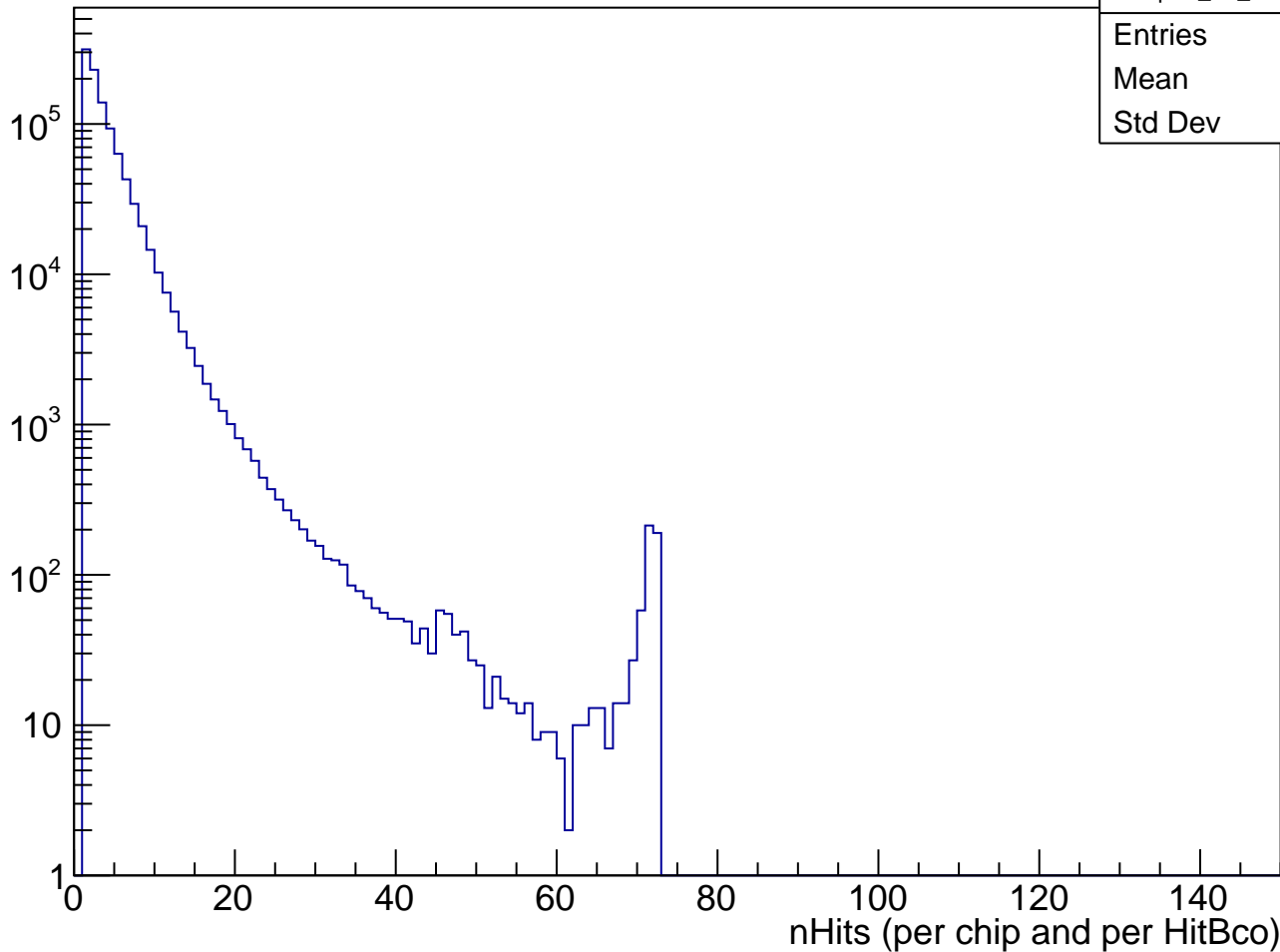
Entries



nChipHit_F1_Fch1_Chip11	
Entries	993818
Mean	3.359
Std Dev	3.725

nChipHit_F1_Fch1_Chip12

Entries

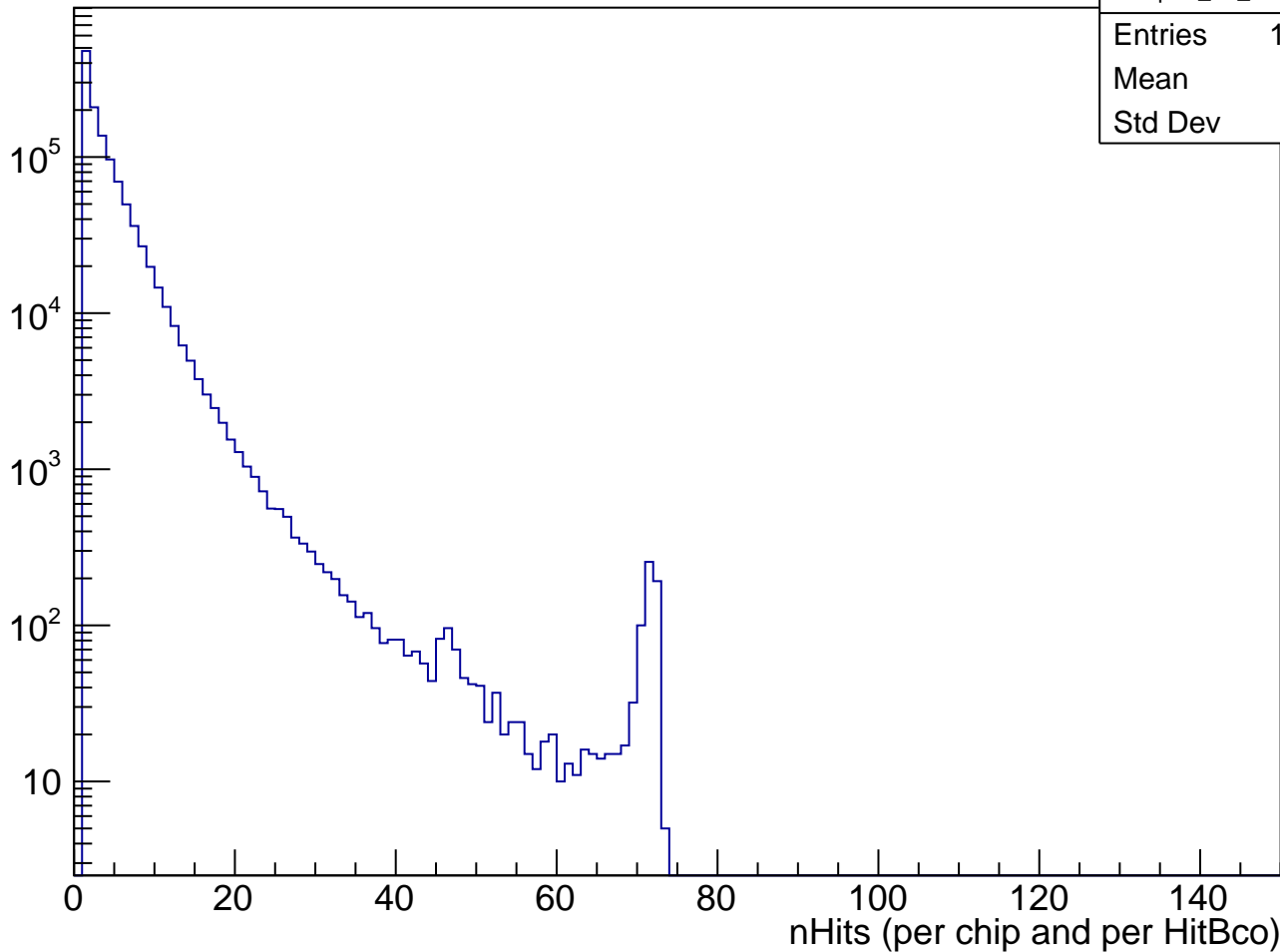


nChipHit_F1_Fch1_Chip12	
Entries	991710
Mean	3.346
Std Dev	3.703

nHits (per chip and per HitBco)

nChipHit_F1_Fch1_Chip13

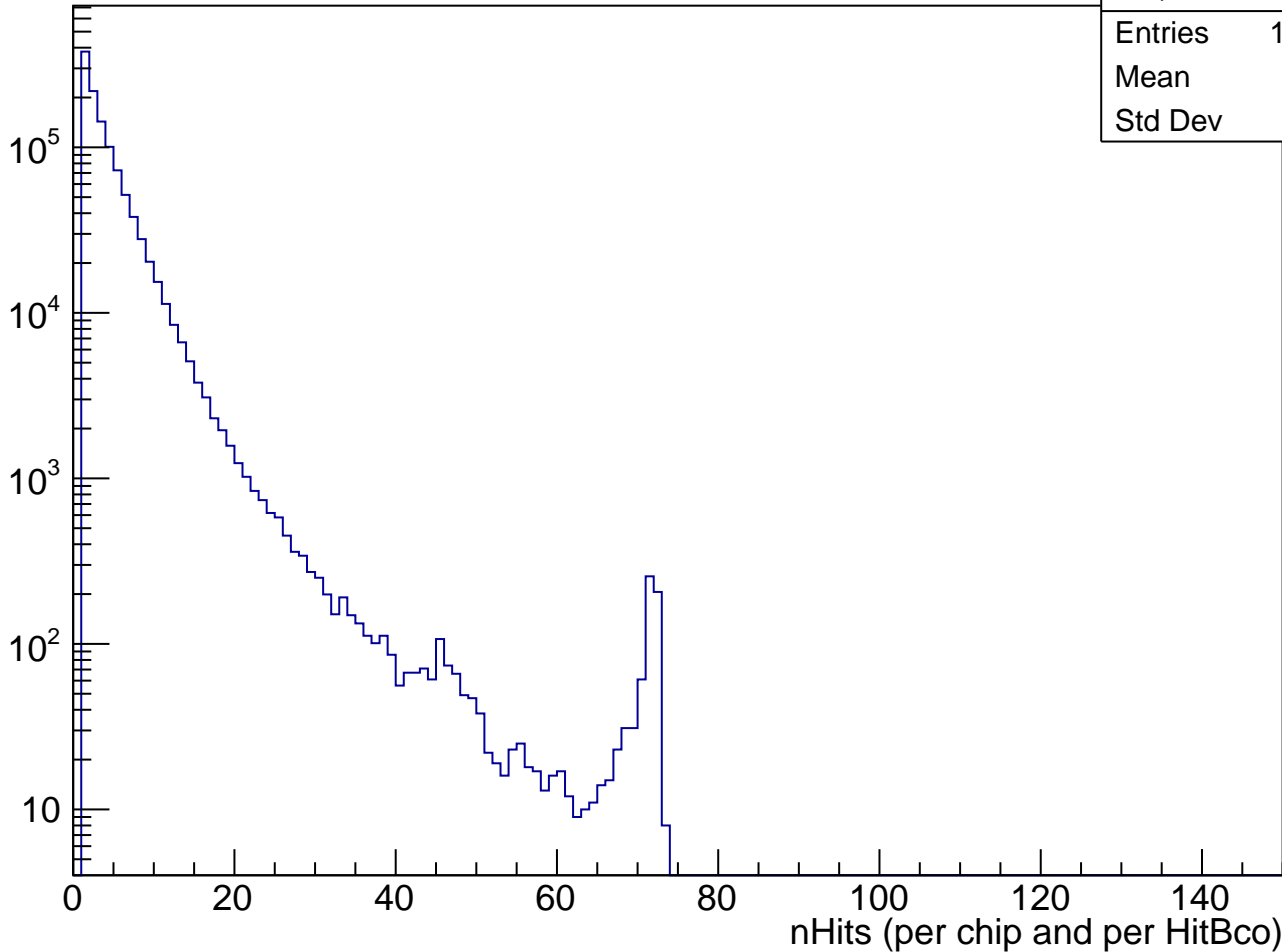
Entries



nChipHit_F1_Fch1_Chip13	
Entries	1189343
Mean	3.366
Std Dev	4.039

nChipHit_F1_Fch1_Chip14

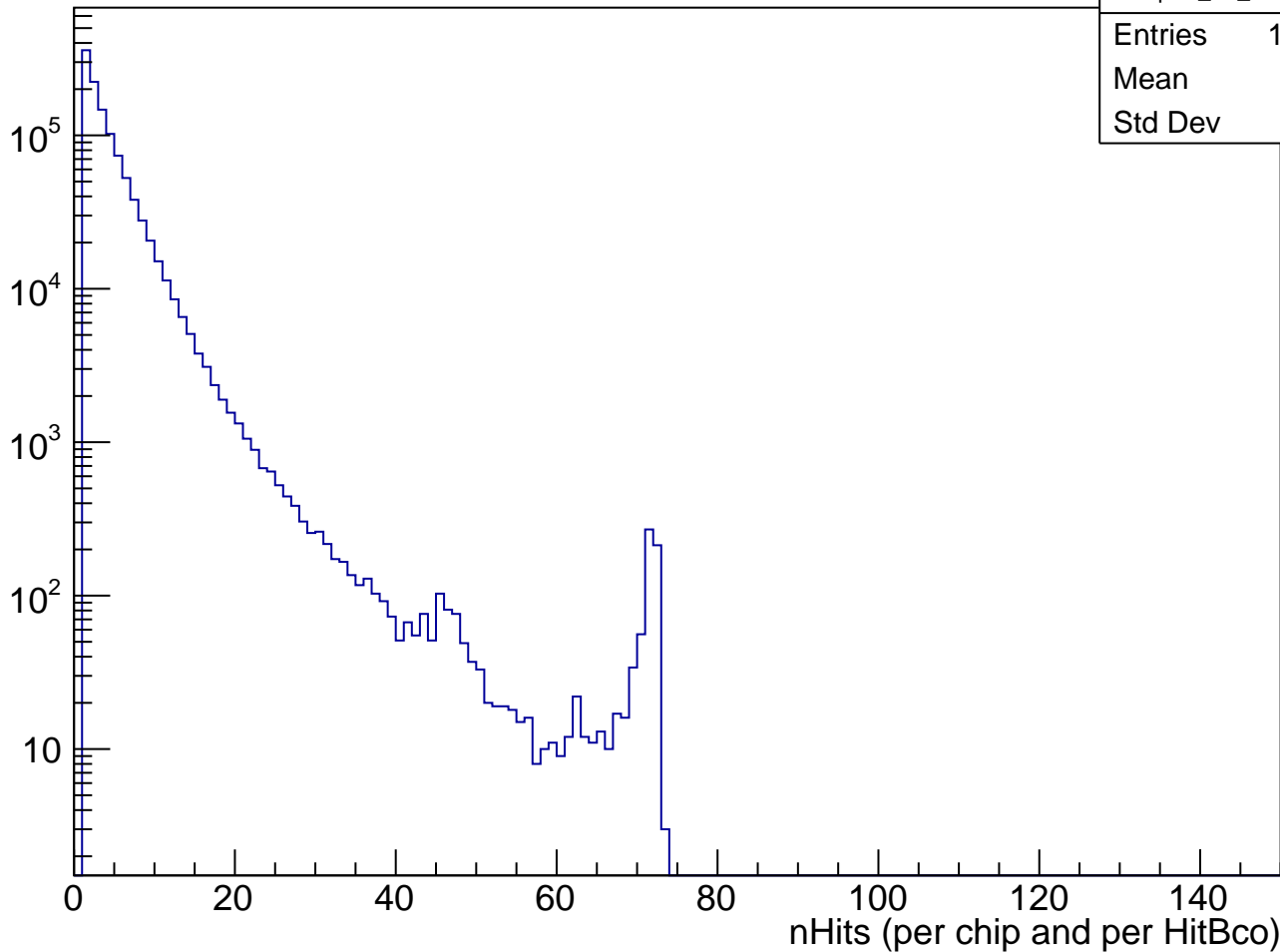
Entries



nChipHit_F1_Fch1_Chip14	
Entries	1120976
Mean	3.596
Std Dev	4.113

nChipHit_F1_Fch1_Chip15

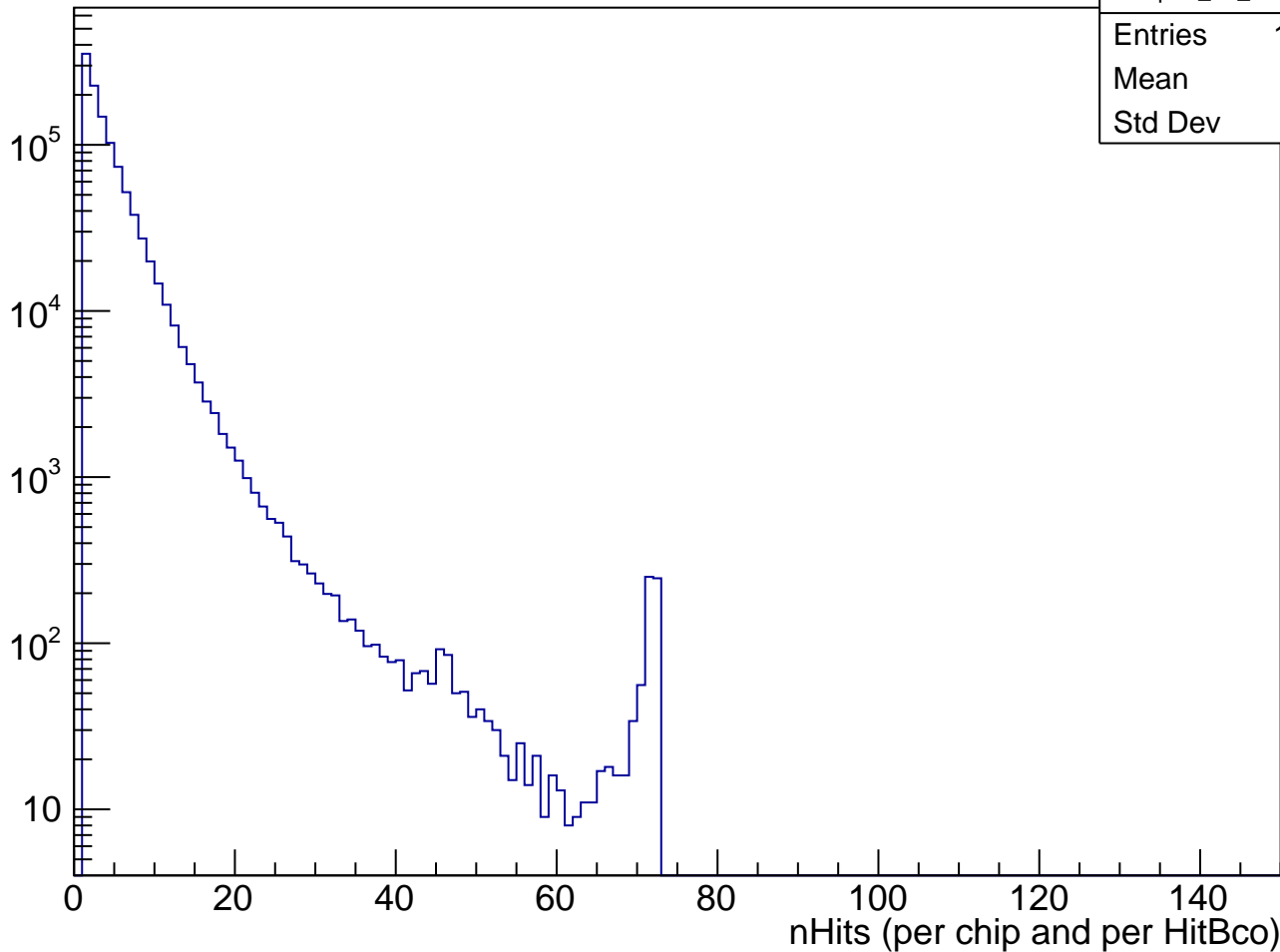
Entries



nChipHit_F1_Fch1_Chip15	
Entries	1112825
Mean	3.634
Std Dev	4.092

nChipHit_F1_Fch1_Chip16

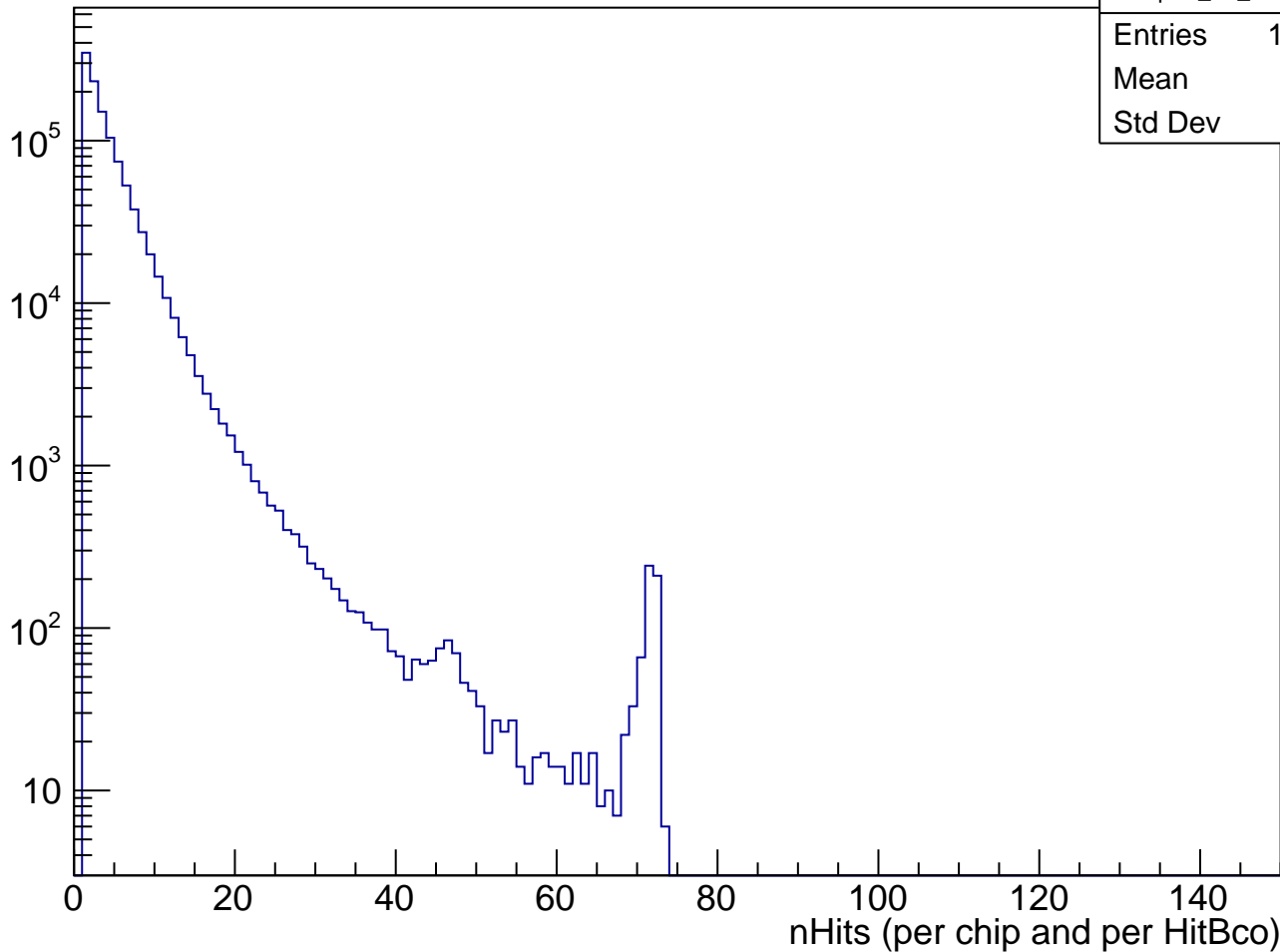
Entries



nChipHit_F1_Fch1_Chip16	
Entries	1107681
Mean	3.607
Std Dev	4.064

nChipHit_F1_Fch1_Chip17

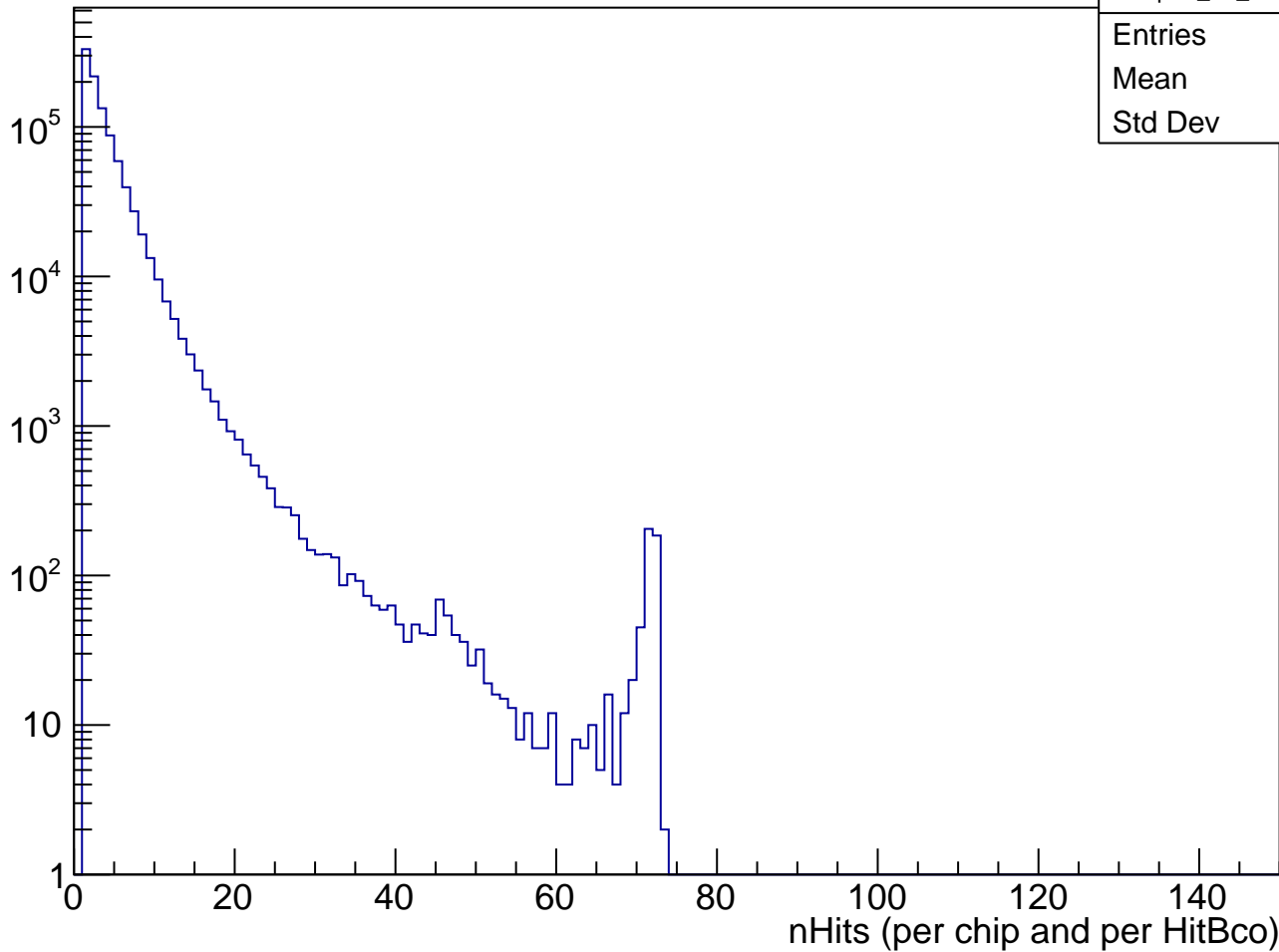
Entries



nChipHit_F1_Fch1_Chip17	
Entries	1112107
Mean	3.607
Std Dev	4.025

nChipHit_F1_Fch1_Chip18

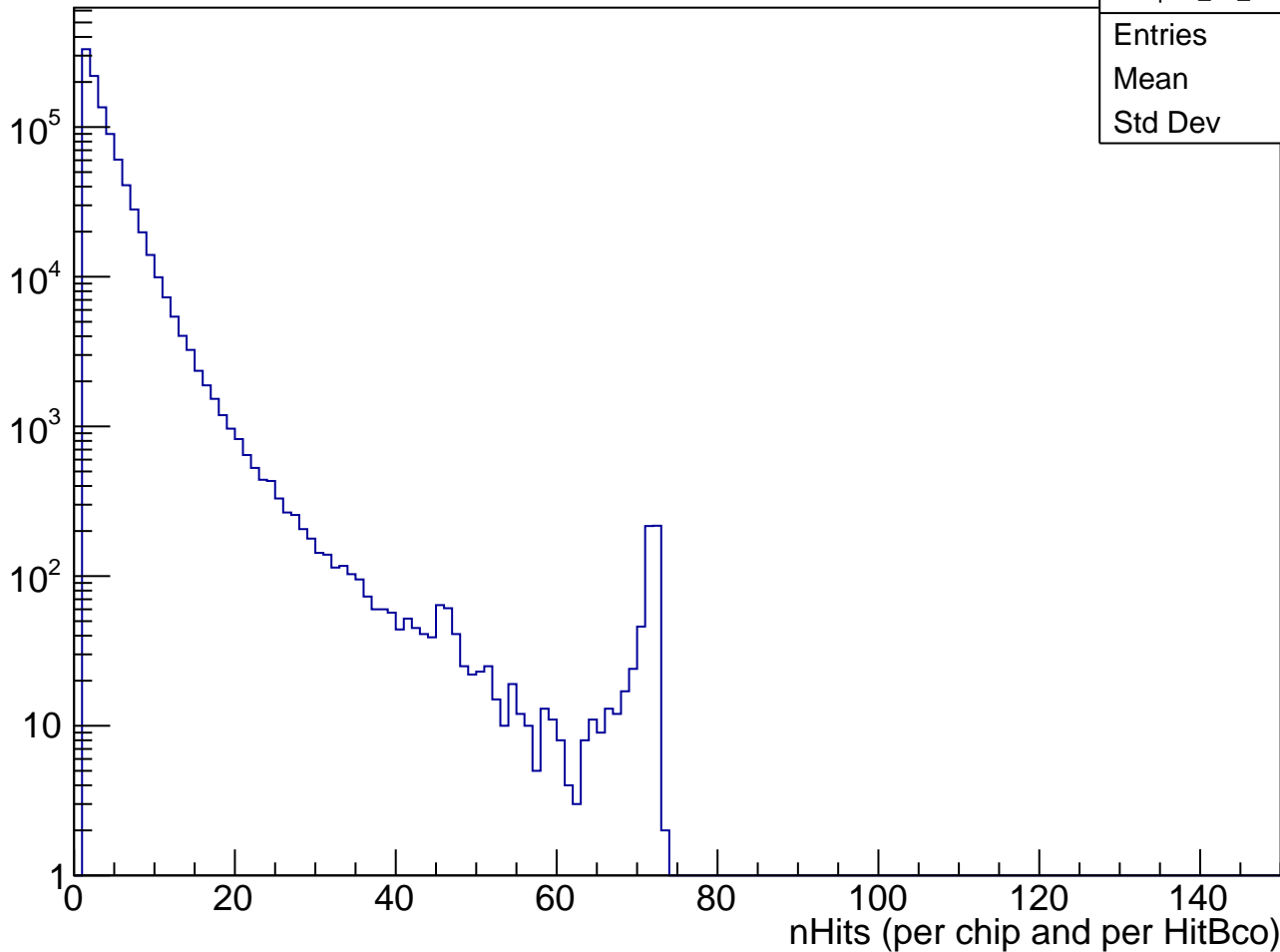
Entries



nChipHit_F1_Fch1_Chip18	
Entries	970630
Mean	3.247
Std Dev	3.669

nChipHit_F1_Fch1_Chip19

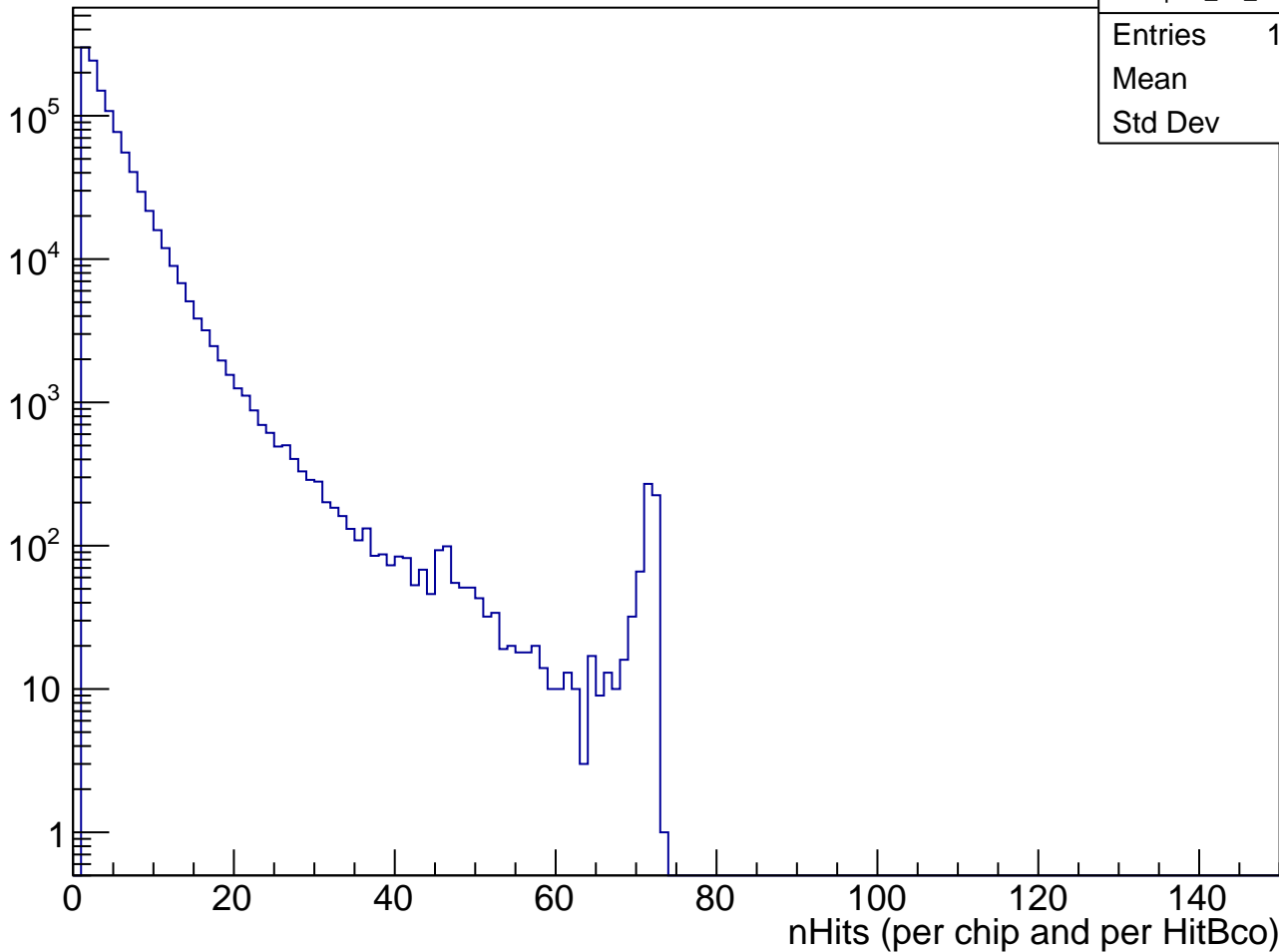
Entries



nChipHit_F1_Fch1_Chip19	
Entries	983613
Mean	3.289
Std Dev	3.727

nChipHit_F1_Fch1_Chip2

Entries



nChipHit_F1_Fch1_Chip2

Entries 1095708

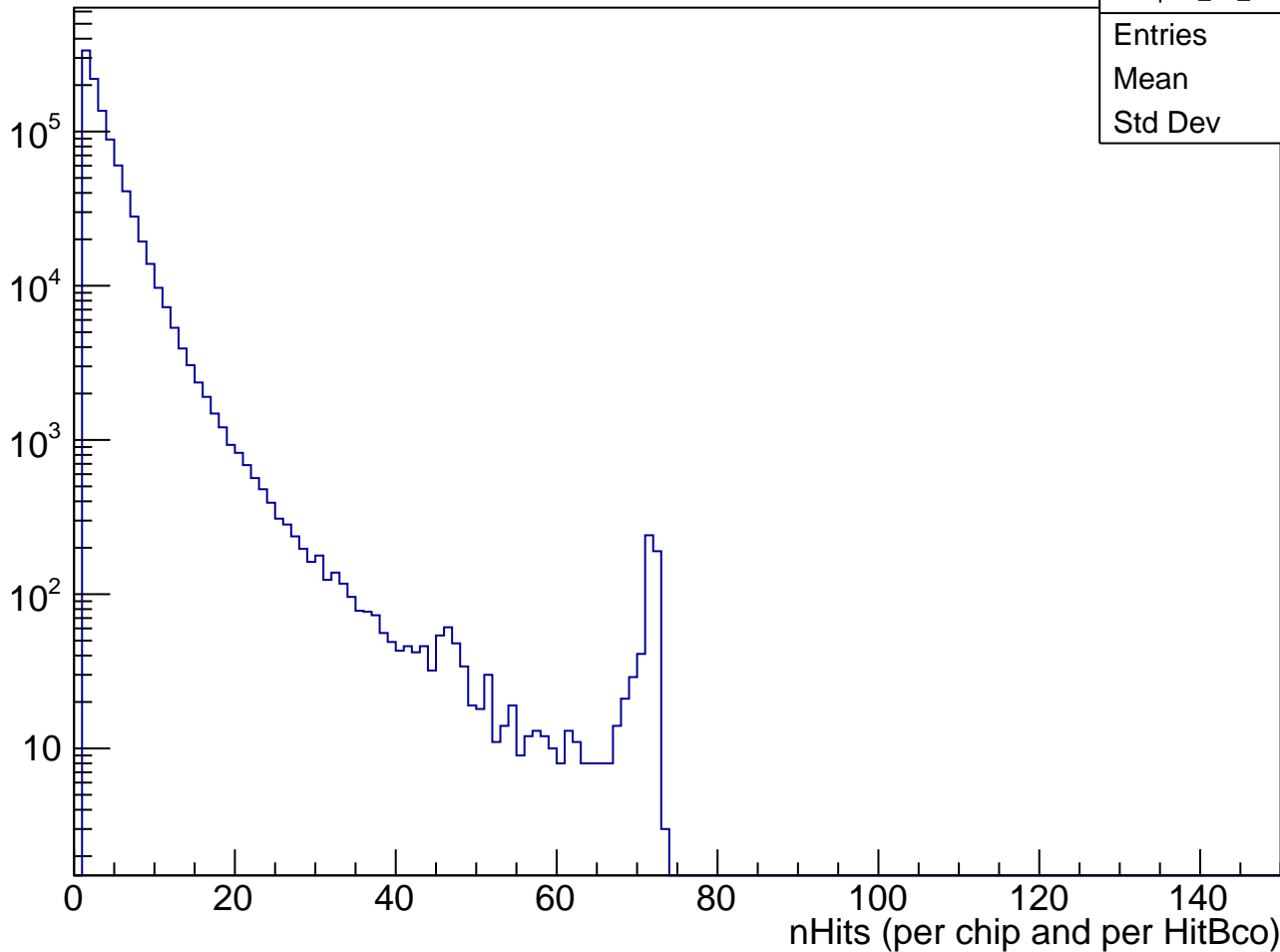
Mean 3.799

Std Dev 4.137

nHits (per chip and per HitBco)

nChipHit_F1_Fch1_Chip20

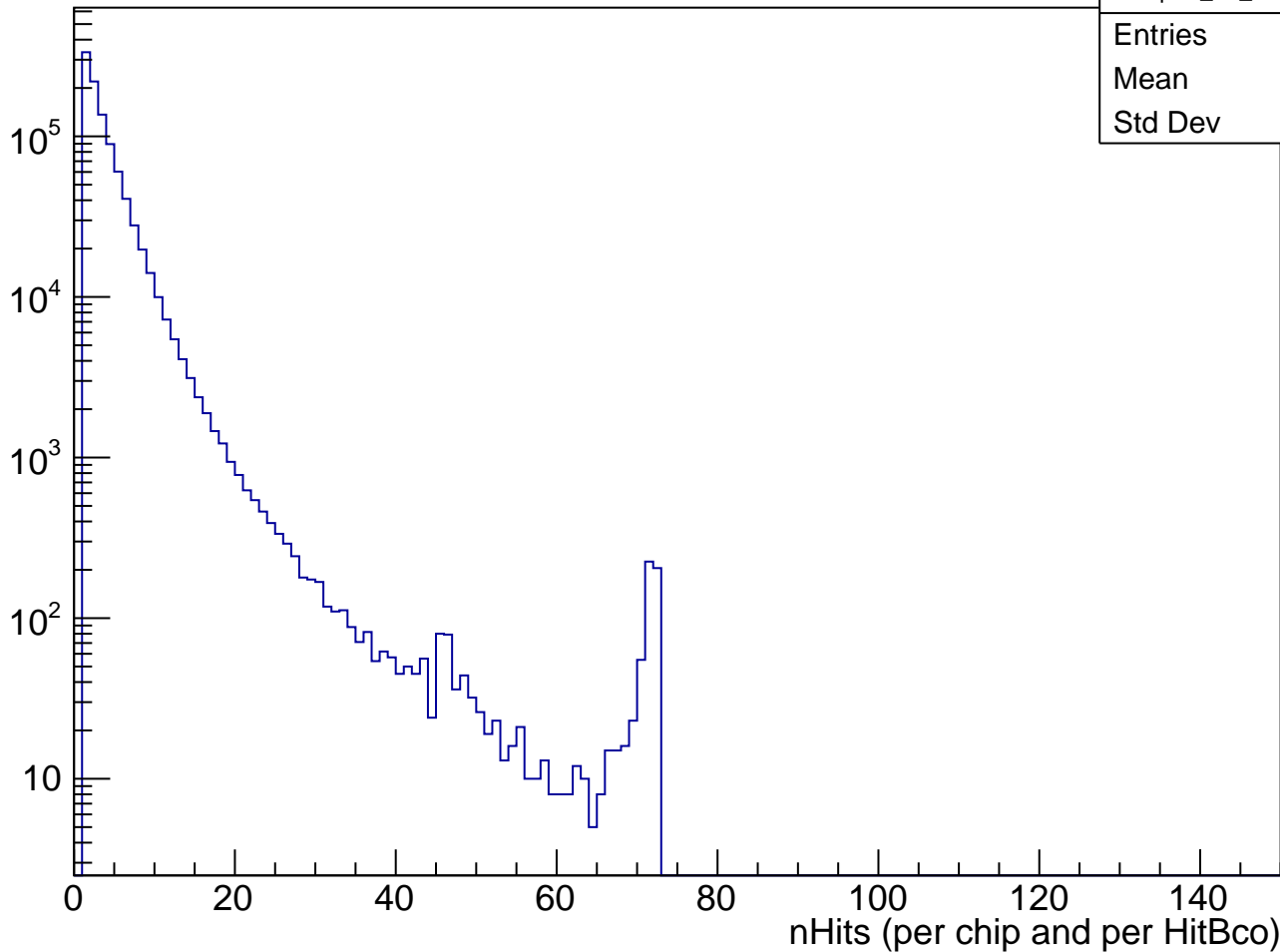
Entries



nChipHit_F1_Fch1_Chip20	
Entries	986123
Mean	3.27
Std Dev	3.722

nChipHit_F1_Fch1_Chip21

Entries

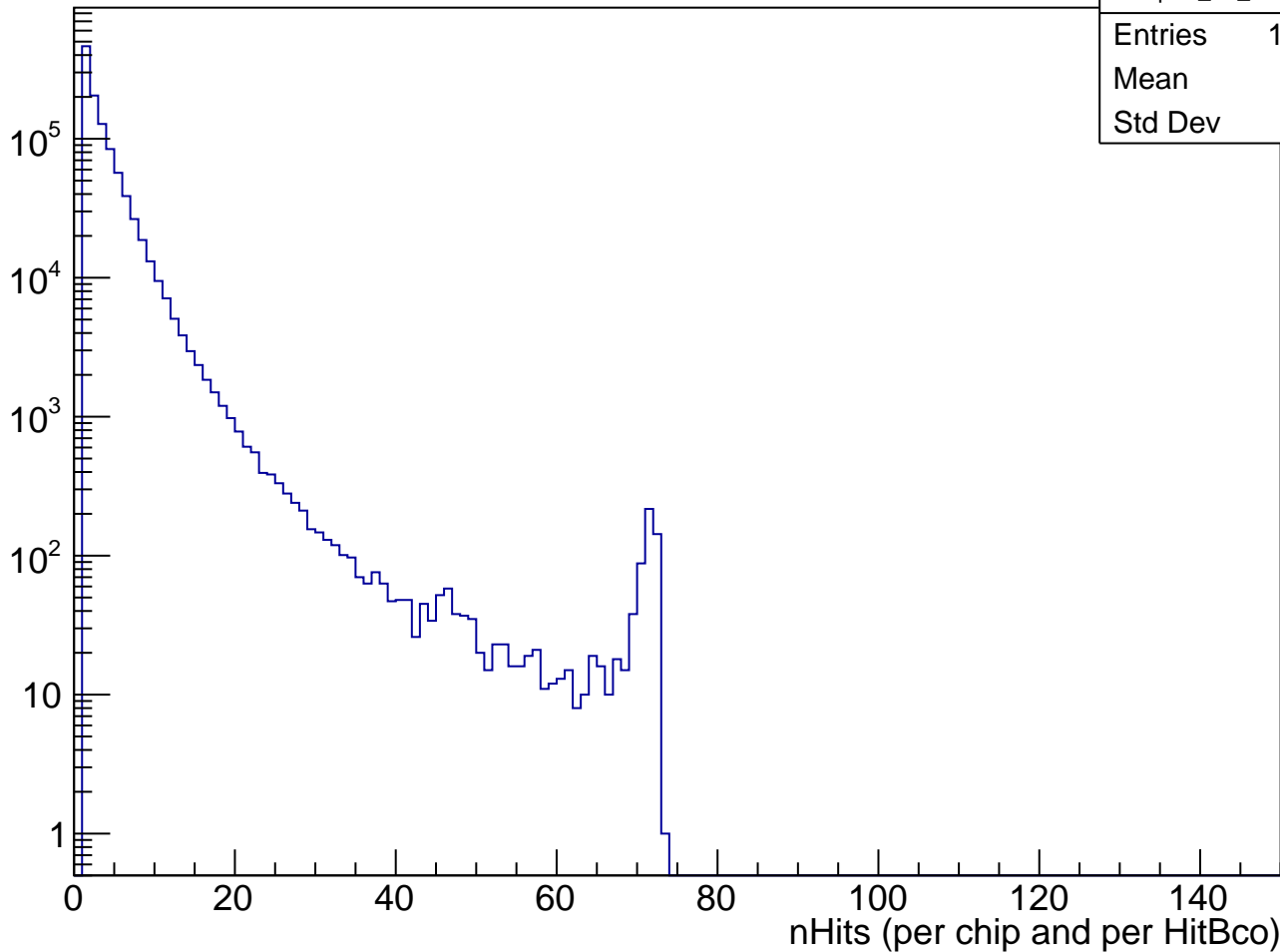


nChipHit_F1_Fch1_Chip21	
Entries	986383
Mean	3.281
Std Dev	3.739

nHits (per chip and per HitBco)

nChipHit_F1_Fch1_Chip22

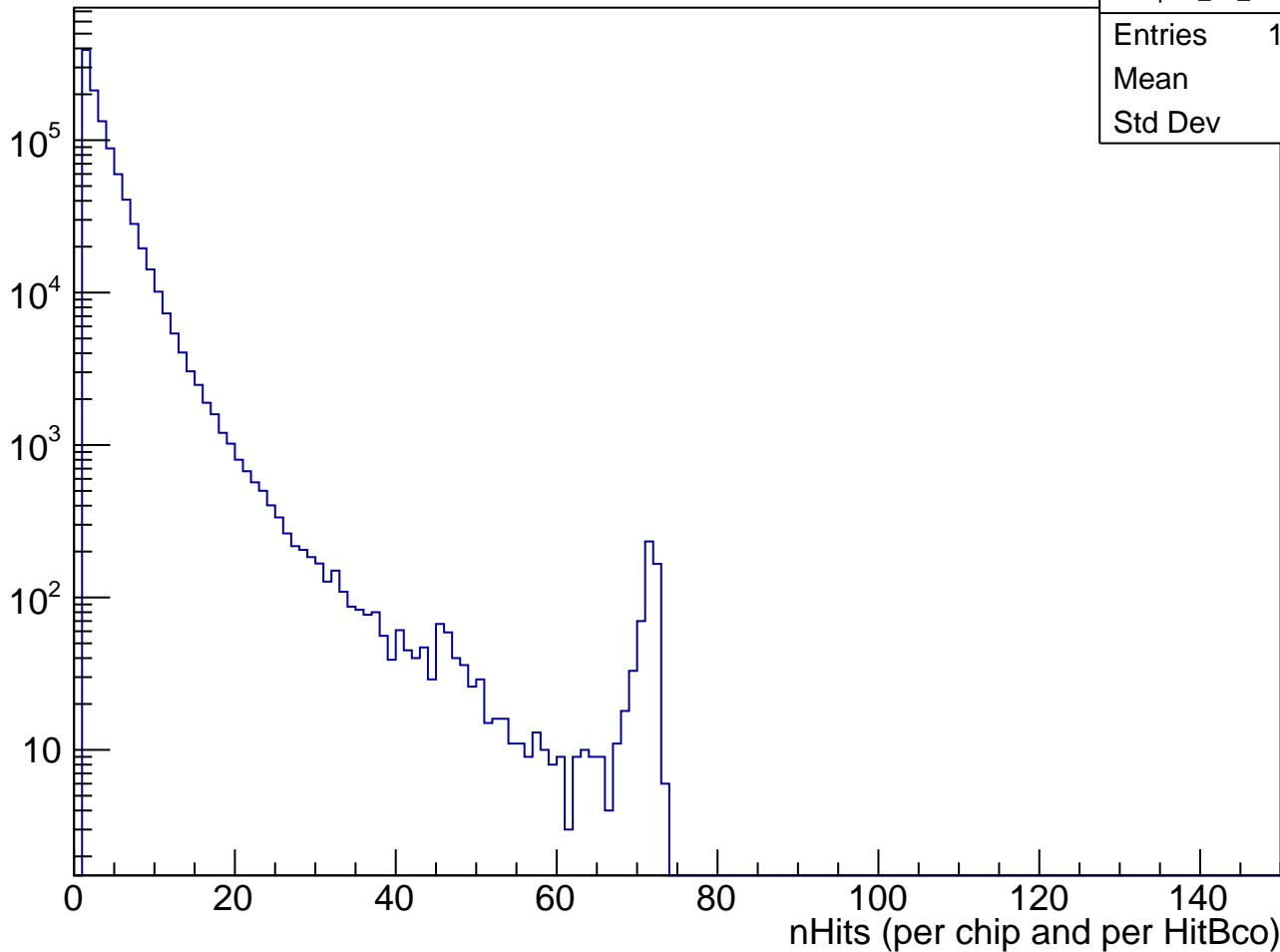
Entries



nChipHit_F1_Fch1_Chip22	
Entries	1076228
Mean	2.983
Std Dev	3.608

nChipHit_F1_Fch1_Chip23

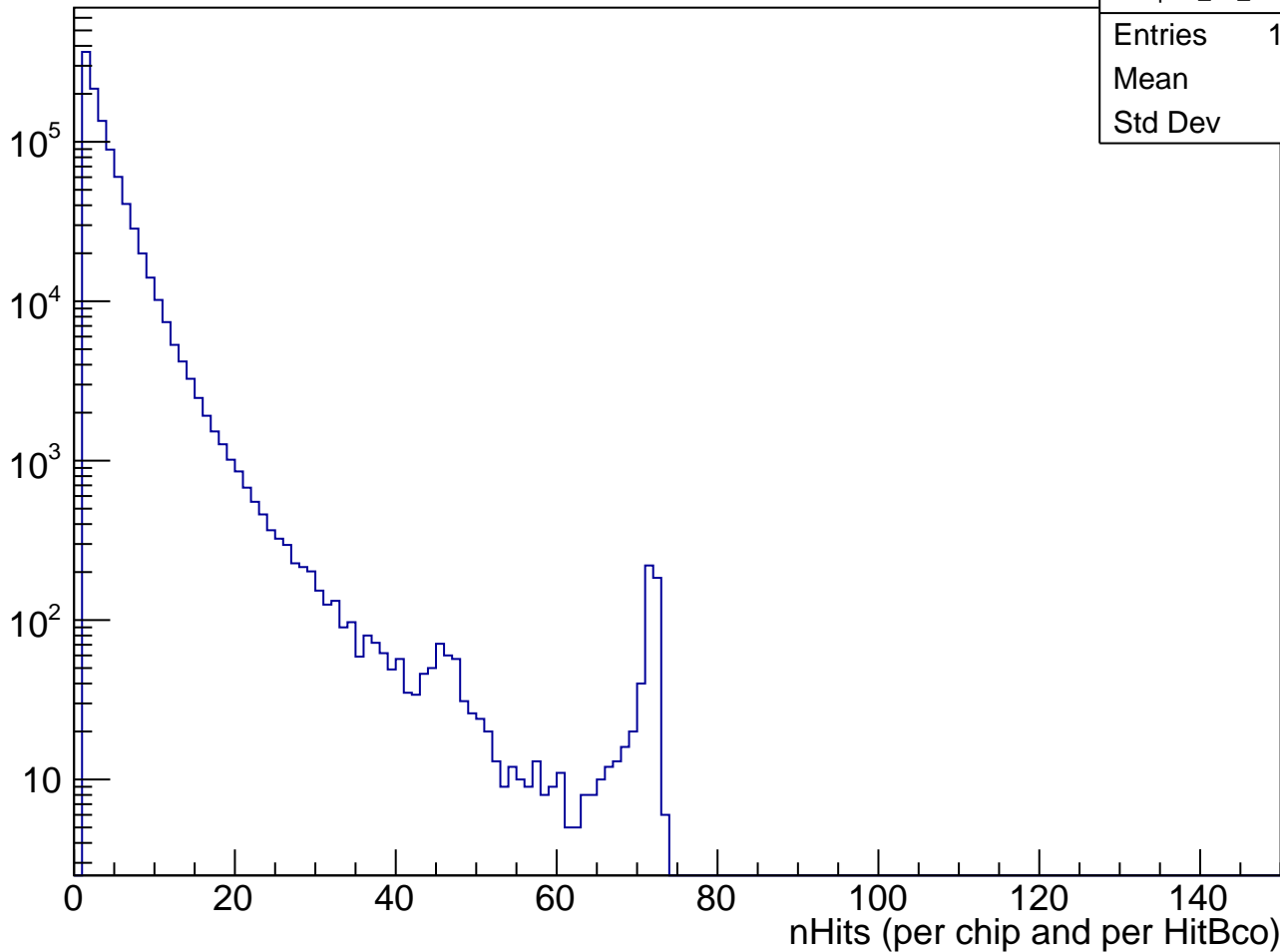
Entries



nChipHit_F1_Fch1_Chip23	
Entries	1031094
Mean	3.168
Std Dev	3.684

nChipHit_F1_Fch1_Chip24

Entries



nChipHit_F1_Fch1_Chip24

Entries 1015258

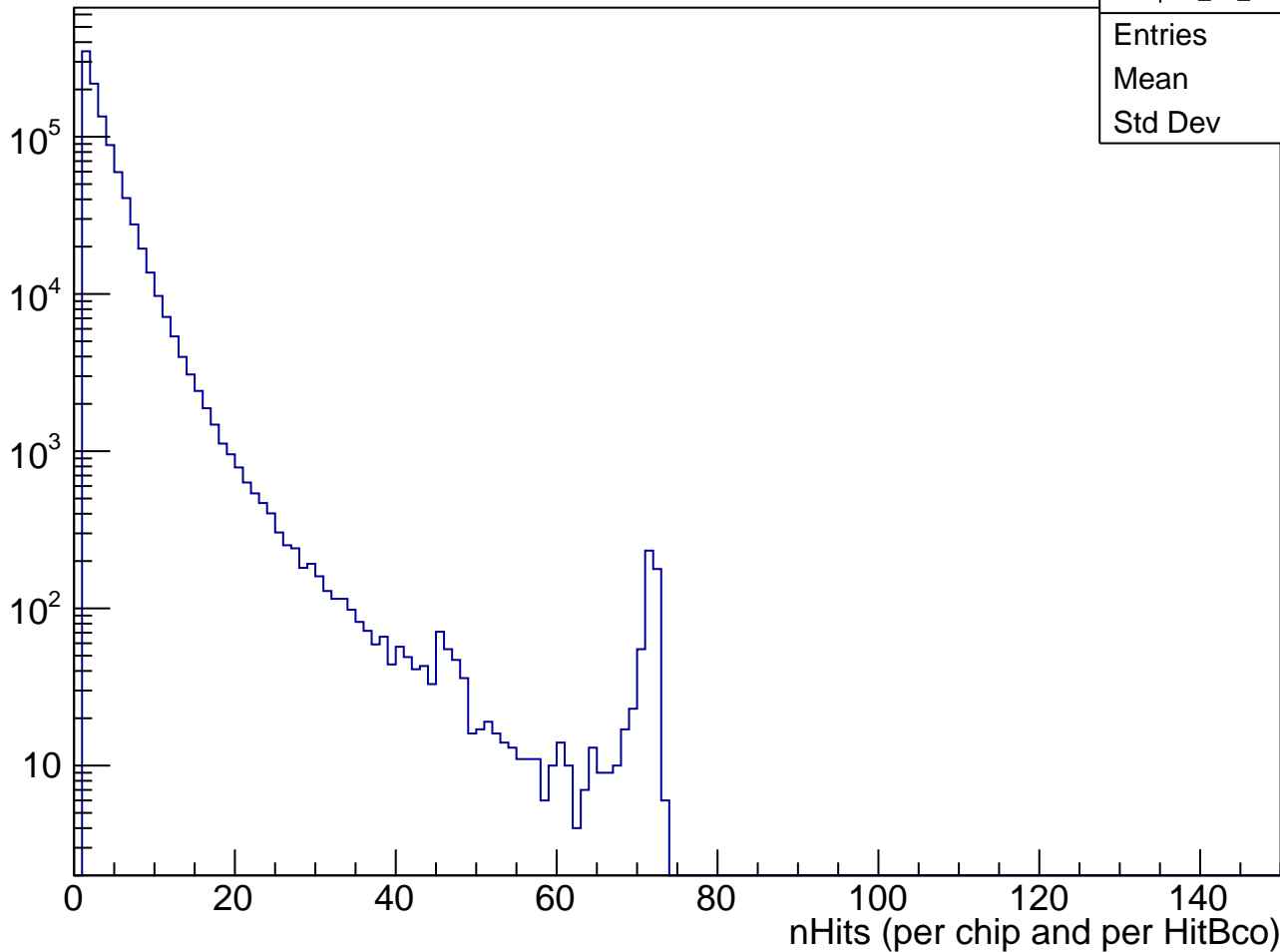
Mean 3.221

Std Dev 3.684

nHits (per chip and per HitBco)

nChipHit_F1_Fch1_Chip25

Entries

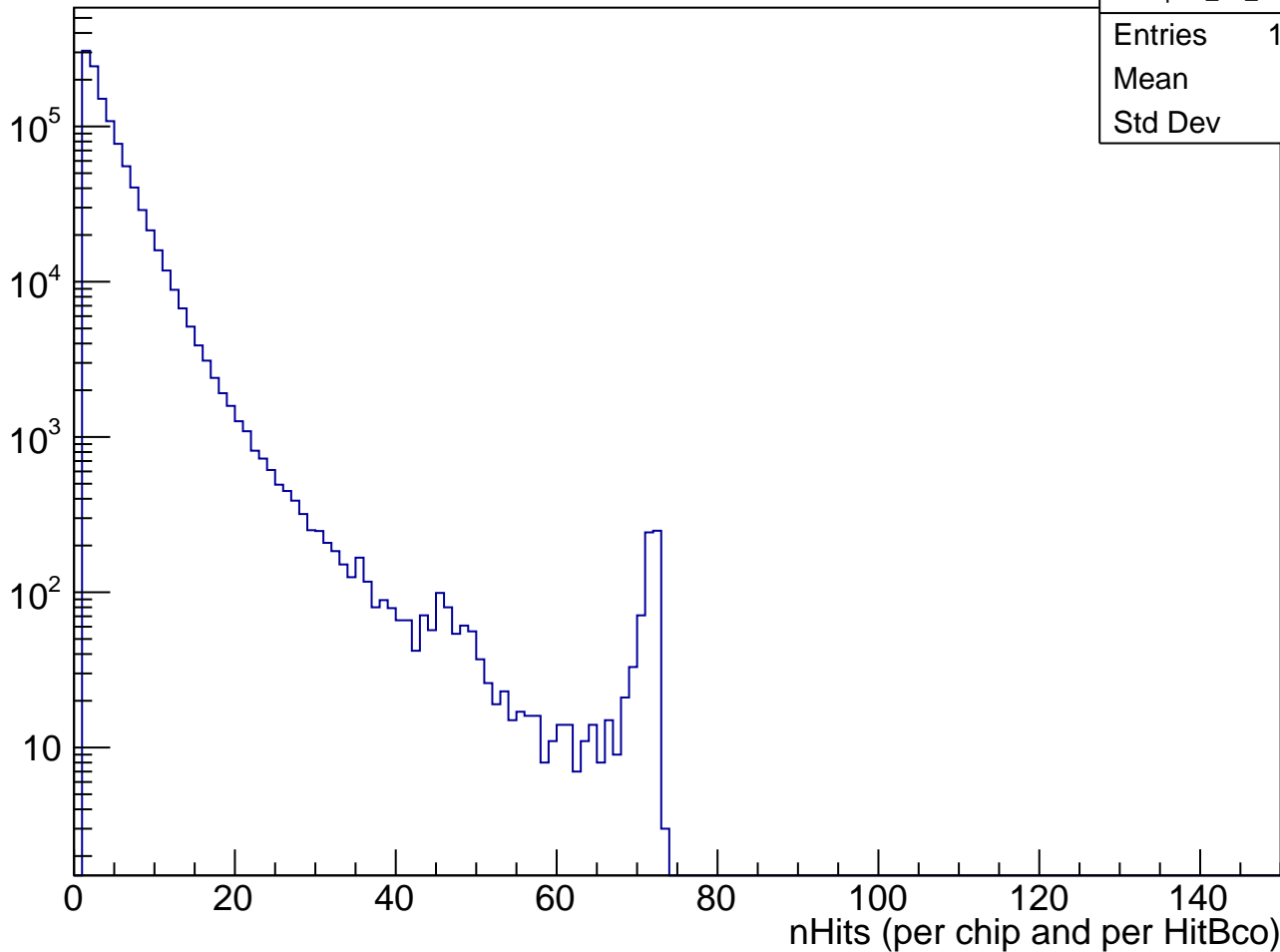


nChipHit_F1_Fch1_Chip25	
Entries	995272
Mean	3.23
Std Dev	3.689

nHits (per chip and per HitBco)

nChipHit_F1_Fch1_Chip3

Entries



nChipHit_F1_Fch1_Chip3

Entries 1104605

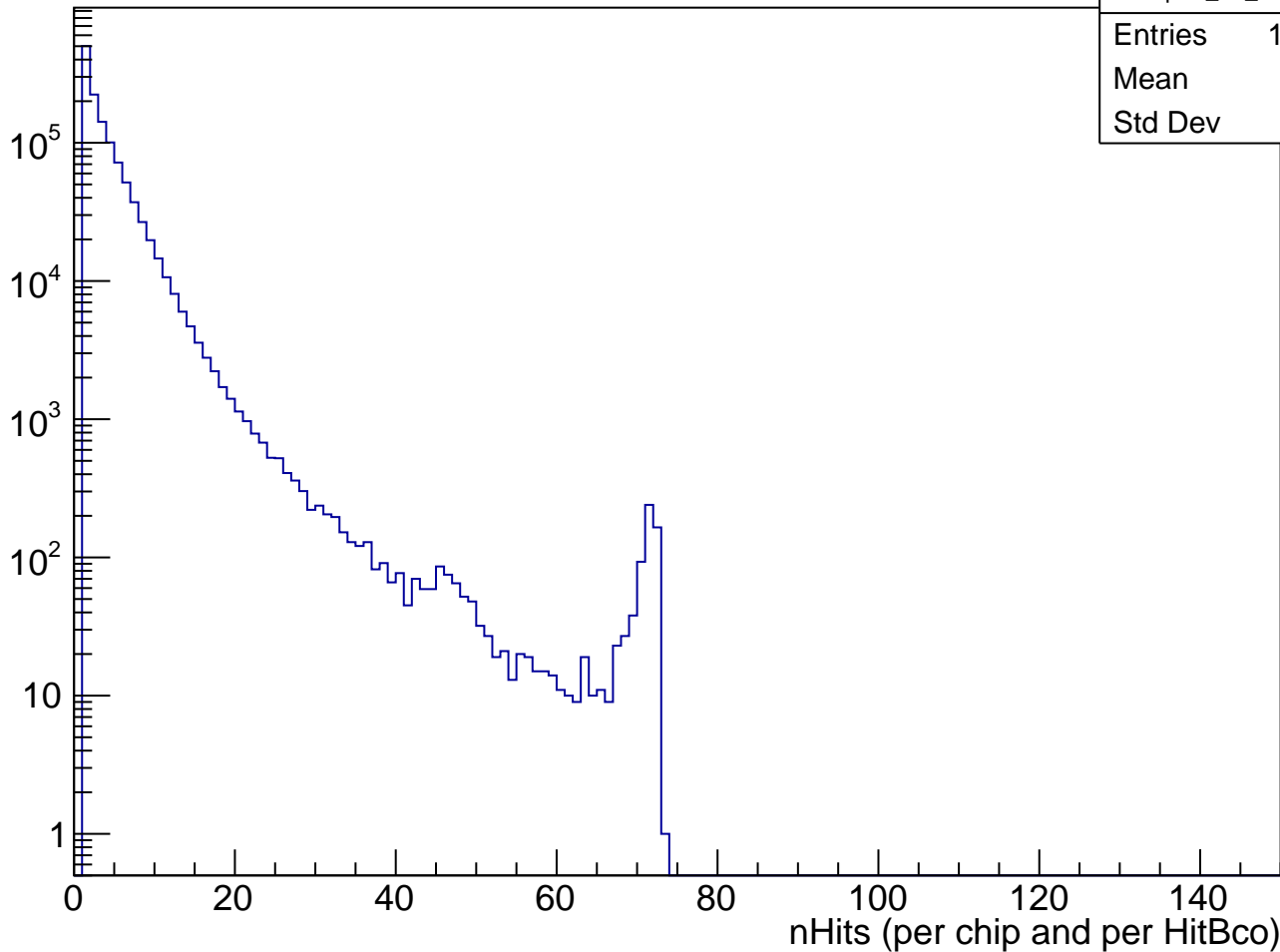
Mean 3.767

Std Dev 4.108

nHits (per chip and per HitBco)

nChipHit_F1_Fch1_Chip4

Entries



nChipHit_F1_Fch1_Chip4

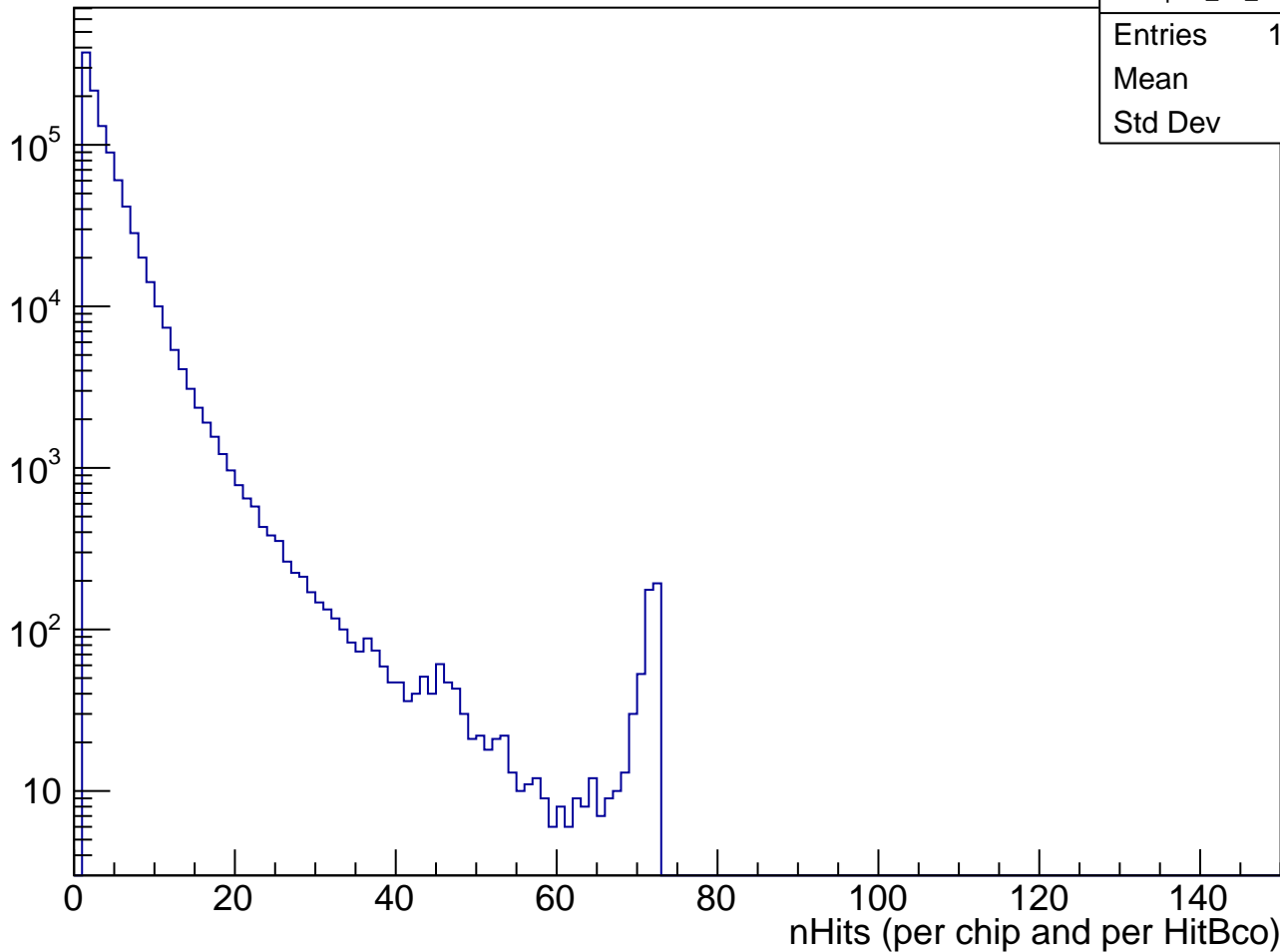
Entries 1239046

Mean 3.279

Std Dev 3.882

nChipHit_F1_Fch1_Chip5

Entries



nChipHit_F1_Fch1_Chip5

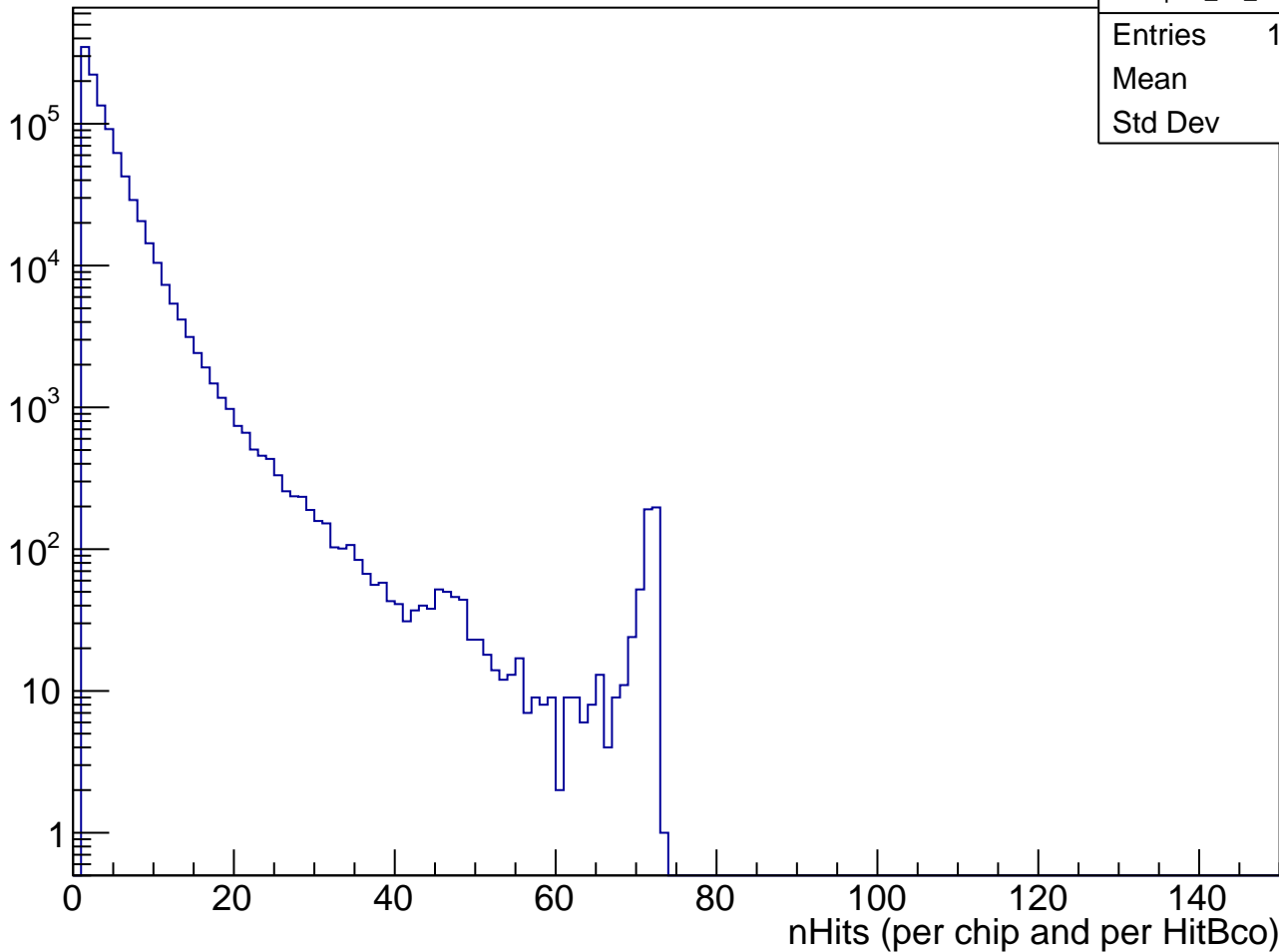
Entries 1018635

Mean 3.198

Std Dev 3.645

nChipHit_F1_Fch1_Chip6

Entries



nChipHit_F1_Fch1_Chip6

Entries 1009548

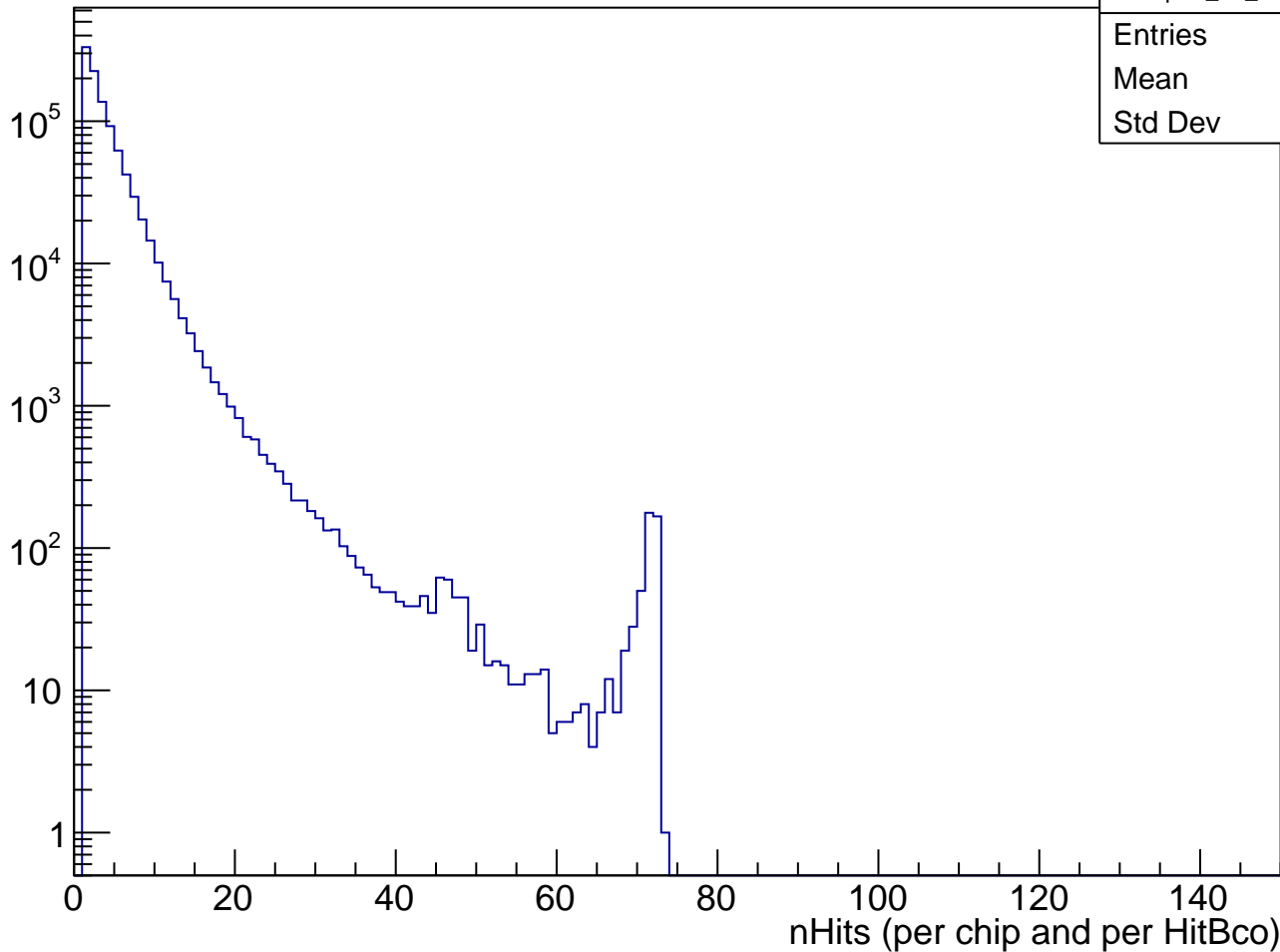
Mean 3.262

Std Dev 3.652

nHits (per chip and per HitBco)

nChipHit_F1_Fch1_Chip7

Entries

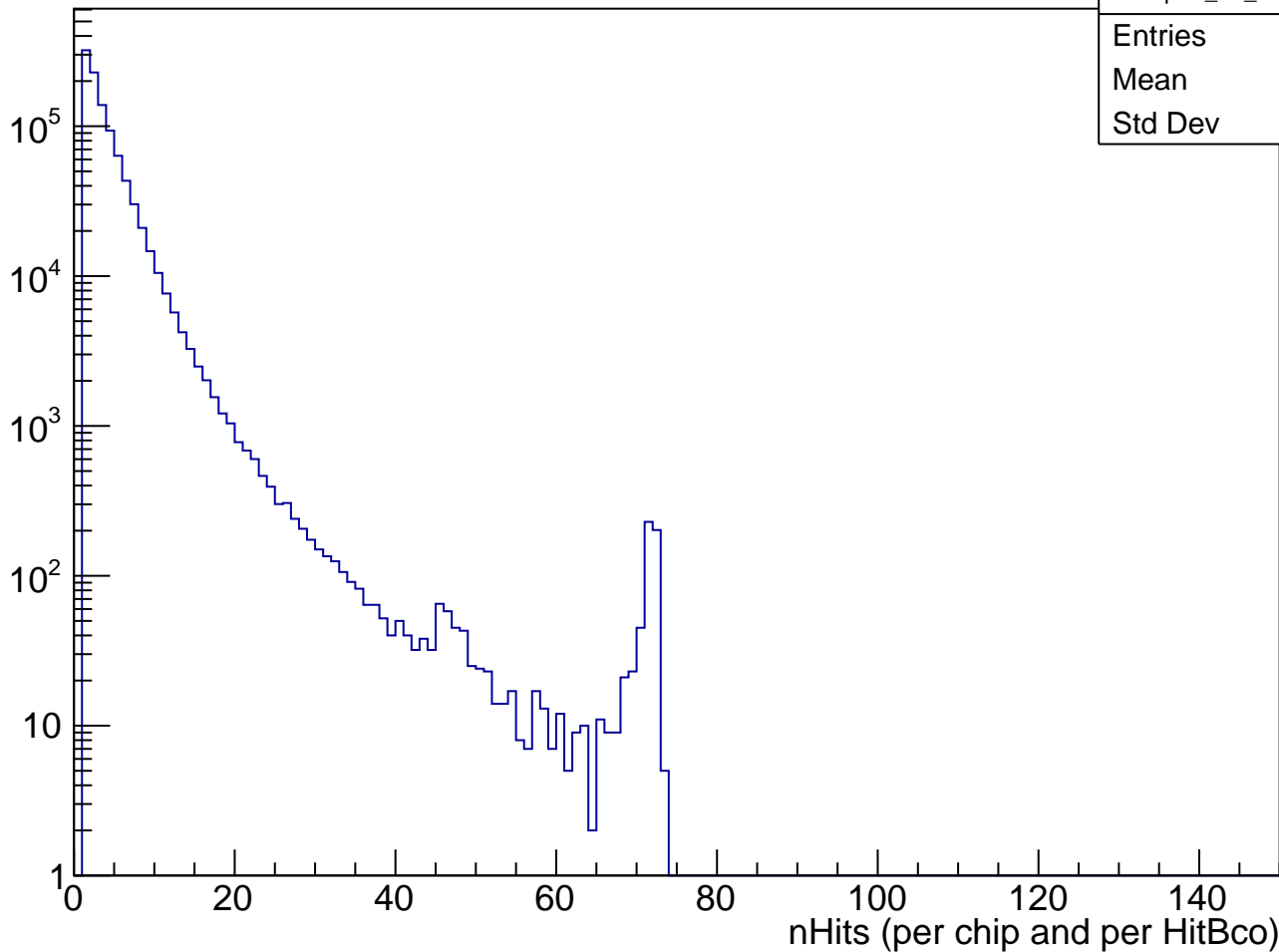


nChipHit_F1_Fch1_Chip7	
Entries	999302
Mean	3.296
Std Dev	3.648

nHits (per chip and per HitBco)

nChipHit_F1_Fch1_Chip8

Entries



nChipHit_F1_Fch1_Chip8

Entries 999860

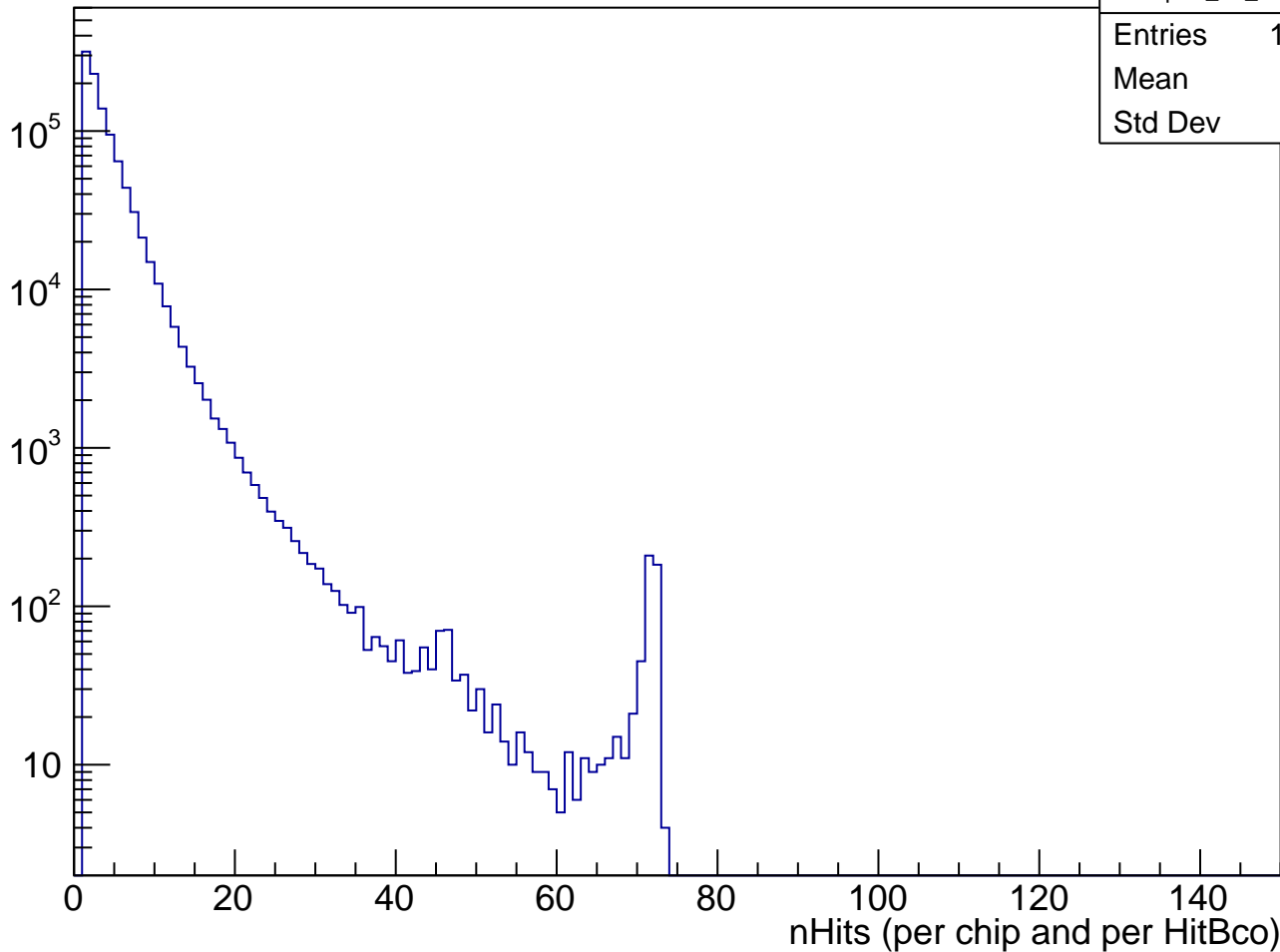
Mean 3.346

Std Dev 3.721

nHits (per chip and per HitBco)

nChipHit_F1_Fch1_Chip9

Entries



nChipHit_F1_Fch1_Chip9

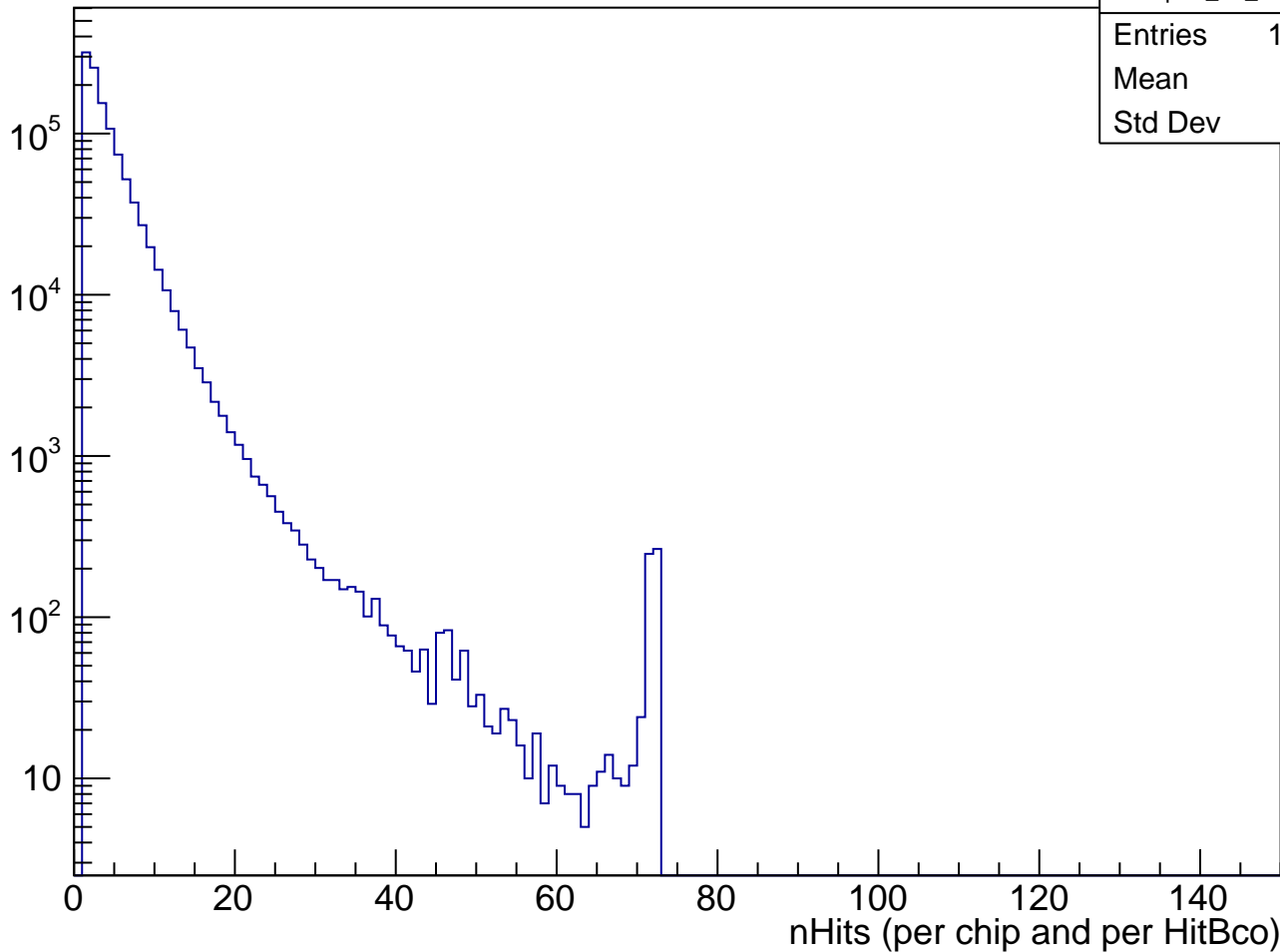
Entries 1002543

Mean 3.375

Std Dev 3.726

nChipHit_F1_Fch2_Chip0

Entries



nChipHit_F1_Fch2_Chip0

Entries 1110857

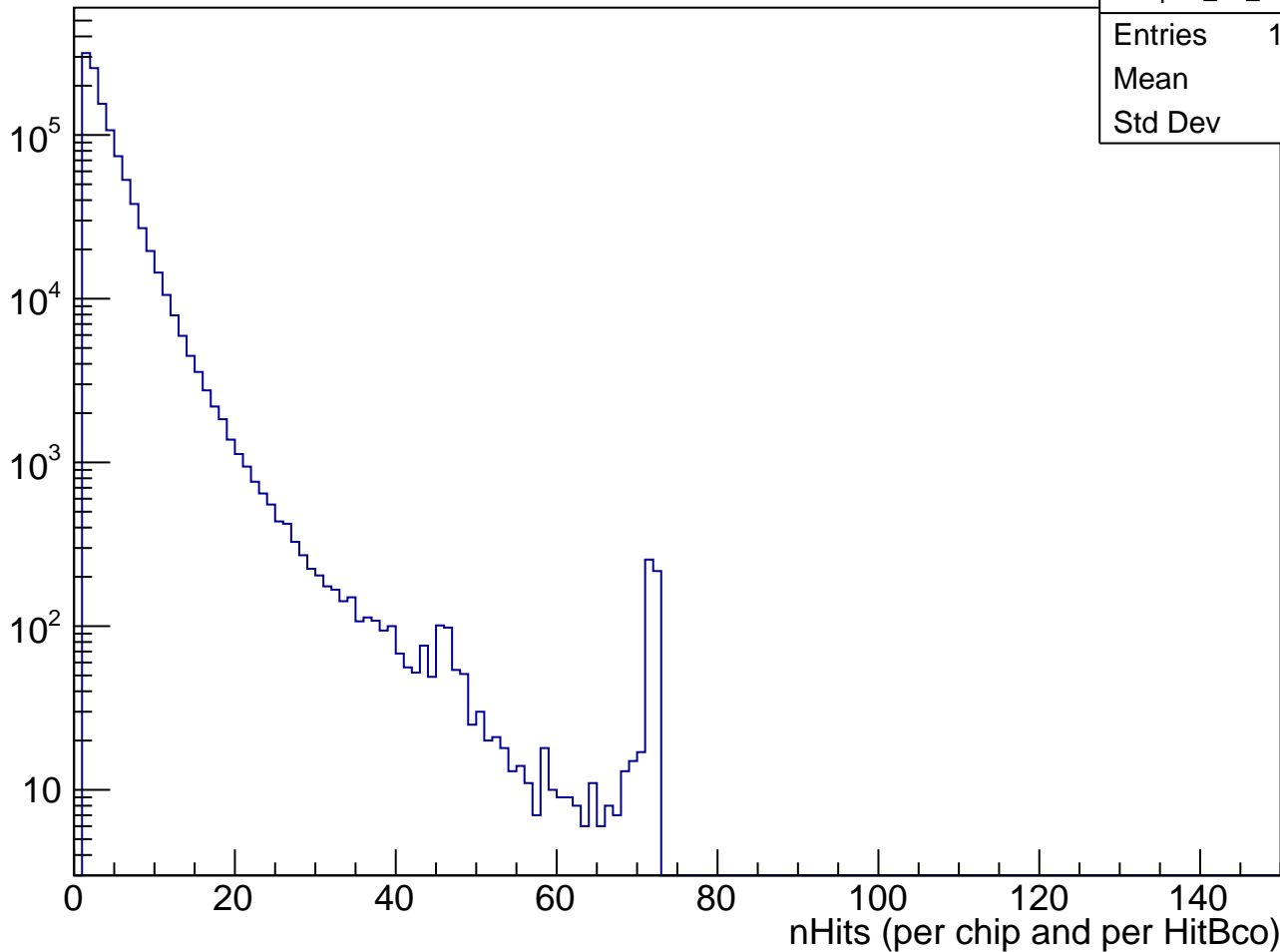
Mean 3.614

Std Dev 3.953

nHits (per chip and per HitBco)

nChipHit_F1_Fch2_Chip1

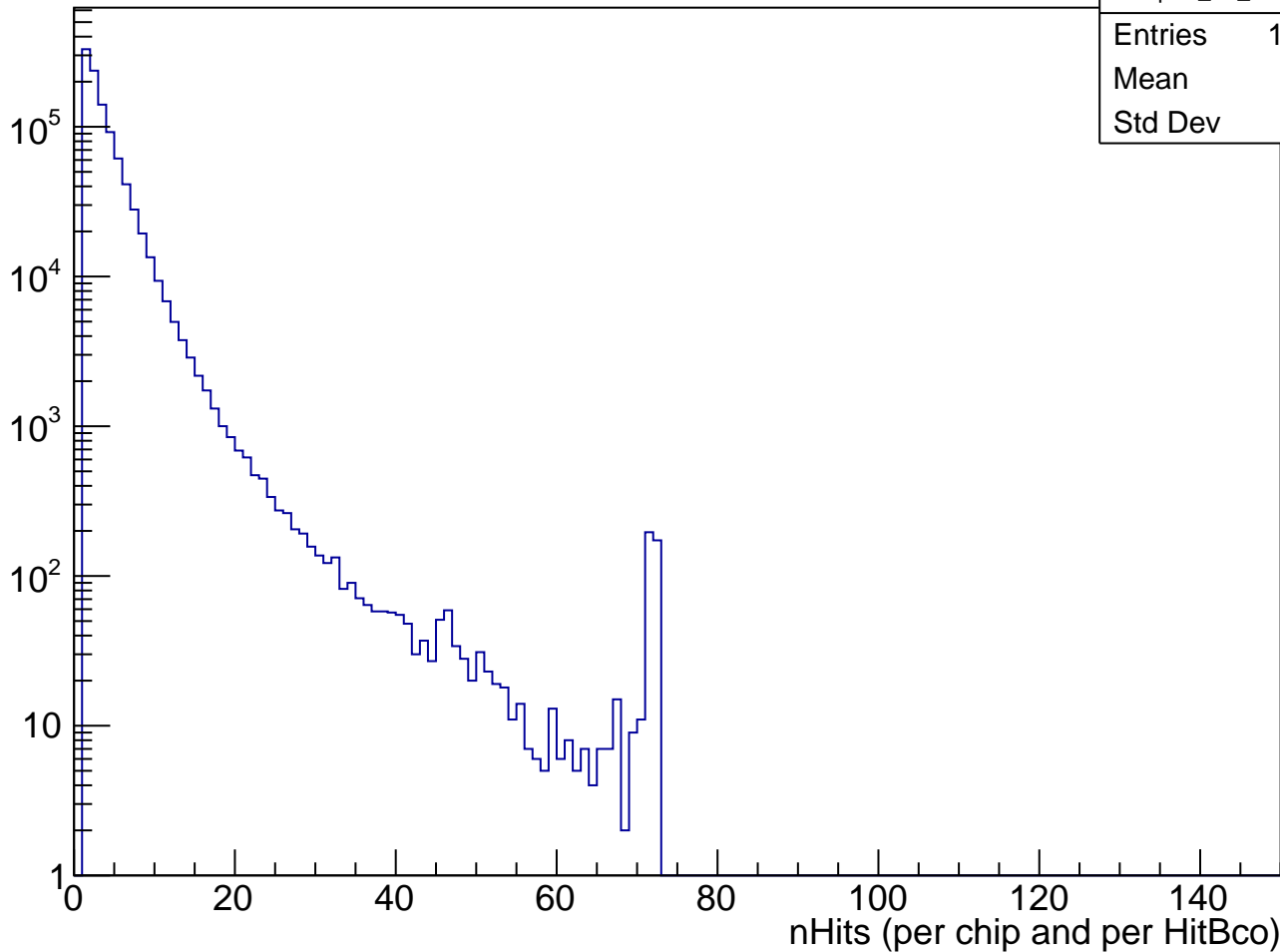
Entries



nChipHit_F1_Fch2_Chip1	
Entries	1109878
Mean	3.616
Std Dev	3.924

nChipHit_F1_Fch2_Chip10

Entries

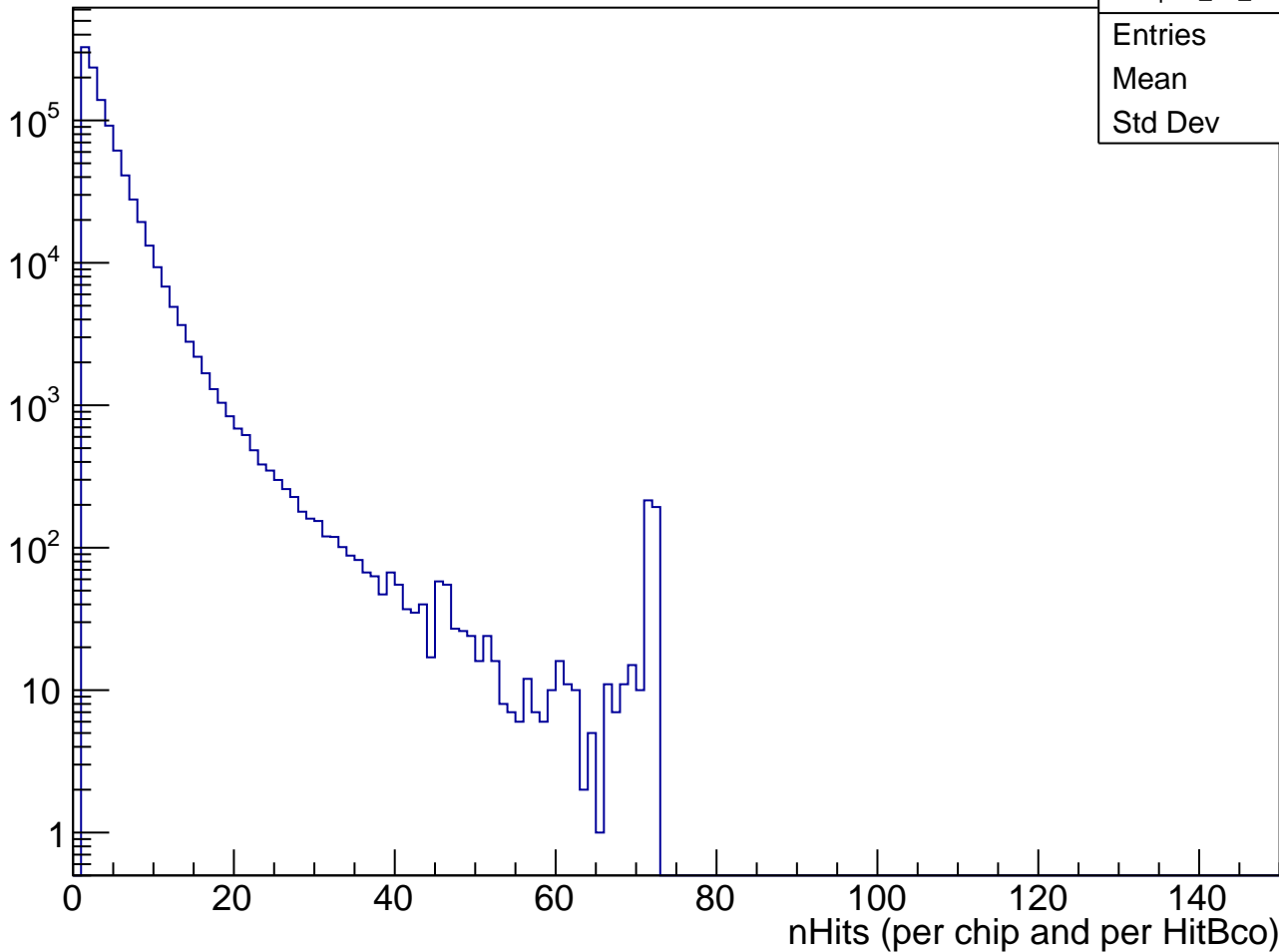


nChipHit_F1_Fch2_Chip10	
Entries	1003266
Mean	3.215
Std Dev	3.512

nHits (per chip and per HitBco)

nChipHit_F1_Fch2_Chip11

Entries

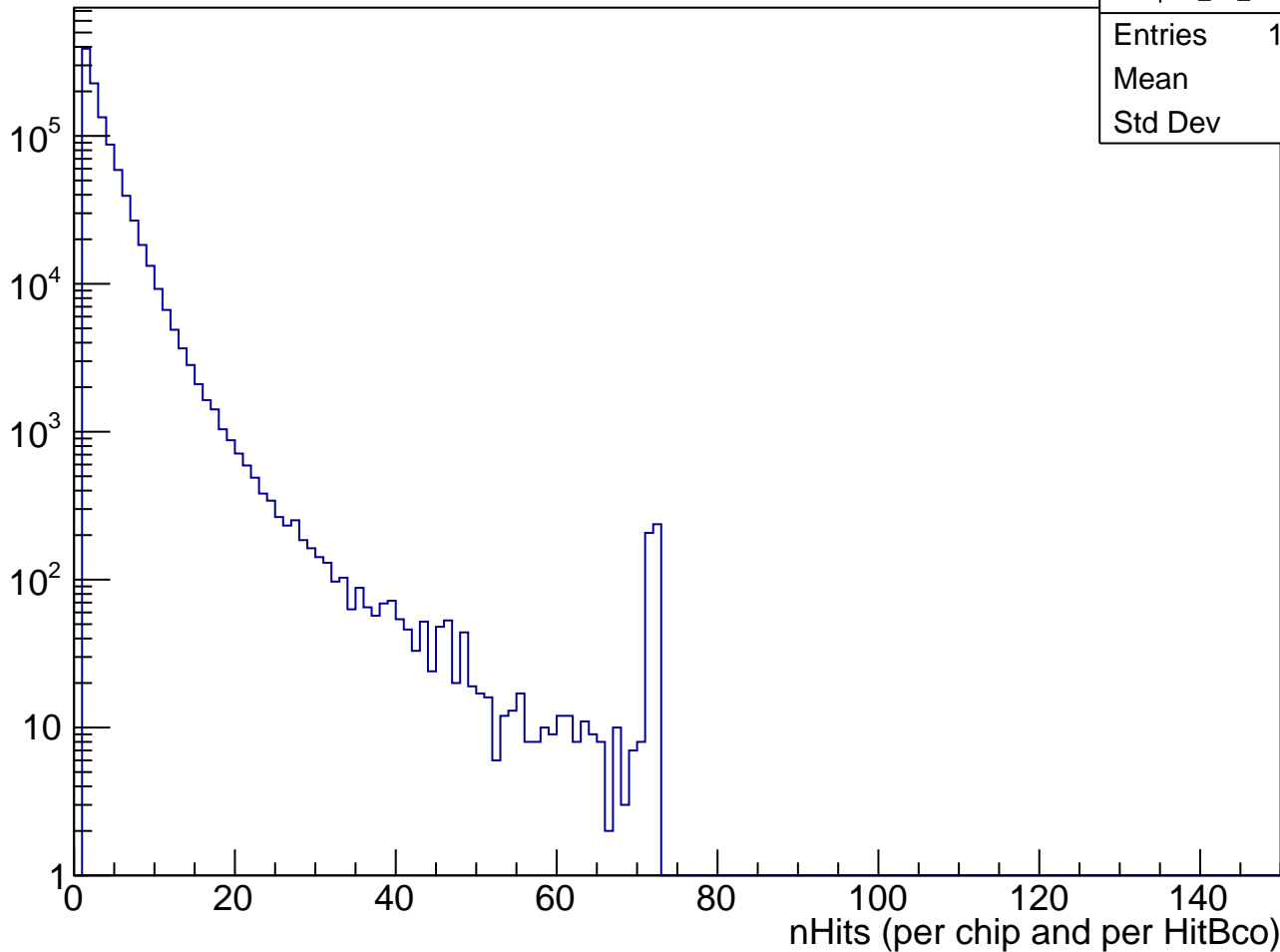


nChipHit_F1_Fch2_Chip11	
Entries	995608
Mean	3.221
Std Dev	3.541

nHits (per chip and per HitBco)

nChipHit_F1_Fch2_Chip12

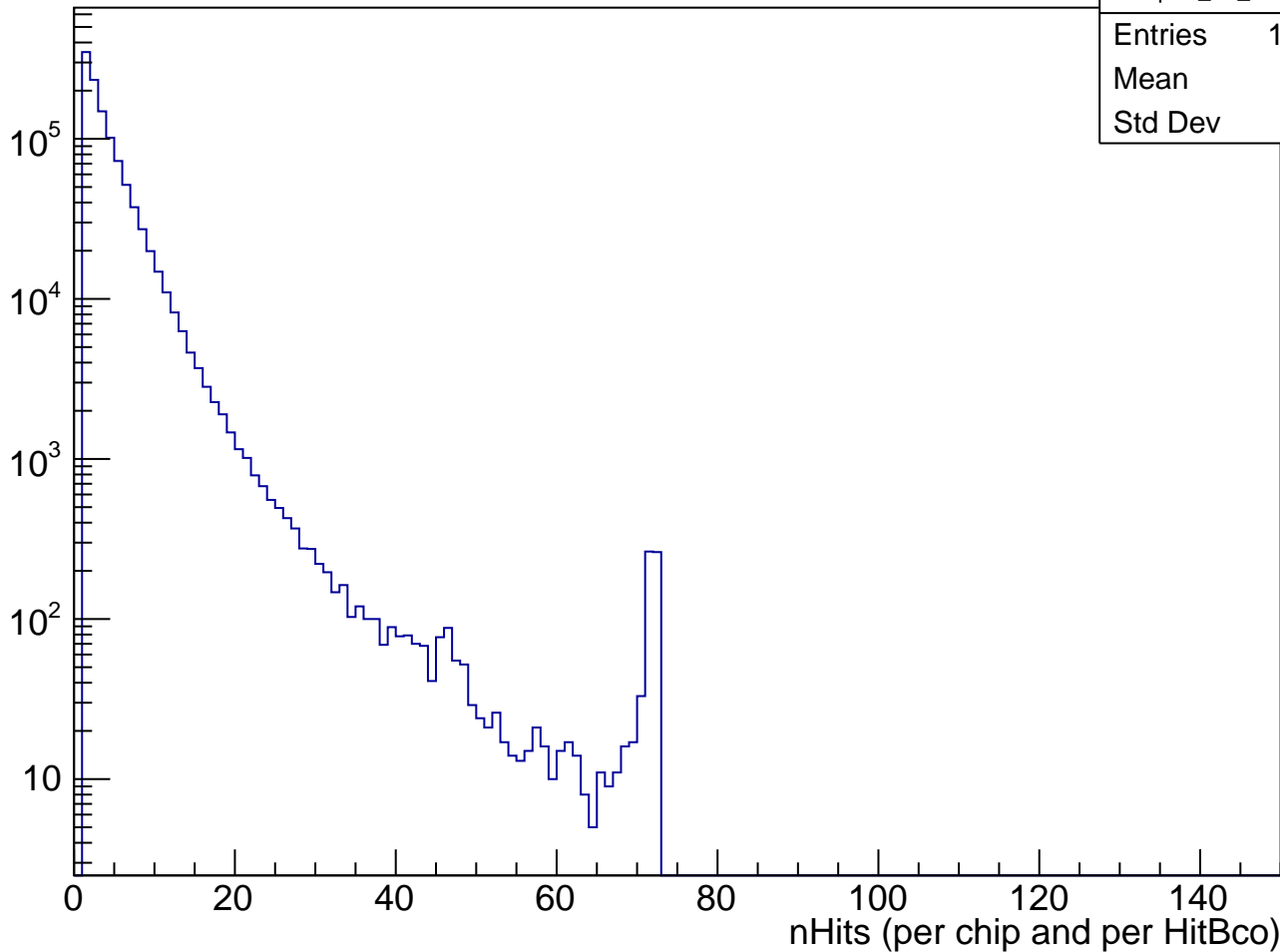
Entries



nChipHit_F1_Fch2_Chip12	
Entries	1032934
Mean	3.078
Std Dev	3.527

nChipHit_F1_Fch2_Chip13

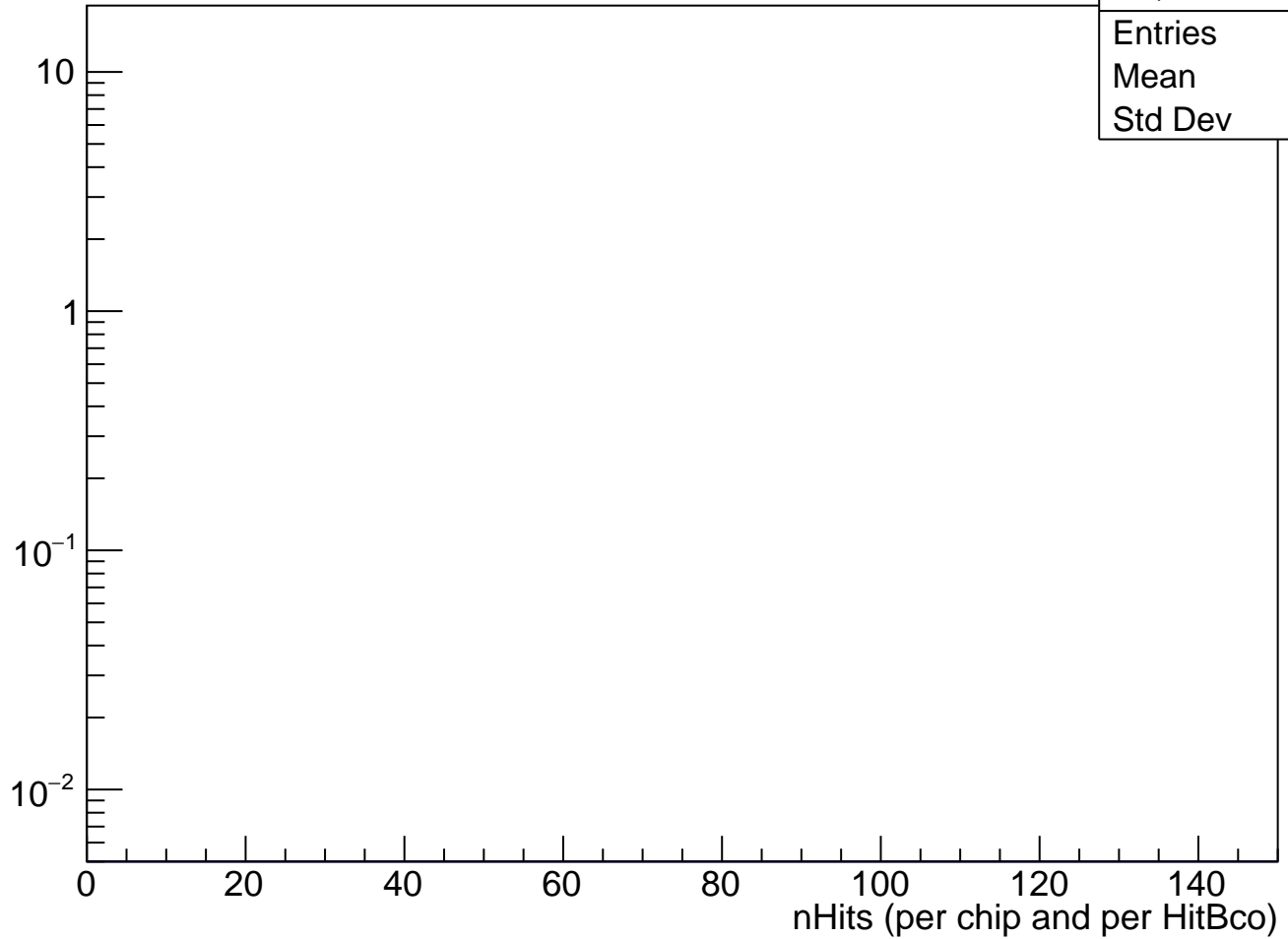
Entries



nChipHit_F1_Fch2_Chip13	
Entries	1107217
Mean	3.599
Std Dev	4.029

nChipHit_F1_Fch2_Chip14

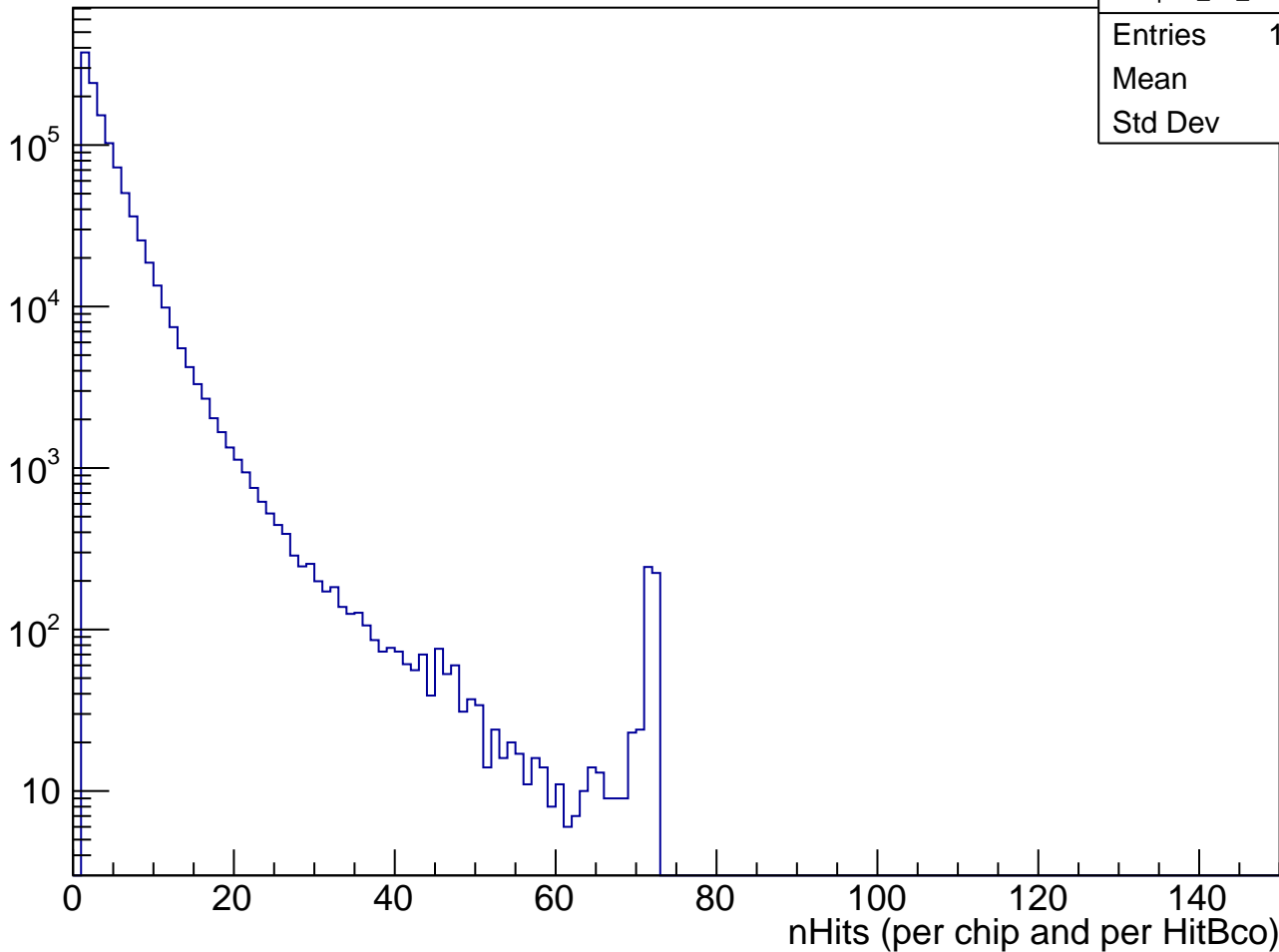
Entries



nChipHit_F1_Fch2_Chip14	
Entries	0
Mean	0
Std Dev	0

nChipHit_F1_Fch2_Chip15

Entries

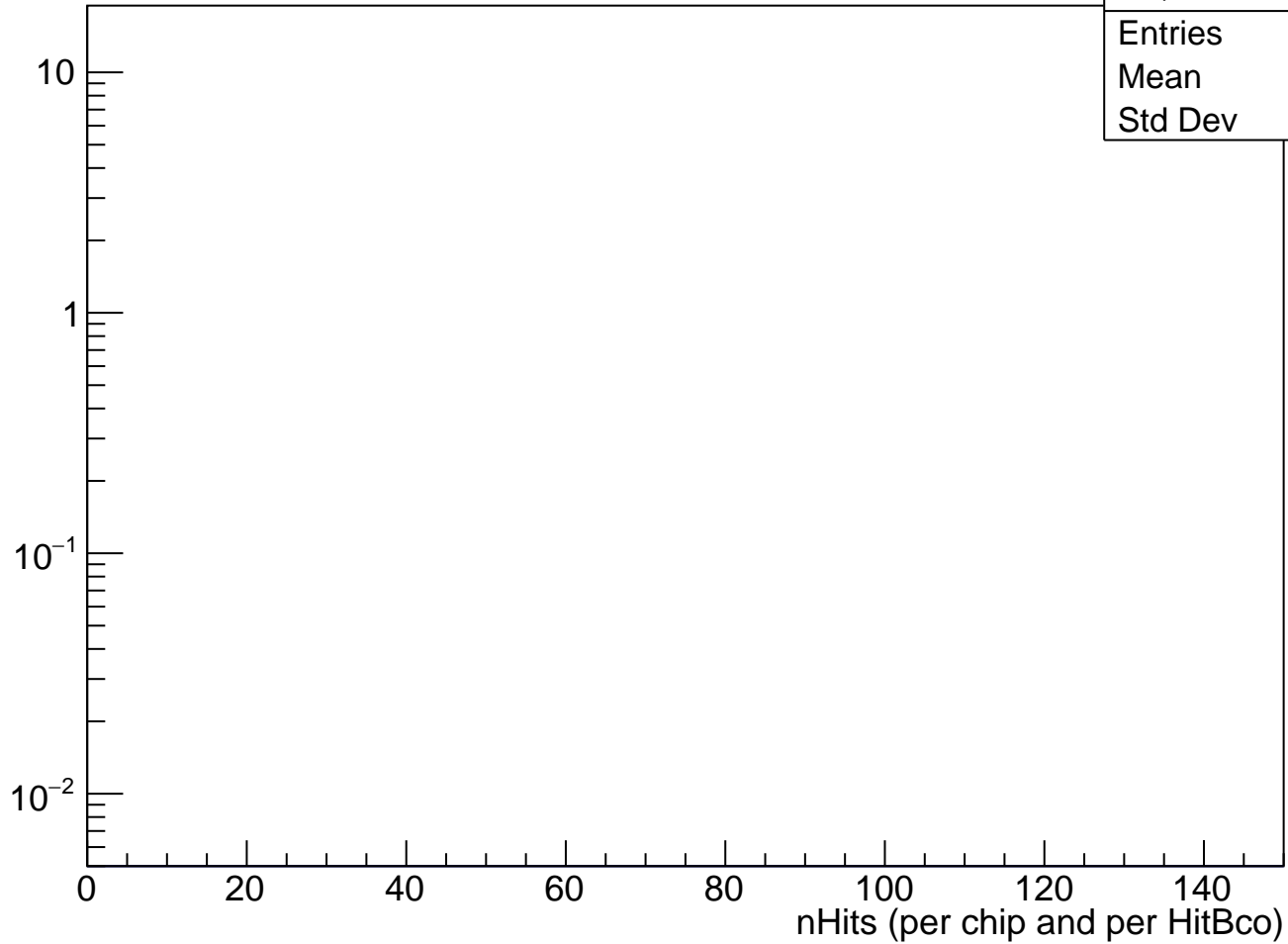


nChipHit_F1_Fch2_Chip15	
Entries	1135423
Mean	3.448
Std Dev	3.856

nHits (per chip and per HitBco)

nChipHit_F1_Fch2_Chip16

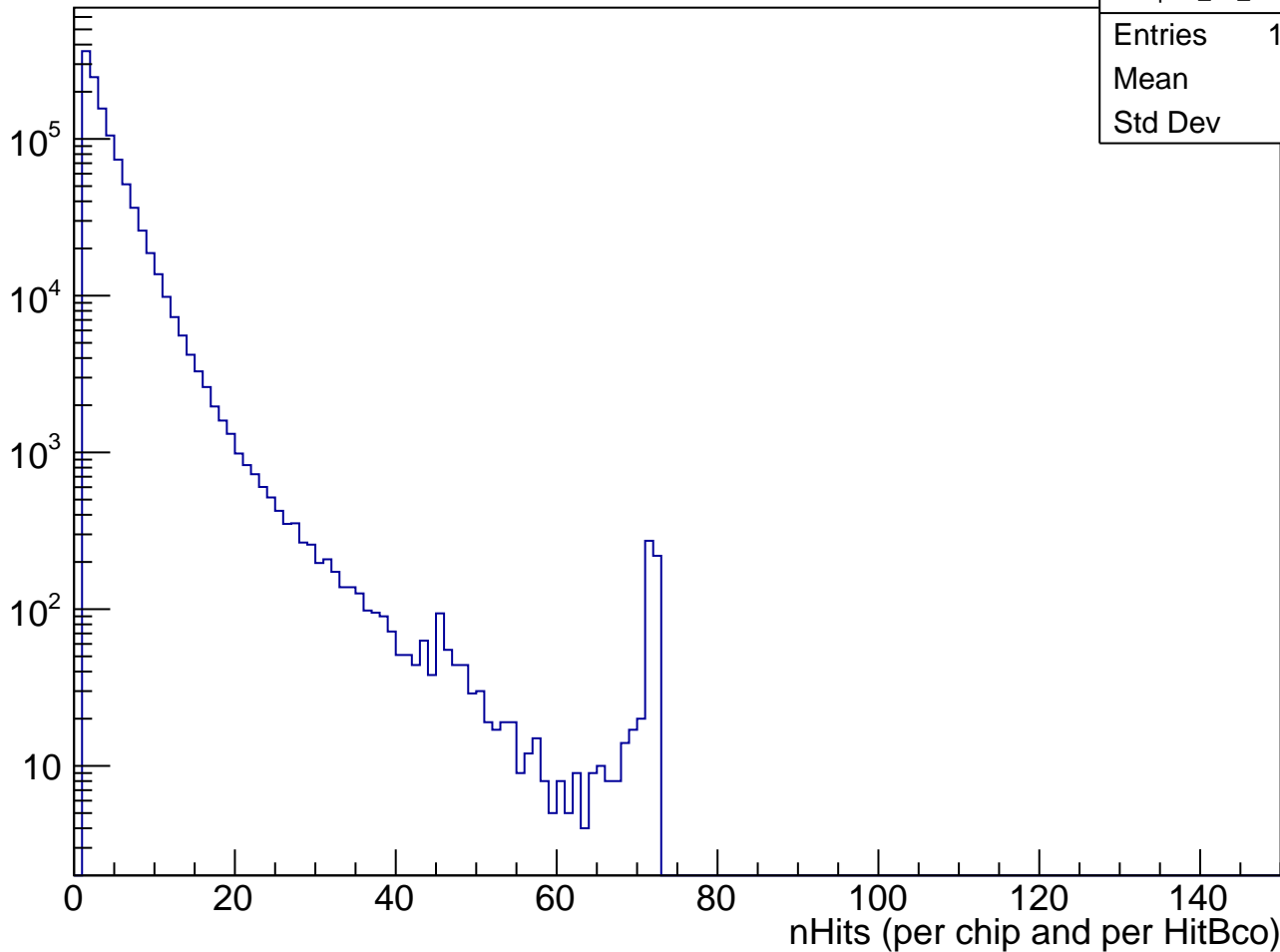
Entries



nChipHit_F1_Fch2_Chip16	
Entries	0
Mean	0
Std Dev	0

nChipHit_F1_Fch2_Chip17

Entries

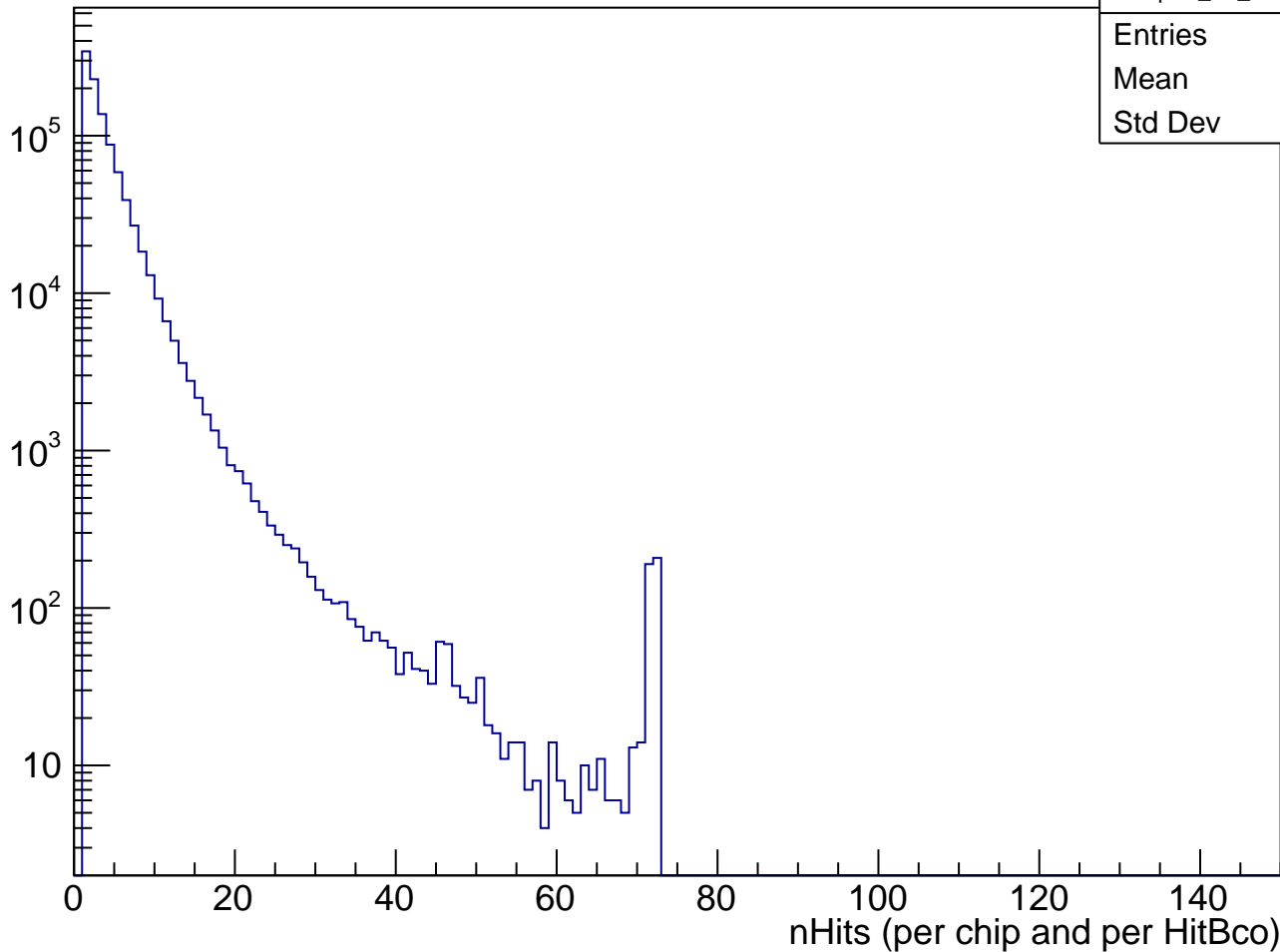


nChipHit_F1_Fch2_Chip17	
Entries	1138029
Mean	3.463
Std Dev	3.828

nHits (per chip and per HitBco)

nChipHit_F1_Fch2_Chip18

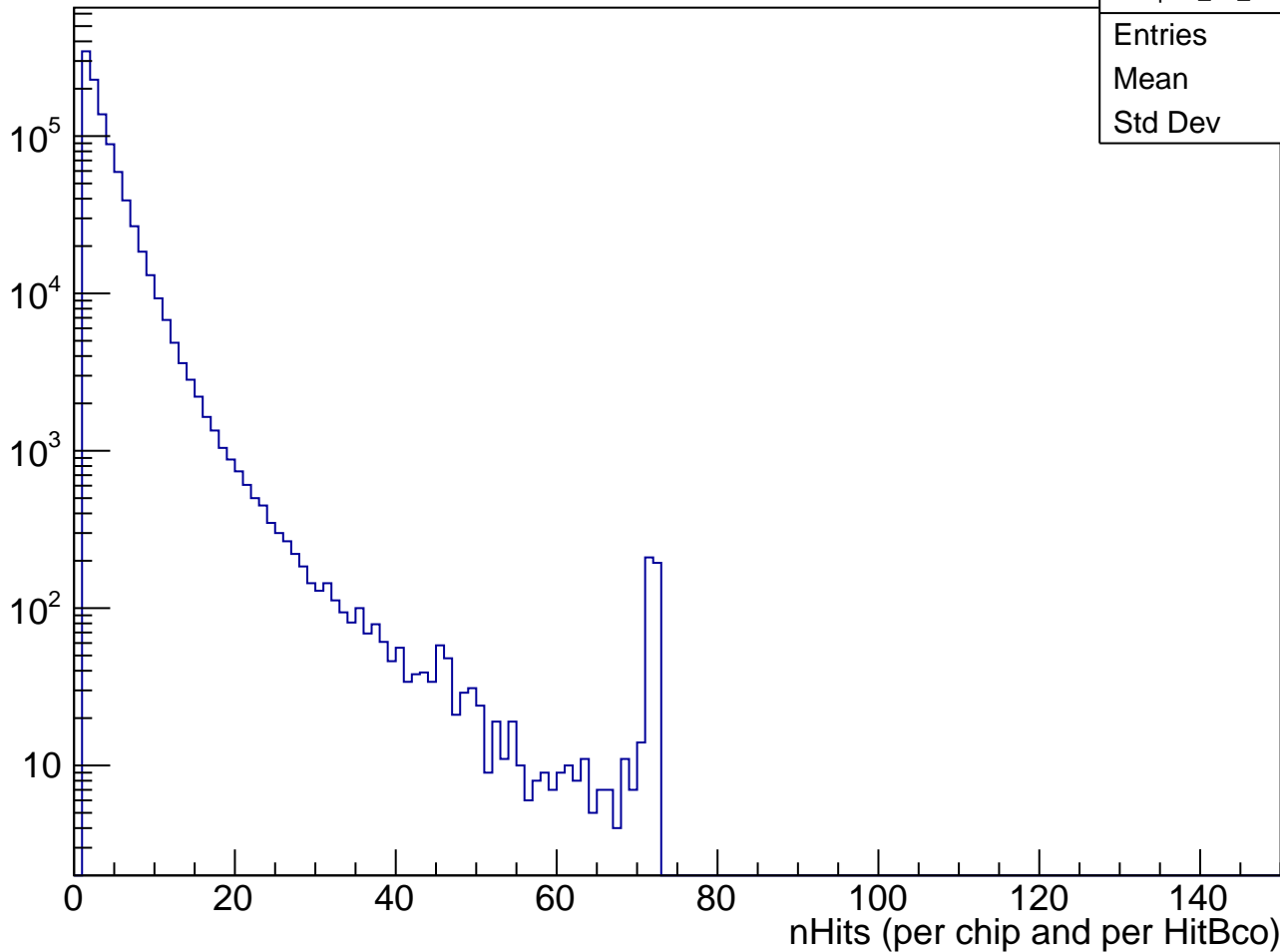
Entries



nChipHit_F1_Fch2_Chip18	
Entries	992663
Mean	3.168
Std Dev	3.553

nChipHit_F1_Fch2_Chip19

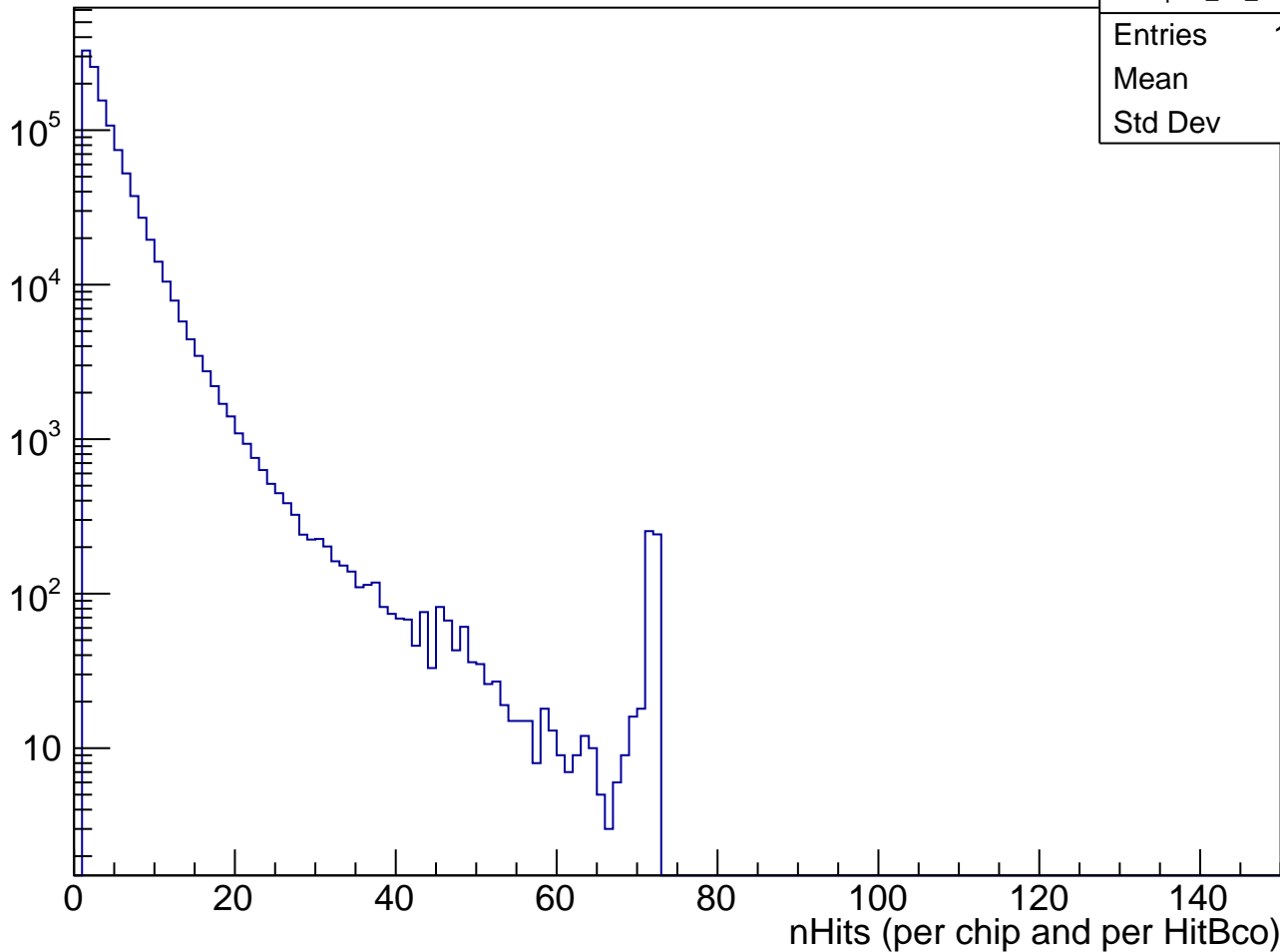
Entries



nChipHit_F1_Fch2_Chip19	
Entries	996809
Mean	3.168
Std Dev	3.551

nChipHit_F1_Fch2_Chip2

Entries



nChipHit_F1_Fch2_Chip2

Entries 1120661

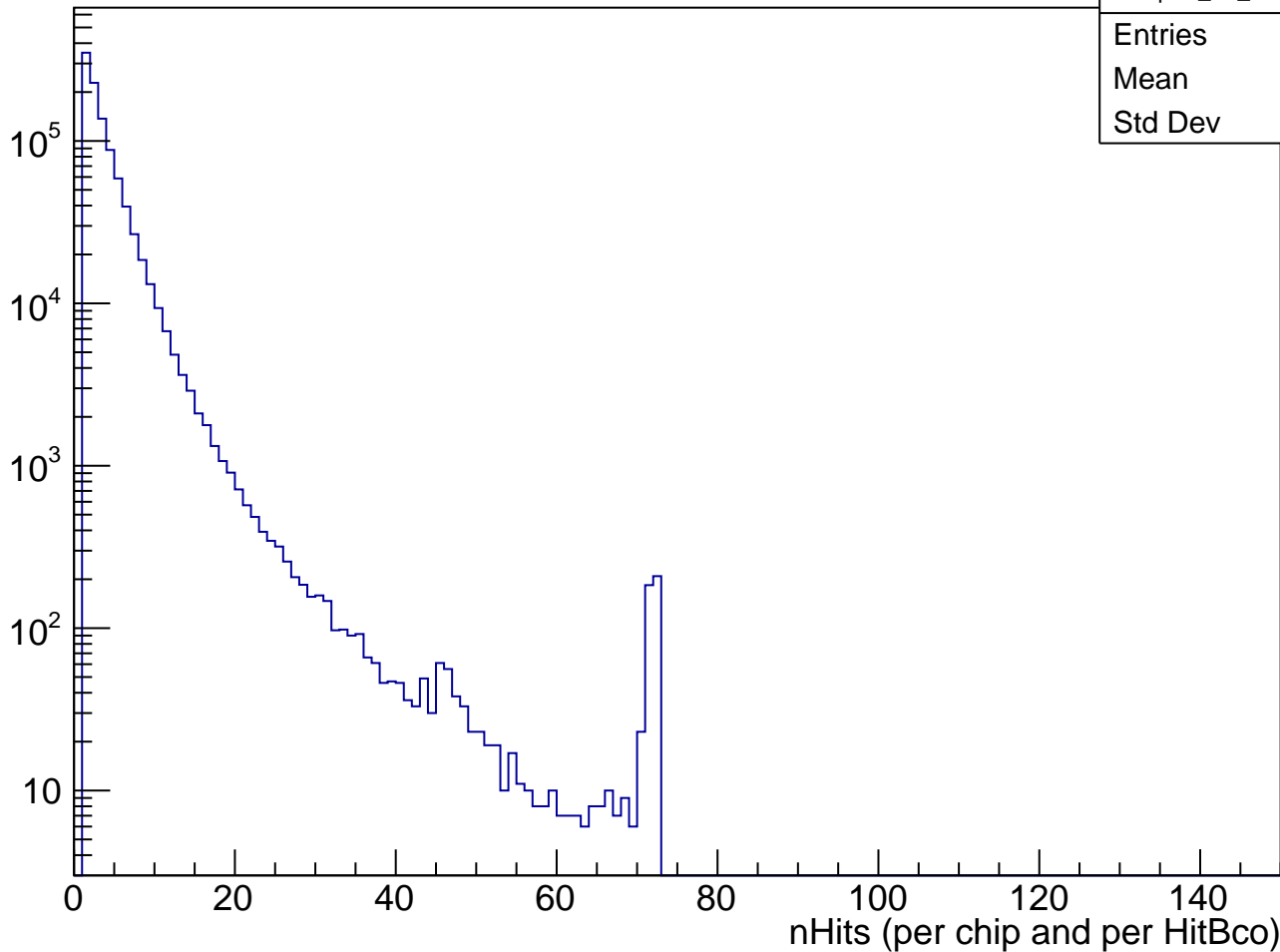
Mean 3.577

Std Dev 3.905

nHits (per chip and per HitBco)

nChipHit_F1_Fch2_Chip20

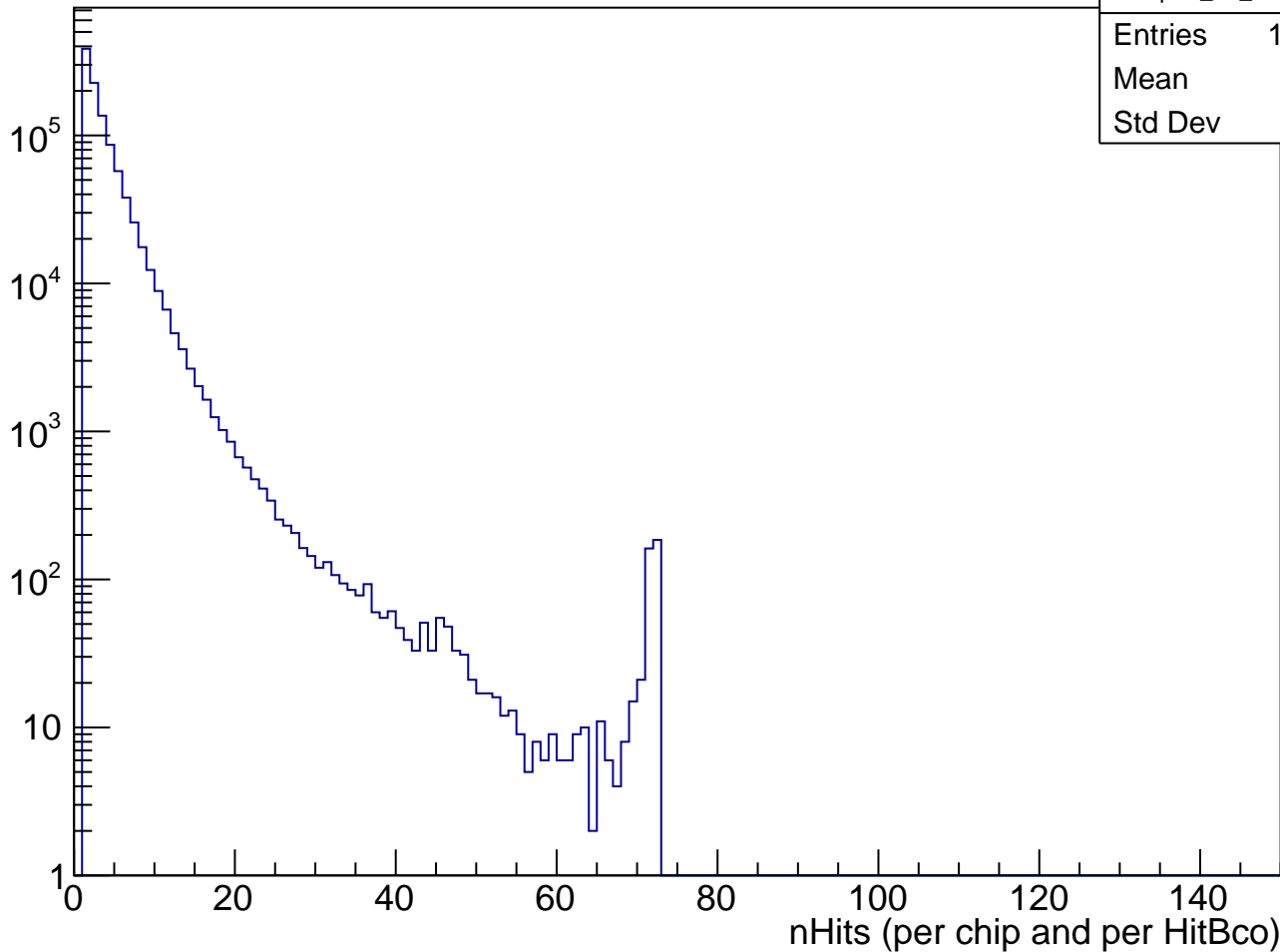
Entries



nChipHit_F1_Fch2_Chip20	
Entries	999390
Mean	3.161
Std Dev	3.549

nChipHit_F1_Fch2_Chip21

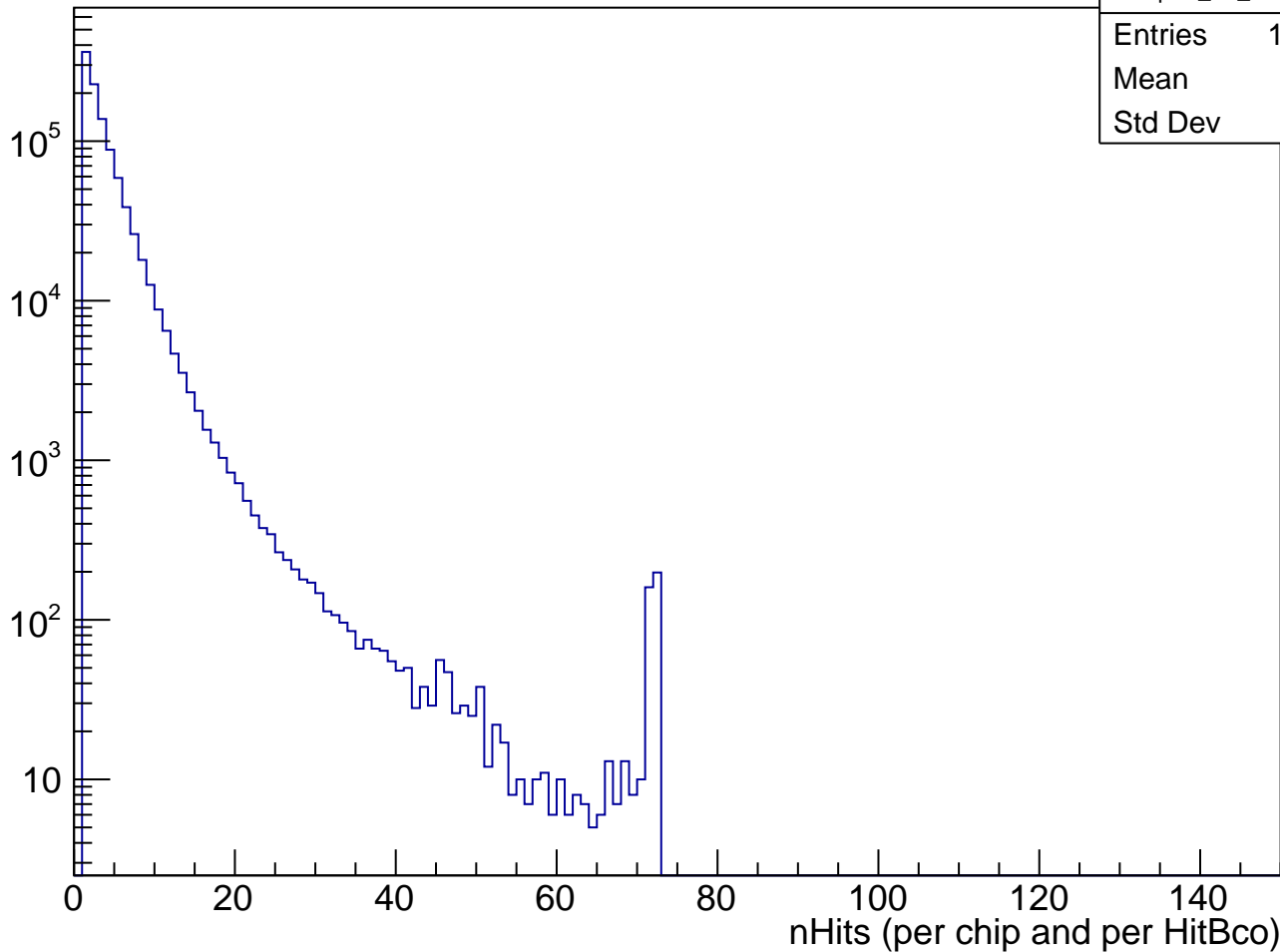
Entries



nChipHit_F1_Fch2_Chip21	
Entries	1023754
Mean	3.045
Std Dev	3.444

nChipHit_F1_Fch2_Chip22

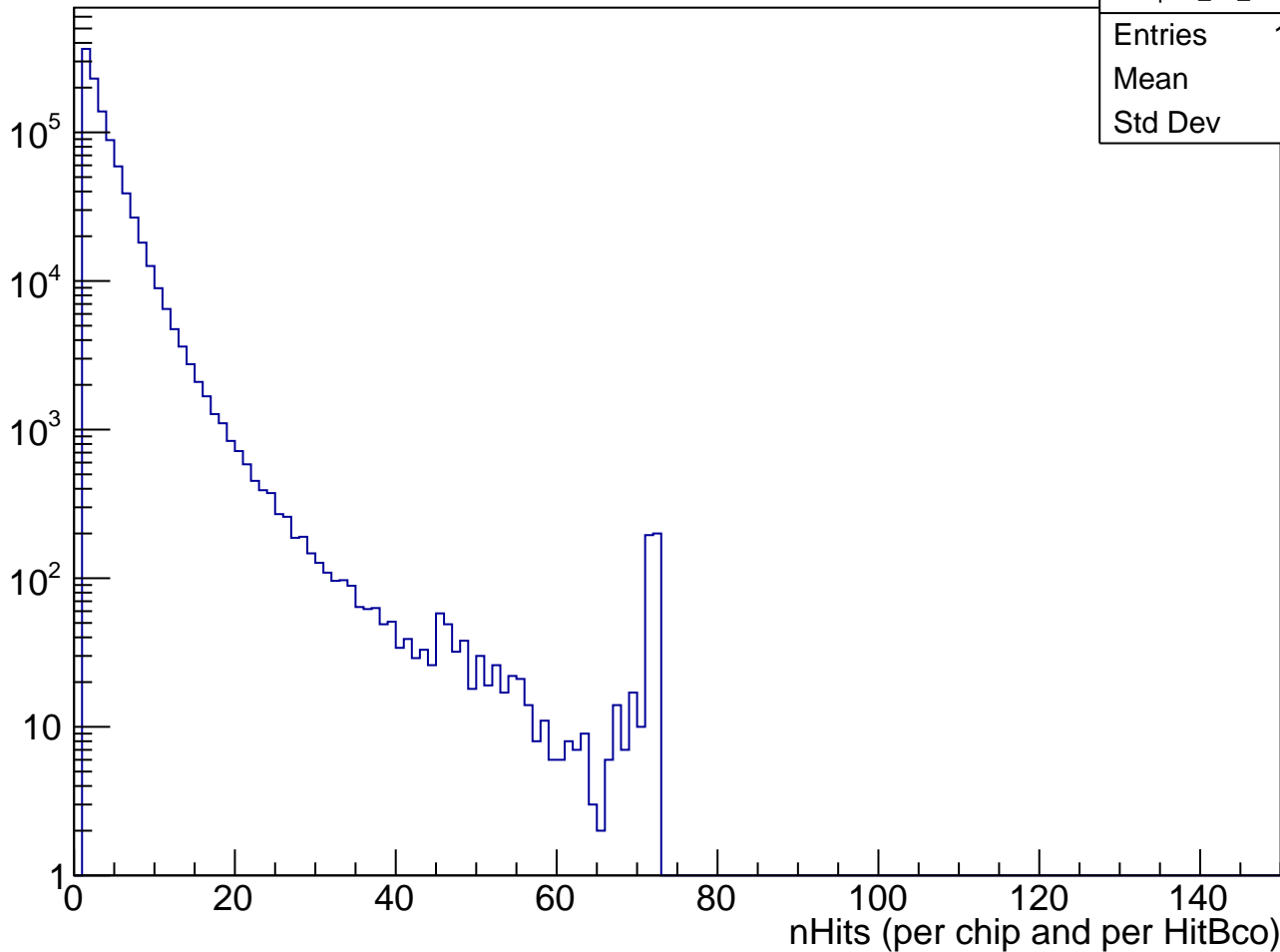
Entries



nChipHit_F1_Fch2_Chip22	
Entries	1008155
Mean	3.101
Std Dev	3.469

nChipHit_F1_Fch2_Chip23

Entries

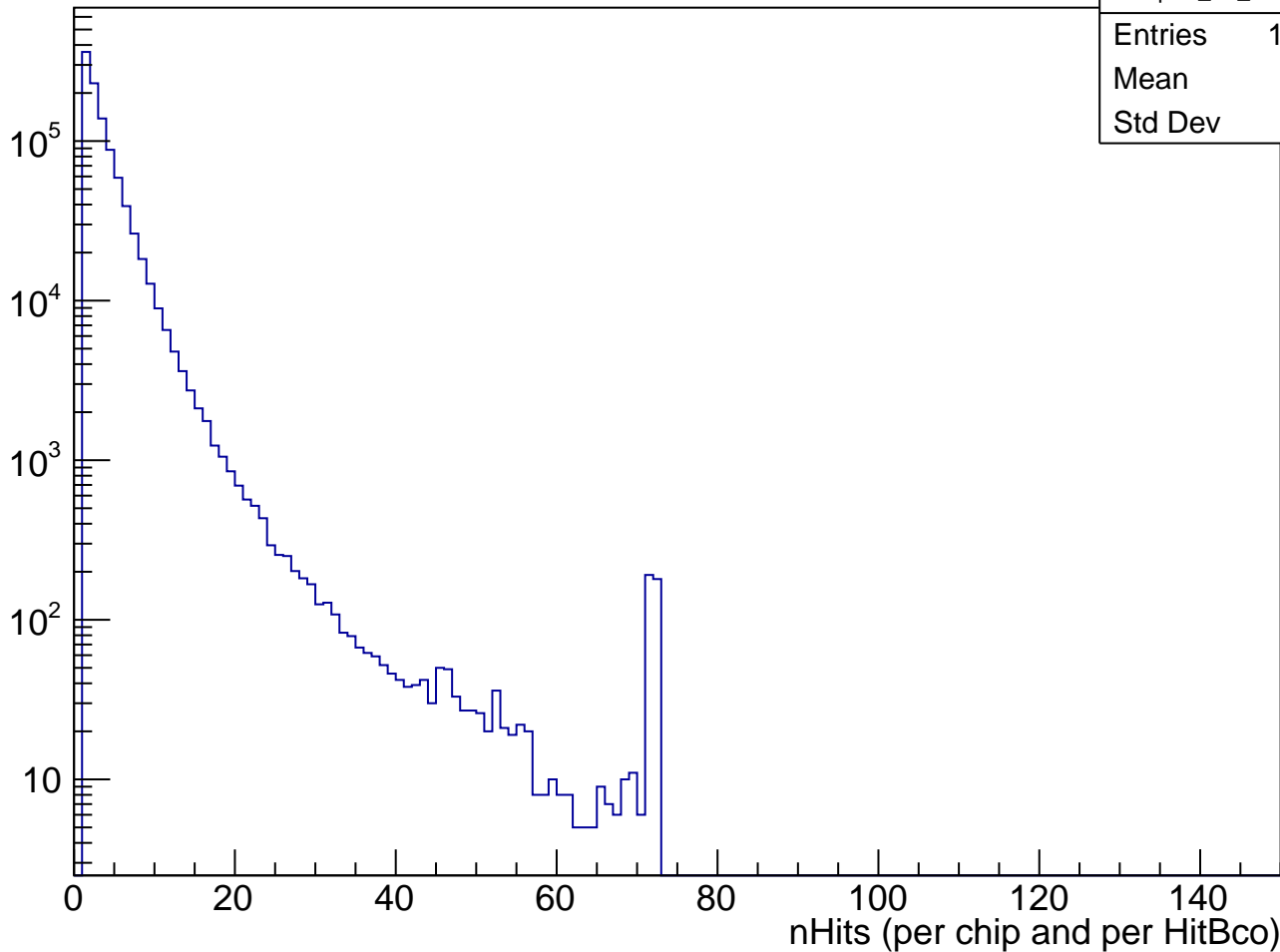


nChipHit_F1_Fch2_Chip23	
Entries	1015791
Mean	3.107
Std Dev	3.496

nHits (per chip and per HitBco)

nChipHit_F1_Fch2_Chip24

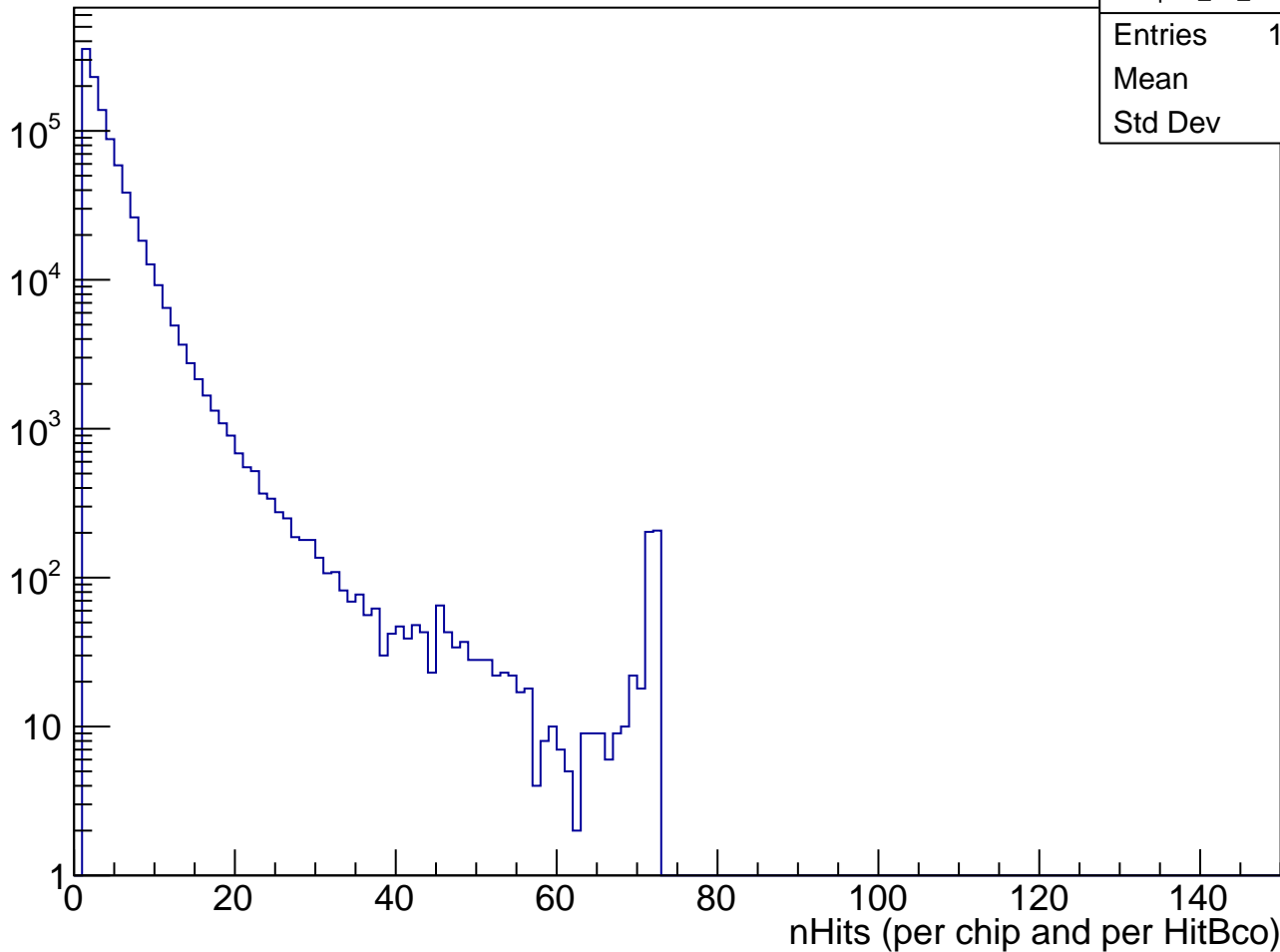
Entries



nChipHit_F1_Fch2_Chip24	
Entries	1012936
Mean	3.112
Std Dev	3.489

nChipHit_F1_Fch2_Chip25

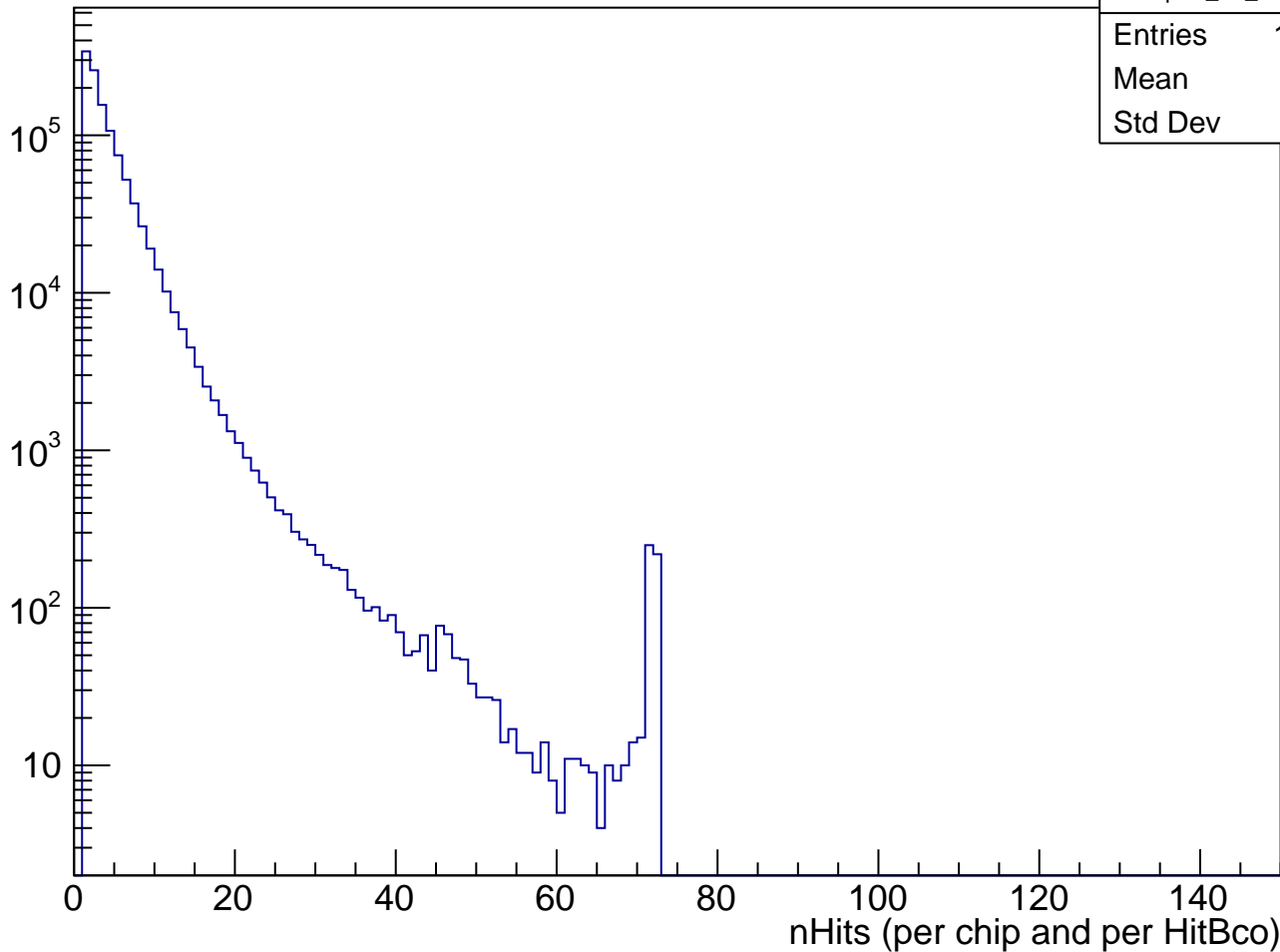
Entries



nChipHit_F1_Fch2_Chip25	
Entries	1005246
Mean	3.133
Std Dev	3.542

nChipHit_F1_Fch2_Chip3

Entries



nChipHit_F1_Fch2_Chip3

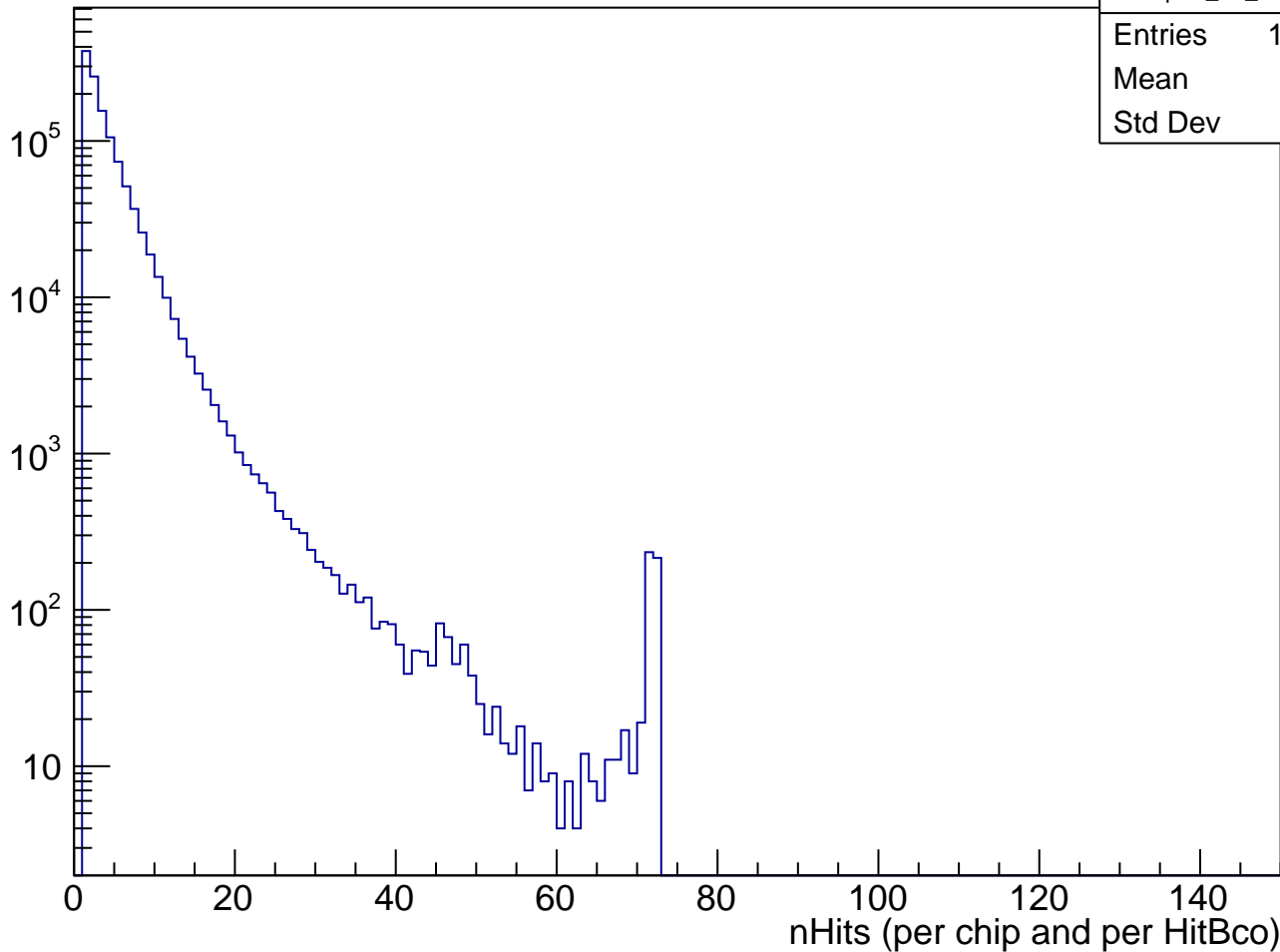
Entries 1133231

Mean 3.524

Std Dev 3.851

nChipHit_F1_Fch2_Chip4

Entries



nChipHit_F1_Fch2_Chip4

Entries 1161272

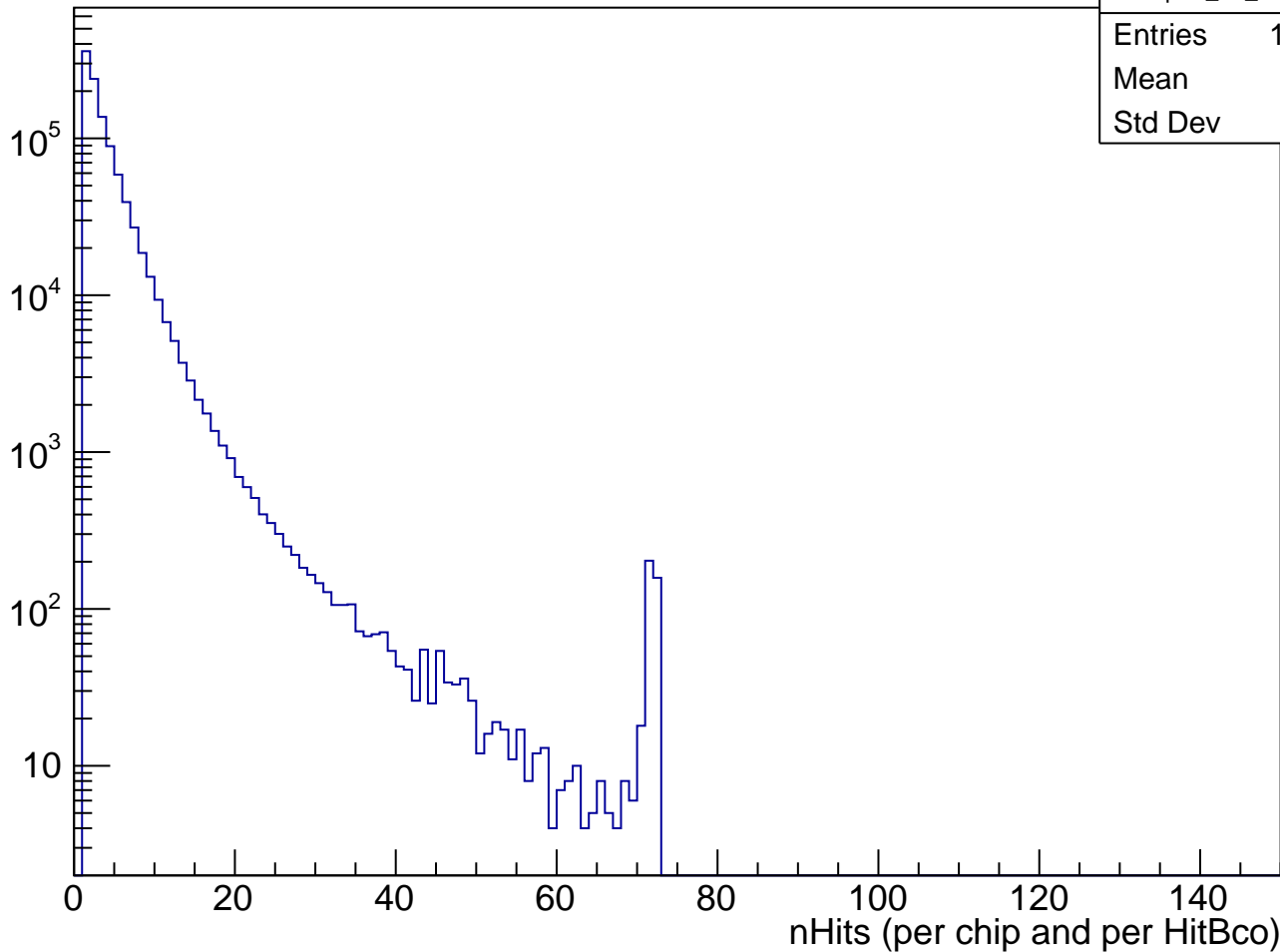
Mean 3.421

Std Dev 3.786

nHits (per chip and per HitBco)

nChipHit_F1_Fch2_Chip5

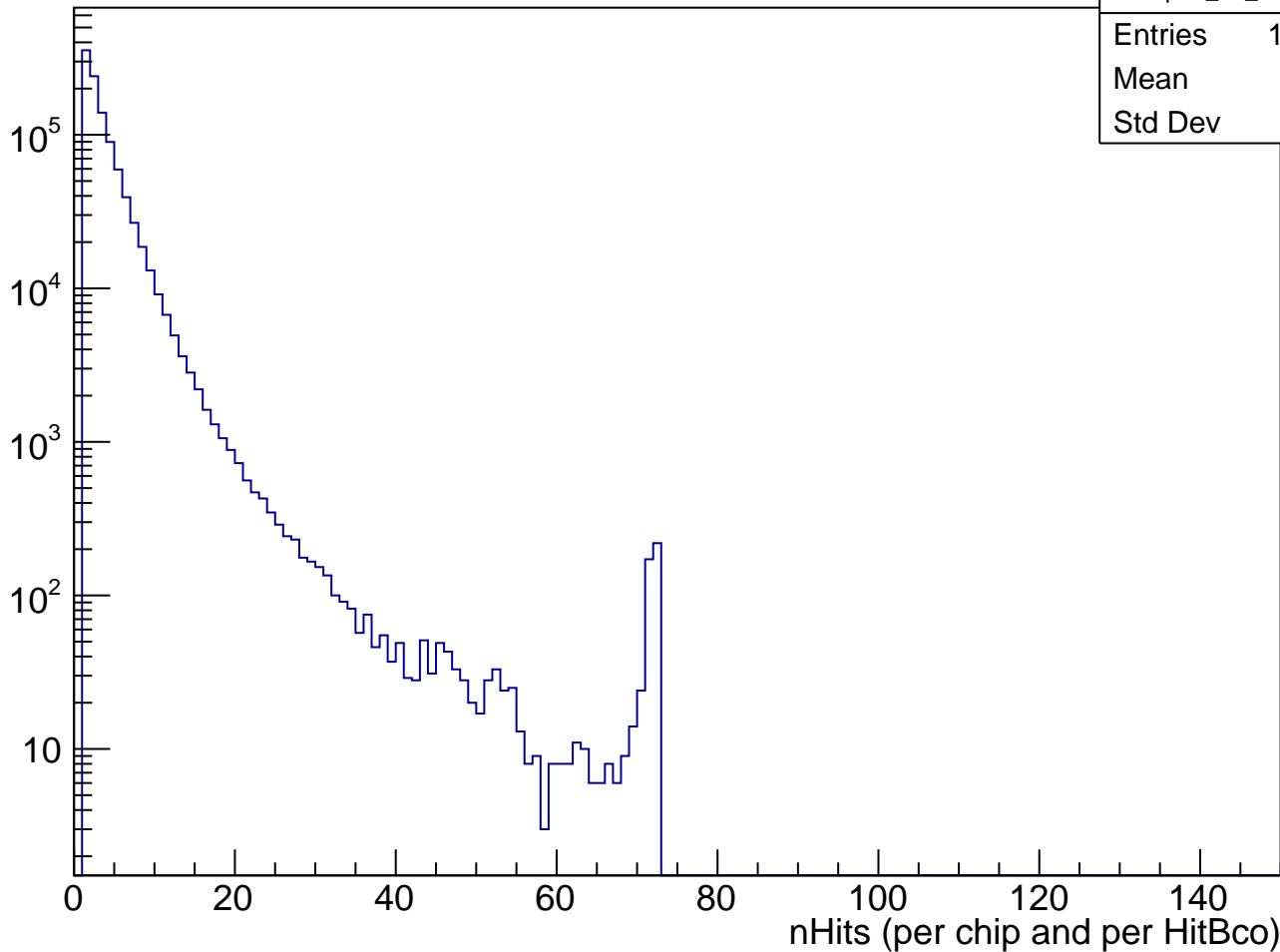
Entries



nChipHit_F1_Fch2_Chip5	
Entries	1022413
Mean	3.13
Std Dev	3.493

nChipHit_F1_Fch2_Chip6

Entries



nChipHit_F1_Fch2_Chip6

Entries 1022137

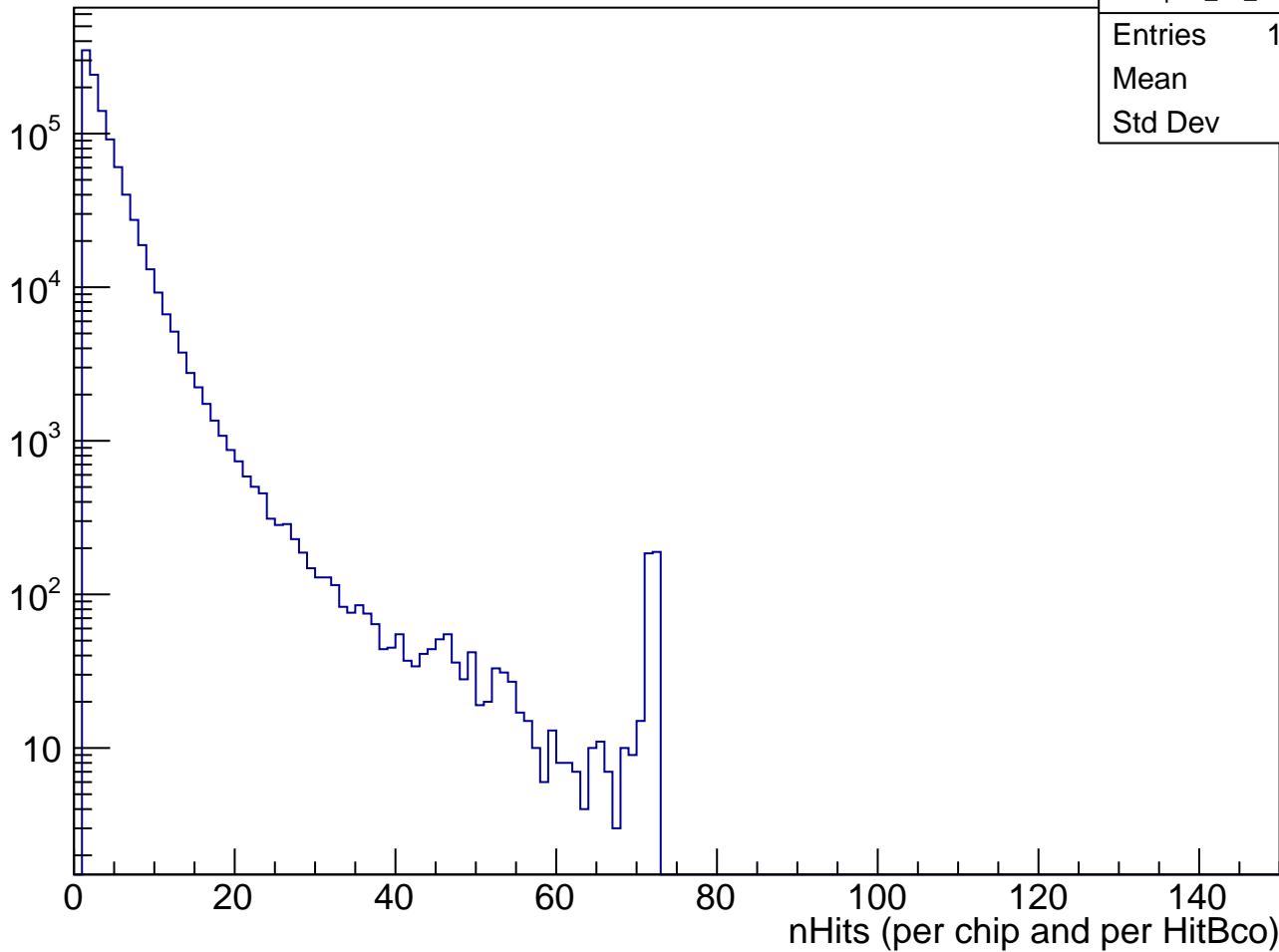
Mean 3.131

Std Dev 3.507

nHits (per chip and per HitBco)

nChipHit_F1_Fch2_Chip7

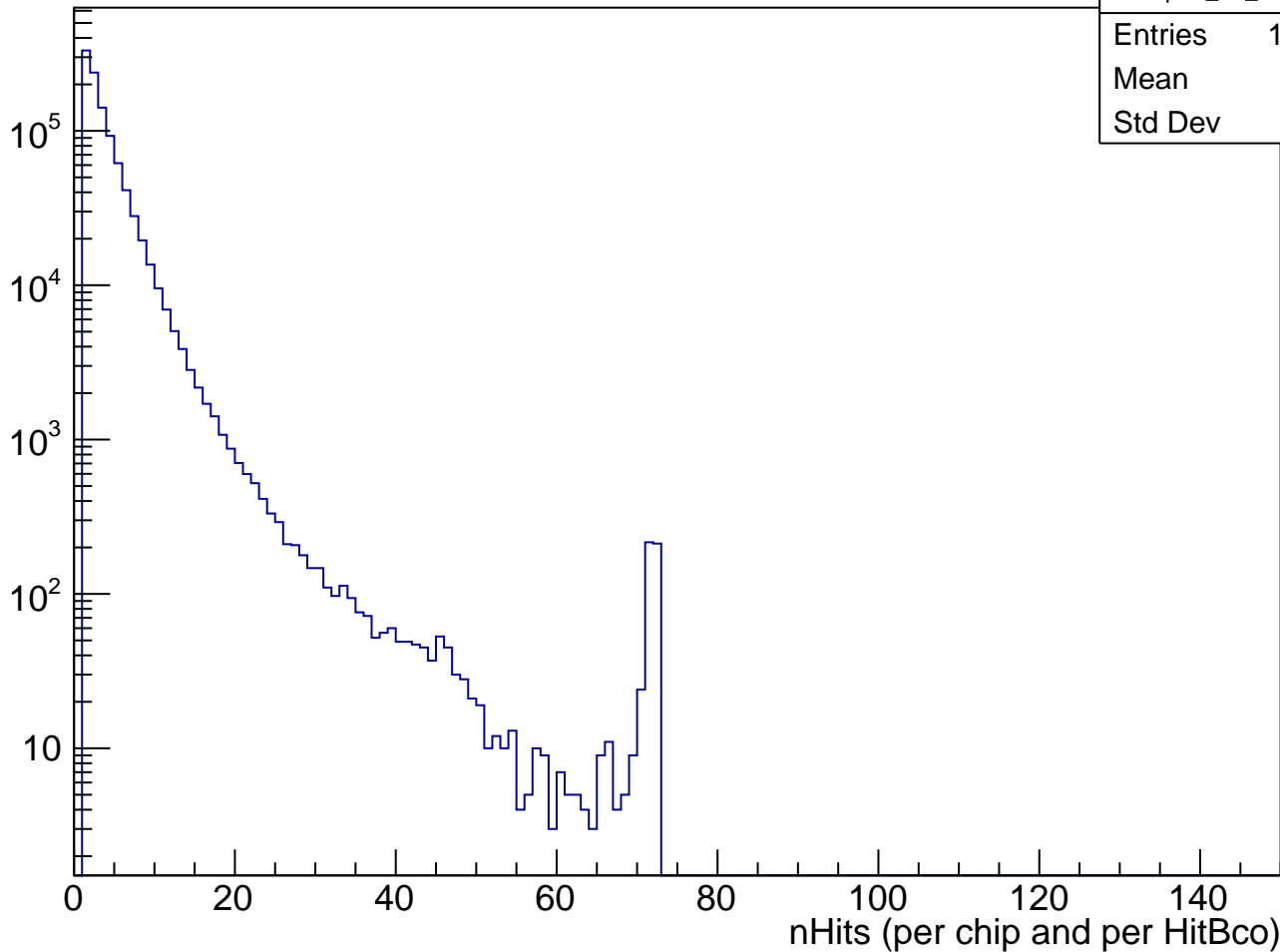
Entries



nChipHit_F1_Fch2_Chip7	
Entries	1023216
Mean	3.162
Std Dev	3.524

nChipHit_F1_Fch2_Chip8

Entries



nChipHit_F1_Fch2_Chip8

Entries 1009626

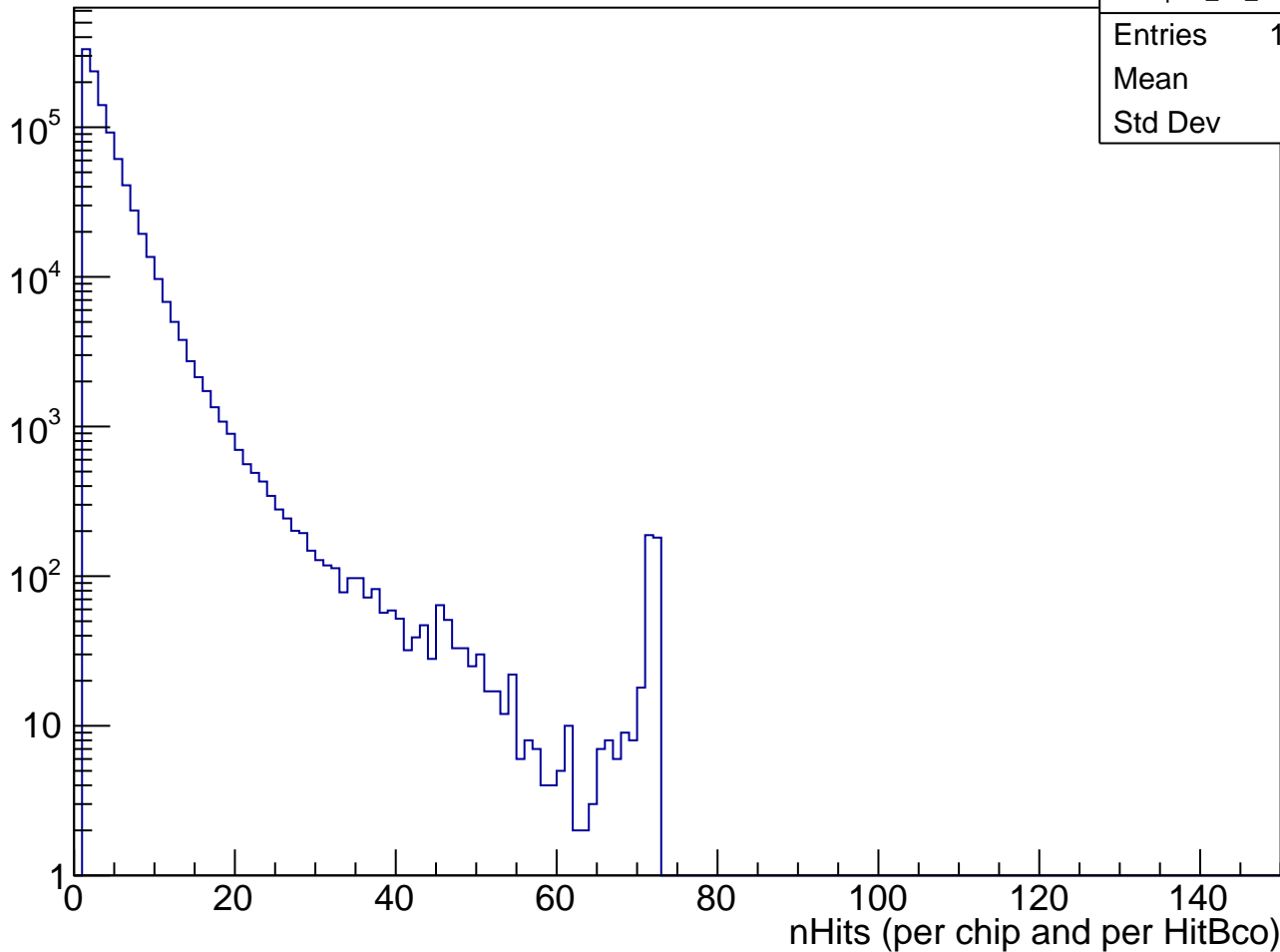
Mean 3.218

Std Dev 3.537

nHits (per chip and per HitBco)

nChipHit_F1_Fch2_Chip9

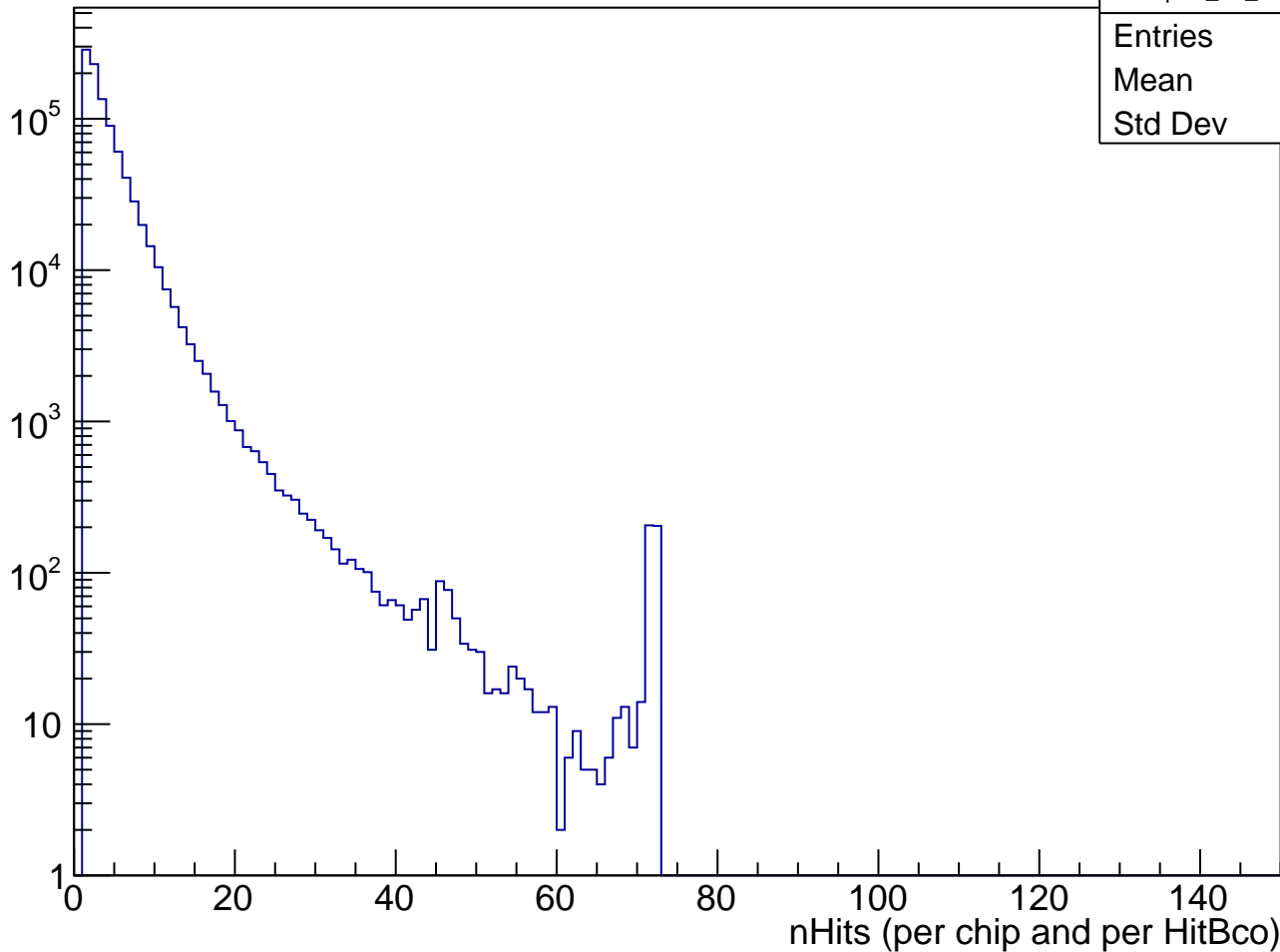
Entries



nChipHit_F1_Fch2_Chip9	
Entries	1005323
Mean	3.211
Std Dev	3.512

nChipHit_F1_Fch3_Chip0

Entries



nChipHit_F1_Fch3_Chip0

Entries 951951

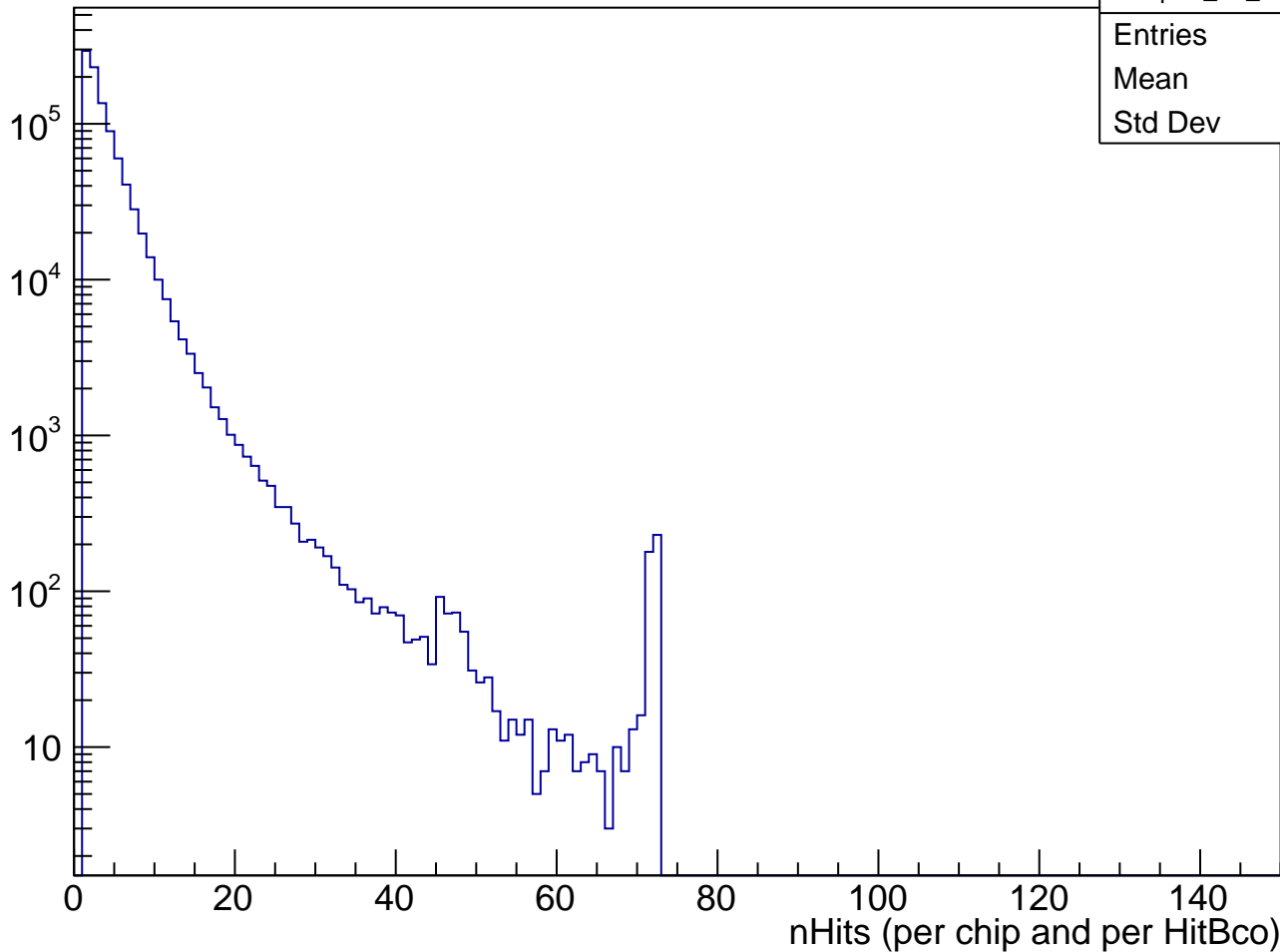
Mean 3.42

Std Dev 3.816

nHits (per chip and per HitBco)

nChipHit_F1_Fch3_Chip1

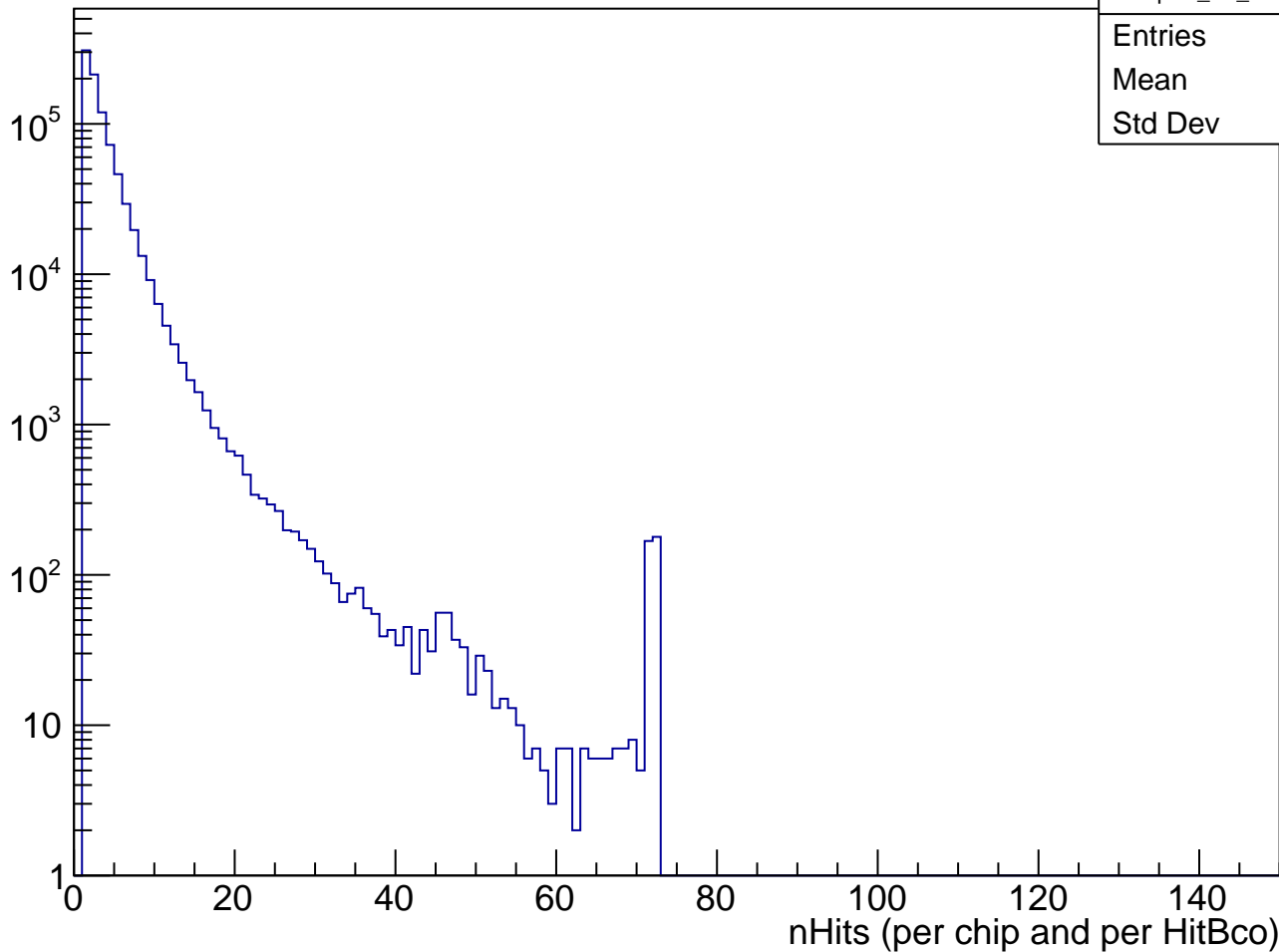
Entries



nChipHit_F1_Fch3_Chip1	
Entries	958499
Mean	3.388
Std Dev	3.8

nChipHit_F1_Fch3_Chip10

Entries

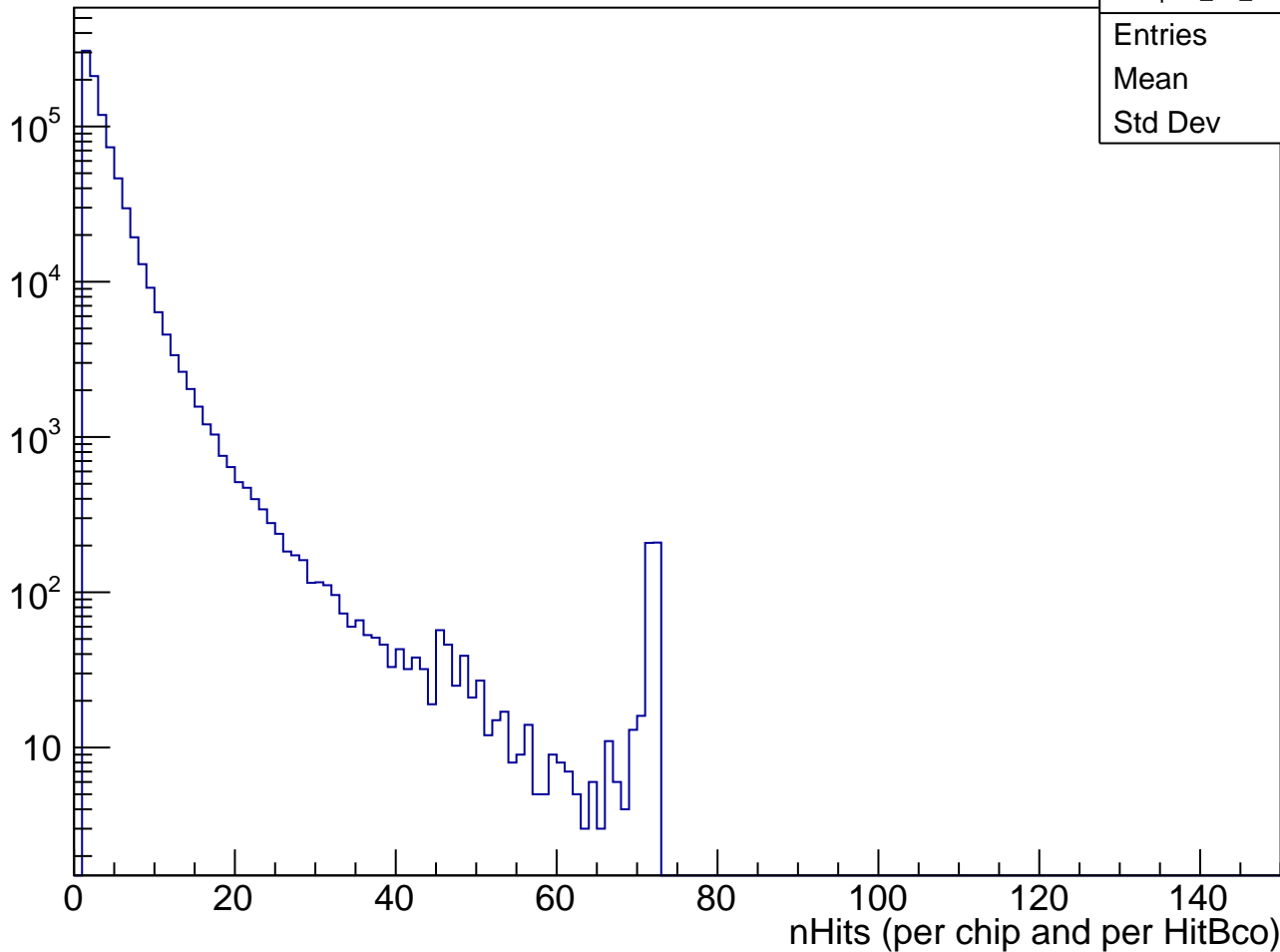


nChipHit_F1_Fch3_Chip10	
Entries	859669
Mean	2.985
Std Dev	3.427

nHits (per chip and per HitBco)

nChipHit_F1_Fch3_Chip11

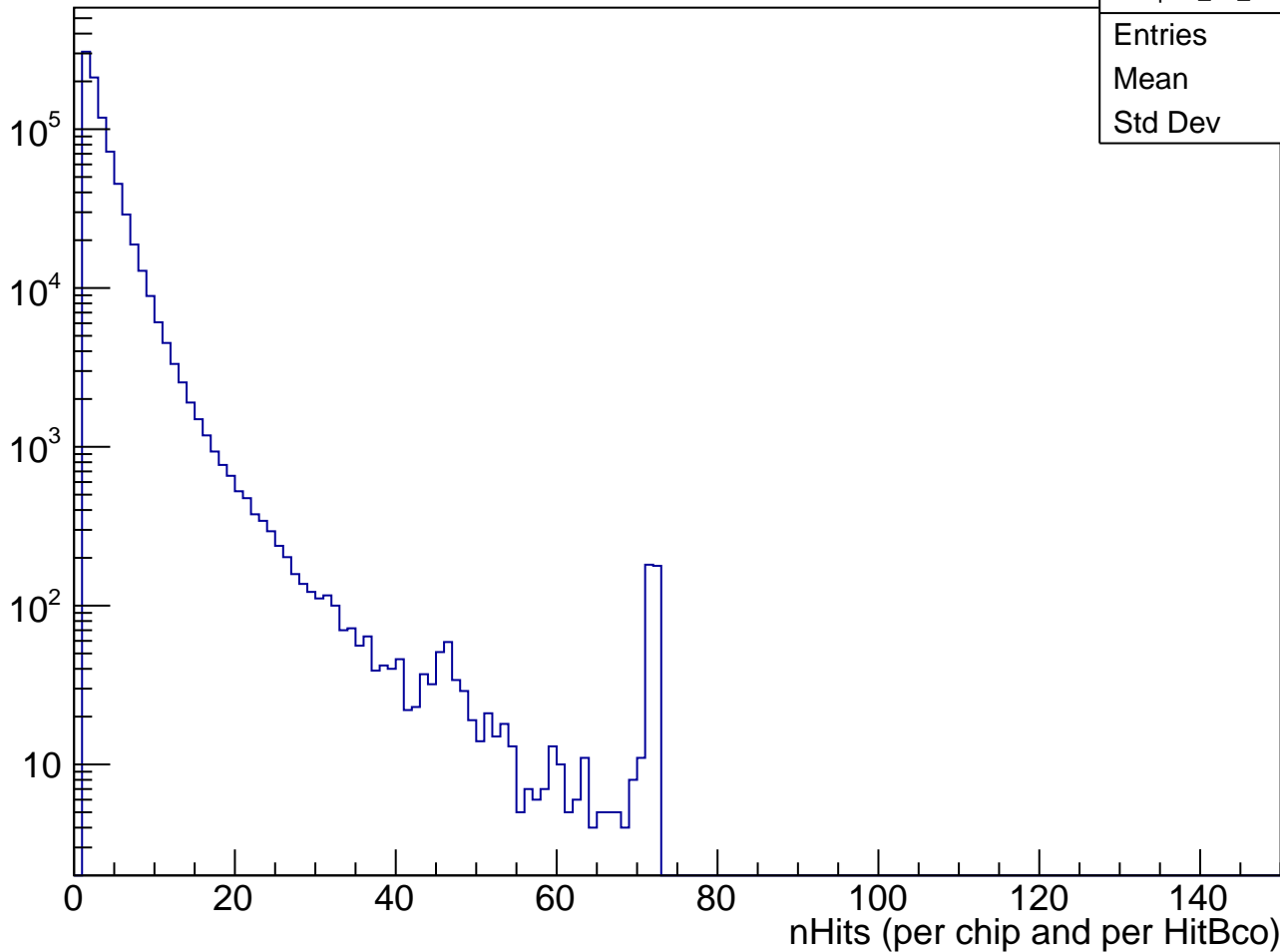
Entries



nChipHit_F1_Fch3_Chip11	
Entries	857074
Mean	2.989
Std Dev	3.472

nChipHit_F1_Fch3_Chip12

Entries

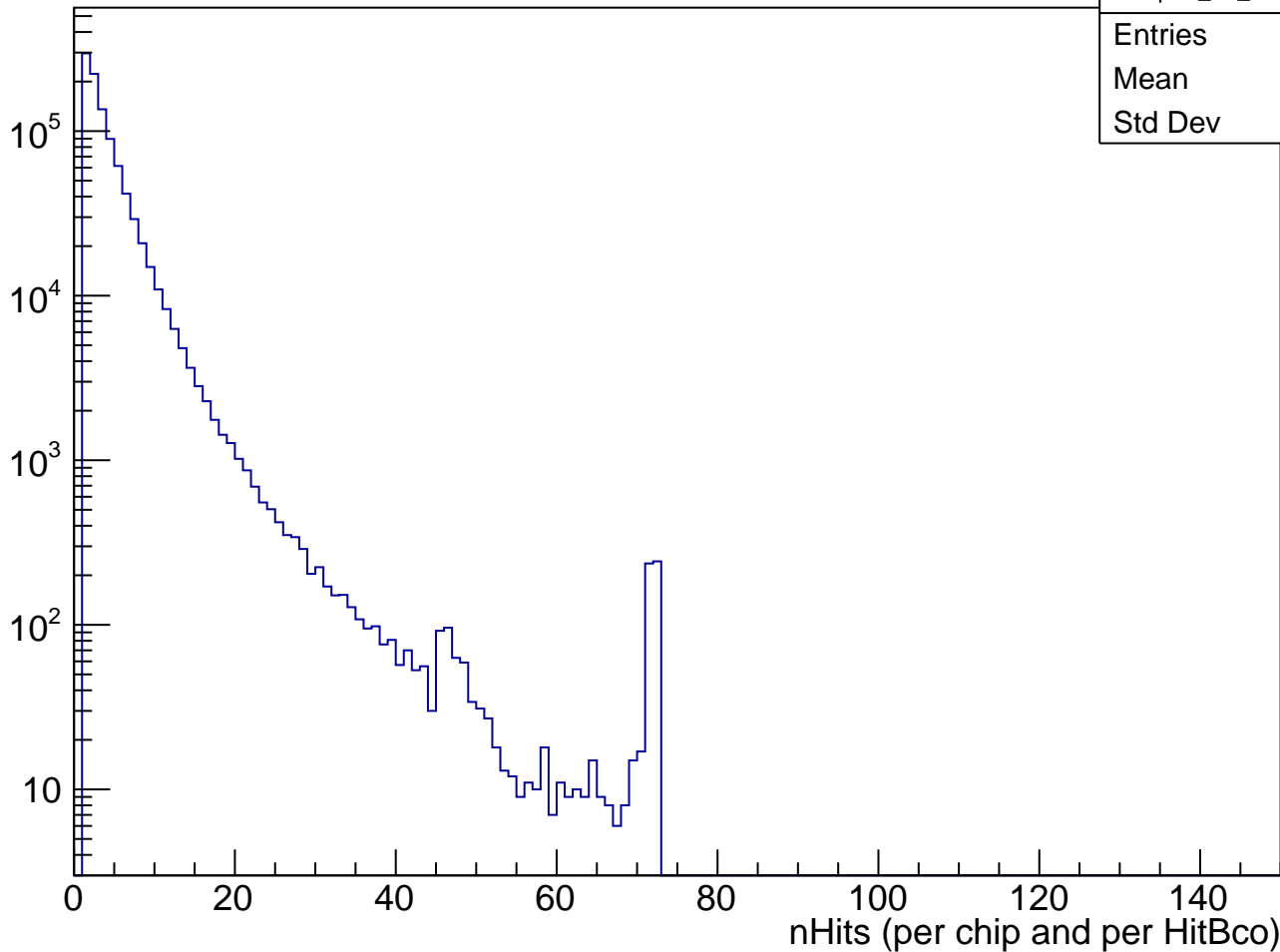


nChipHit_F1_Fch3_Chip12	
Entries	852222
Mean	2.963
Std Dev	3.41

nHits (per chip and per HitBco)

nChipHit_F1_Fch3_Chip13

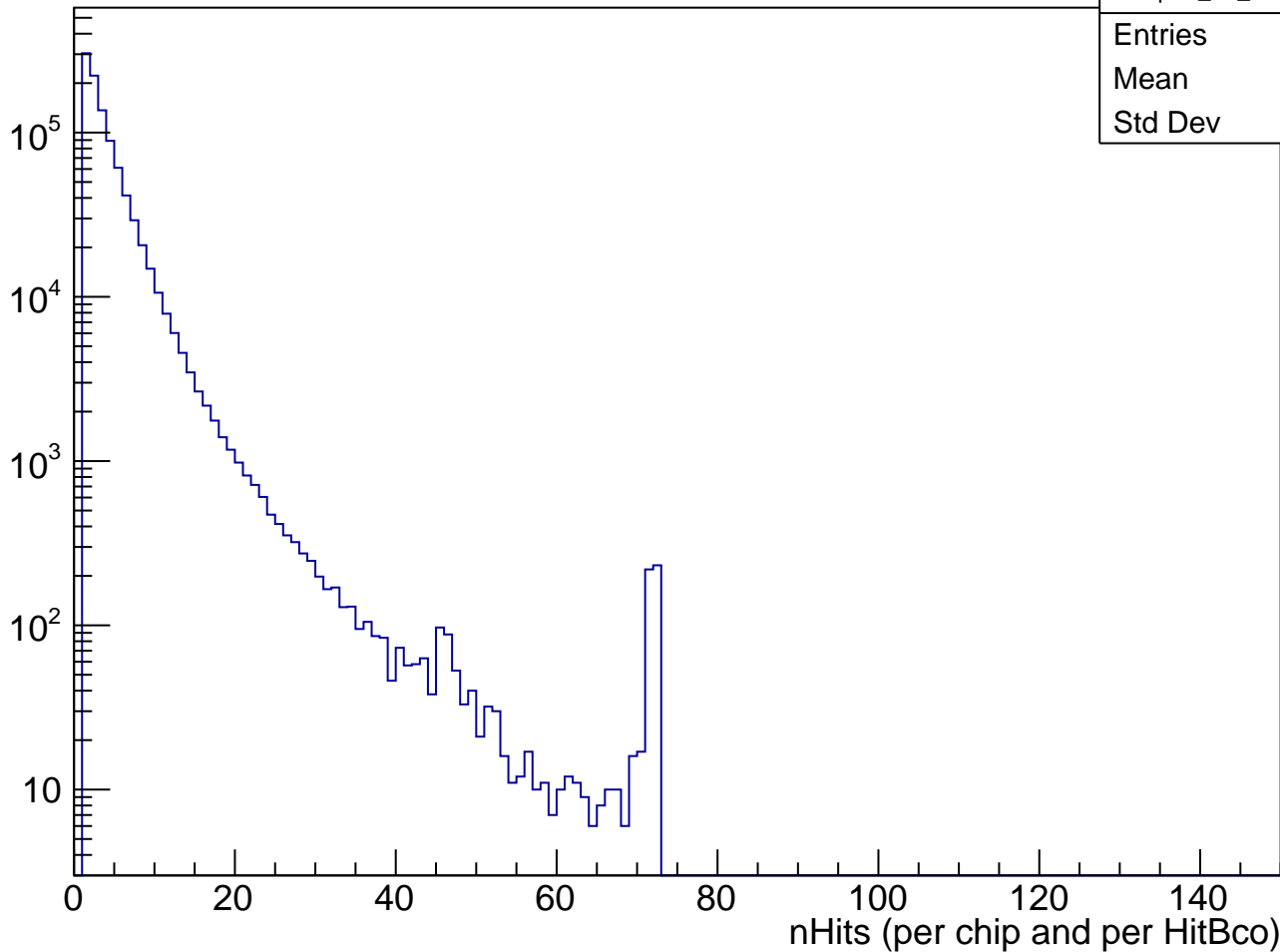
Entries



nChipHit_F1_Fch3_Chip13	
Entries	964366
Mean	3.484
Std Dev	3.975

nChipHit_F1_Fch3_Chip14

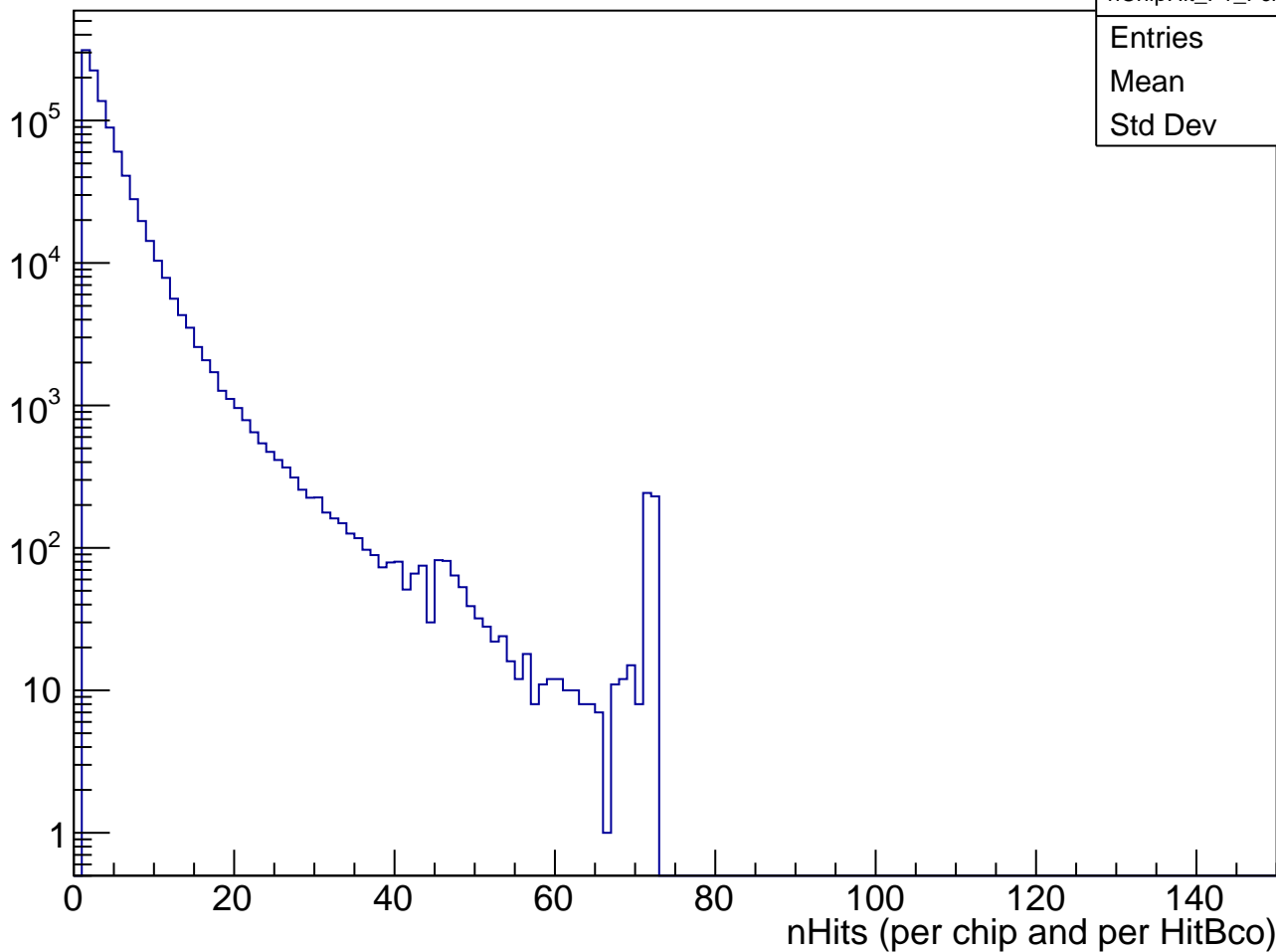
Entries



nChipHit_F1_Fch3_Chip14	
Entries	968696
Mean	3.441
Std Dev	3.92

nChipHit_F1_Fch3_Chip15

Entries

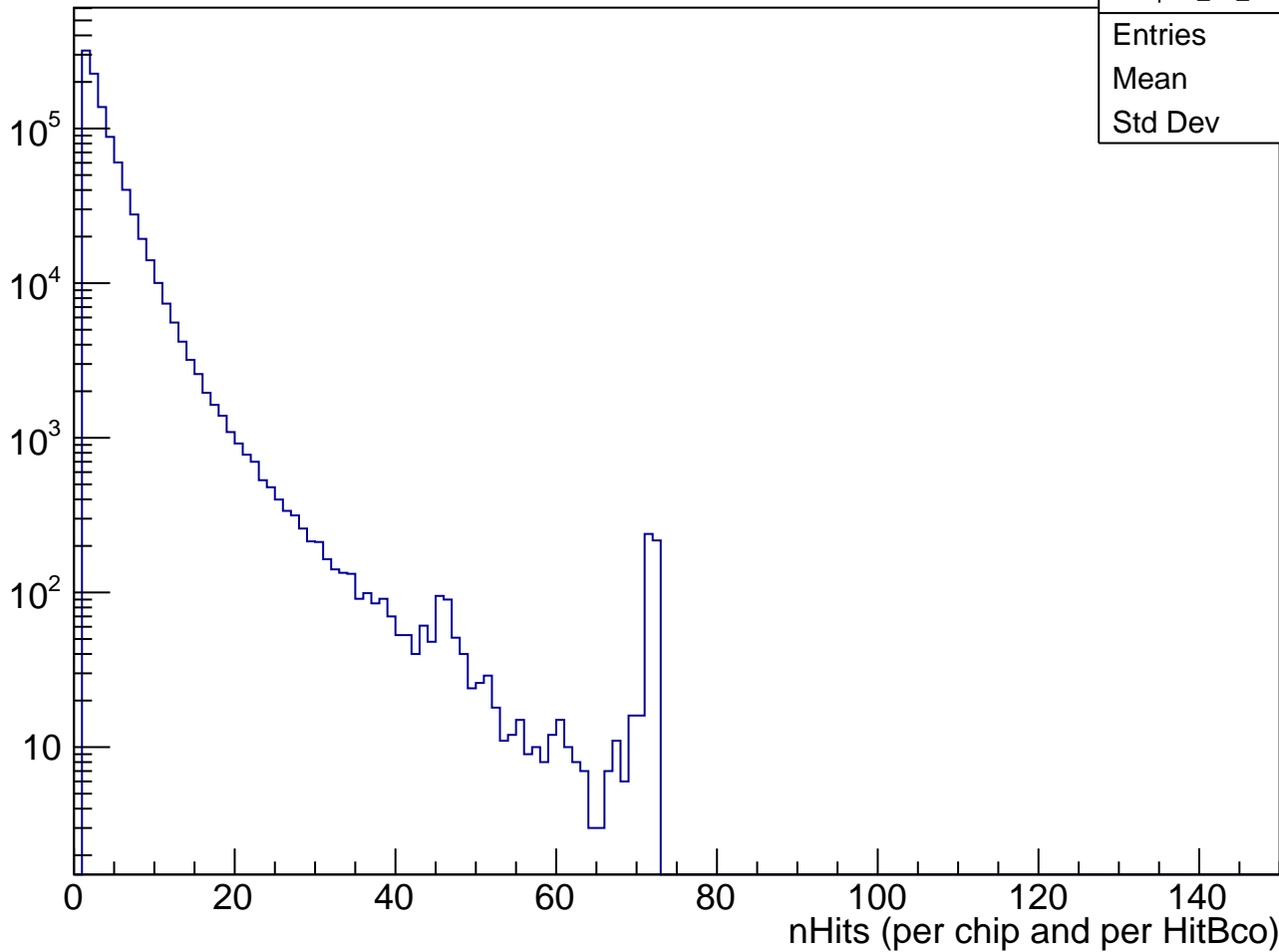


nChipHit_F1_Fch3_Chip15	
Entries	974008
Mean	3.391
Std Dev	3.906

nHits (per chip and per HitBco)

nChipHit_F1_Fch3_Chip16

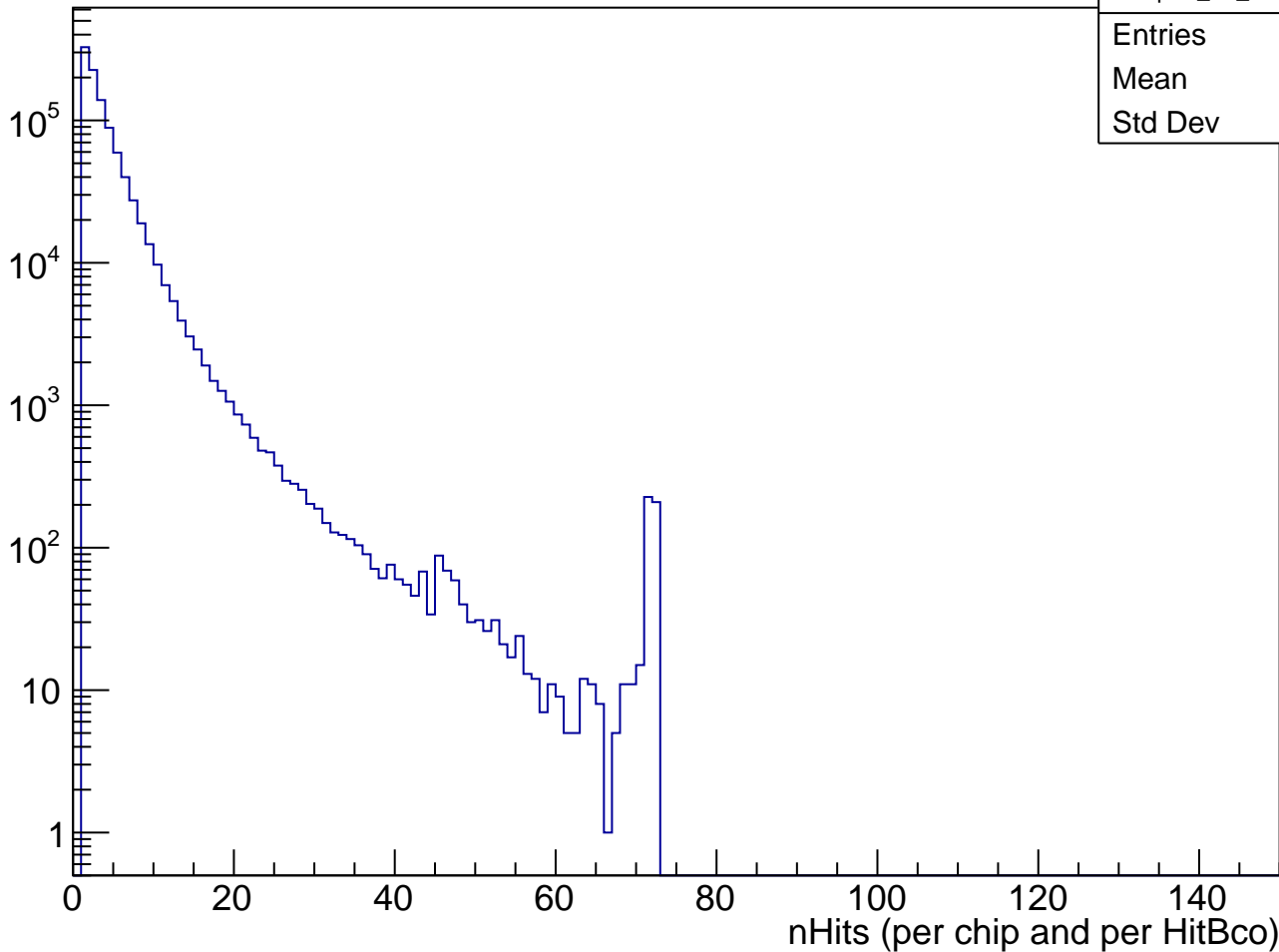
Entries



nChipHit_F1_Fch3_Chip16	
Entries	979329
Mean	3.343
Std Dev	3.837

nChipHit_F1_Fch3_Chip17

Entries

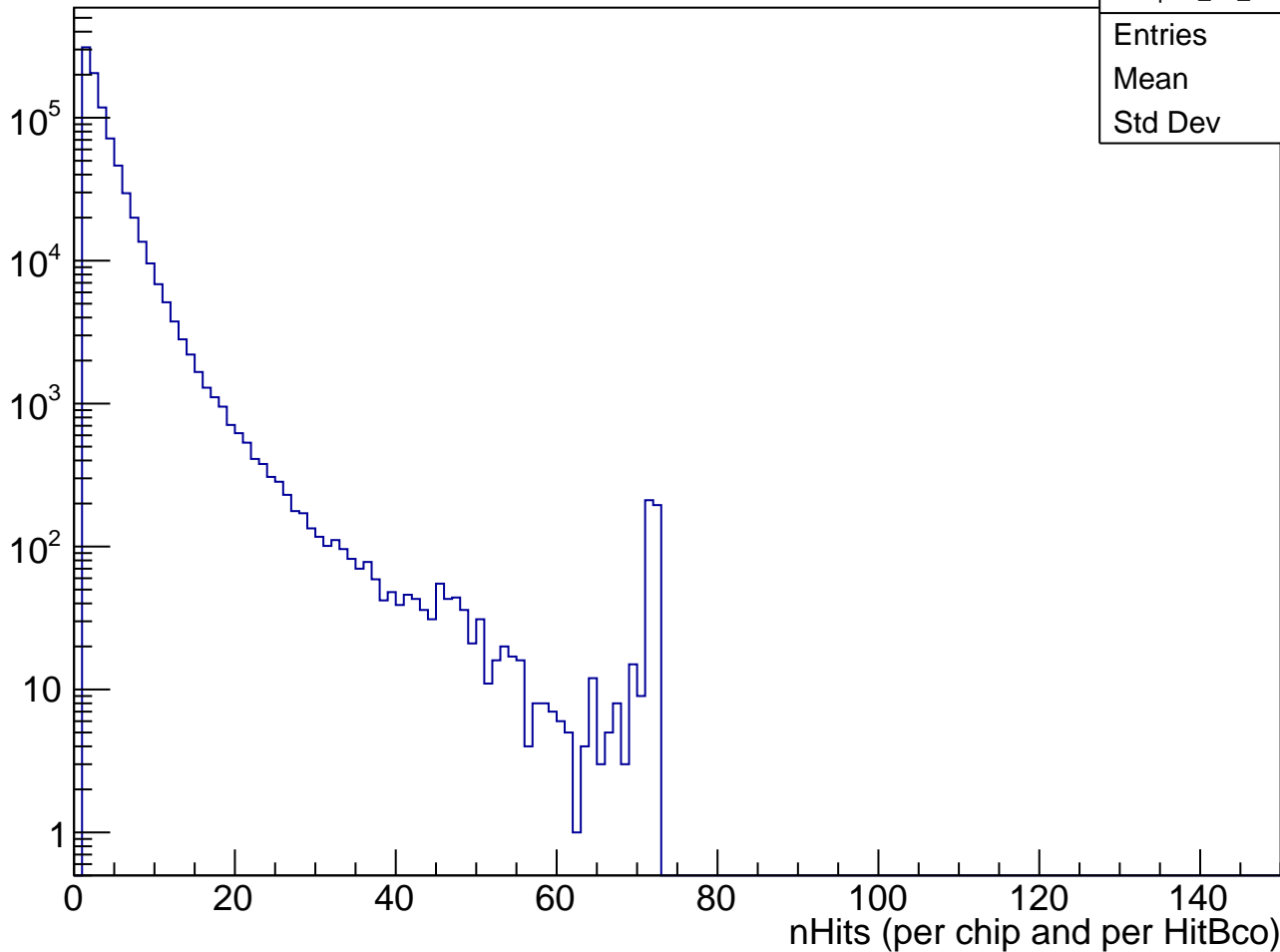


nChipHit_F1_Fch3_Chip17	
Entries	983522
Mean	3.288
Std Dev	3.767

nHits (per chip and per HitBco)

nChipHit_F1_Fch3_Chip18

Entries

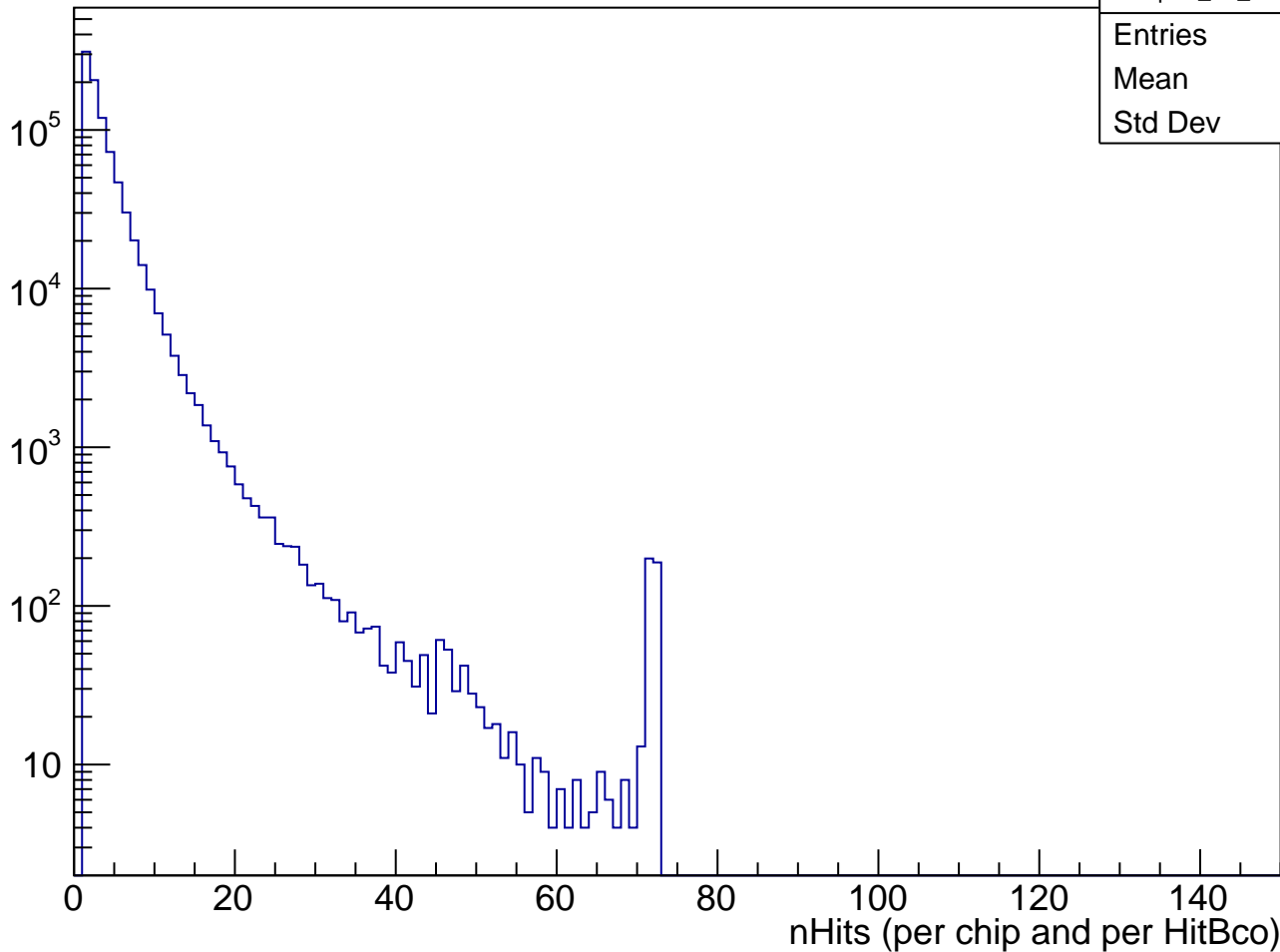


nChipHit_F1_Fch3_Chip18	
Entries	856155
Mean	3.035
Std Dev	3.564

nHits (per chip and per HitBco)

nChipHit_F1_Fch3_Chip19

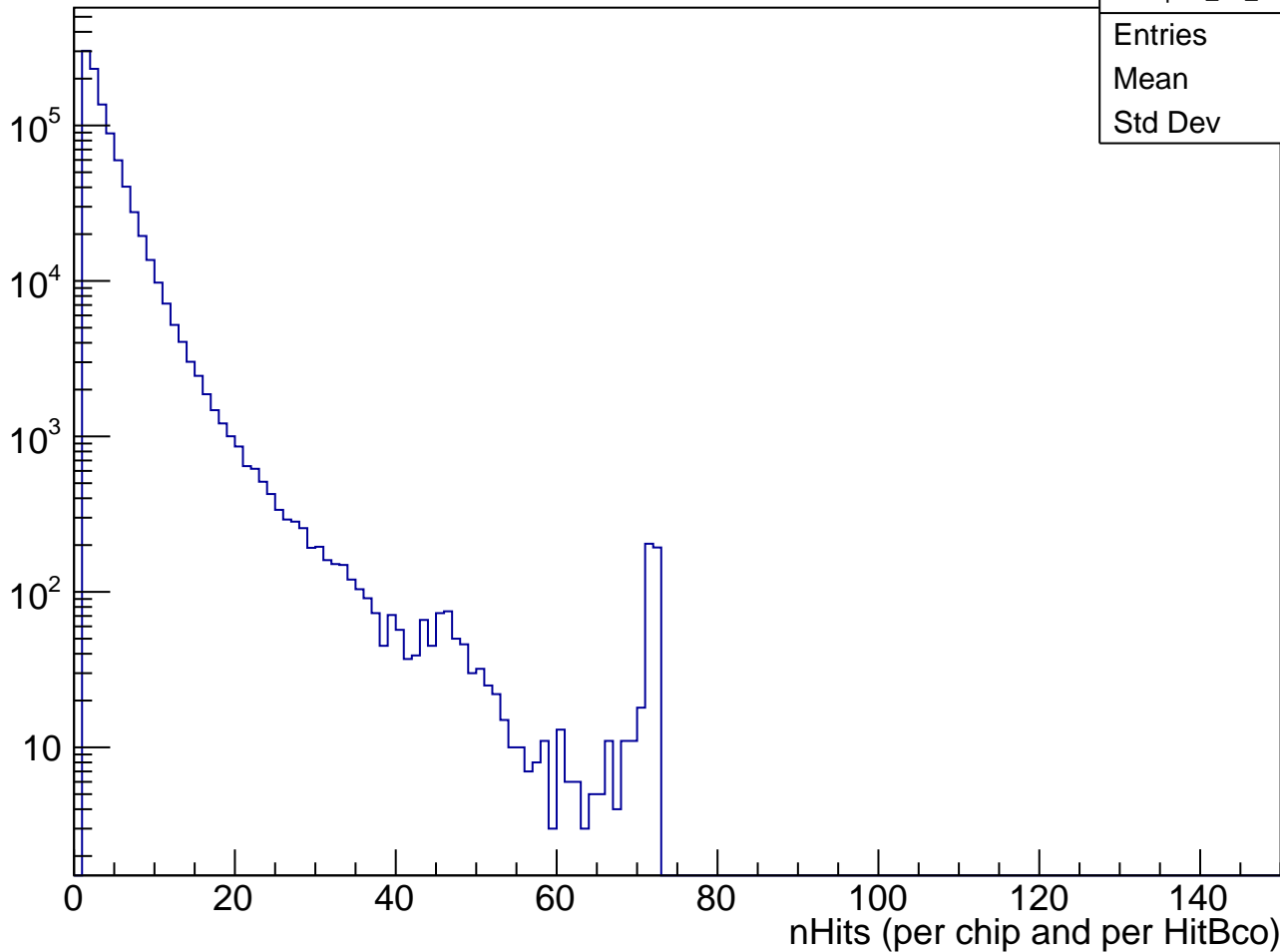
Entries



nChipHit_F1_Fch3_Chip19	
Entries	862528
Mean	3.048
Std Dev	3.558

nChipHit_F1_Fch3_Chip2

Entries

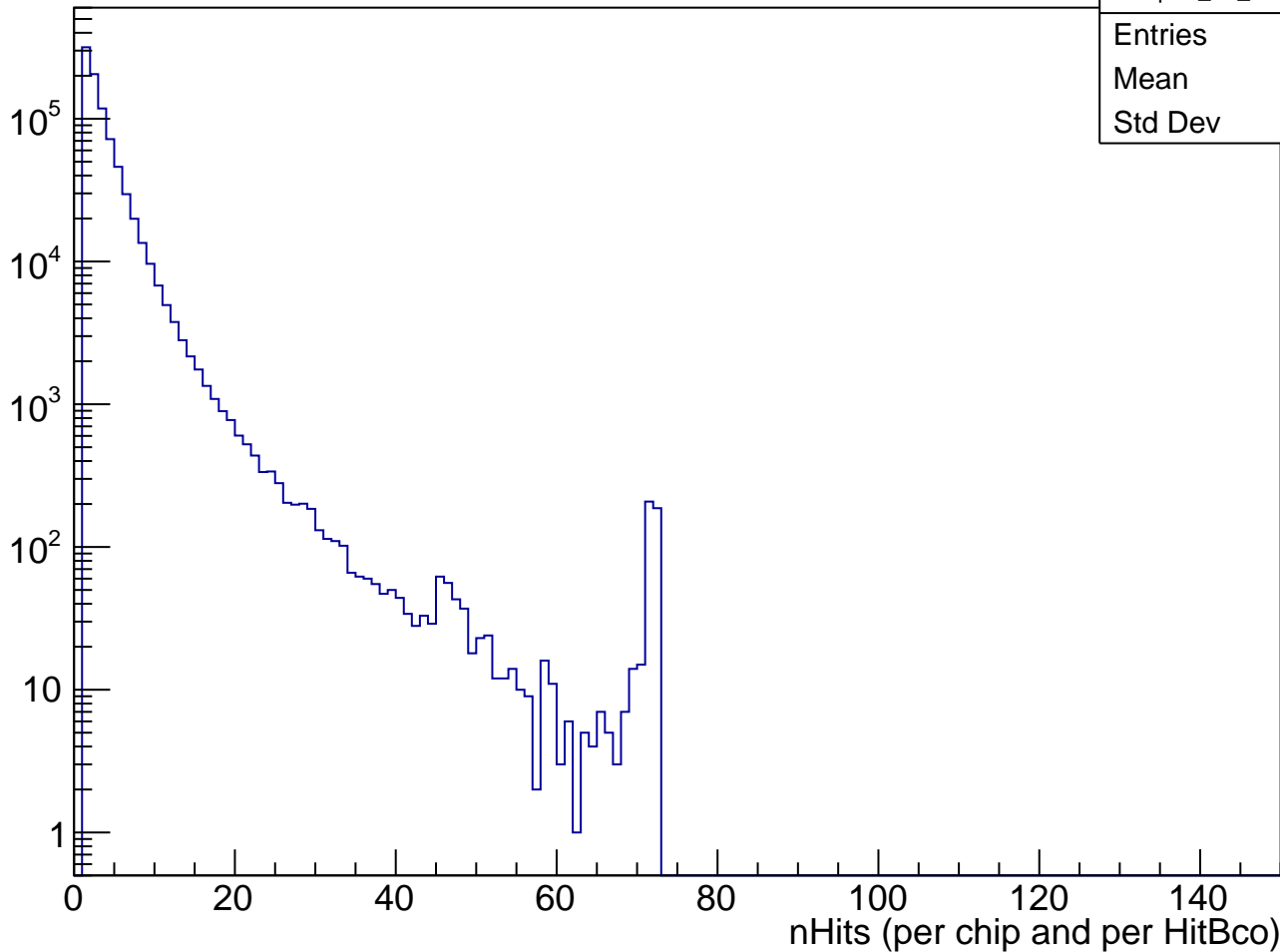


nChipHit_F1_Fch3_Chip2	
Entries	963537
Mean	3.337
Std Dev	3.738

nHits (per chip and per HitBco)

nChipHit_F1_Fch3_Chip20

Entries

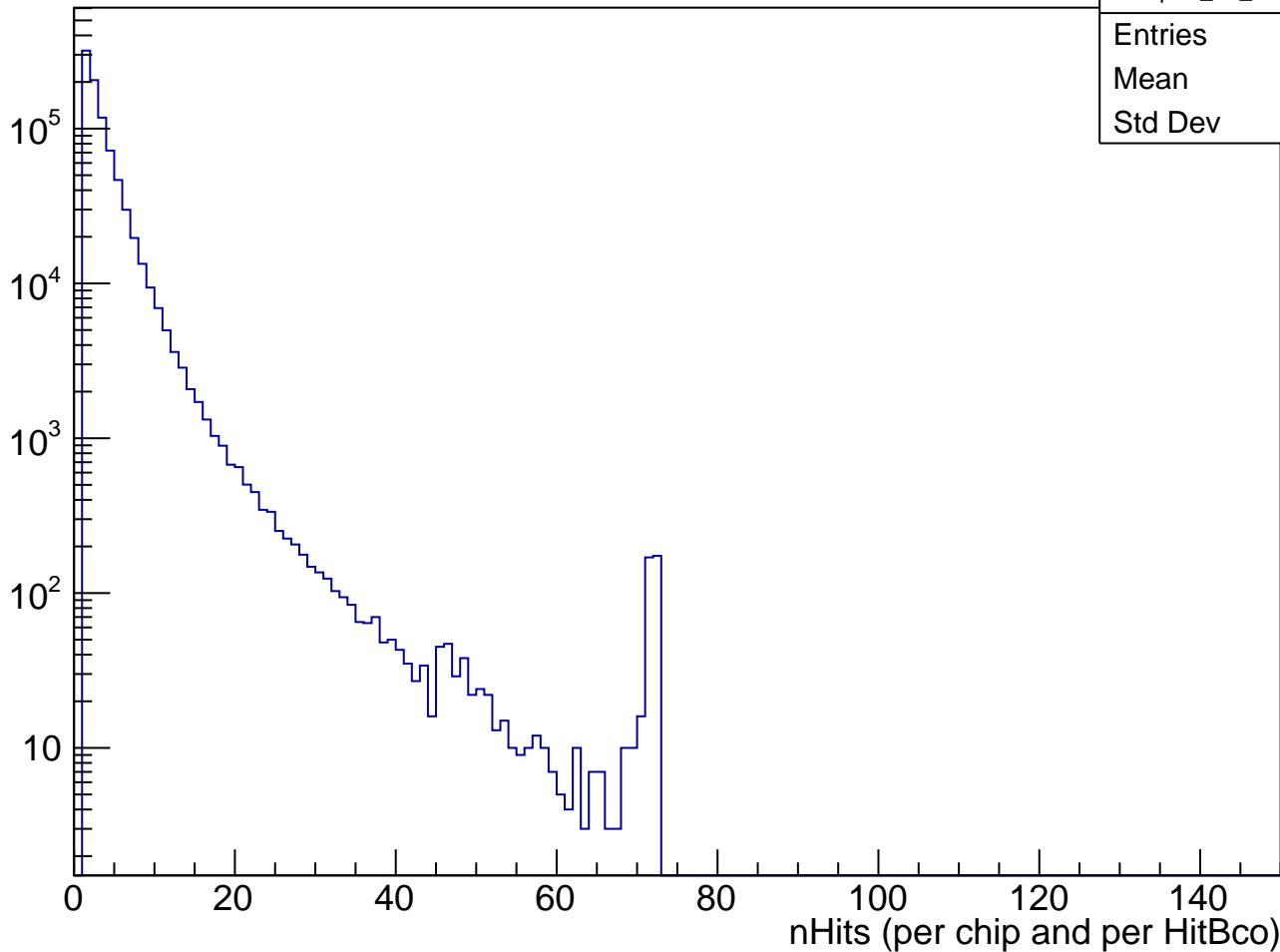


nChipHit_F1_Fch3_Chip20	
Entries	862218
Mean	3.02
Std Dev	3.55

nHits (per chip and per HitBco)

nChipHit_F1_Fch3_Chip21

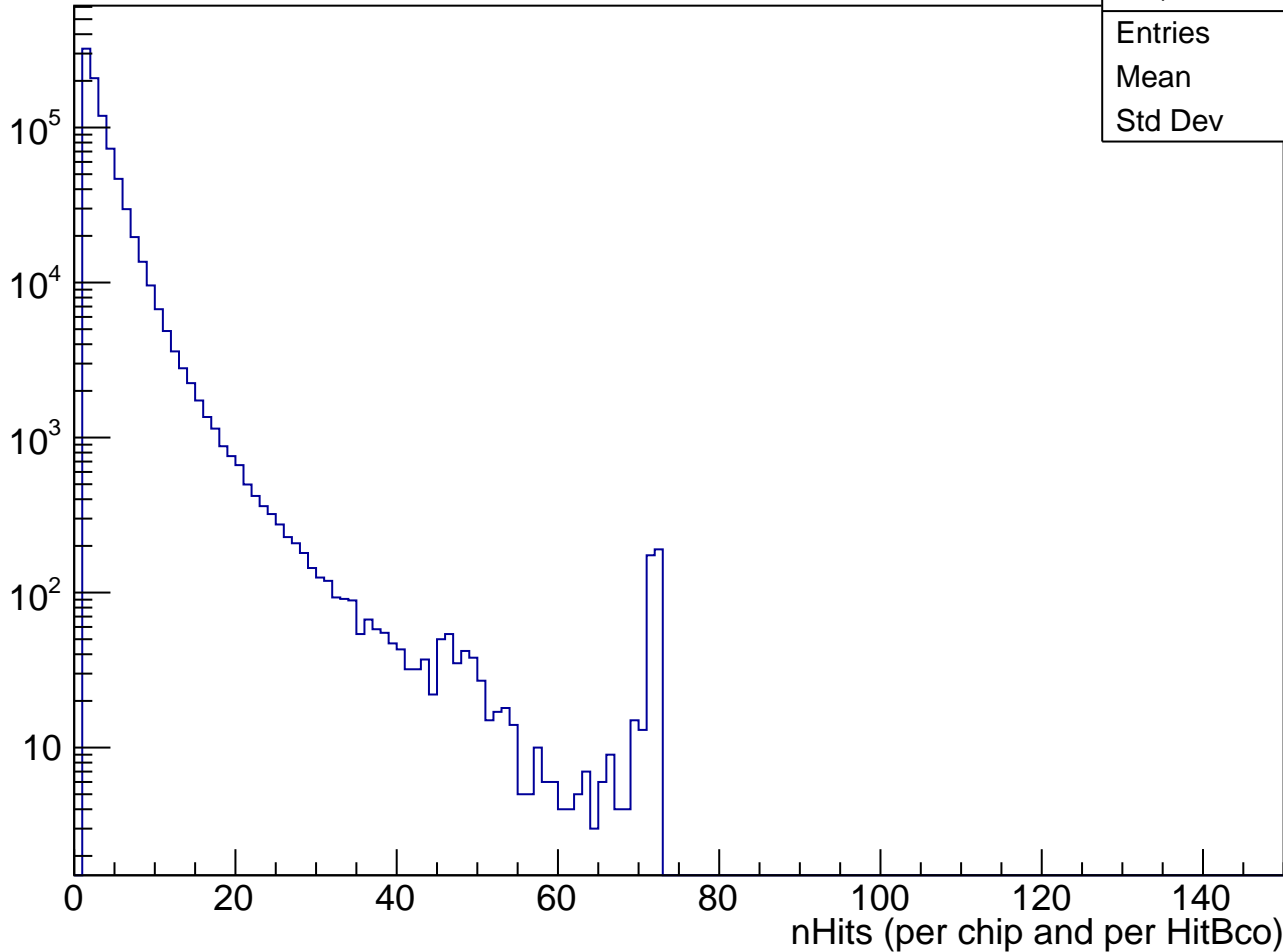
Entries



nChipHit_F1_Fch3_Chip21	
Entries	864540
Mean	3.004
Std Dev	3.49

nChipHit_F1_Fch3_Chip22

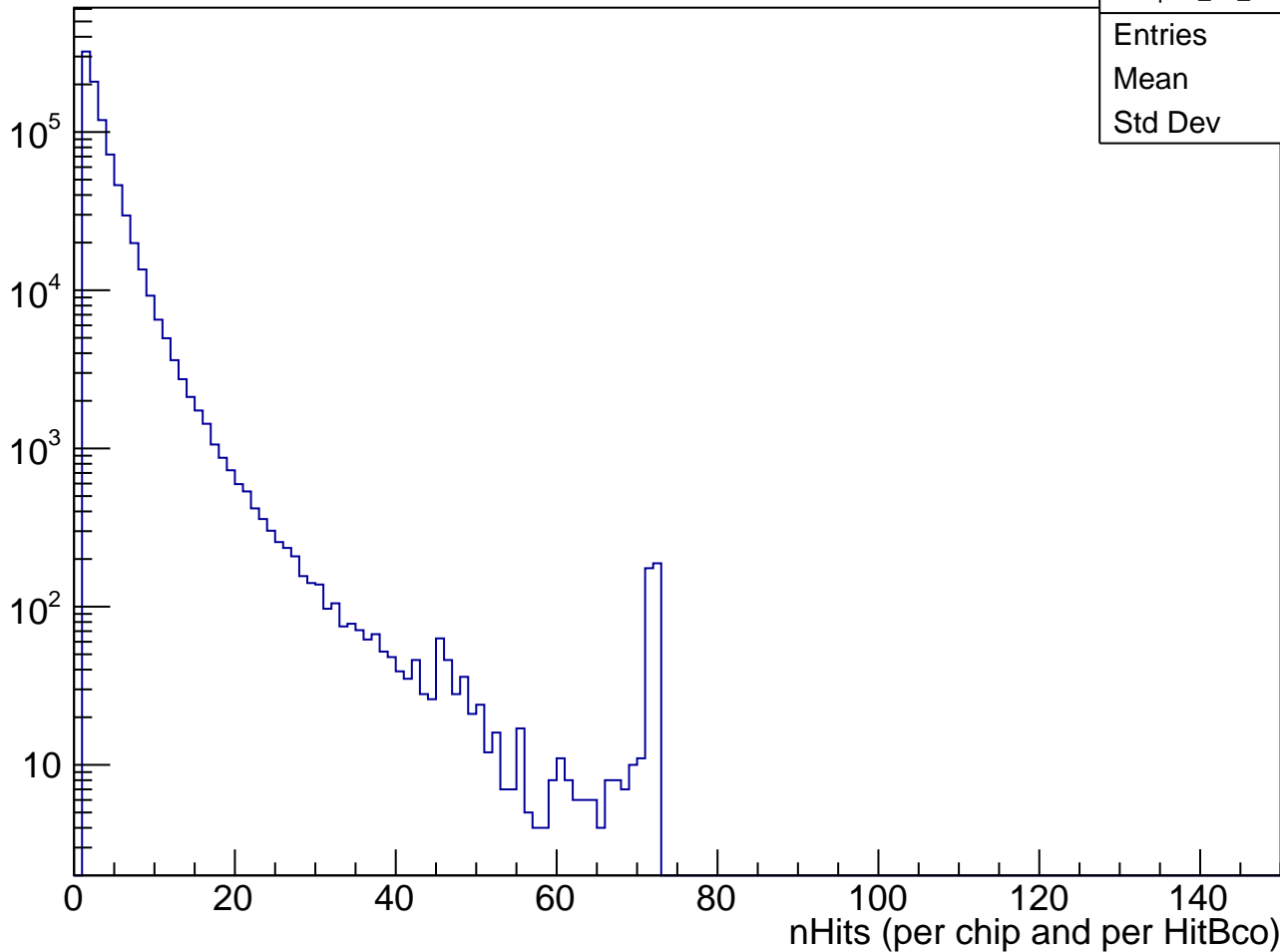
Entries



nChipHit_F1_Fch3_Chip22	
Entries	873089
Mean	3
Std Dev	3.504

nChipHit_F1_Fch3_Chip23

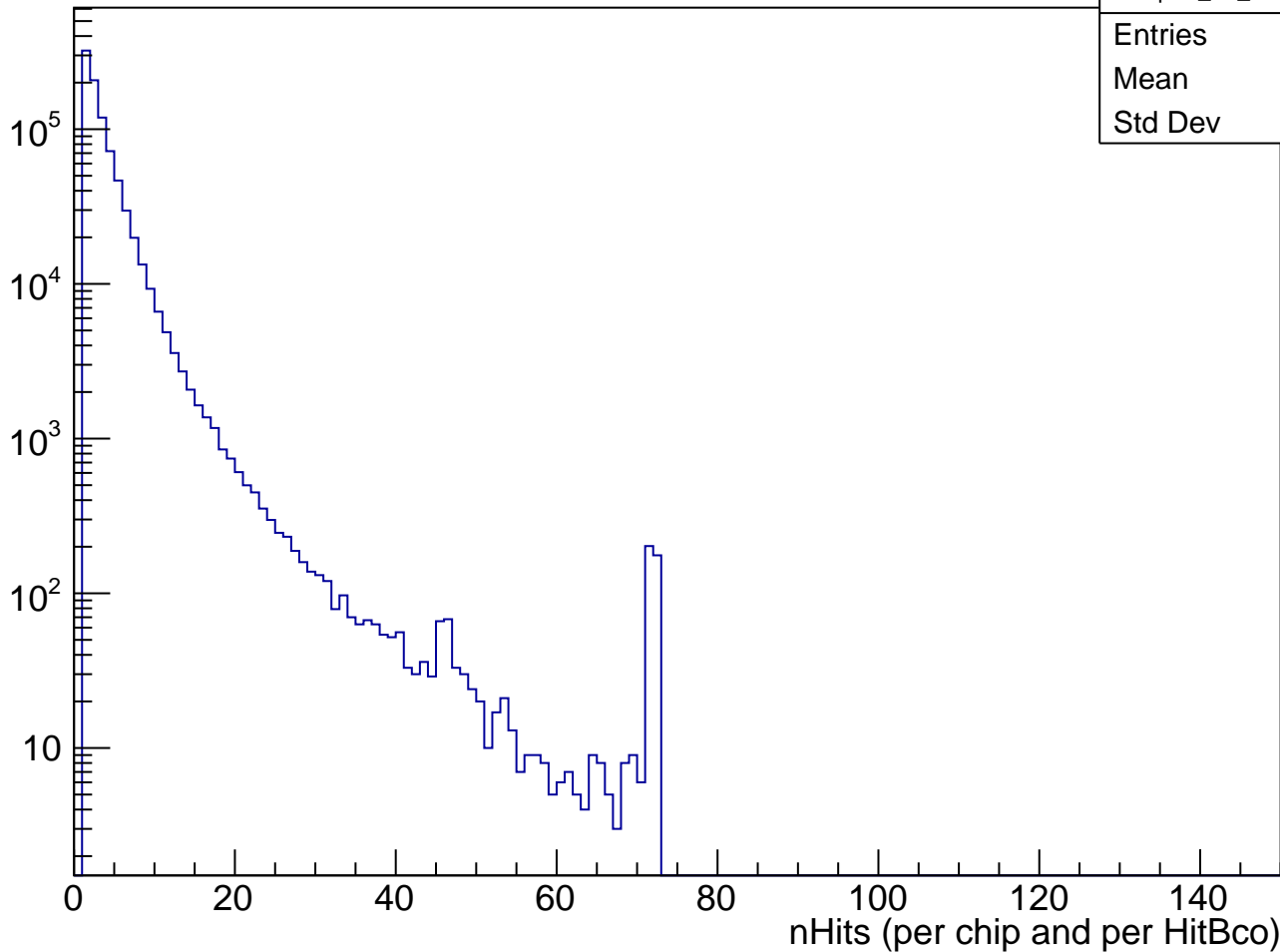
Entries



nChipHit_F1_Fch3_Chip23	
Entries	870859
Mean	2.989
Std Dev	3.483

nChipHit_F1_Fch3_Chip24

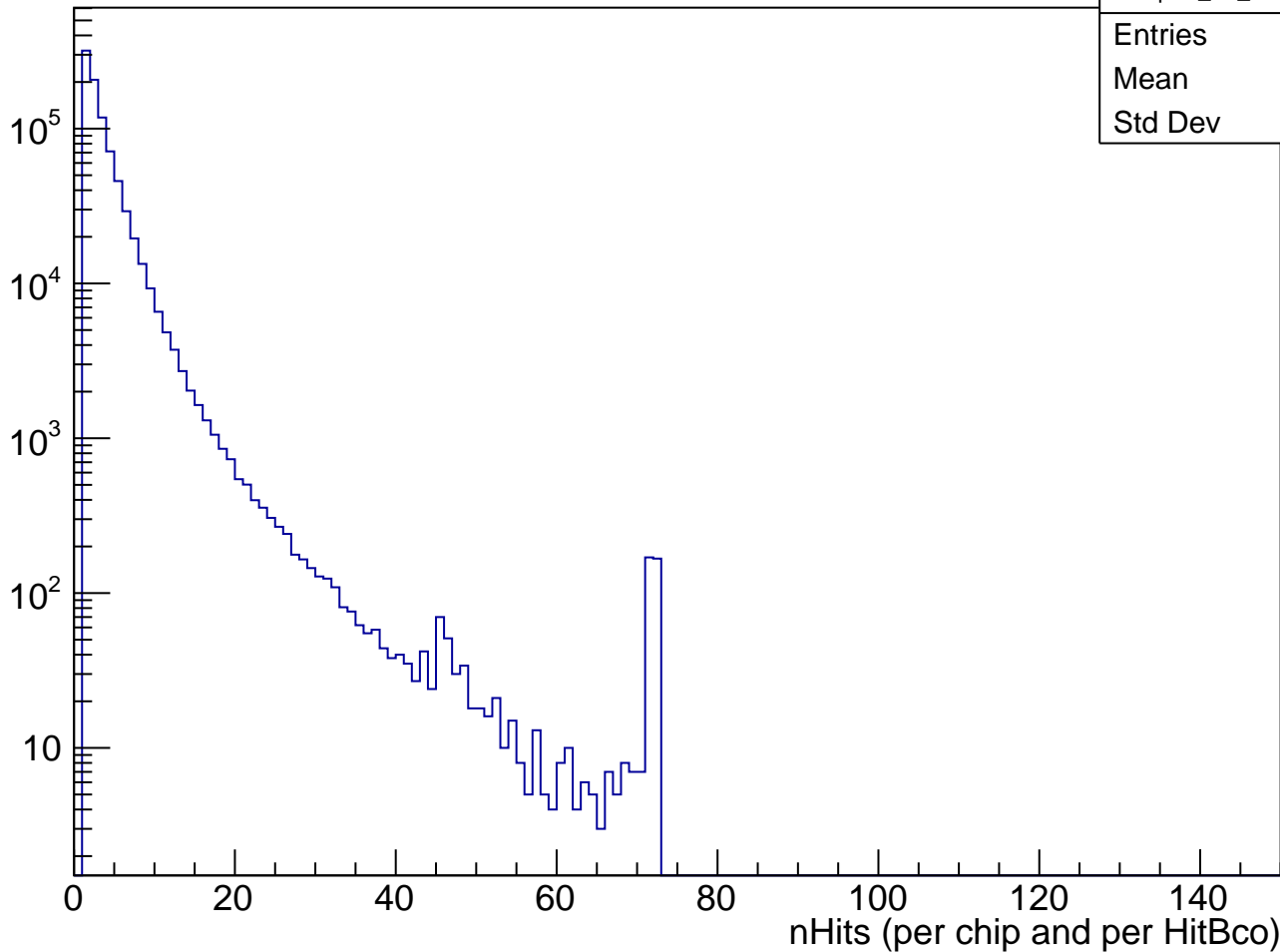
Entries



nChipHit_F1_Fch3_Chip24	
Entries	869883
Mean	2.991
Std Dev	3.498

nChipHit_F1_Fch3_Chip25

Entries

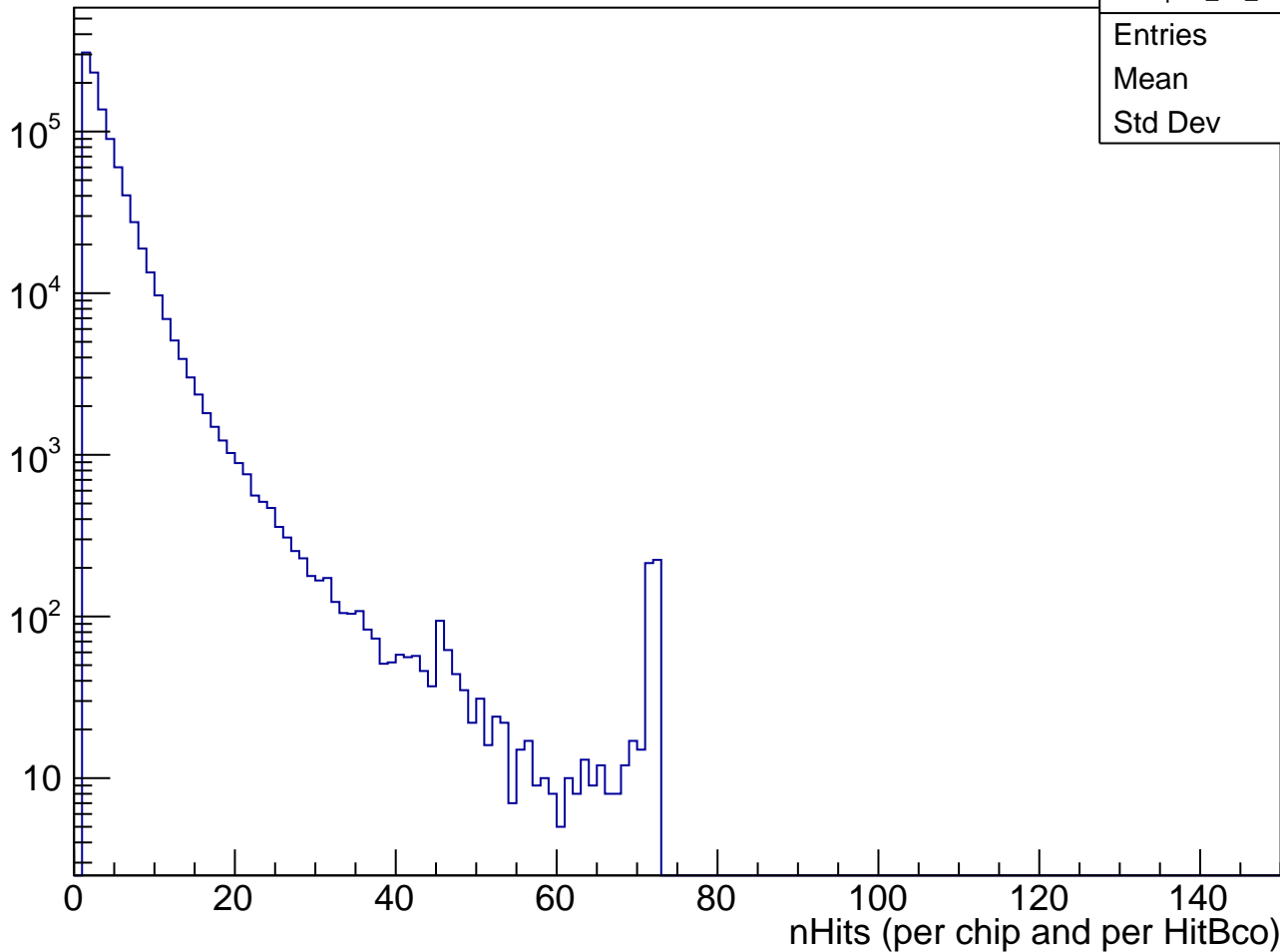


nChipHit_F1_Fch3_Chip25	
Entries	862639
Mean	2.983
Std Dev	3.452

nHits (per chip and per HitBco)

nChipHit_F1_Fch3_Chip3

Entries



nChipHit_F1_Fch3_Chip3

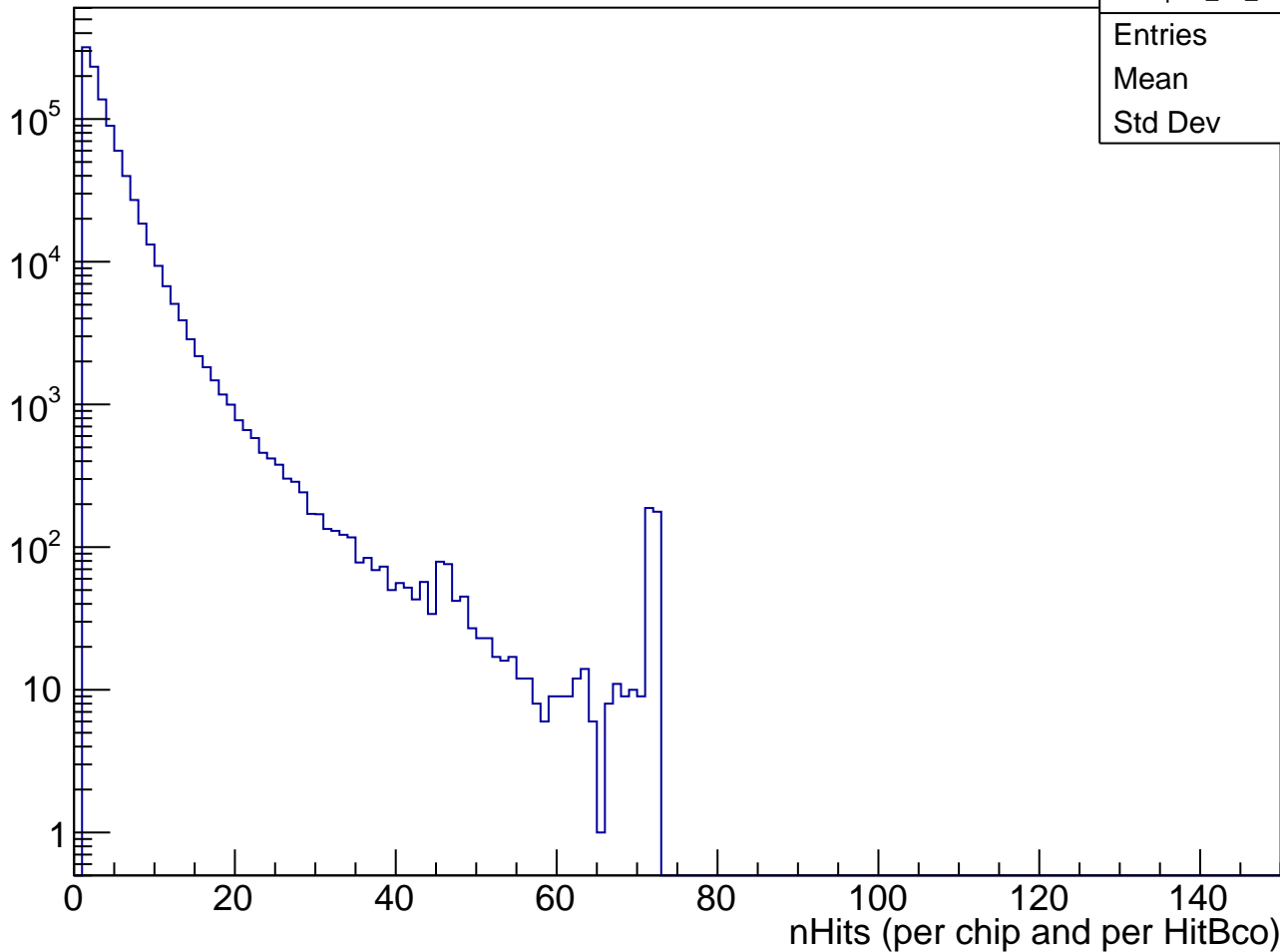
Entries 970370

Mean 3.315

Std Dev 3.749

nChipHit_F1_Fch3_Chip4

Entries



nChipHit_F1_Fch3_Chip4

Entries 978557

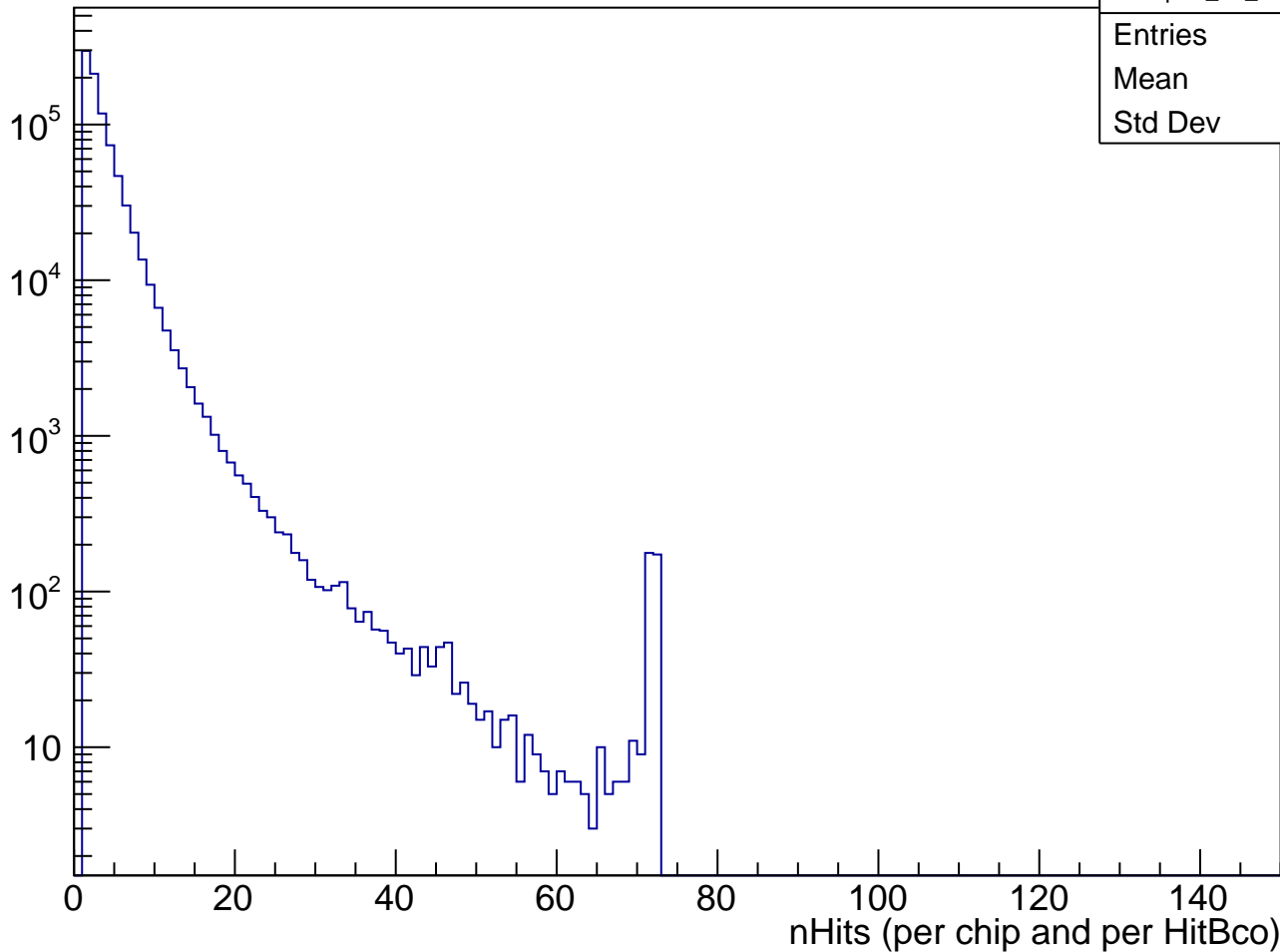
Mean 3.262

Std Dev 3.651

nHits (per chip and per HitBco)

nChipHit_F1_Fch3_Chip5

Entries

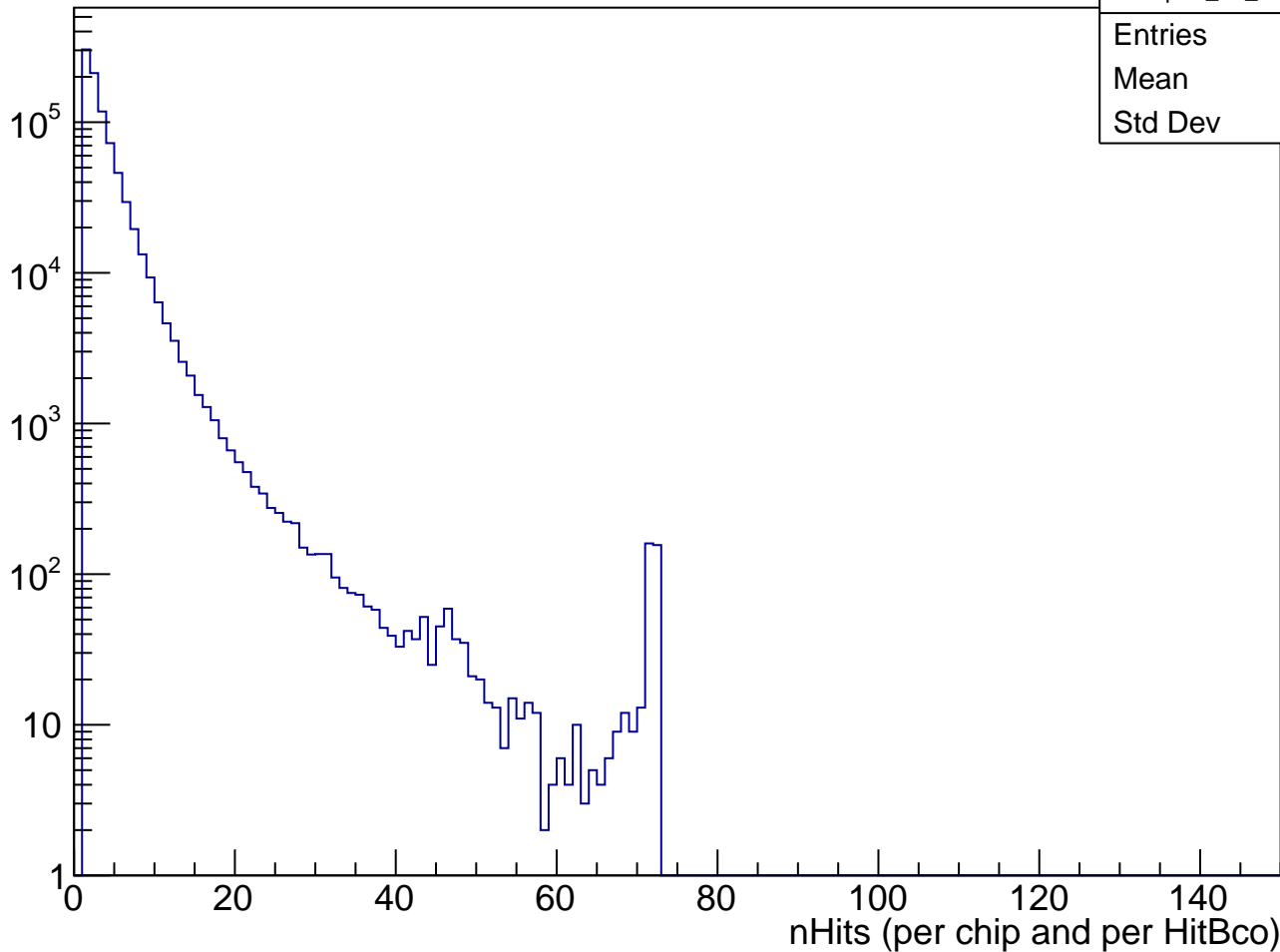


nChipHit_F1_Fch3_Chip5	
Entries	850624
Mean	3.035
Std Dev	3.464

nHits (per chip and per HitBco)

nChipHit_F1_Fch3_Chip6

Entries



nChipHit_F1_Fch3_Chip6

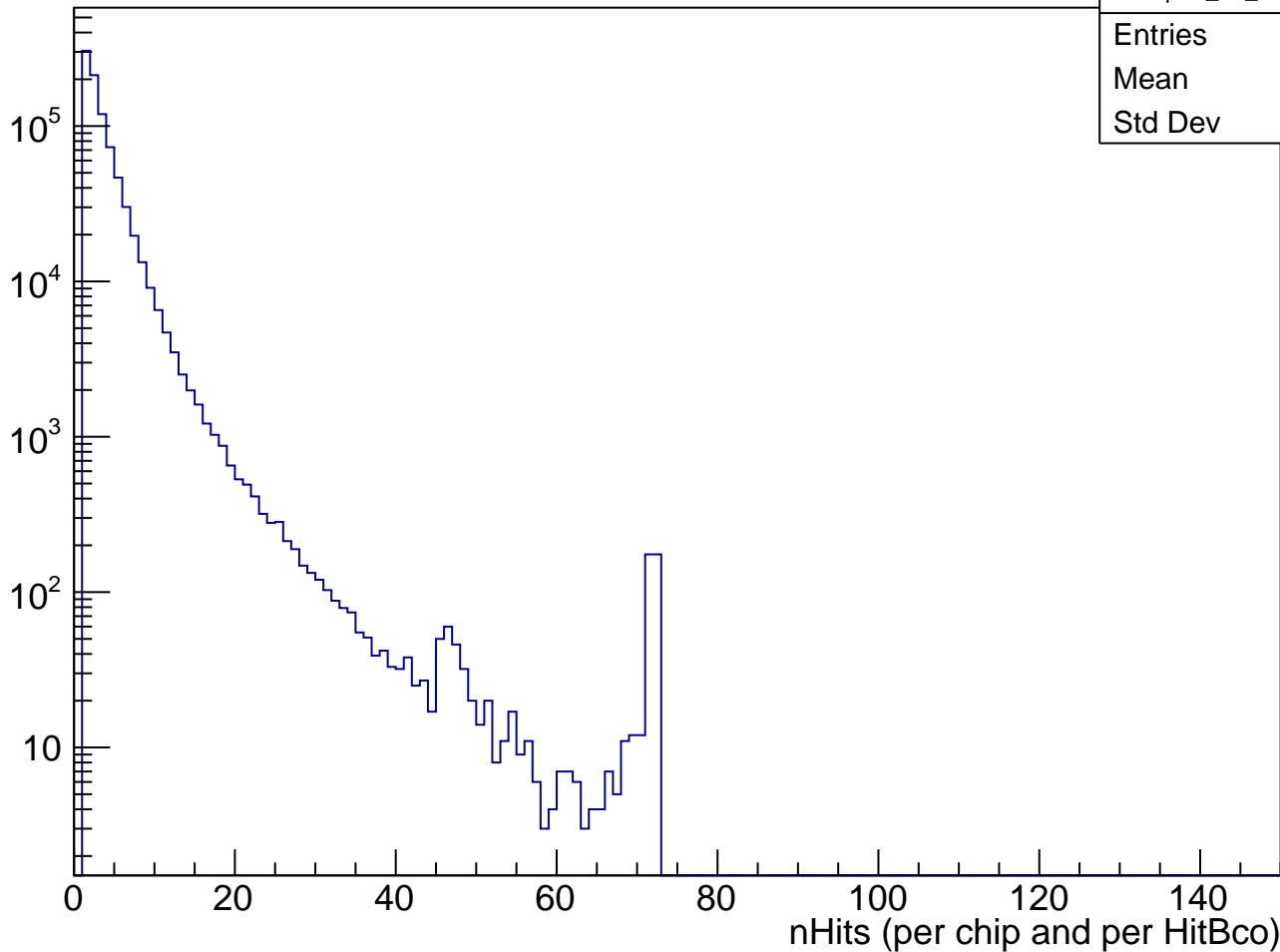
Entries 853247

Mean 3.002

Std Dev 3.438

nChipHit_F1_Fch3_Chip7

Entries

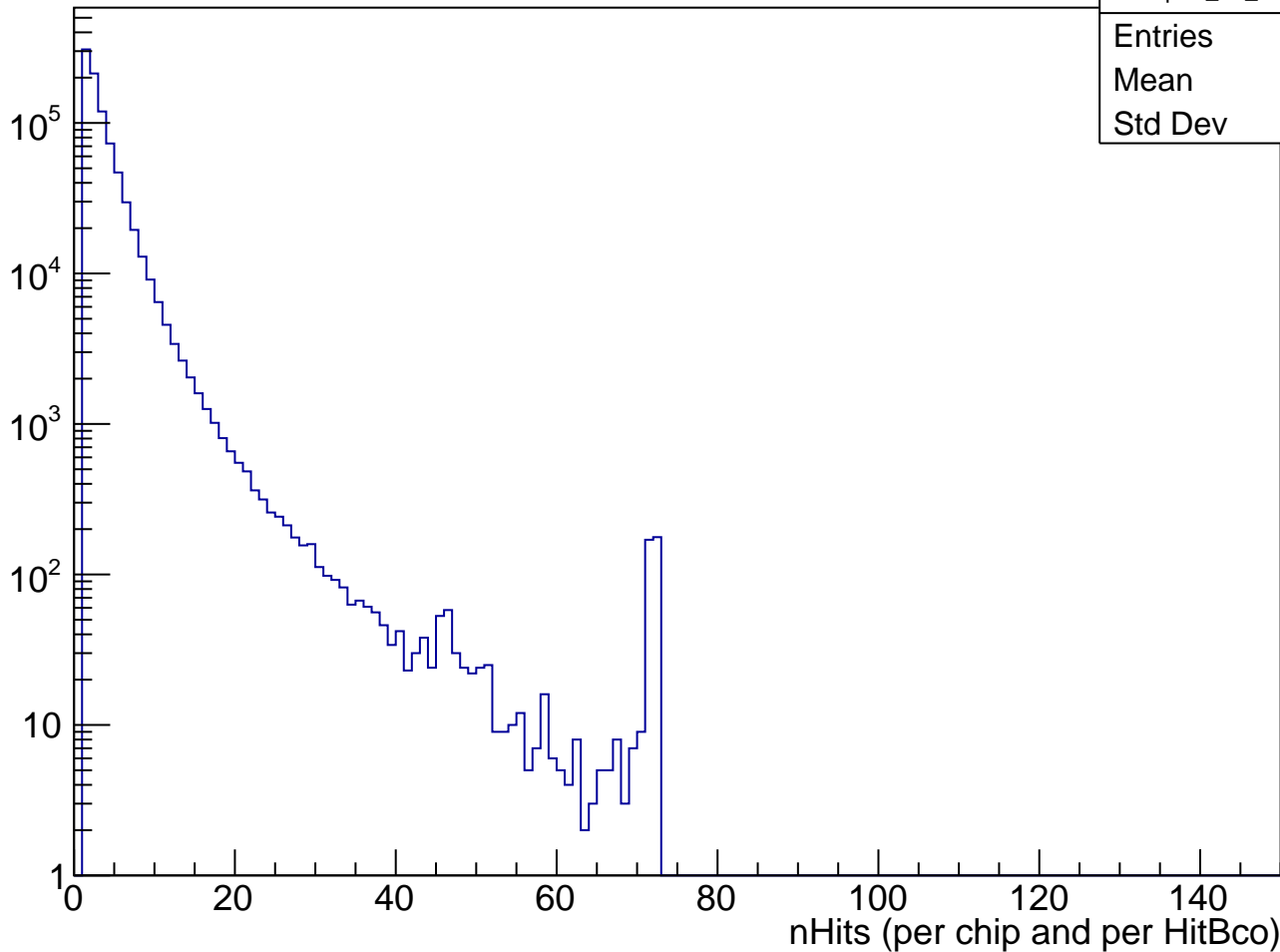


nChipHit_F1_Fch3_Chip7	
Entries	858130
Mean	2.998
Std Dev	3.421

nHits (per chip and per HitBco)

nChipHit_F1_Fch3_Chip8

Entries



nChipHit_F1_Fch3_Chip8

Entries 860581

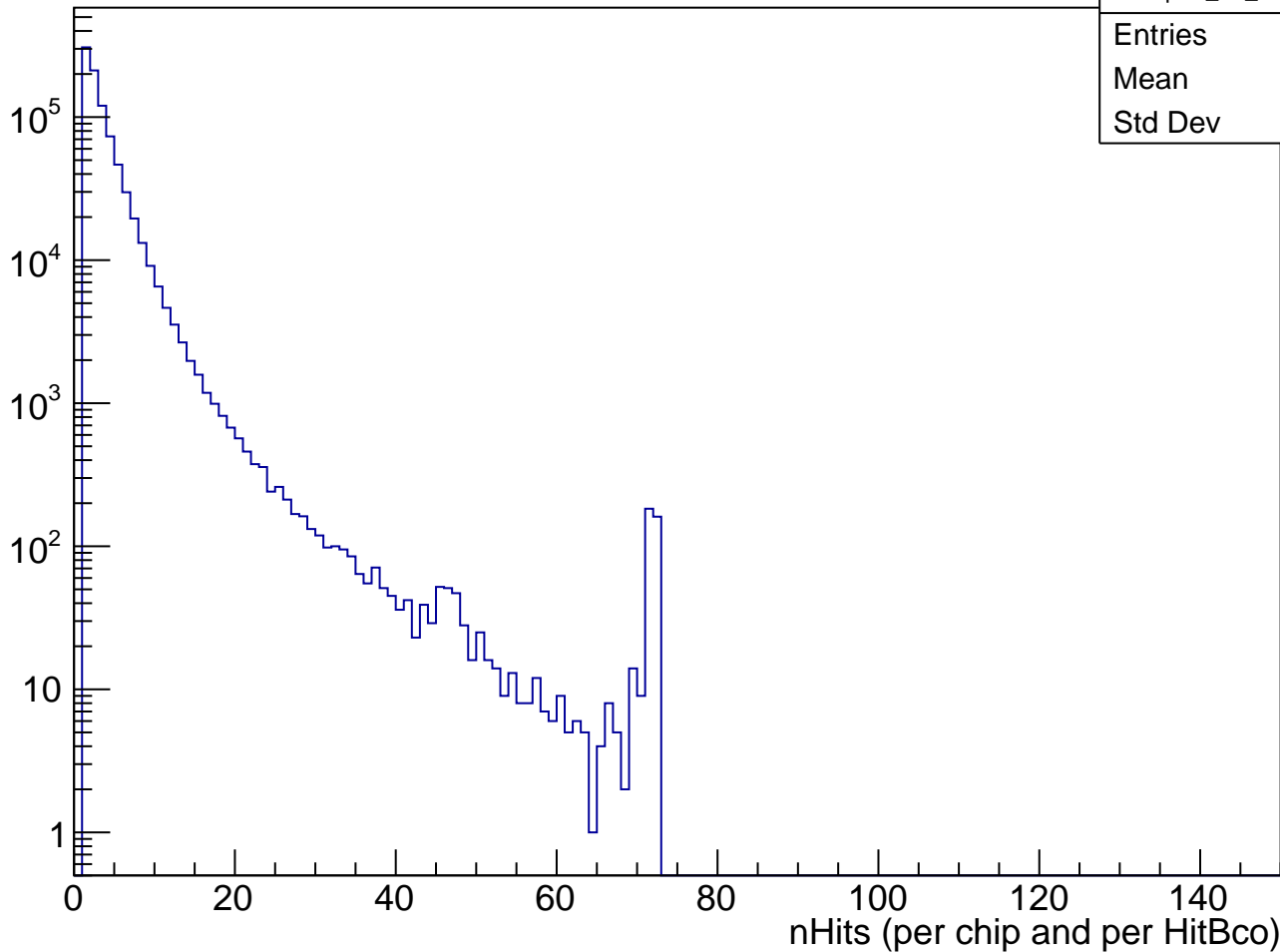
Mean 2.984

Std Dev 3.406

nHits (per chip and per HitBco)

nChipHit_F1_Fch3_Chip9

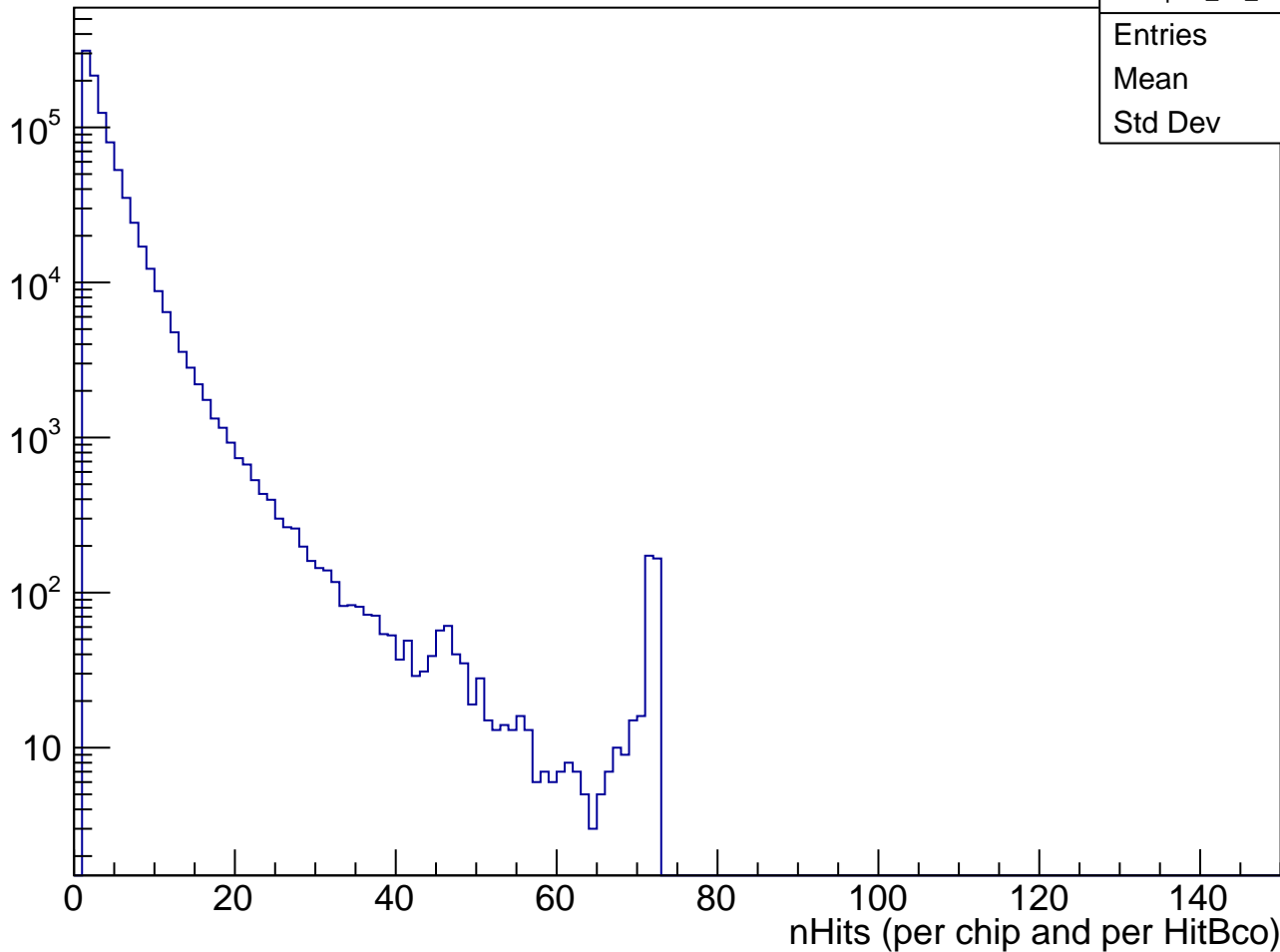
Entries



nChipHit_F1_Fch3_Chip9	
Entries	859939
Mean	2.994
Std Dev	3.427

nChipHit_F1_Fch4_Chip0

Entries



nChipHit_F1_Fch4_Chip0

Entries 913545

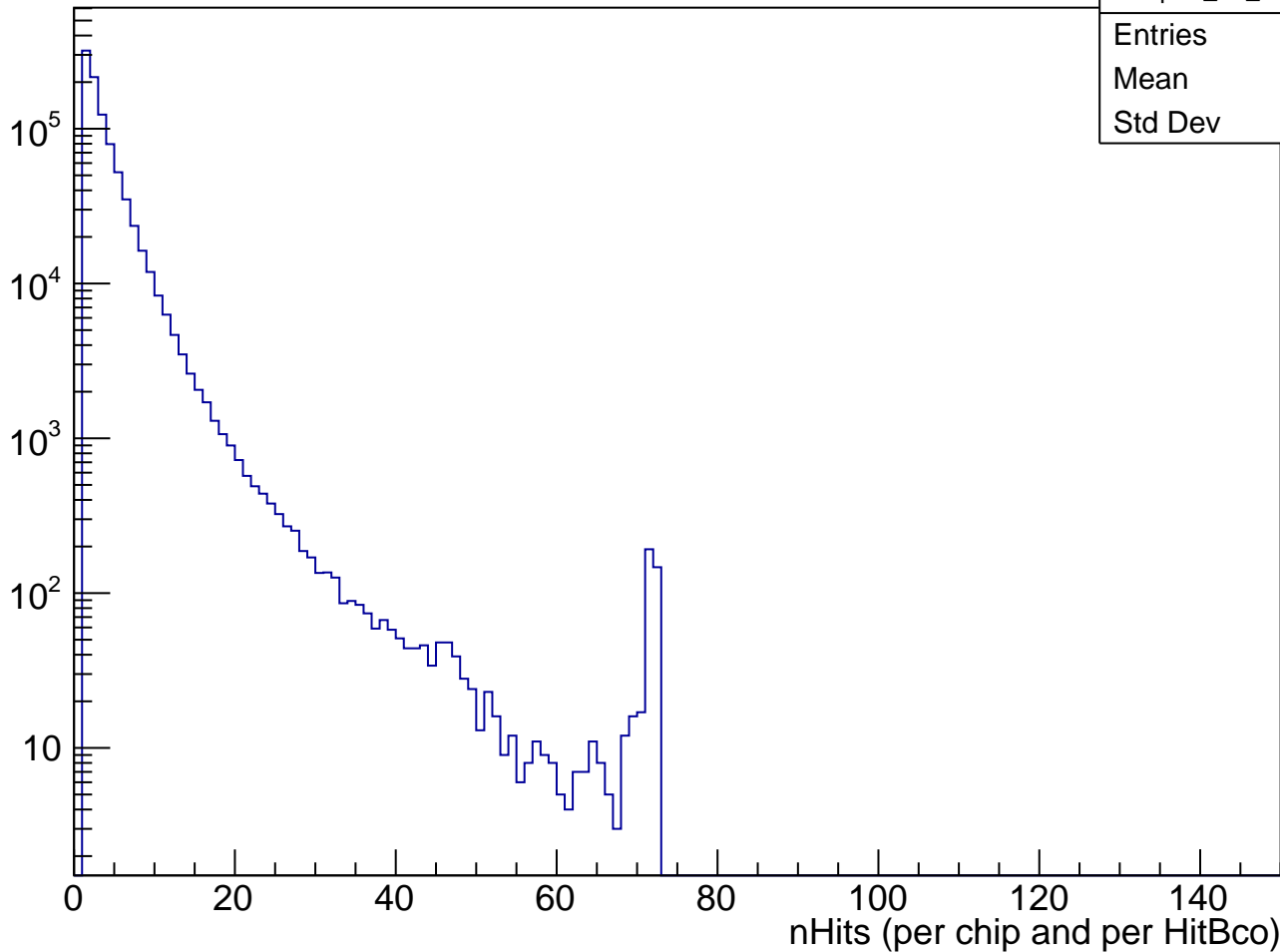
Mean 3.206

Std Dev 3.63

nHits (per chip and per HitBco)

nChipHit_F1_Fch4_Chip1

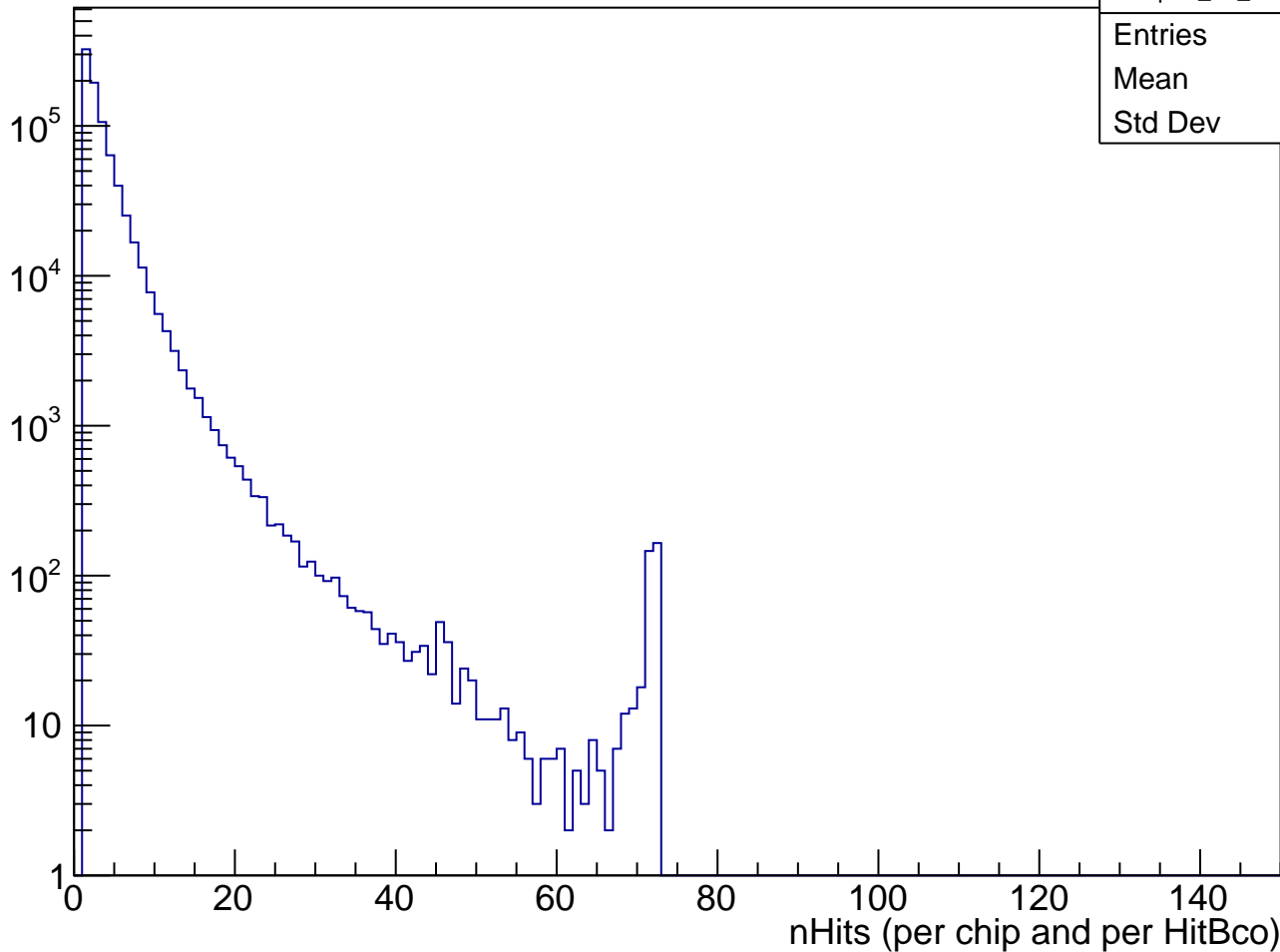
Entries



nChipHit_F1_Fch4_Chip1	
Entries	915145
Mean	3.162
Std Dev	3.6

nChipHit_F1_Fch4_Chip10

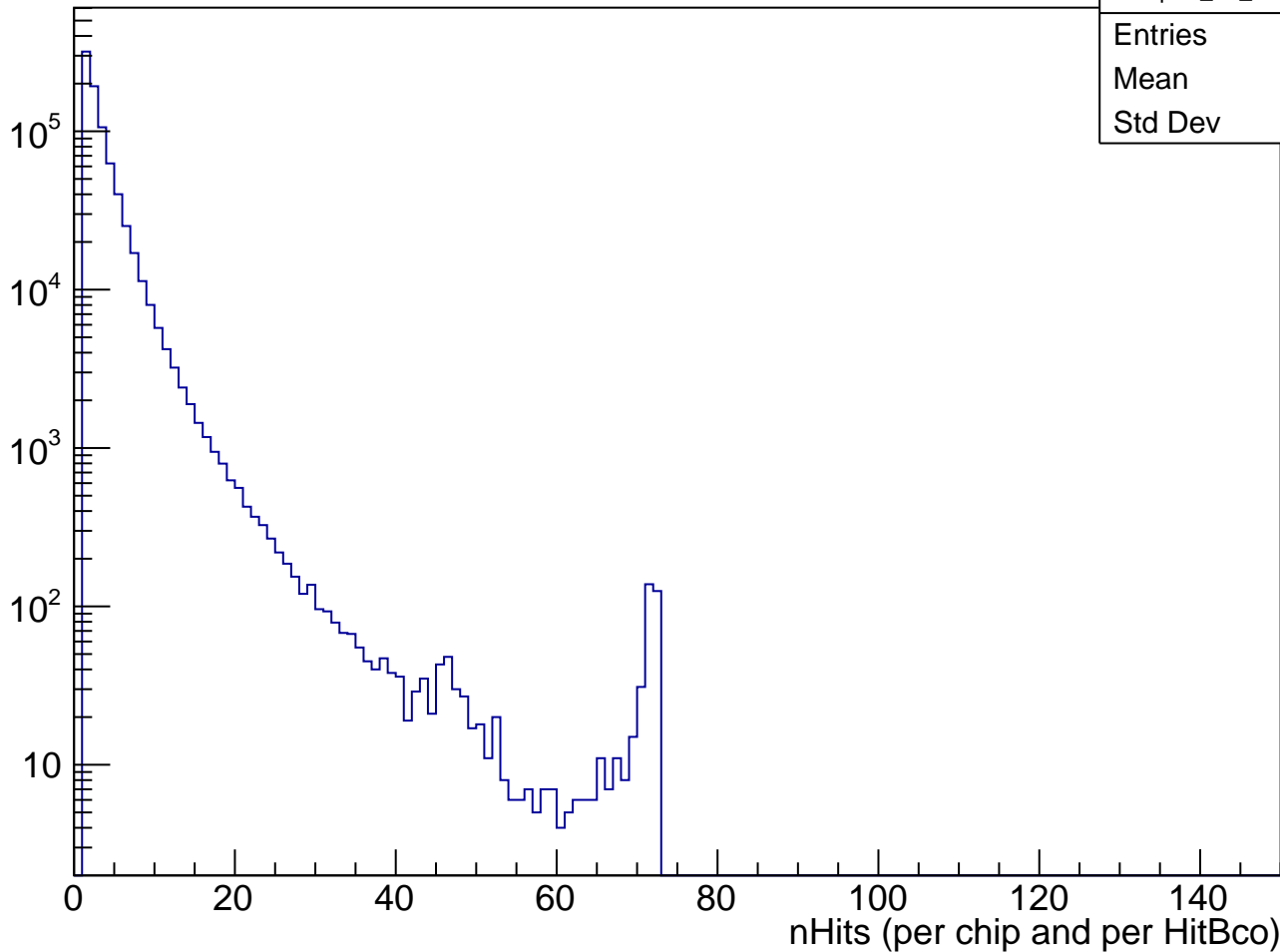
Entries



nChipHit_F1_Fch4_Chip10	
Entries	815755
Mean	2.846
Std Dev	3.35

nChipHit_F1_Fch4_Chip11

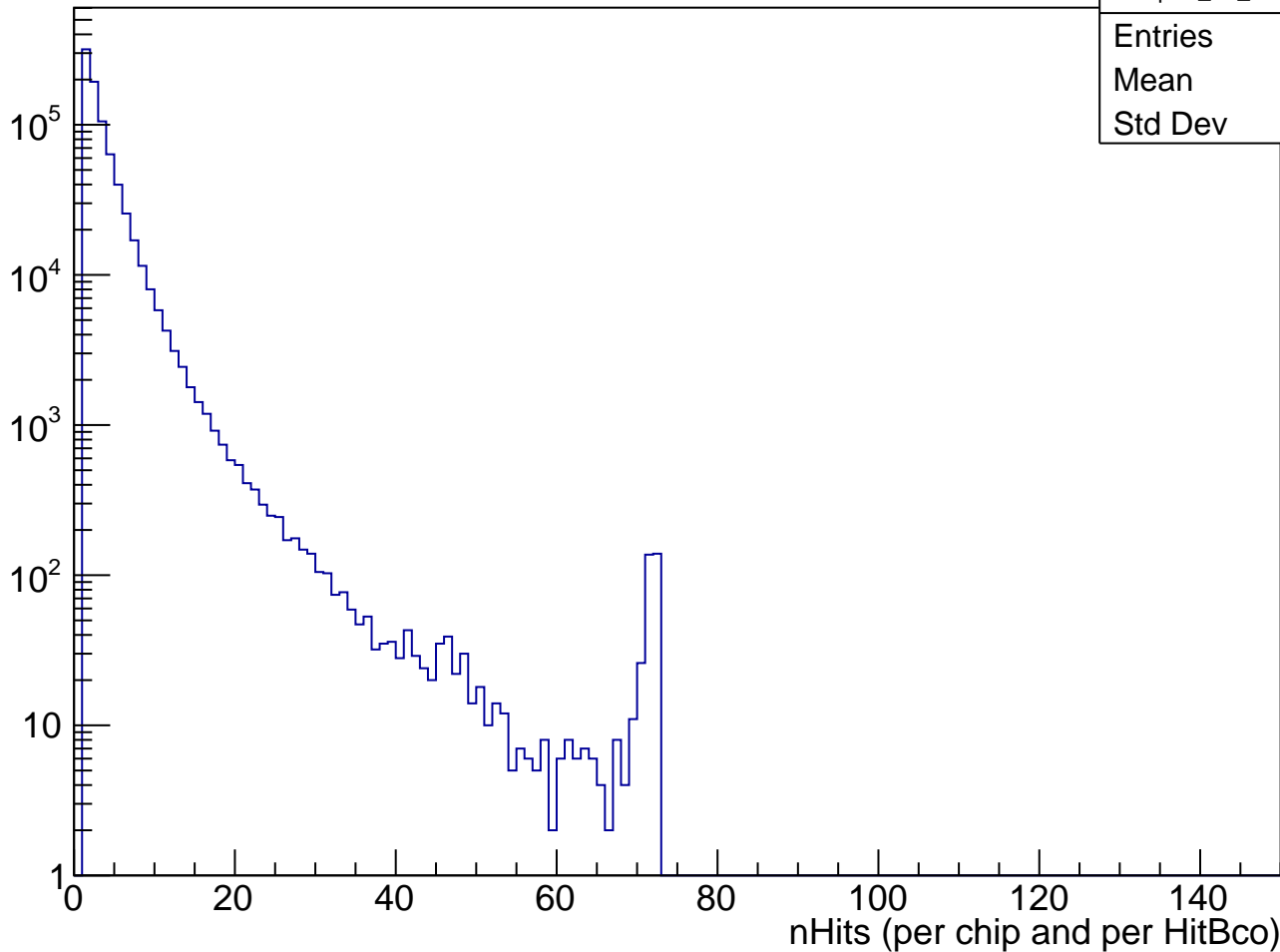
Entries



nChipHit_F1_Fch4_Chip11	
Entries	807883
Mean	2.869
Std Dev	3.364

nChipHit_F1_Fch4_Chip12

Entries

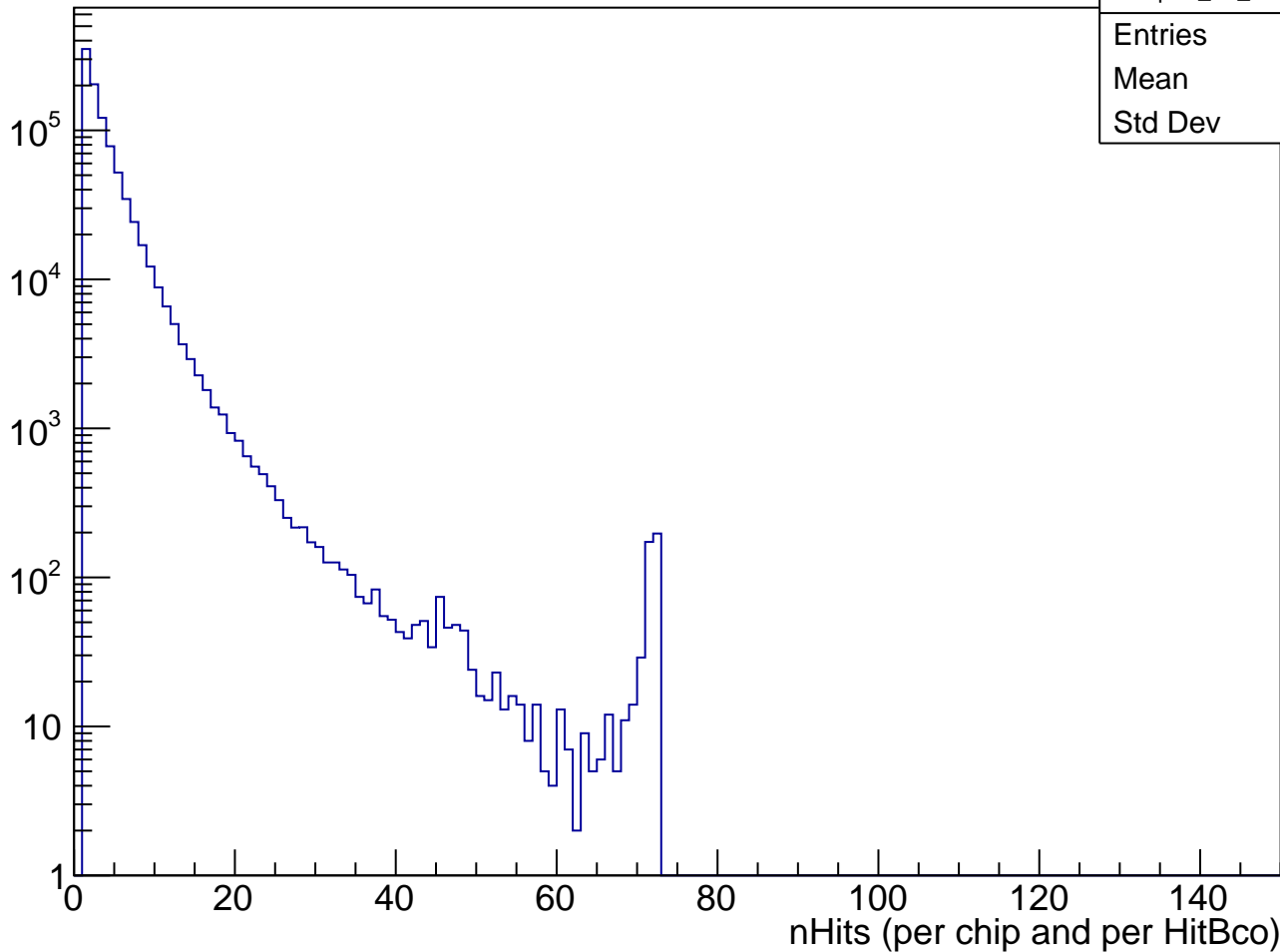


nChipHit_F1_Fch4_Chip12	
Entries	808711
Mean	2.866
Std Dev	3.336

nHits (per chip and per HitBco)

nChipHit_F1_Fch4_Chip13

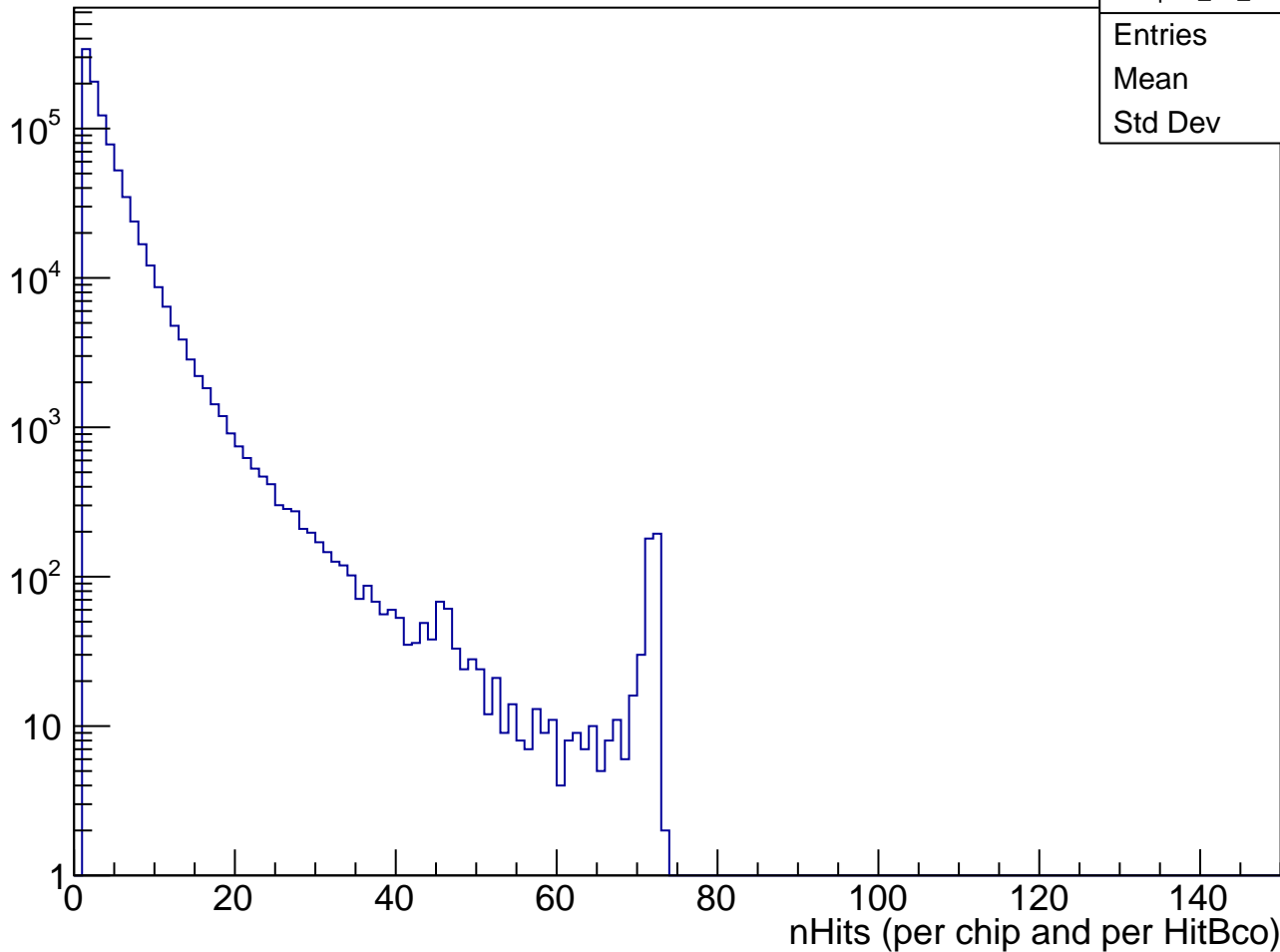
Entries



nChipHit_F1_Fch4_Chip13	
Entries	935516
Mean	3.146
Std Dev	3.689

nChipHit_F1_Fch4_Chip14

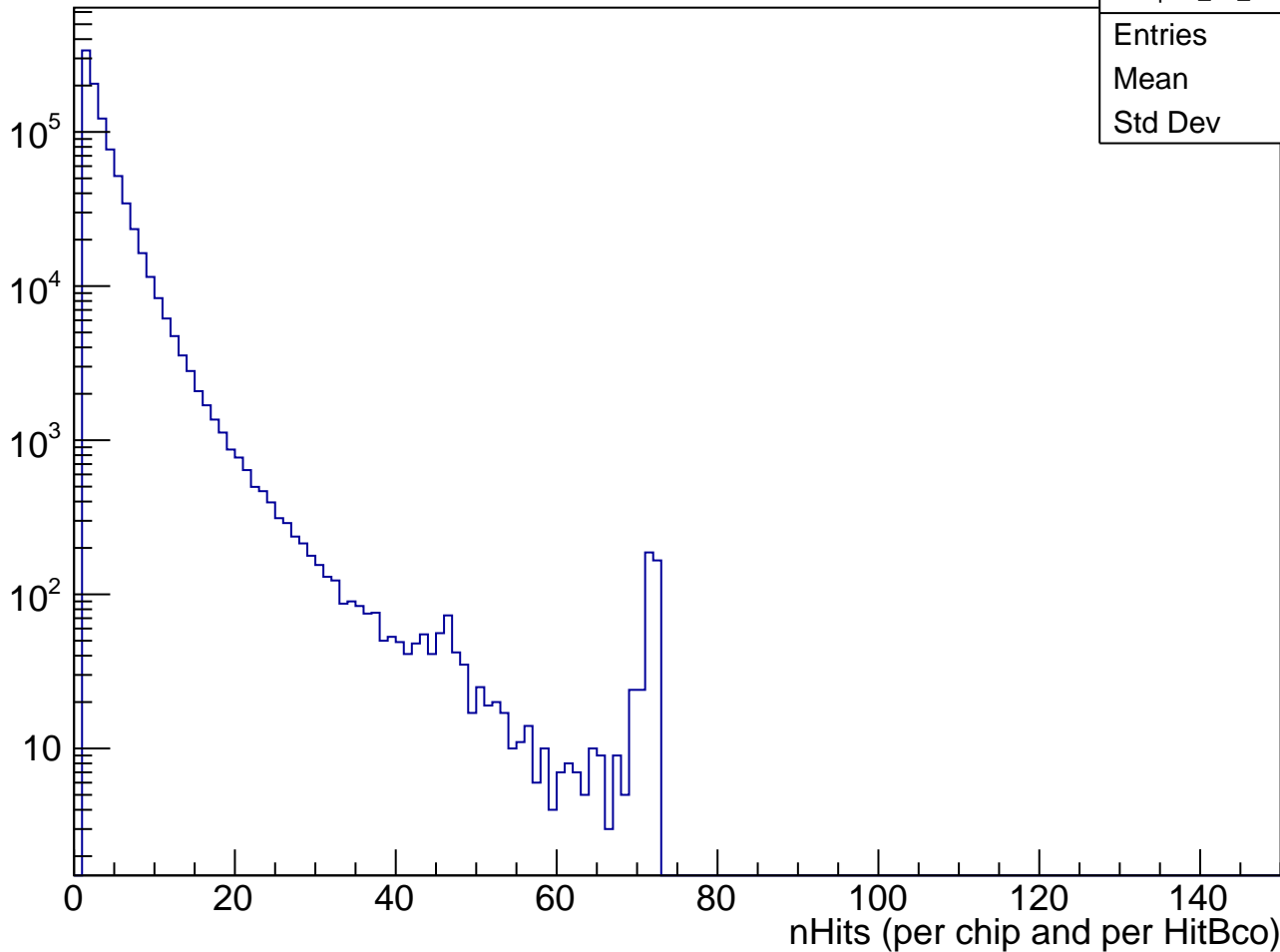
Entries



nChipHit_F1_Fch4_Chip14	
Entries	927202
Mean	3.162
Std Dev	3.695

nChipHit_F1_Fch4_Chip15

Entries

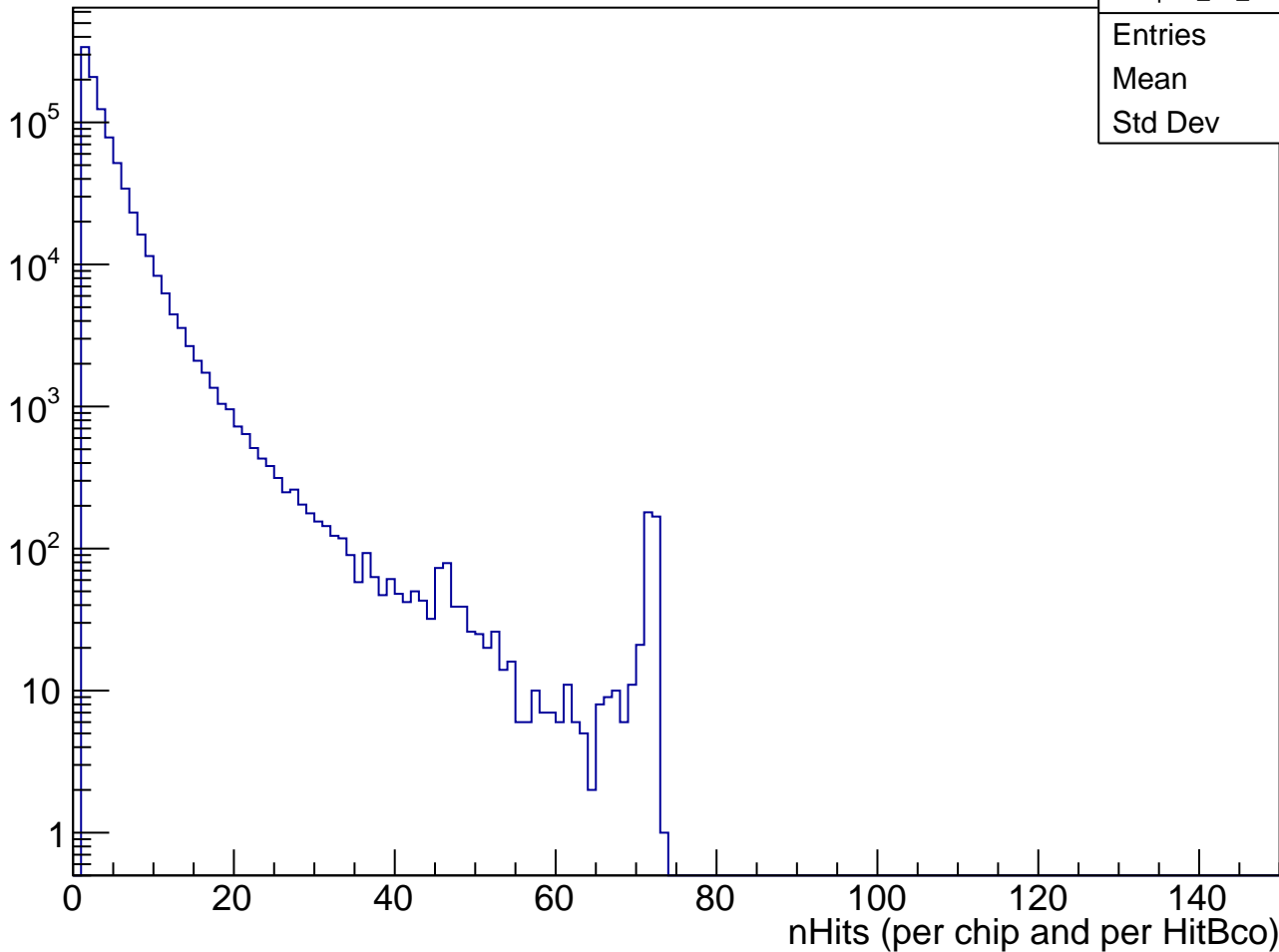


nChipHit_F1_Fch4_Chip15	
Entries	918825
Mean	3.137
Std Dev	3.658

nHits (per chip and per HitBco)

nChipHit_F1_Fch4_Chip16

Entries

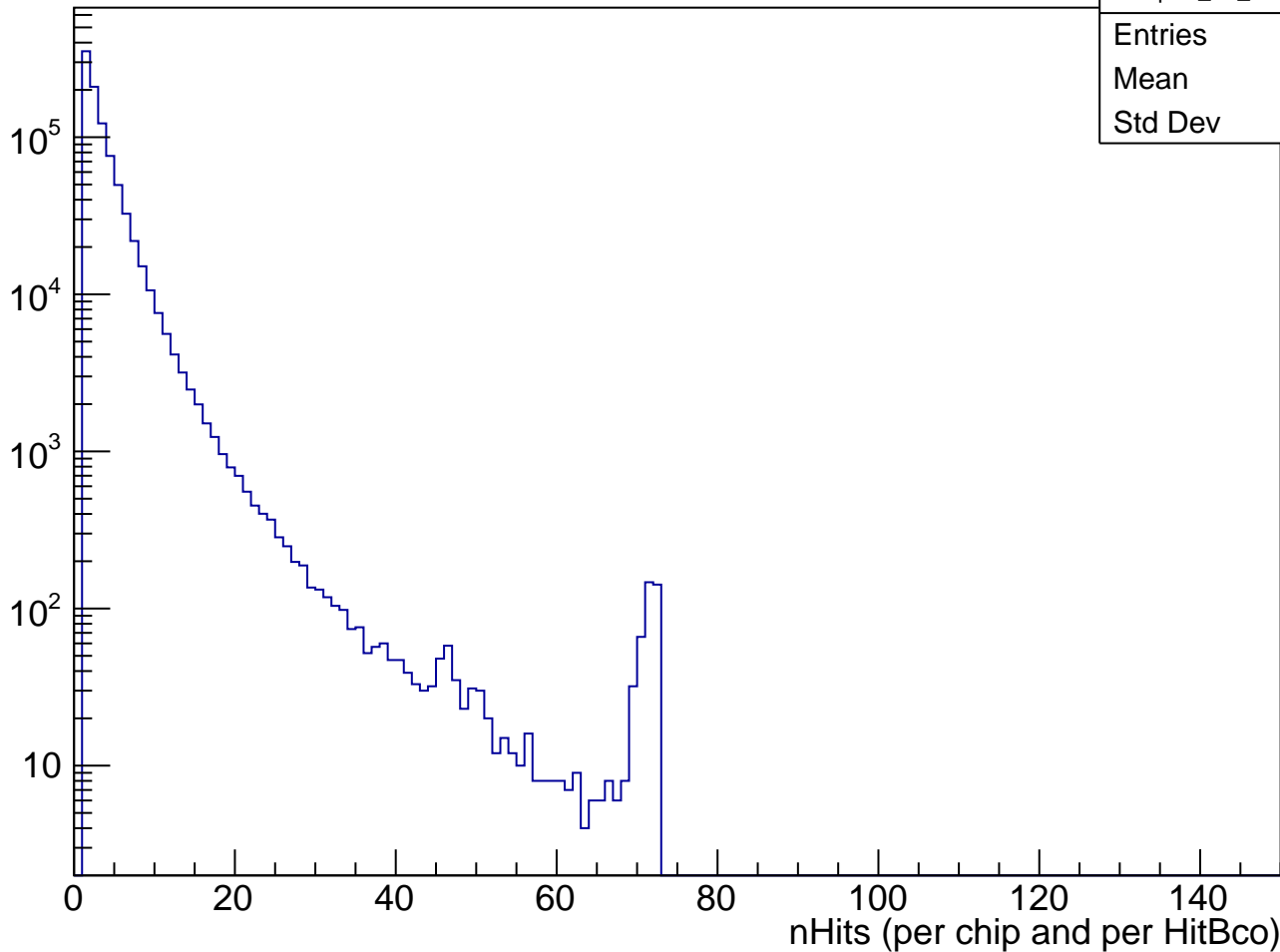


nChipHit_F1_Fch4_Chip16	
Entries	924978
Mean	3.124
Std Dev	3.632

nHits (per chip and per HitBco)

nChipHit_F1_Fch4_Chip17

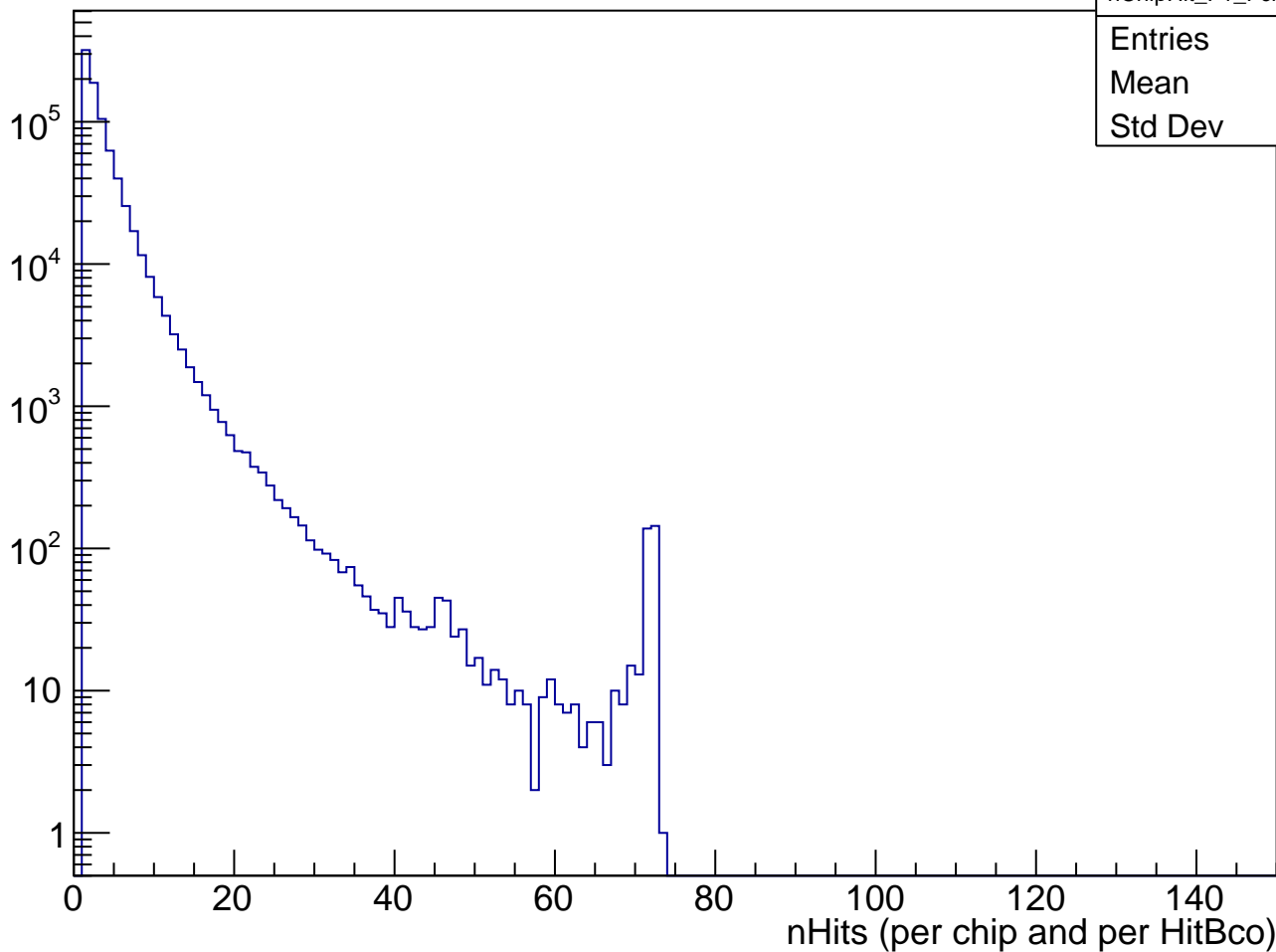
Entries



nChipHit_F1_Fch4_Chip17	
Entries	924813
Mean	3.017
Std Dev	3.516

nChipHit_F1_Fch4_Chip18

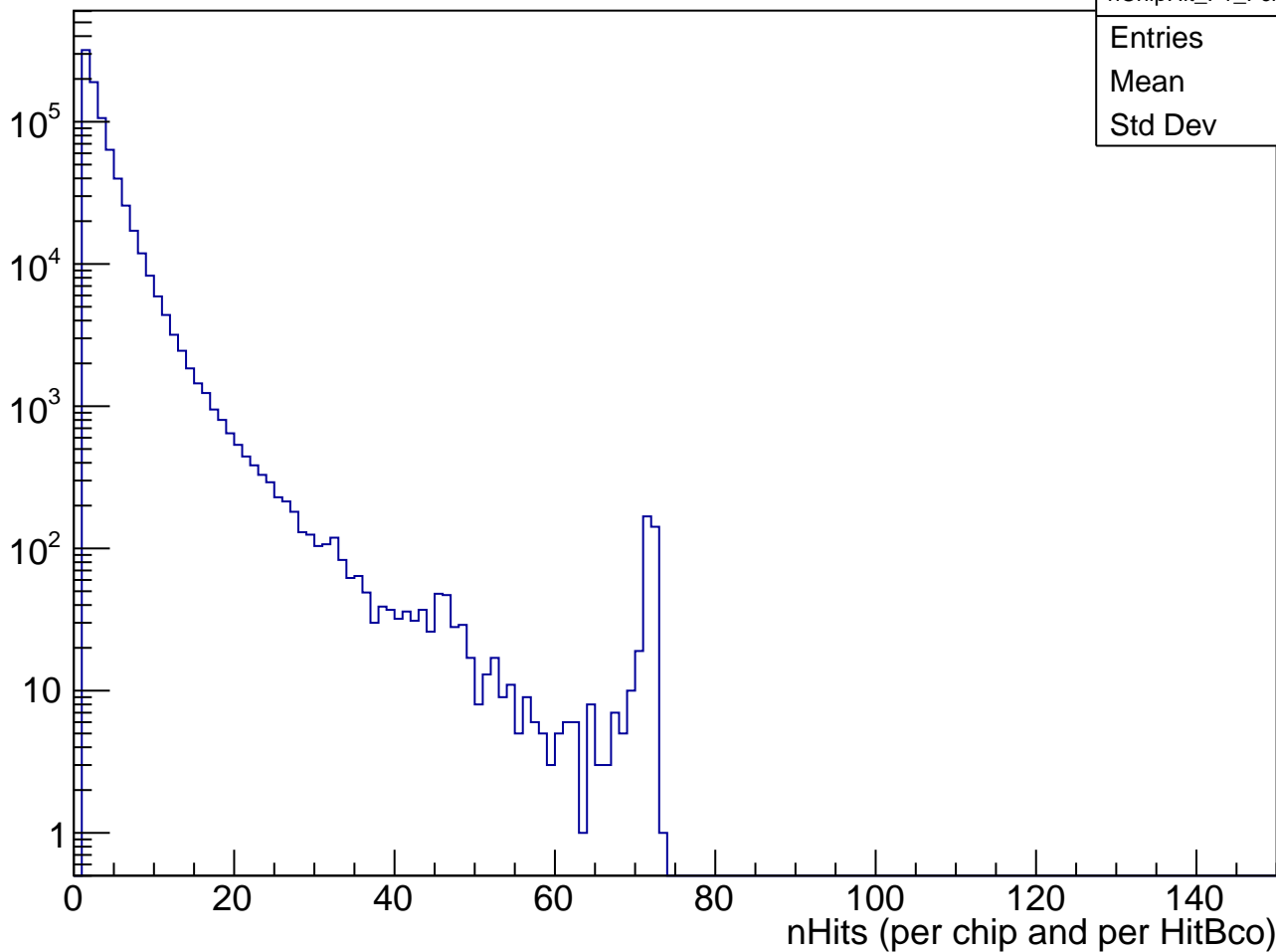
Entries



nChipHit_F1_Fch4_Chip18	
Entries	804111
Mean	2.88
Std Dev	3.385

nChipHit_F1_Fch4_Chip19

Entries

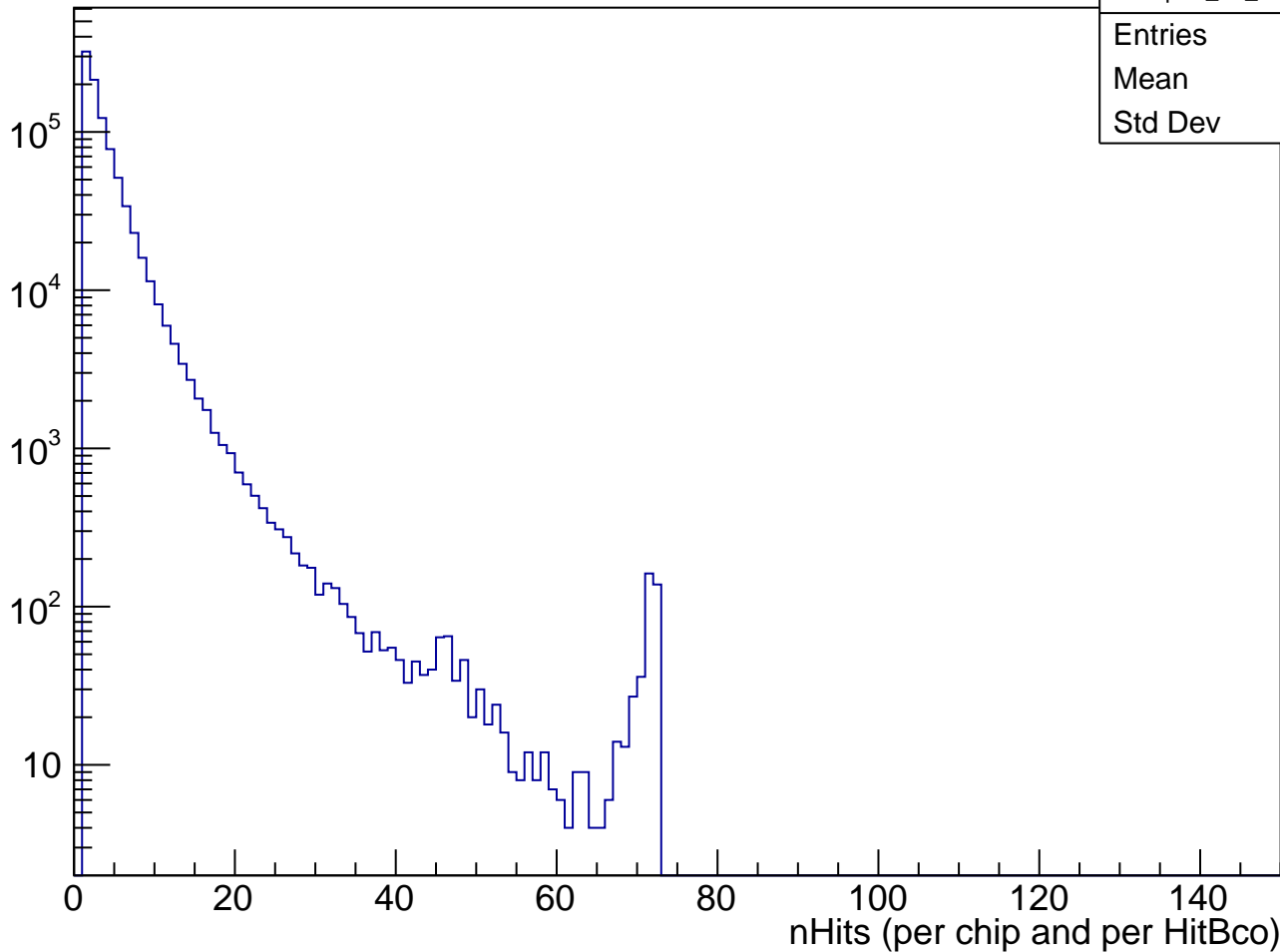


nChipHit_F1_Fch4_Chip19	
Entries	808853
Mean	2.891
Std Dev	3.409

nHits (per chip and per HitBco)

nChipHit_F1_Fch4_Chip2

Entries



nChipHit_F1_Fch4_Chip2

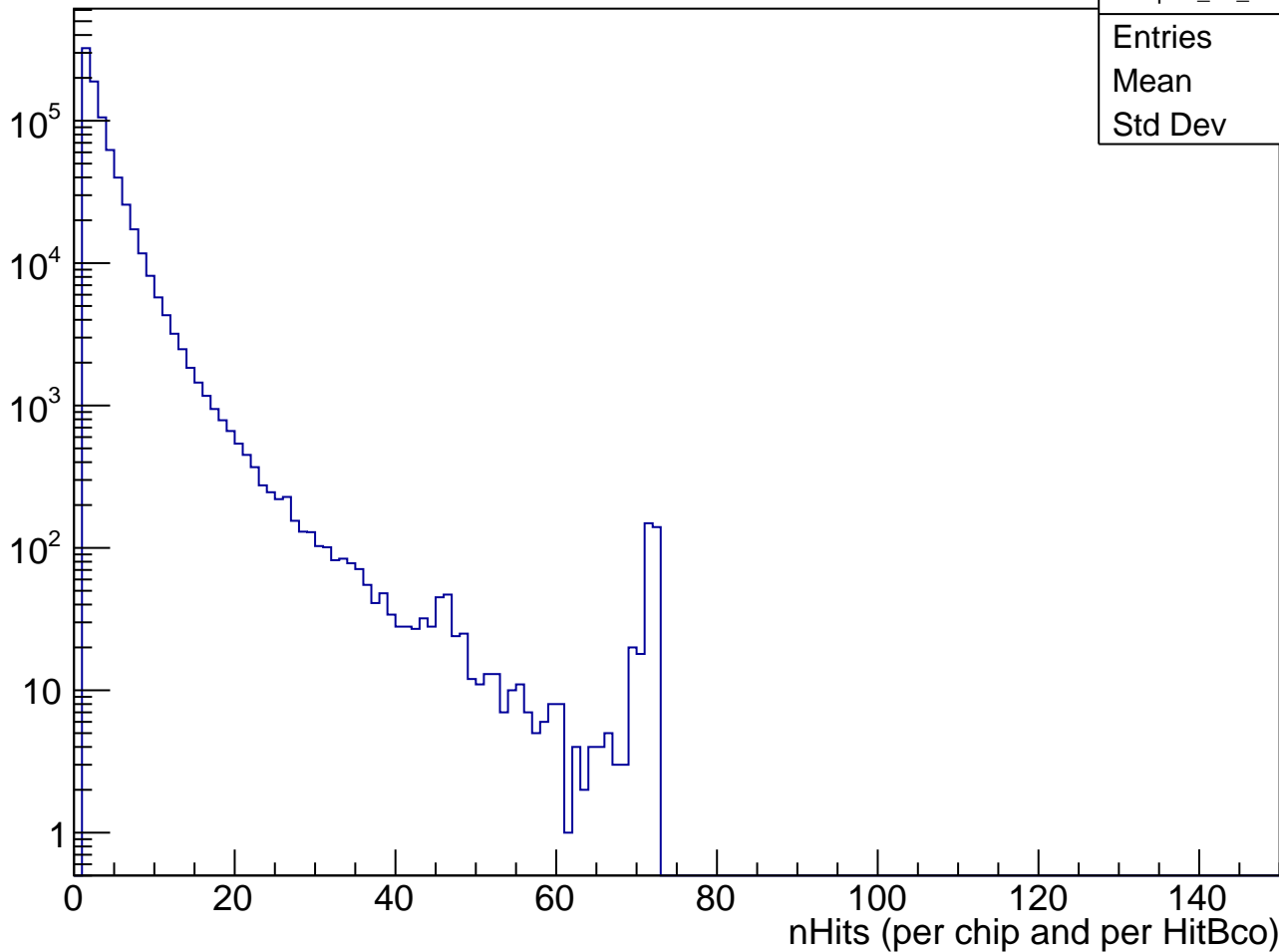
Entries 909417

Mean 3.139

Std Dev 3.604

nChipHit_F1_Fch4_Chip20

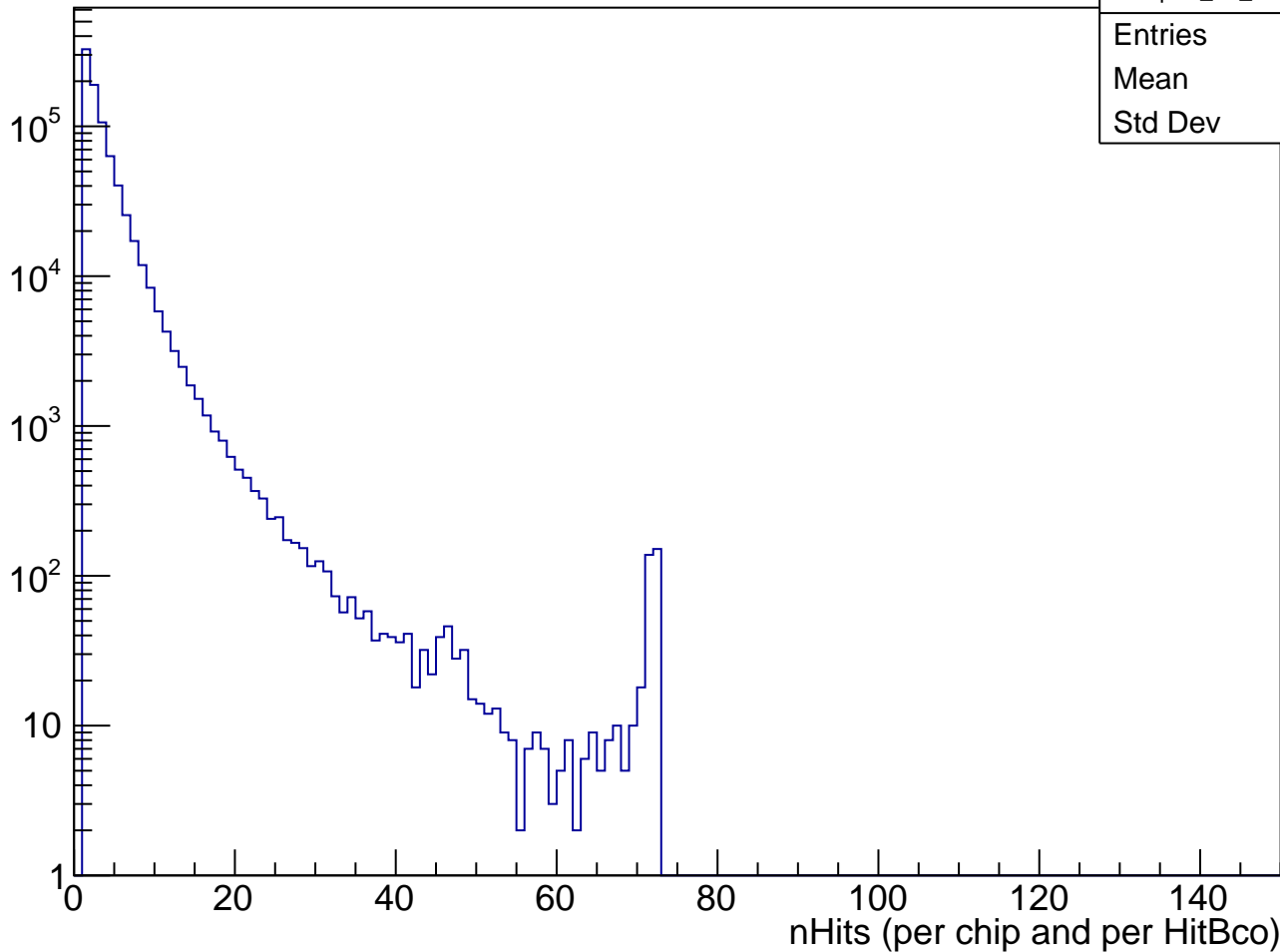
Entries



nChipHit_F1_Fch4_Chip20	
Entries	809501
Mean	2.87
Std Dev	3.371

nChipHit_F1_Fch4_Chip21

Entries

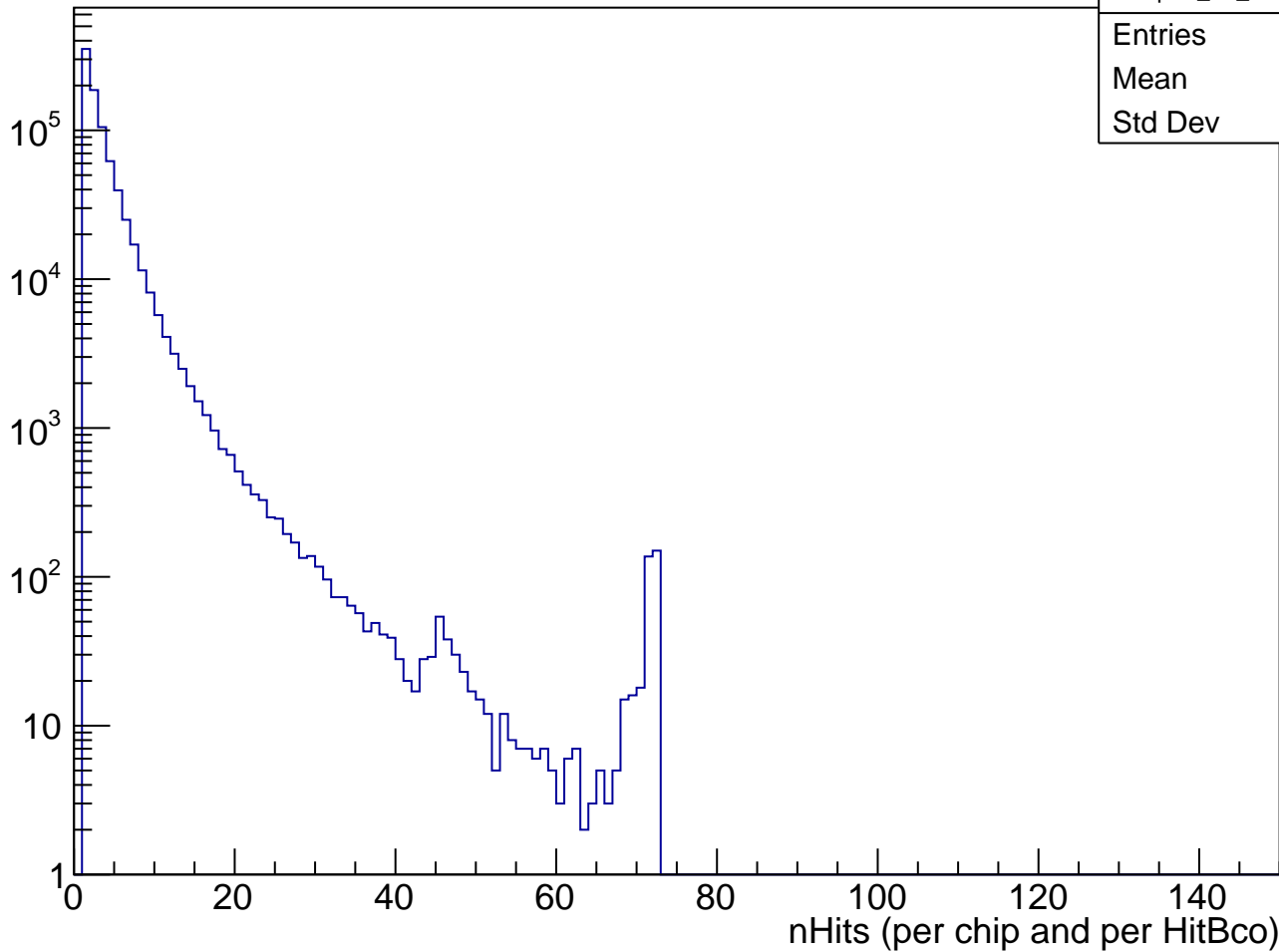


nChipHit_F1_Fch4_Chip21	
Entries	815459
Mean	2.866
Std Dev	3.365

nHits (per chip and per HitBco)

nChipHit_F1_Fch4_Chip22

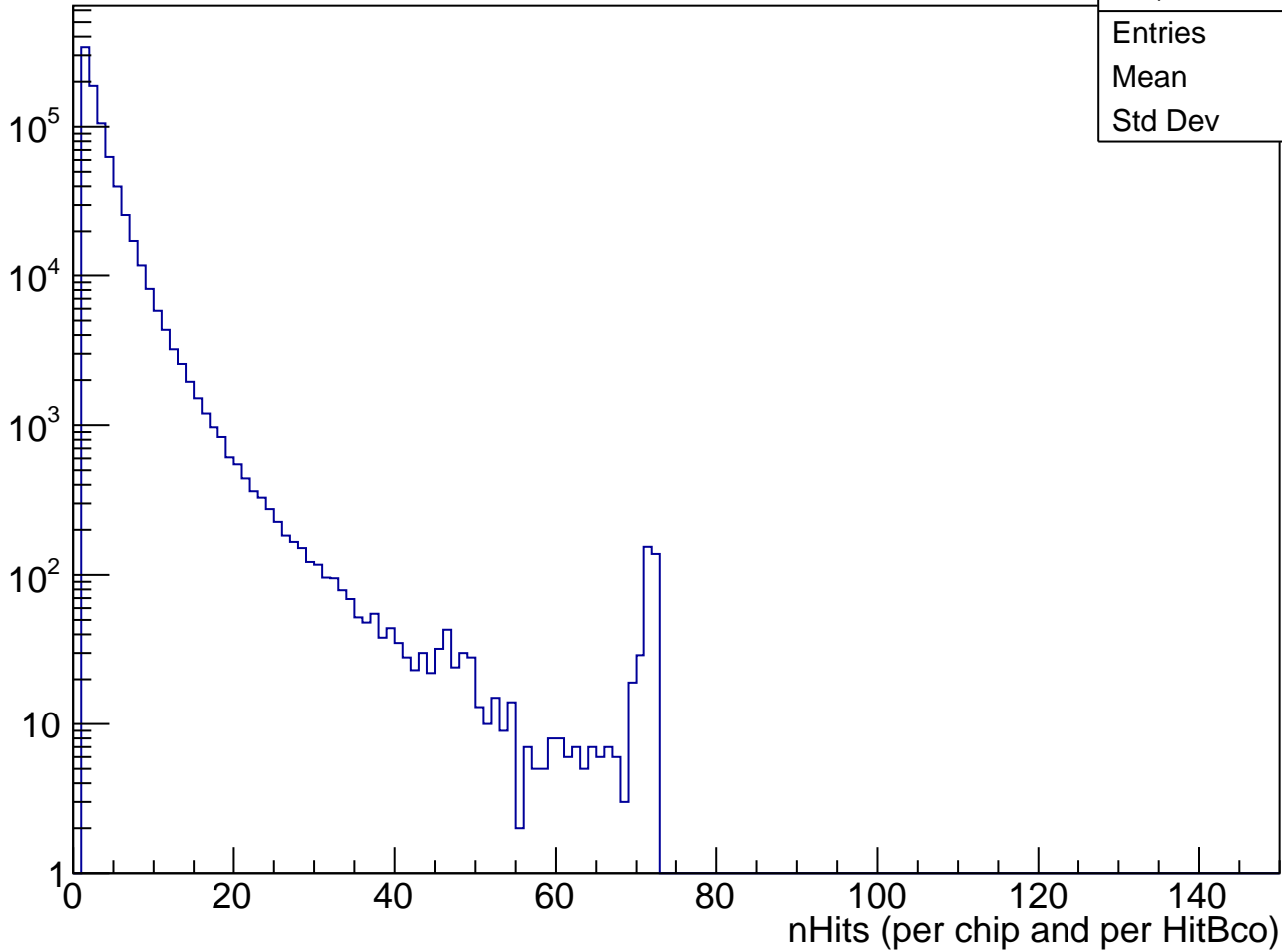
Entries



nChipHit_F1_Fch4_Chip22	
Entries	833528
Mean	2.799
Std Dev	3.329

nChipHit_F1_Fch4_Chip23

Entries

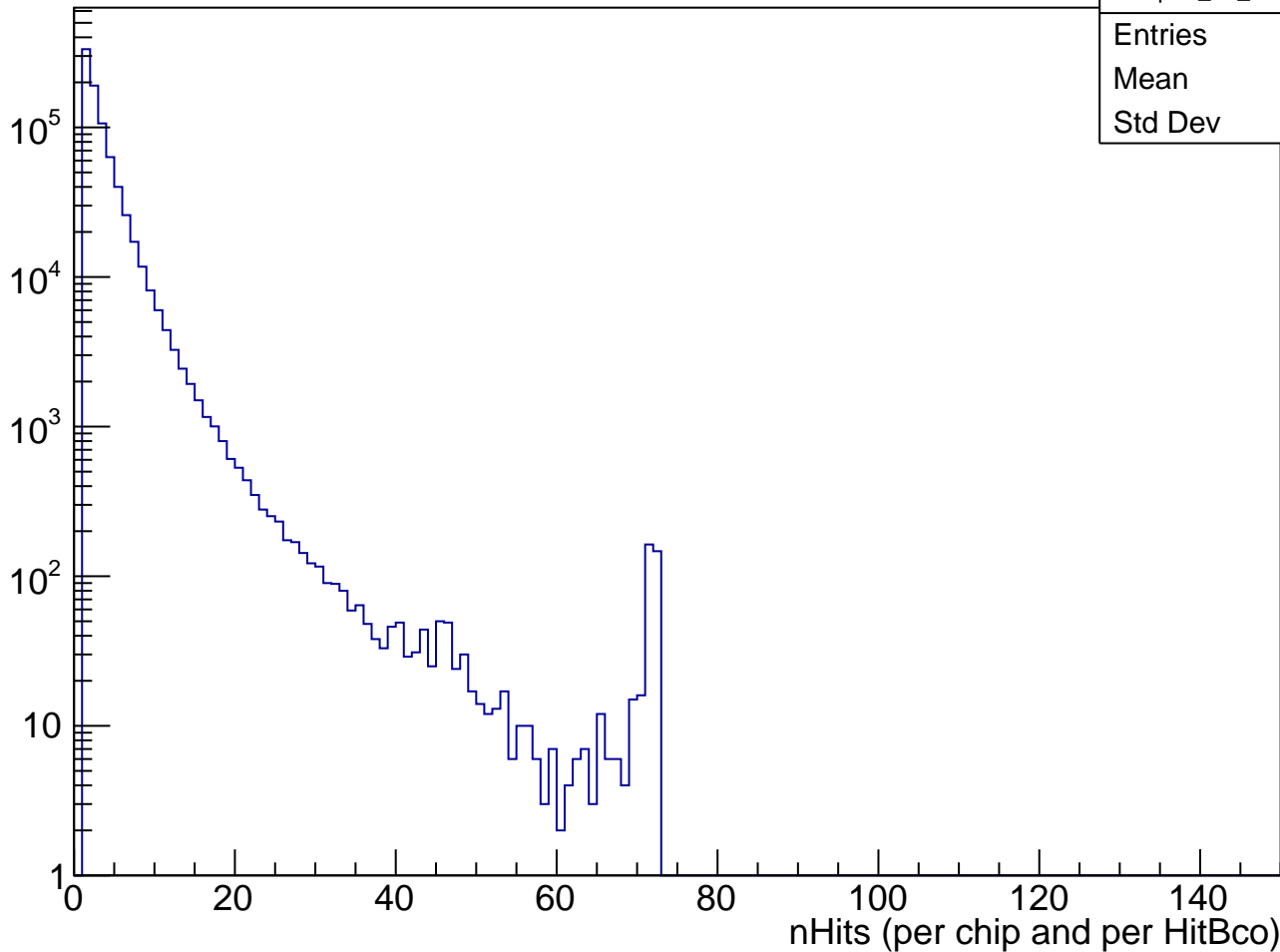


nChipHit_F1_Fch4_Chip23	
Entries	825540
Mean	2.842
Std Dev	3.382

nHits (per chip and per HitBco)

nChipHit_F1_Fch4_Chip24

Entries

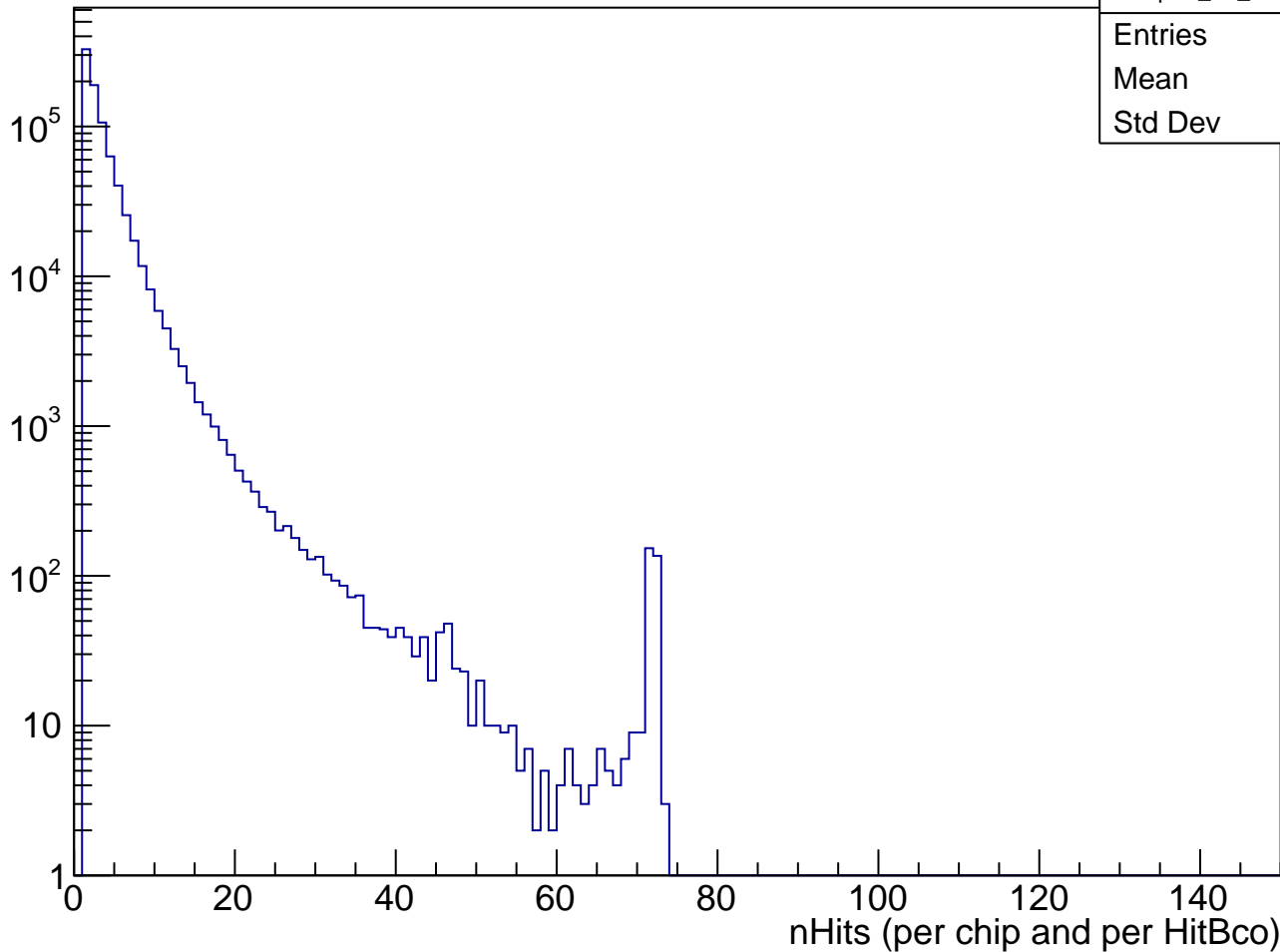


nChipHit_F1_Fch4_Chip24	
Entries	823494
Mean	2.856
Std Dev	3.387

nHits (per chip and per HitBco)

nChipHit_F1_Fch4_Chip25

Entries

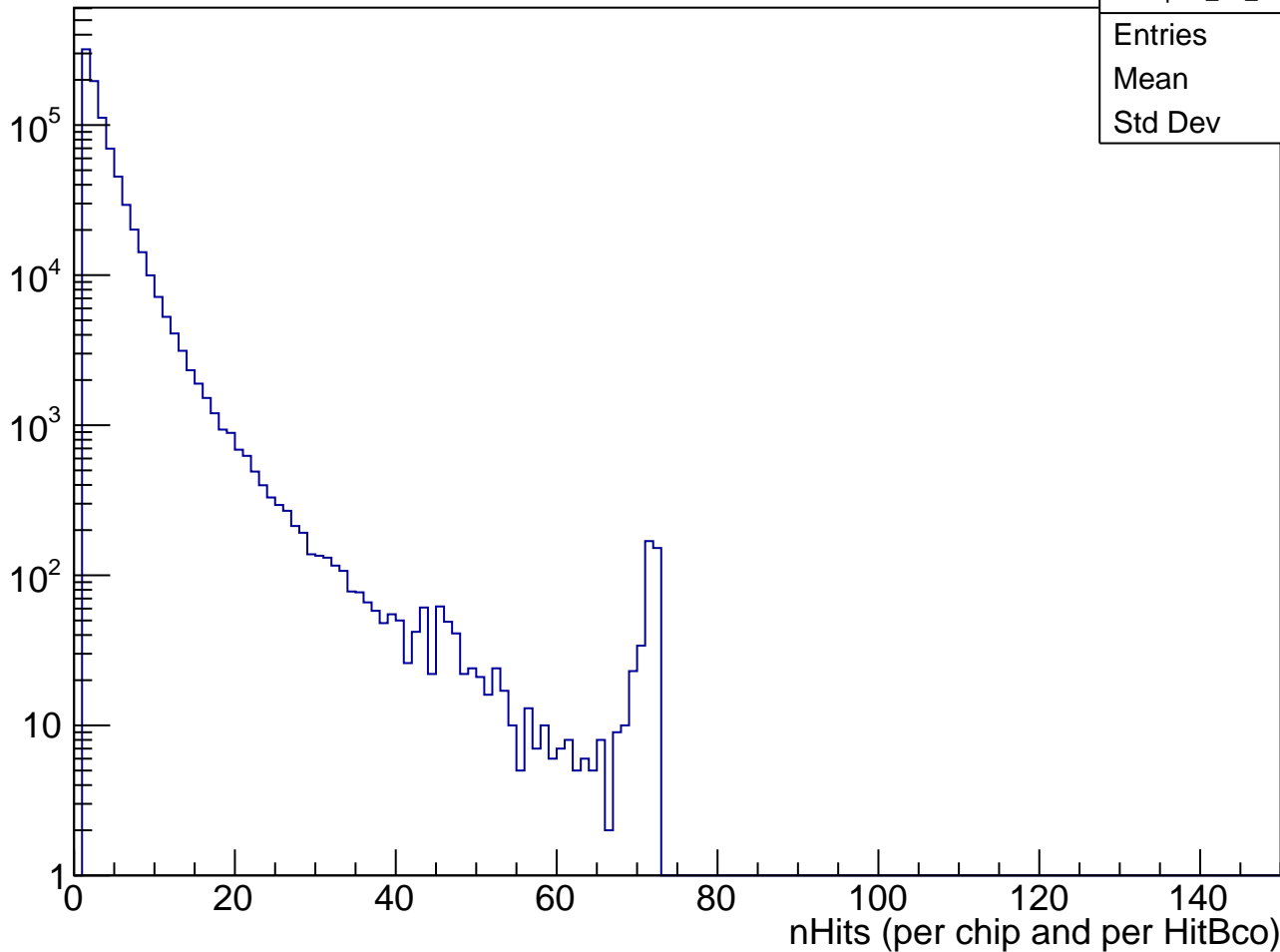


nChipHit_F1_Fch4_Chip25	
Entries	816670
Mean	2.869
Std Dev	3.367

nHits (per chip and per HitBco)

nChipHit_F1_Fch4_Chip3

Entries



nChipHit_F1_Fch4_Chip3

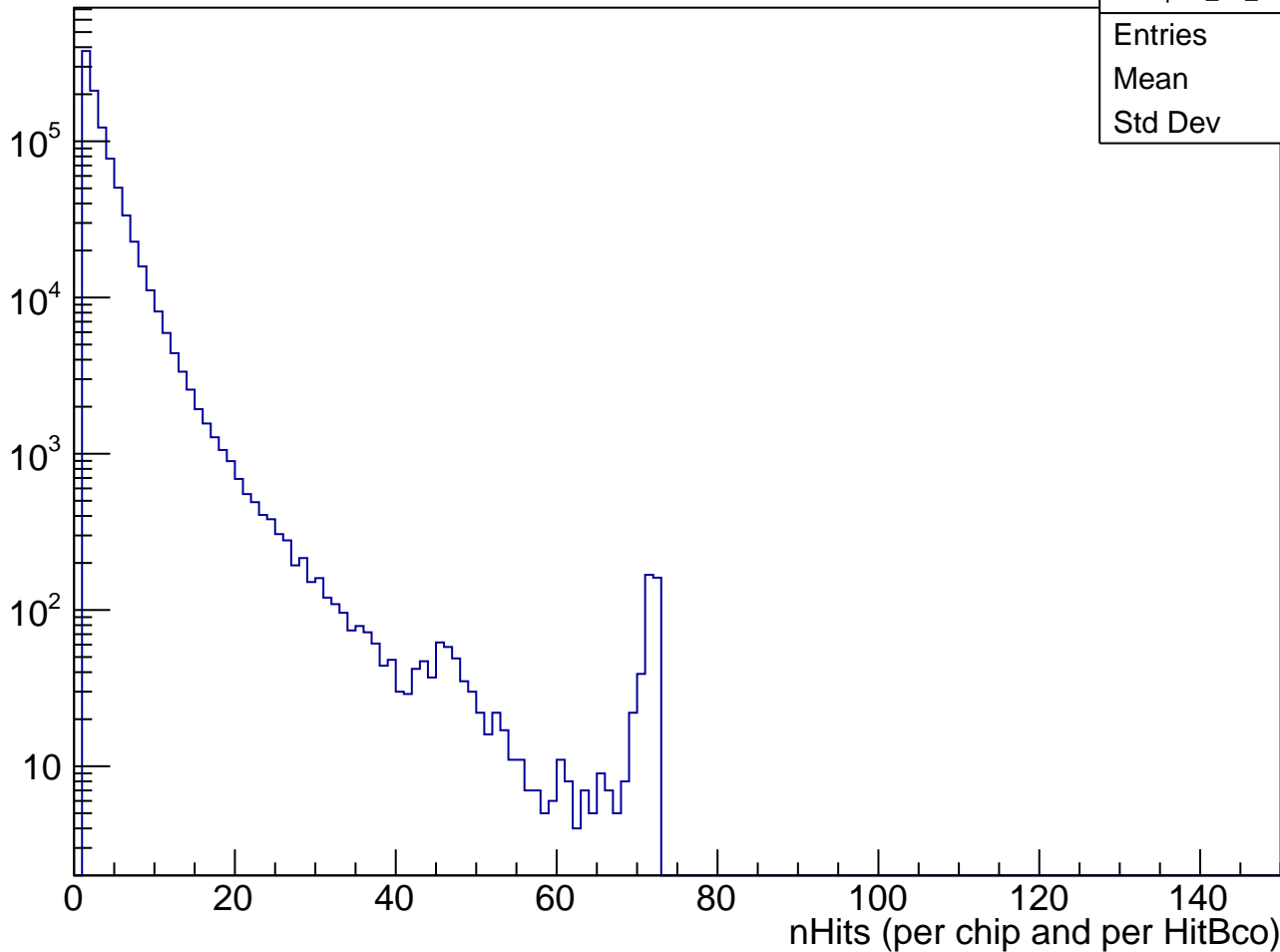
Entries 850433

Mean 3.059

Std Dev 3.621

nChipHit_F1_Fch4_Chip4

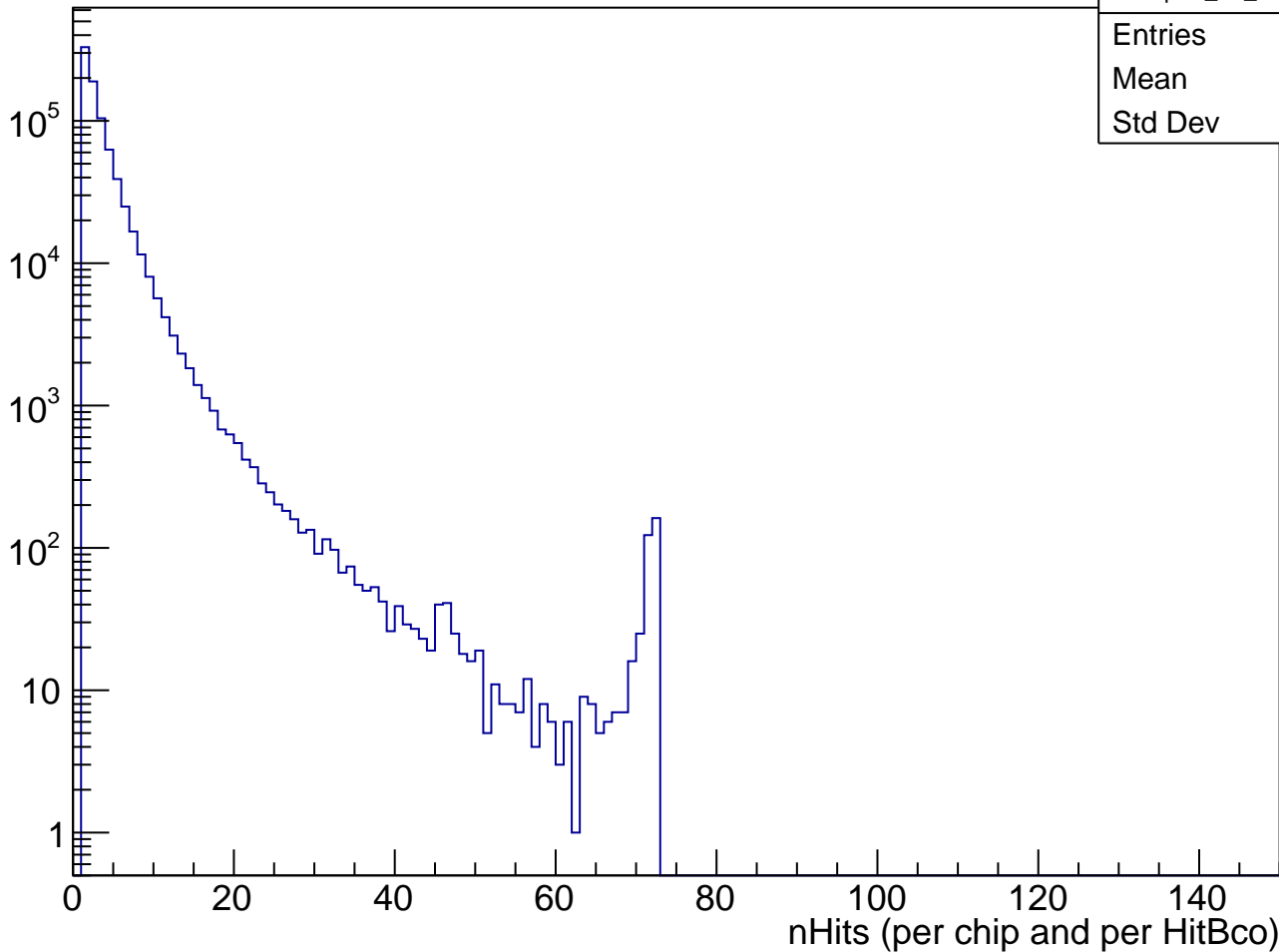
Entries



nChipHit_F1_Fch4_Chip4	
Entries	959264
Mean	3.001
Std Dev	3.527

nChipHit_F1_Fch4_Chip5

Entries



nChipHit_F1_Fch4_Chip5

Entries 811873

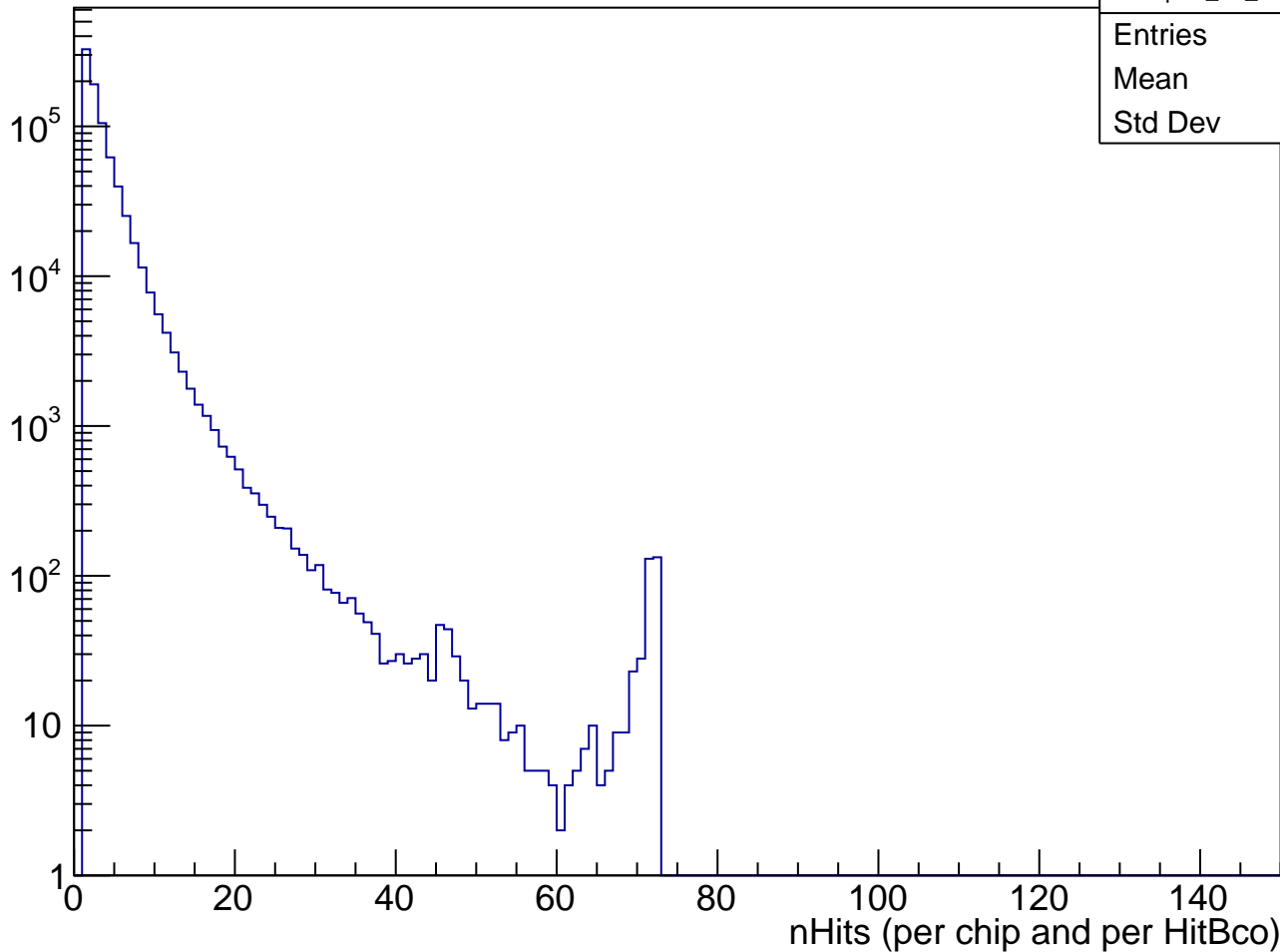
Mean 2.833

Std Dev 3.34

nHits (per chip and per HitBco)

nChipHit_F1_Fch4_Chip6

Entries



nChipHit_F1_Fch4_Chip6

Entries 811844

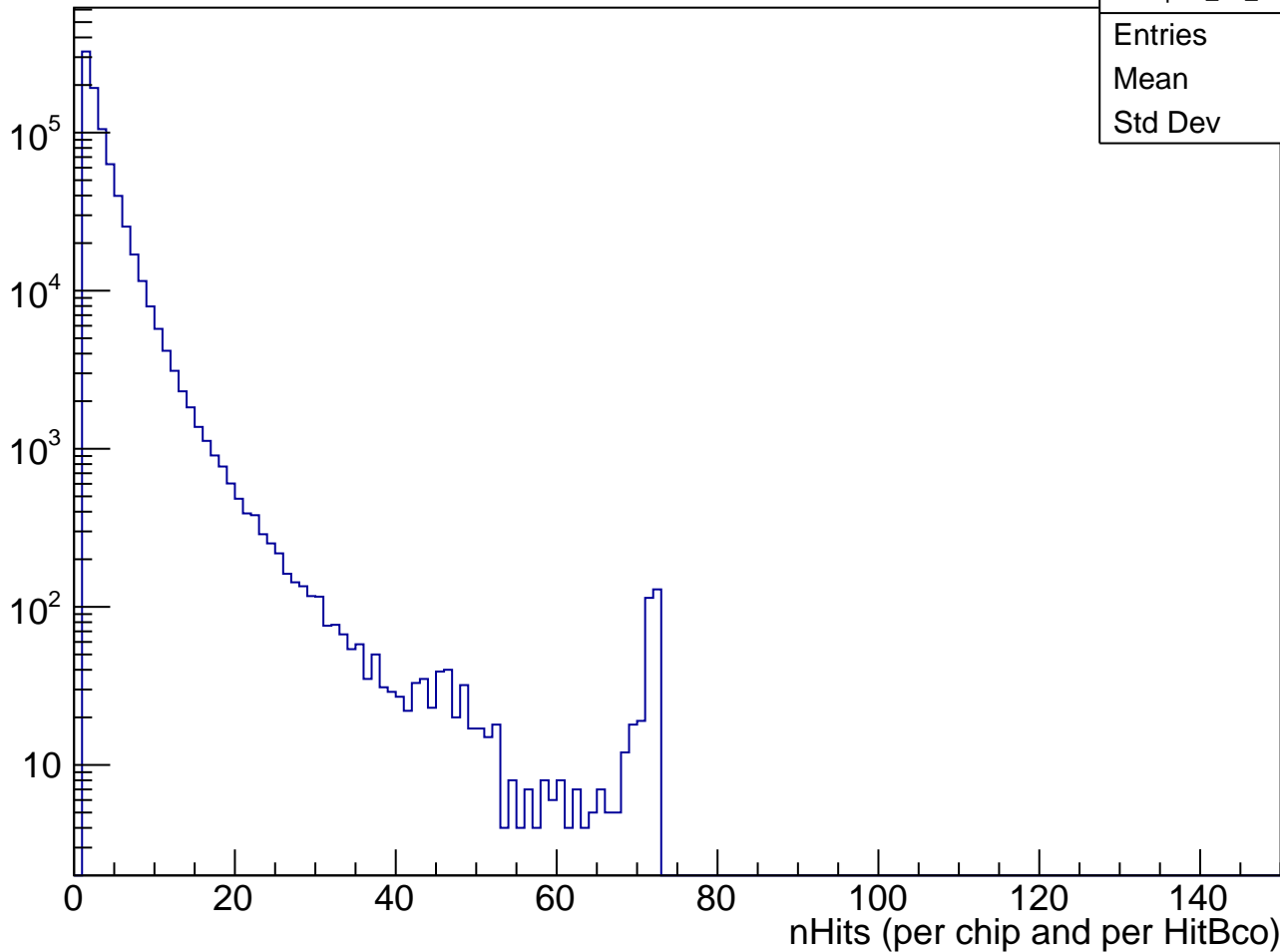
Mean 2.832

Std Dev 3.318

nHits (per chip and per HitBco)

nChipHit_F1_Fch4_Chip7

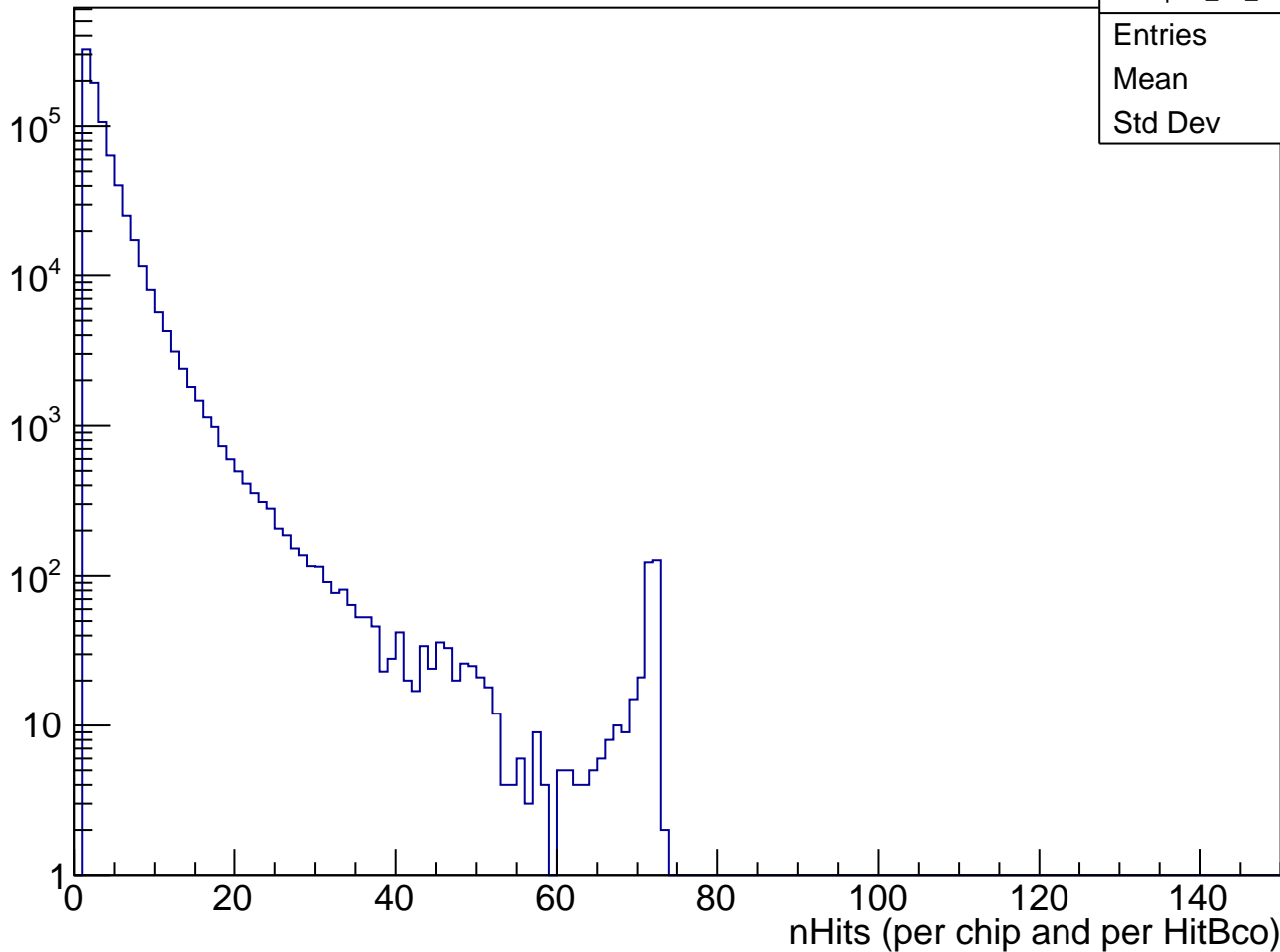
Entries



nChipHit_F1_Fch4_Chip7	
Entries	813079
Mean	2.837
Std Dev	3.286

nChipHit_F1_Fch4_Chip8

Entries



nChipHit_F1_Fch4_Chip8

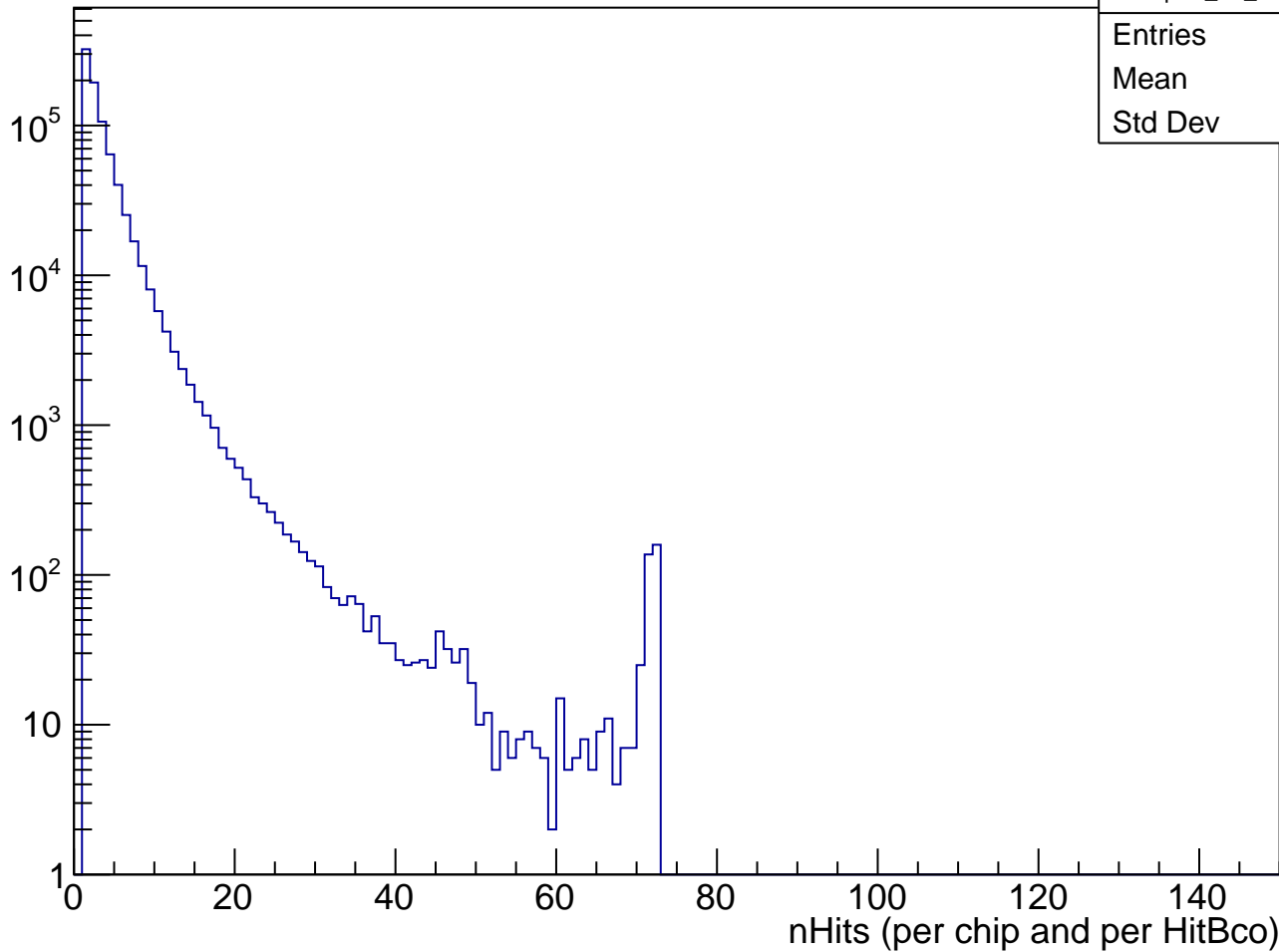
Entries 817353

Mean 2.848

Std Dev 3.294

nChipHit_F1_Fch4_Chip9

Entries



nChipHit_F1_Fch4_Chip9

Entries 815003

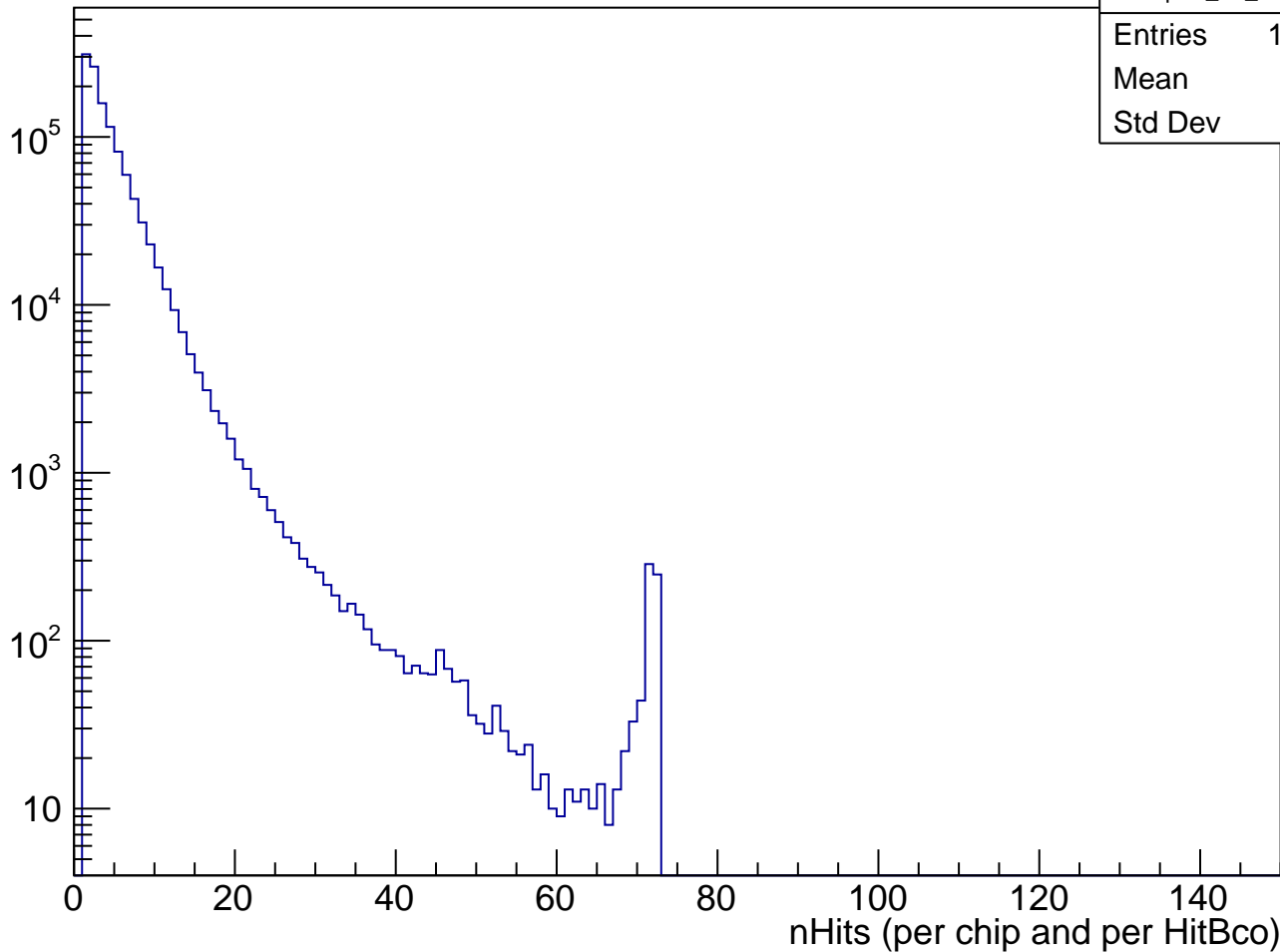
Mean 2.855

Std Dev 3.344

nHits (per chip and per HitBco)

nChipHit_F1_Fch5_Chip0

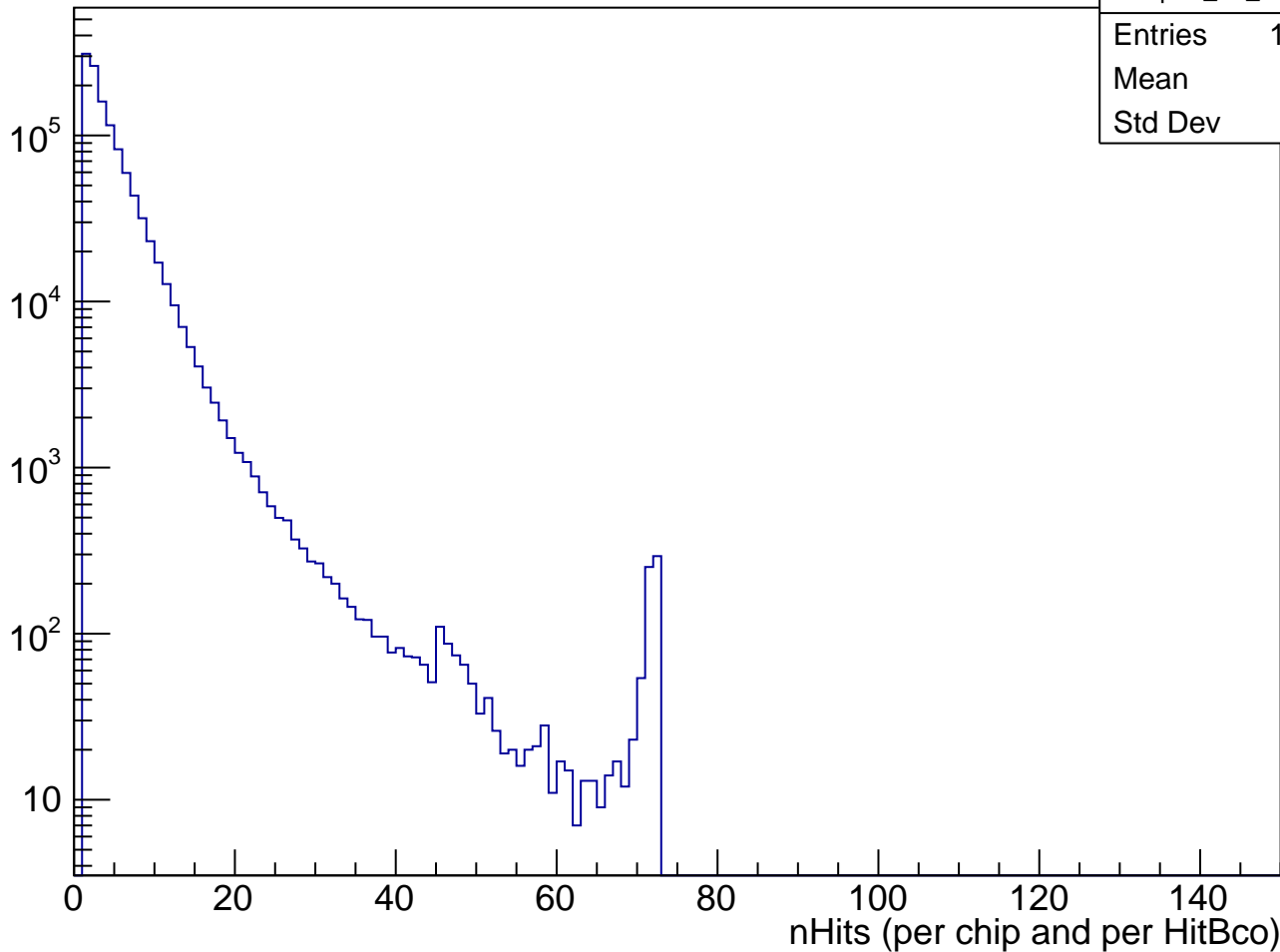
Entries



nChipHit_F1_Fch5_Chip0	
Entries	1157139
Mean	3.774
Std Dev	4.066

nChipHit_F1_Fch5_Chip1

Entries

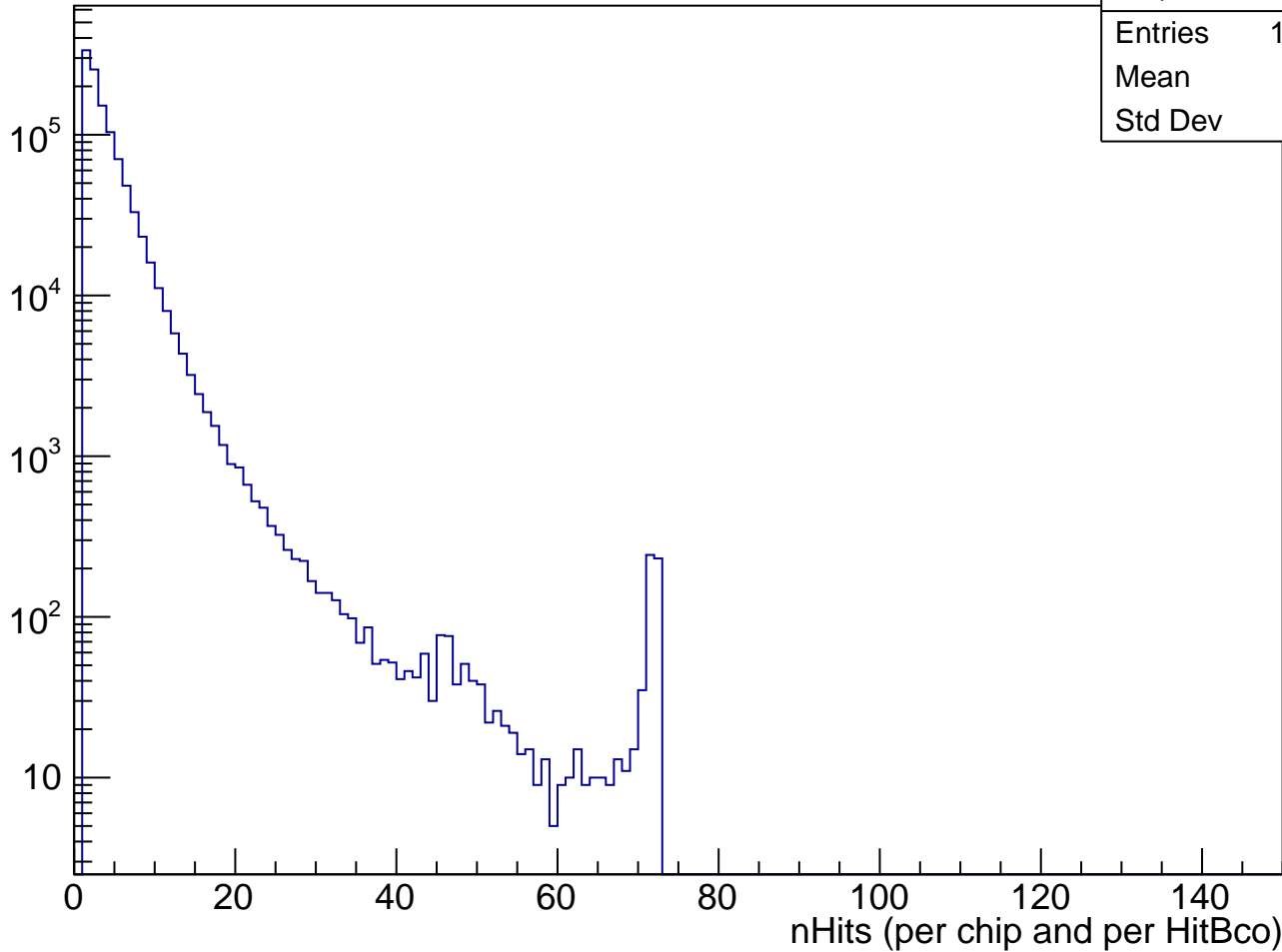


nChipHit_F1_Fch5_Chip1	
Entries	1162570
Mean	3.796
Std Dev	4.096

nHits (per chip and per HitBco)

nChipHit_F1_Fch5_Chip10

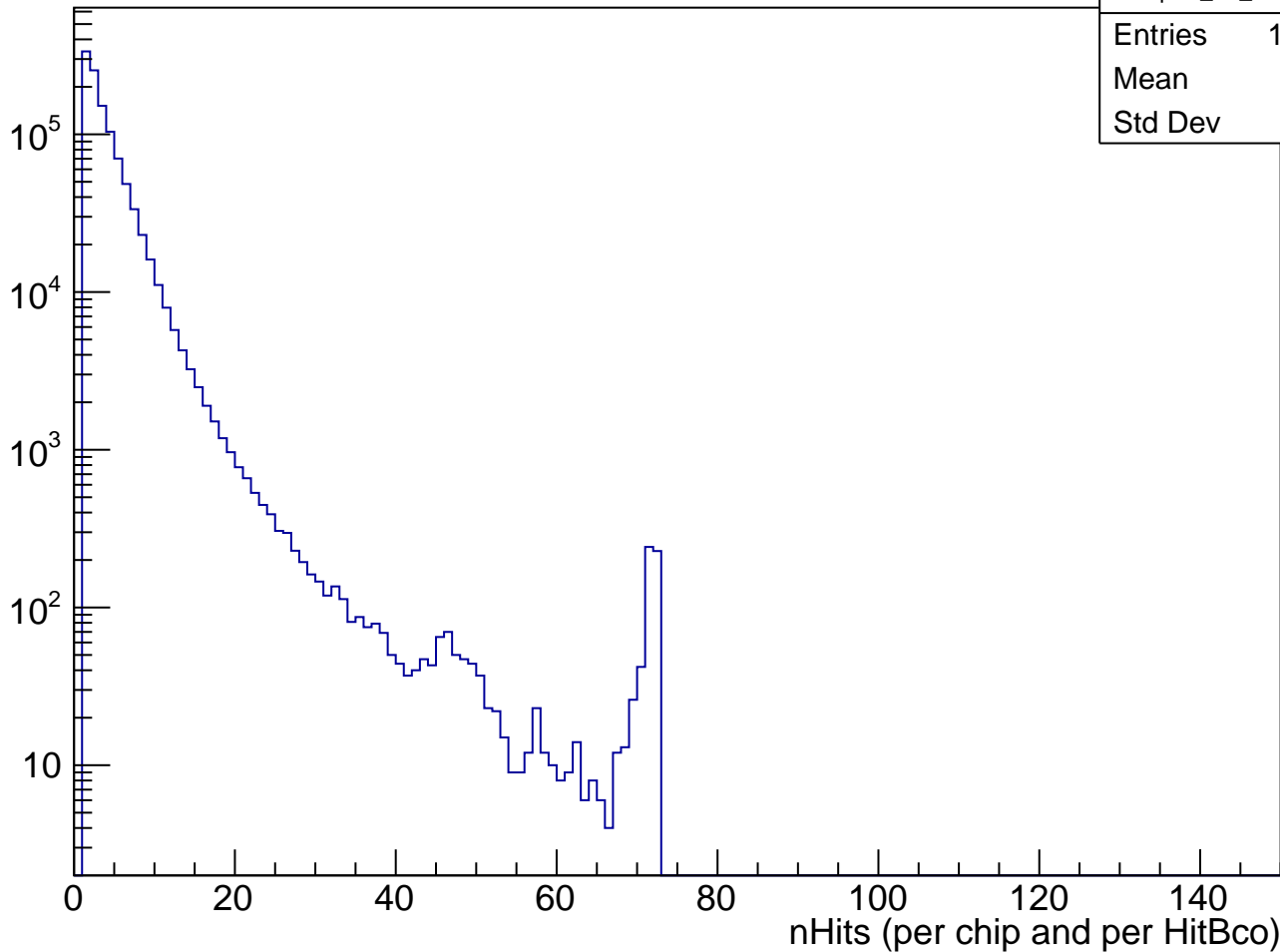
Entries



nChipHit_F1_Fch5_Chip10	
Entries	1083519
Mean	3.341
Std Dev	3.654

nChipHit_F1_Fch5_Chip11

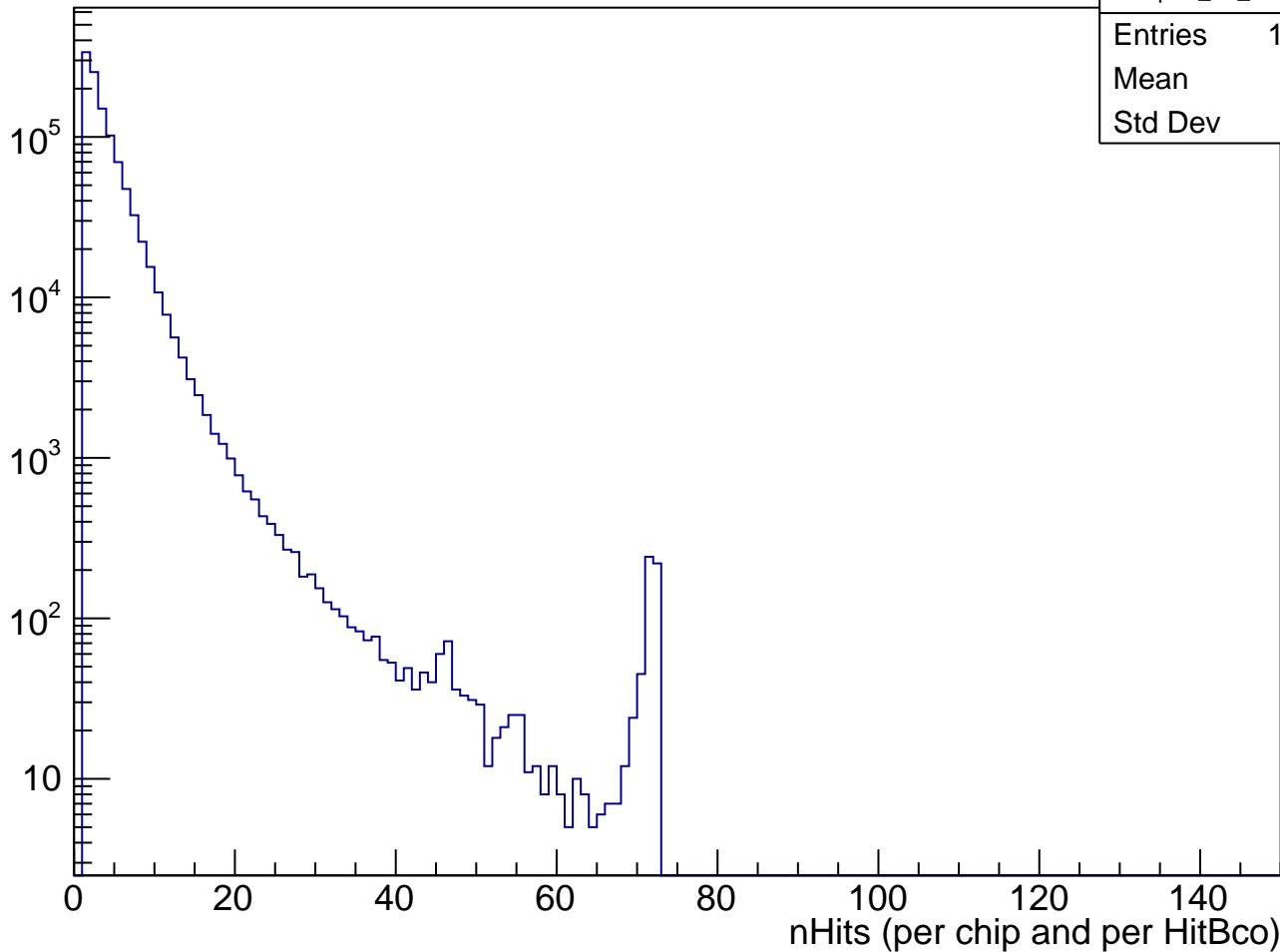
Entries



nChipHit_F1_Fch5_Chip11	
Entries	1083239
Mean	3.342
Std Dev	3.653

nChipHit_F1_Fch5_Chip12

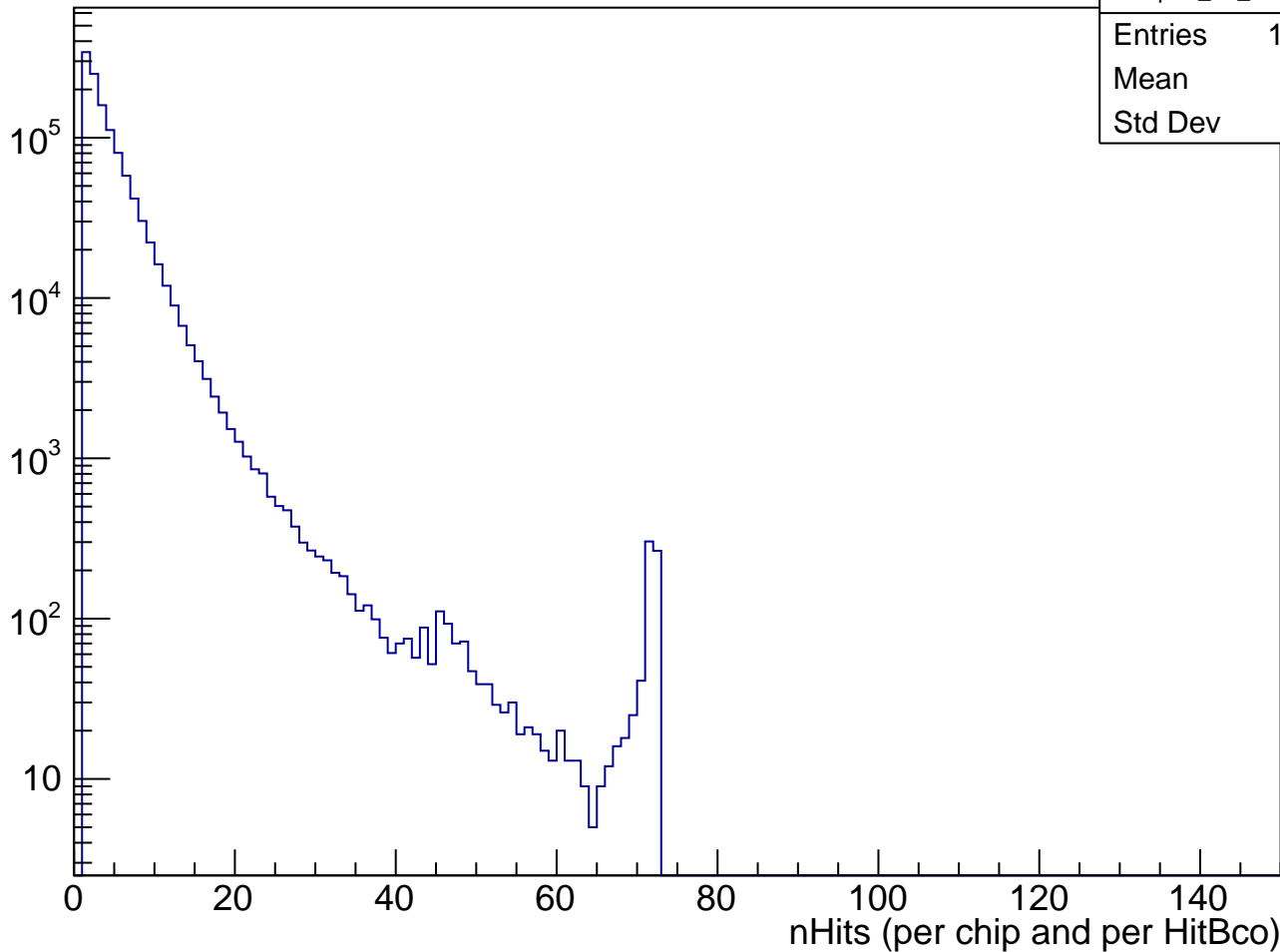
Entries



nChipHit_F1_Fch5_Chip12	
Entries	1076249
Mean	3.314
Std Dev	3.628

nChipHit_F1_Fch5_Chip13

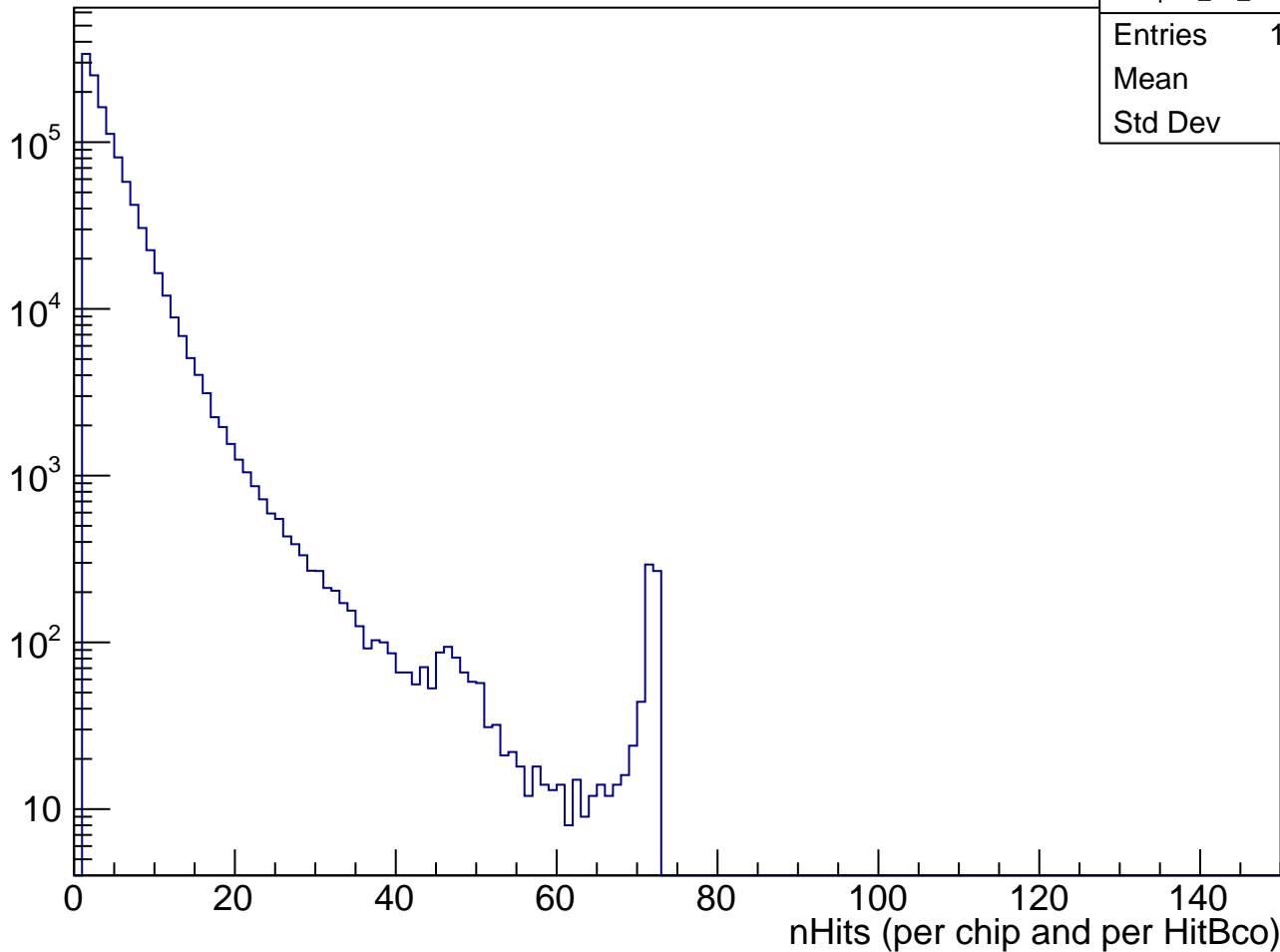
Entries



nChipHit_F1_Fch5_Chip13	
Entries	1167249
Mean	3.703
Std Dev	4.088

nChipHit_F1_Fch5_Chip14

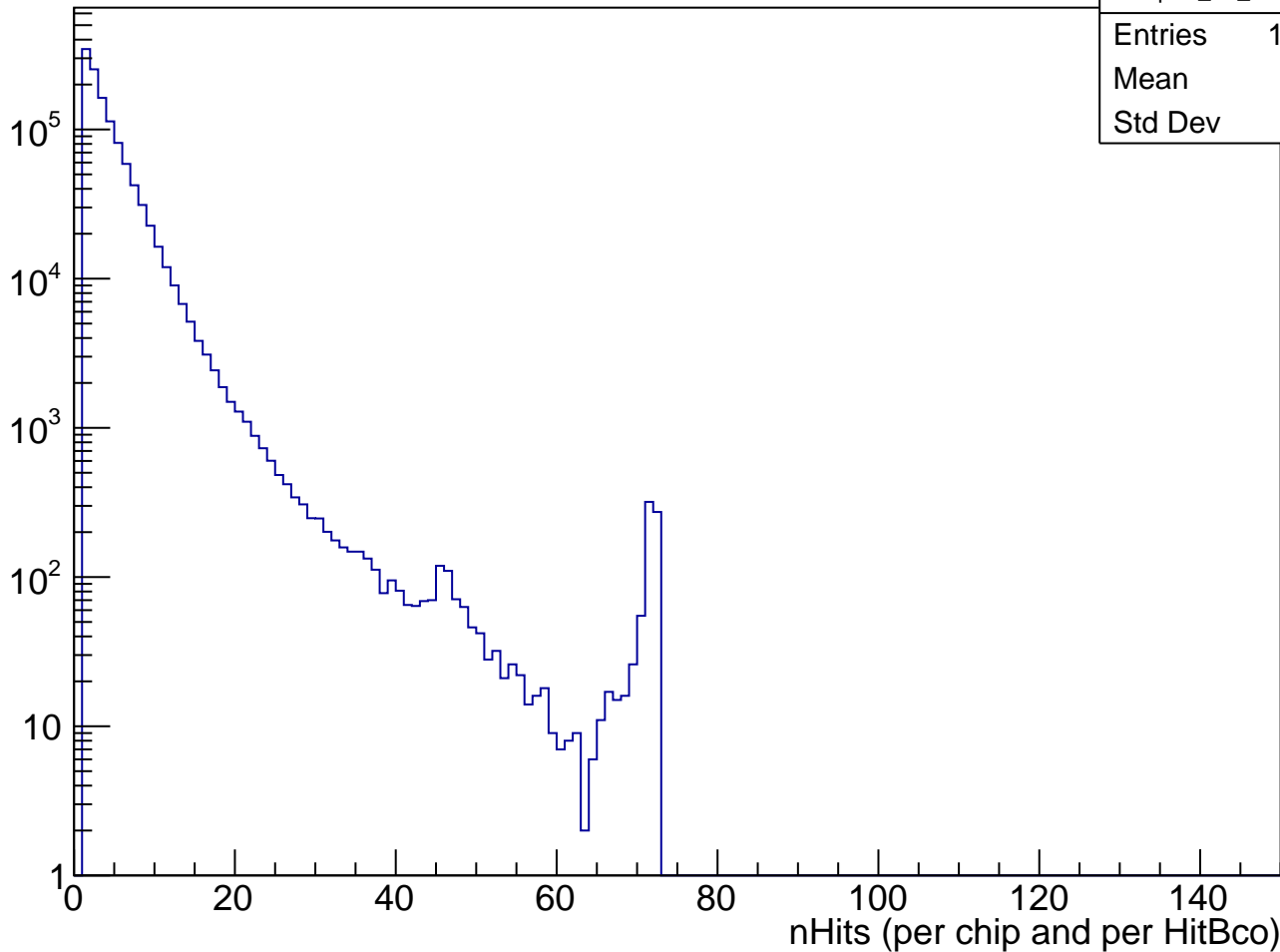
Entries



nChipHit_F1_Fch5_Chip14	
Entries	1169833
Mean	3.711
Std Dev	4.078

nChipHit_F1_Fch5_Chip15

Entries

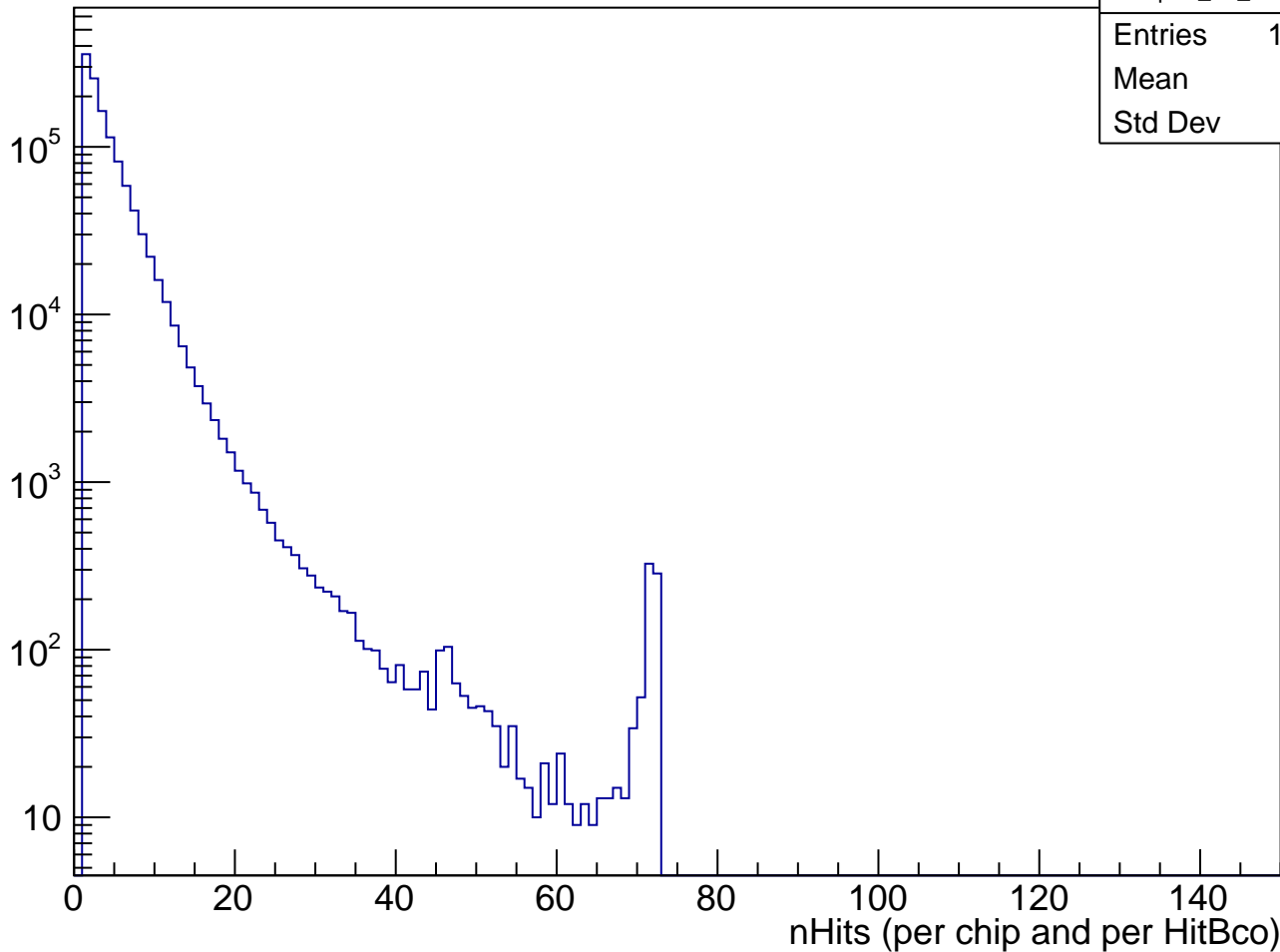


nChipHit_F1_Fch5_Chip15	
Entries	1182655
Mean	3.697
Std Dev	4.076

nHits (per chip and per HitBco)

nChipHit_F1_Fch5_Chip16

Entries

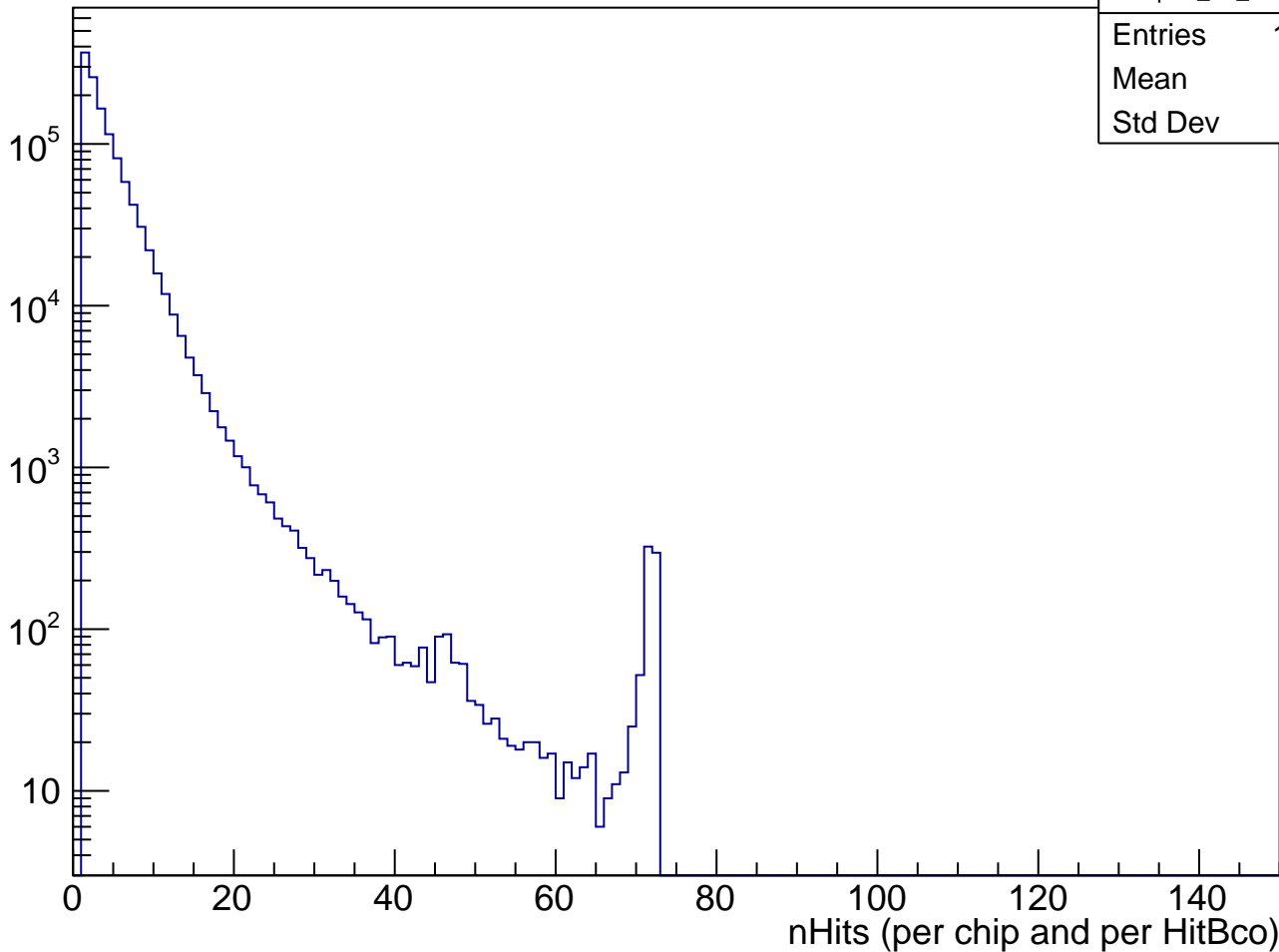


nChipHit_F1_Fch5_Chip16	
Entries	1194865
Mean	3.64
Std Dev	4.041

nHits (per chip and per HitBco)

nChipHit_F1_Fch5_Chip17

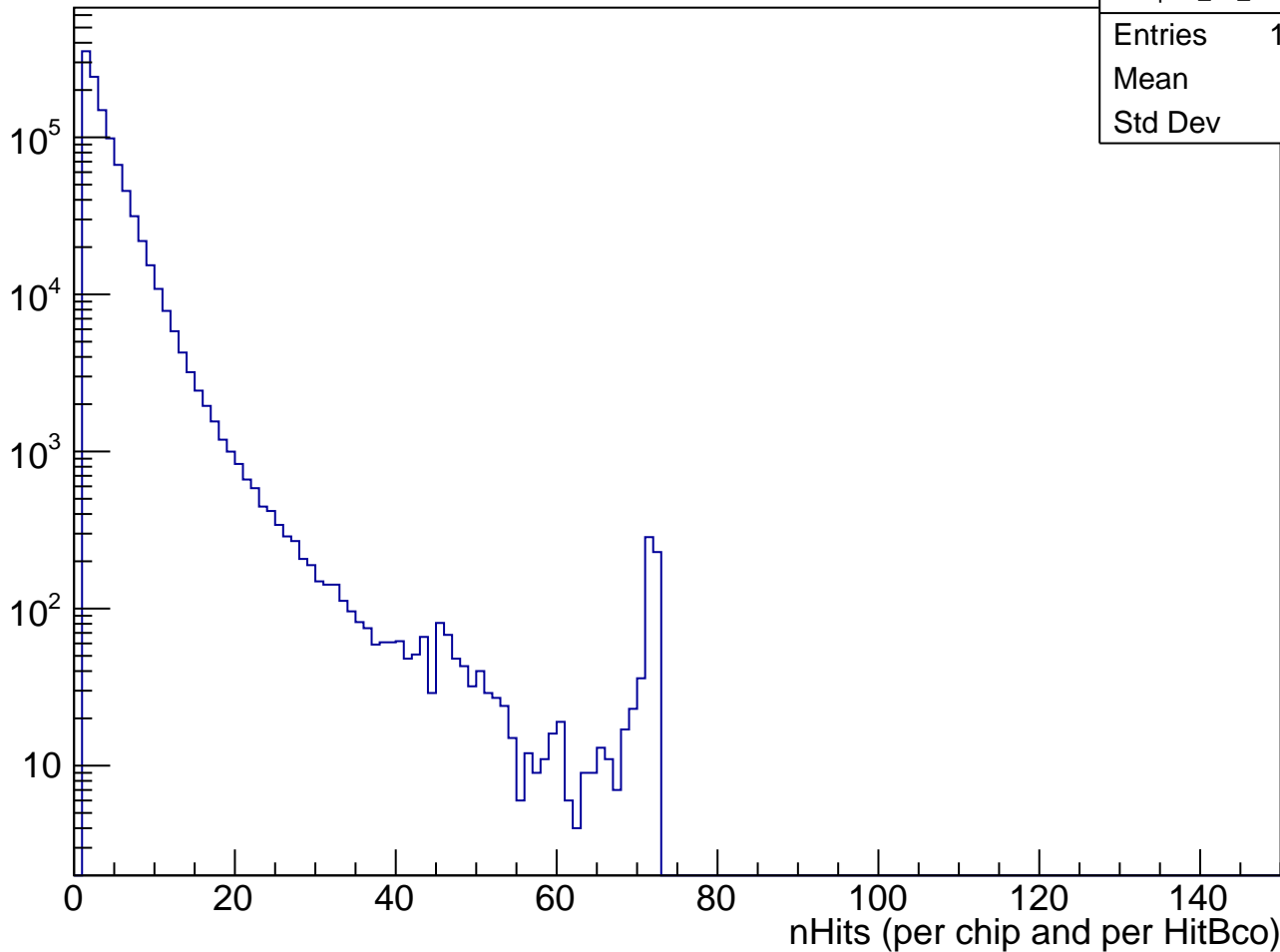
Entries



nChipHit_F1_Fch5_Chip17	
Entries	1210441
Mean	3.611
Std Dev	4.008

nChipHit_F1_Fch5_Chip18

Entries

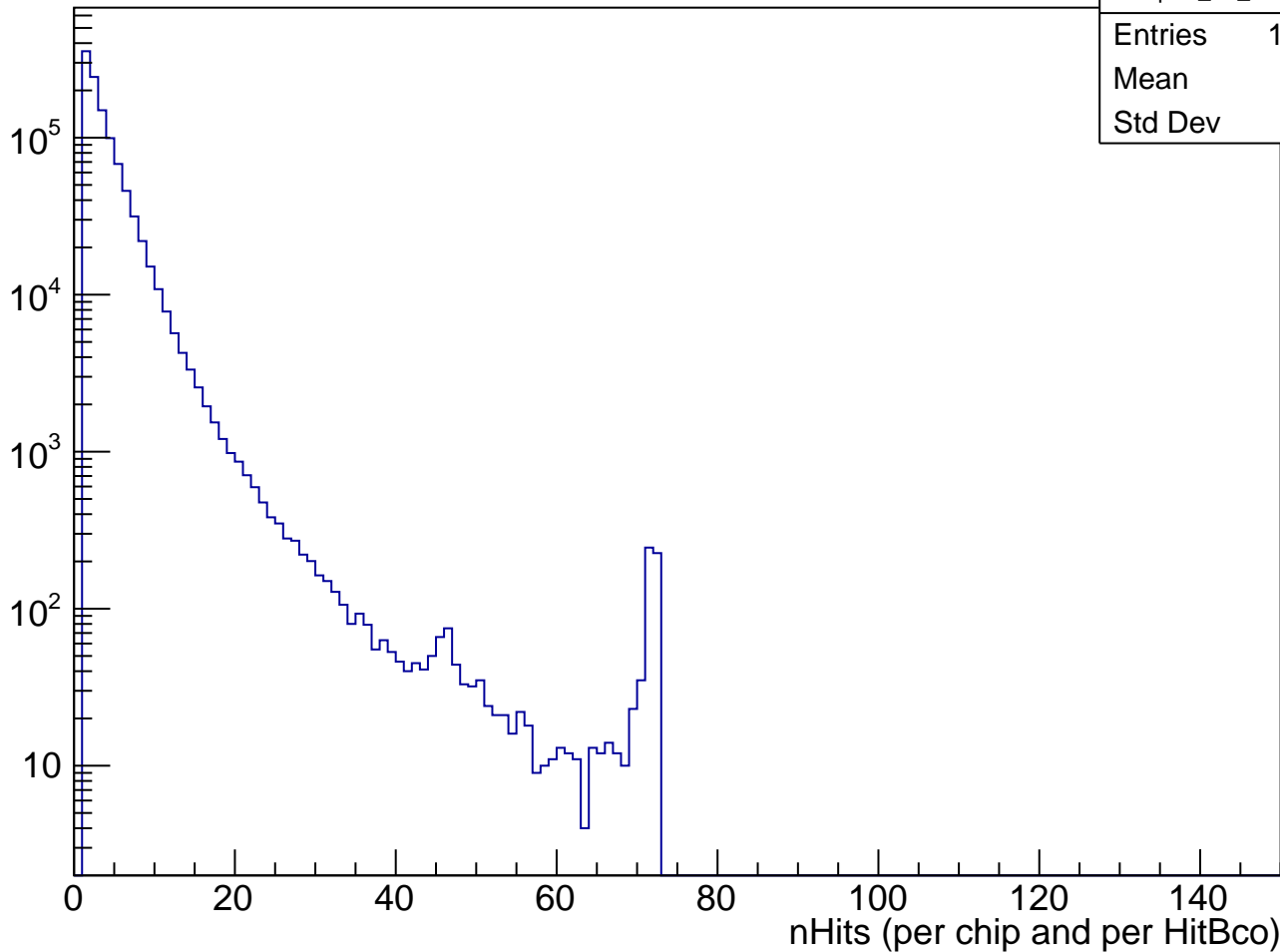


nChipHit_F1_Fch5_Chip18	
Entries	1071173
Mean	3.296
Std Dev	3.724

nHits (per chip and per HitBco)

nChipHit_F1_Fch5_Chip19

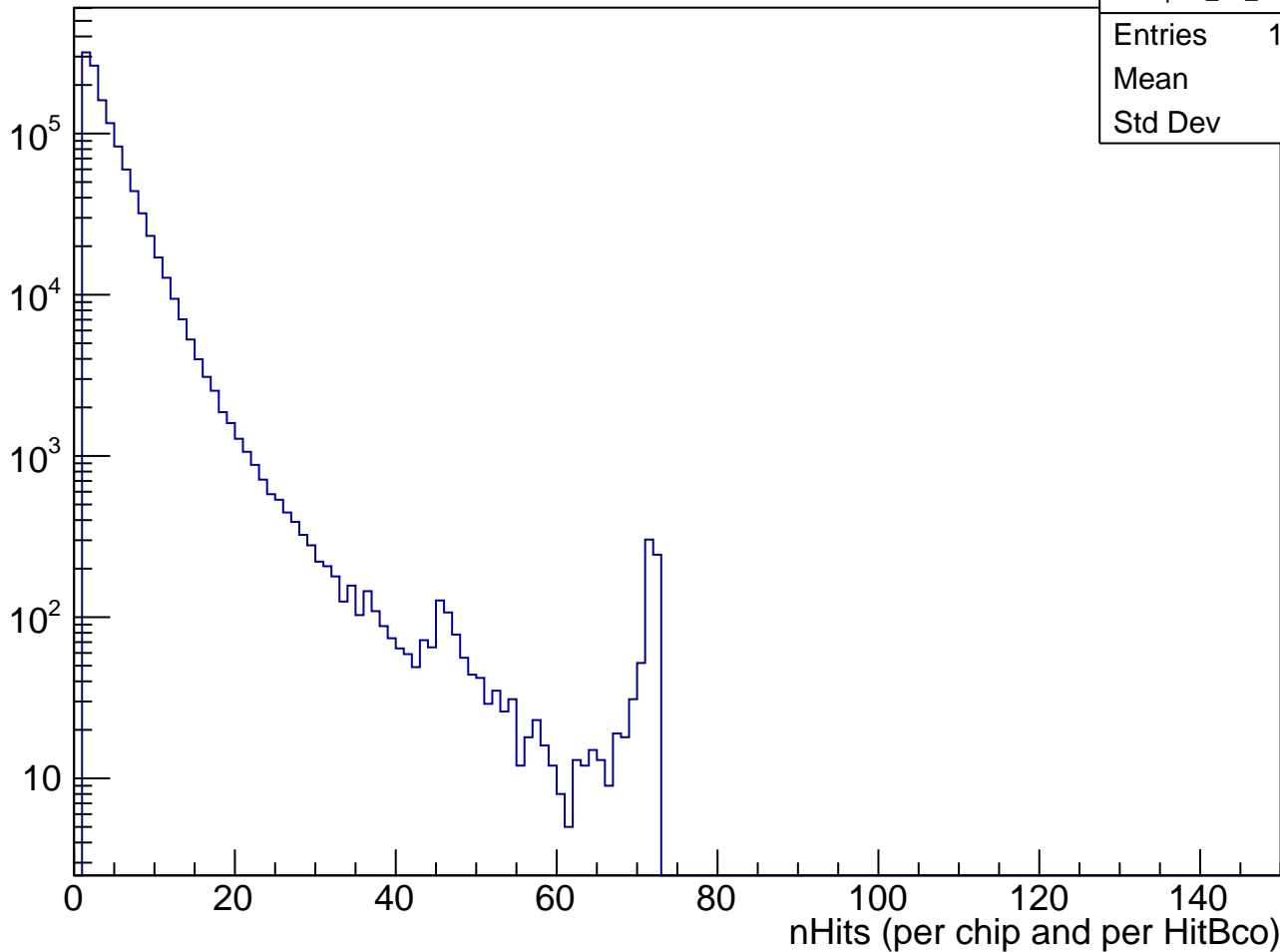
Entries



nChipHit_F1_Fch5_Chip19	
Entries	1076878
Mean	3.291
Std Dev	3.687

nChipHit_F1_Fch5_Chip2

Entries



nChipHit_F1_Fch5_Chip2

Entries 1175445

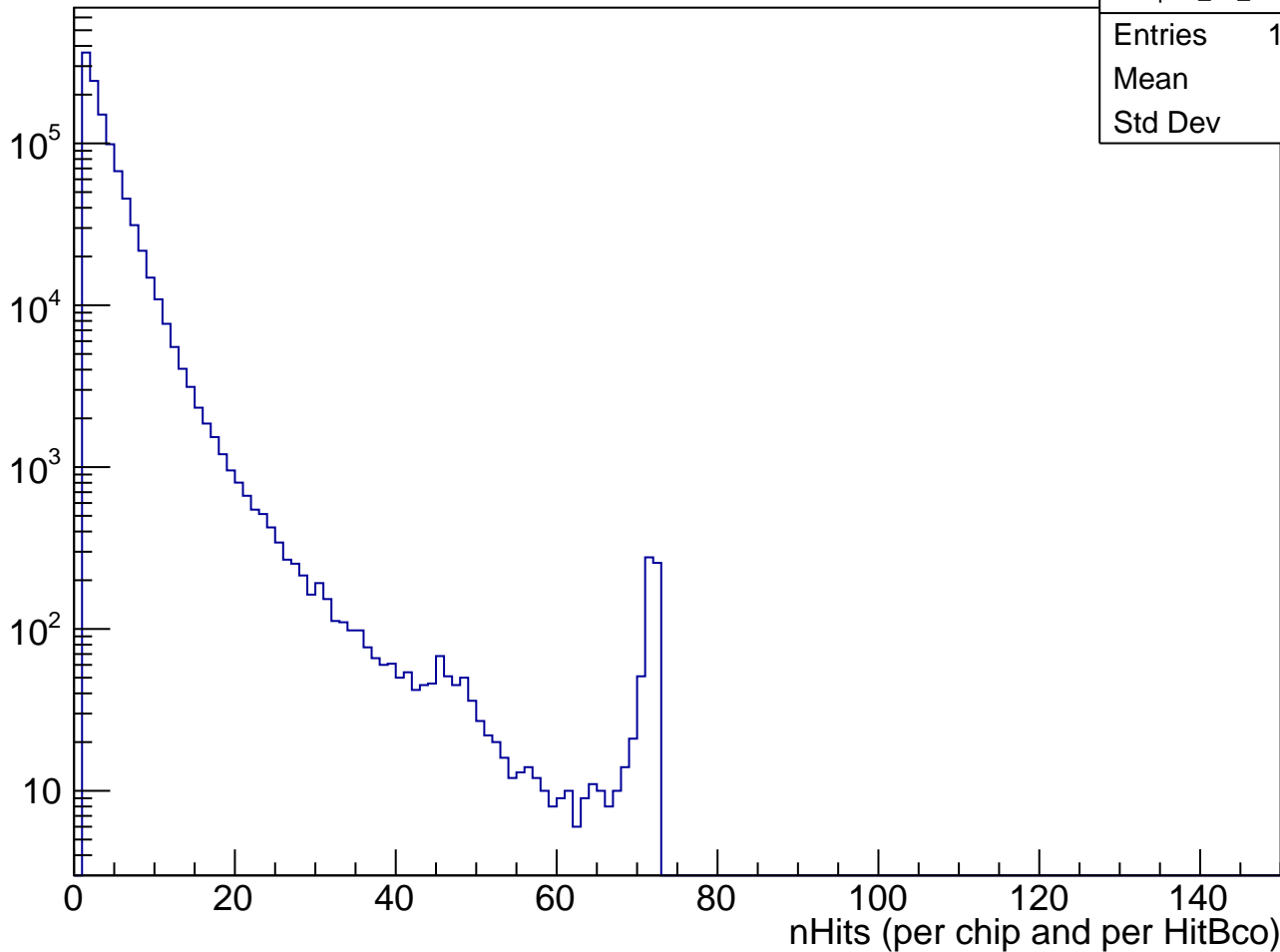
Mean 3.777

Std Dev 4.081

nHits (per chip and per HitBco)

nChipHit_F1_Fch5_Chip20

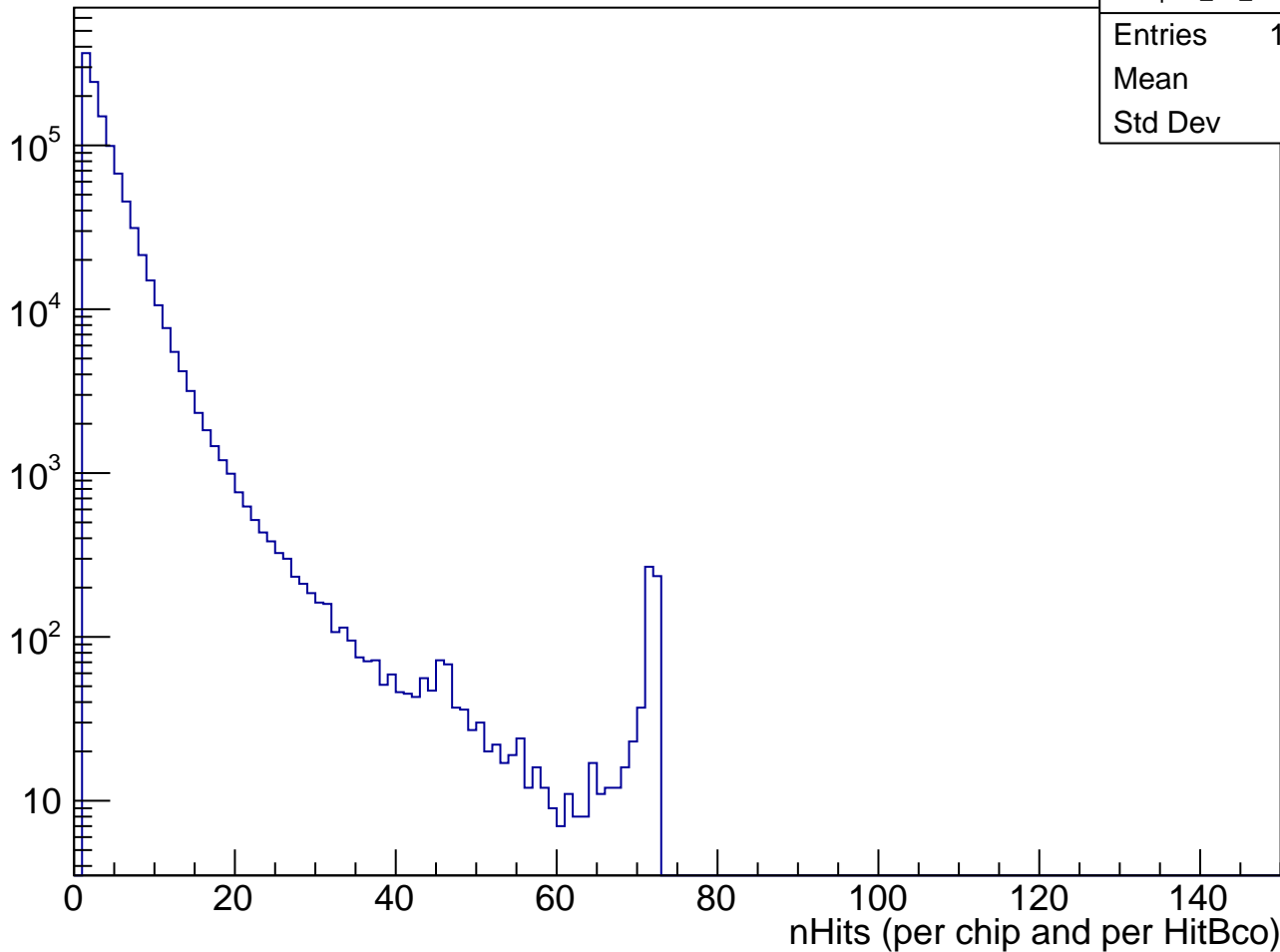
Entries



nChipHit_F1_Fch5_Chip20	
Entries	1082934
Mean	3.259
Std Dev	3.694

nChipHit_F1_Fch5_Chip21

Entries

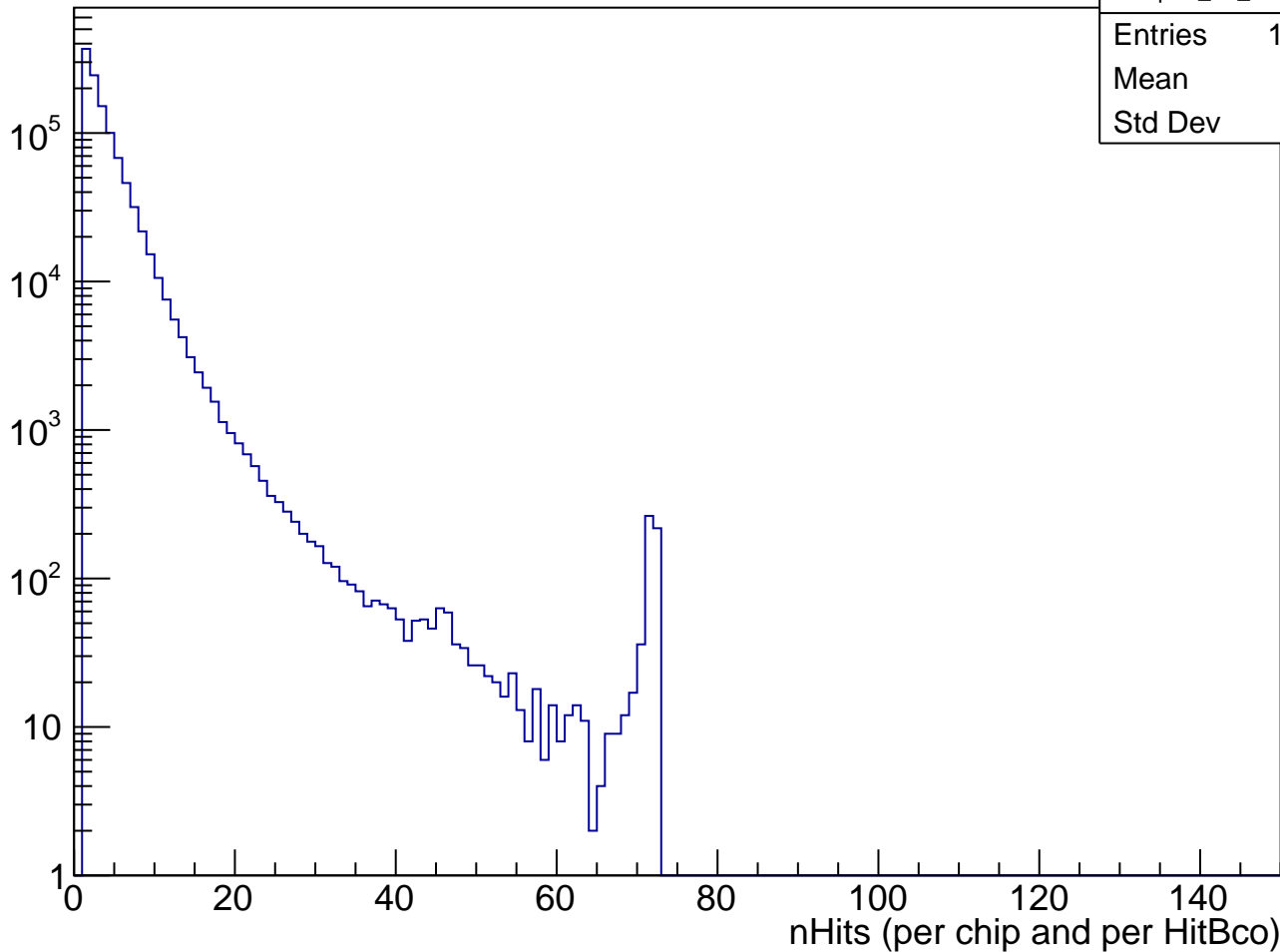


nChipHit_F1_Fch5_Chip21	
Entries	1084630
Mean	3.247
Std Dev	3.664

nHits (per chip and per HitBco)

nChipHit_F1_Fch5_Chip22

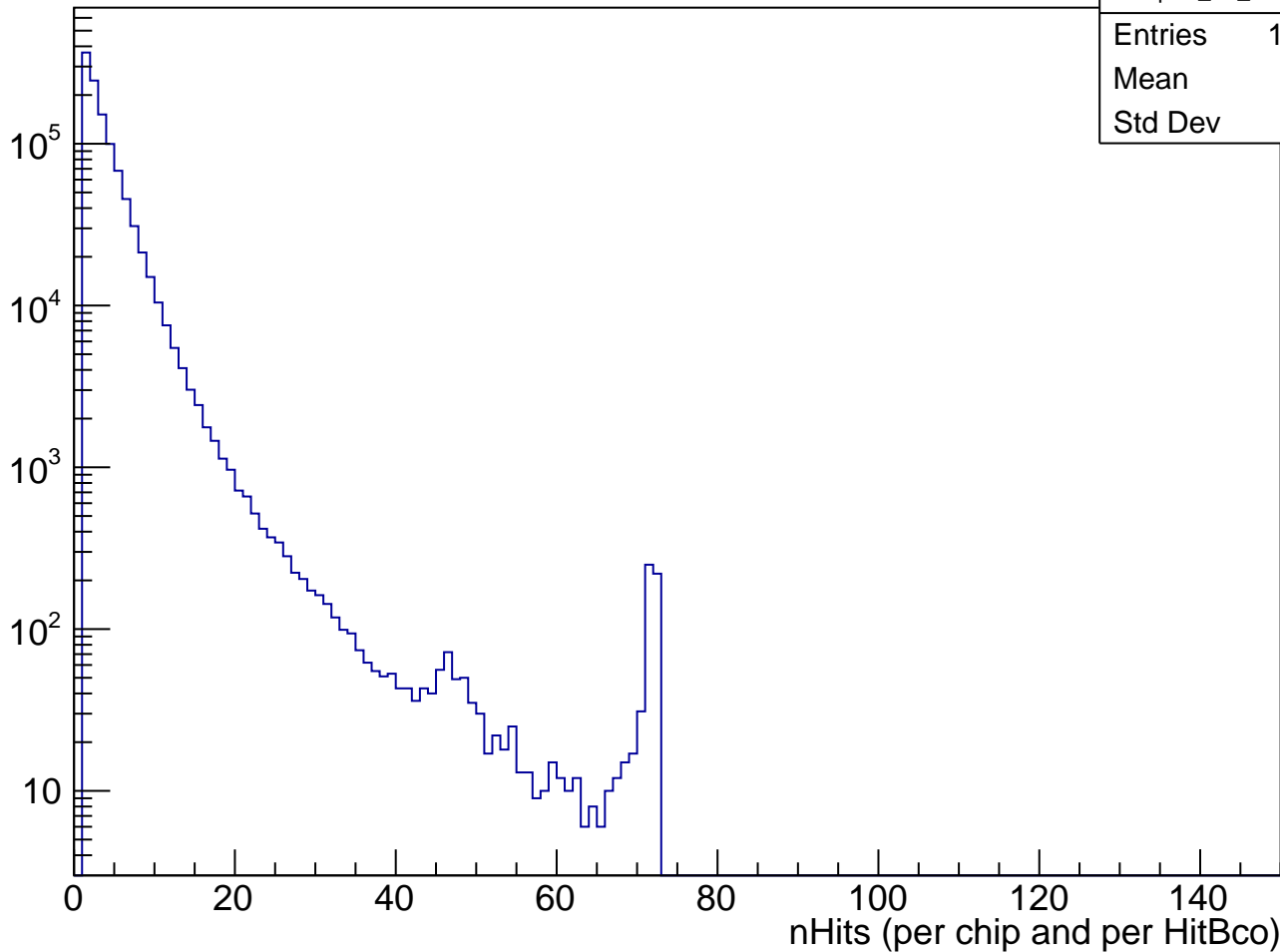
Entries



nChipHit_F1_Fch5_Chip22	
Entries	1093837
Mean	3.245
Std Dev	3.624

nChipHit_F1_Fch5_Chip23

Entries

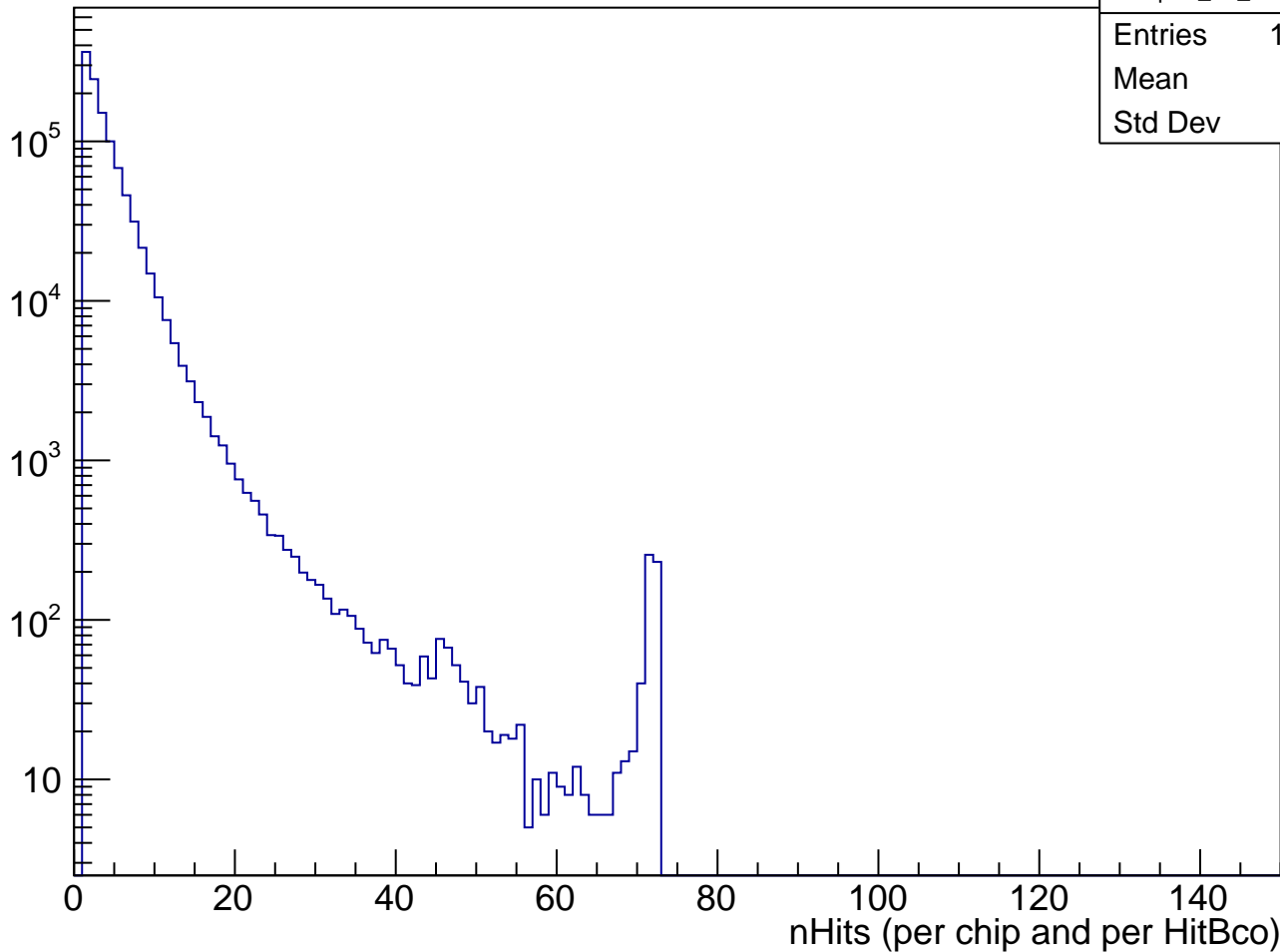


nChipHit_F1_Fch5_Chip23	
Entries	1088572
Mean	3.232
Std Dev	3.604

nHits (per chip and per HitBco)

nChipHit_F1_Fch5_Chip24

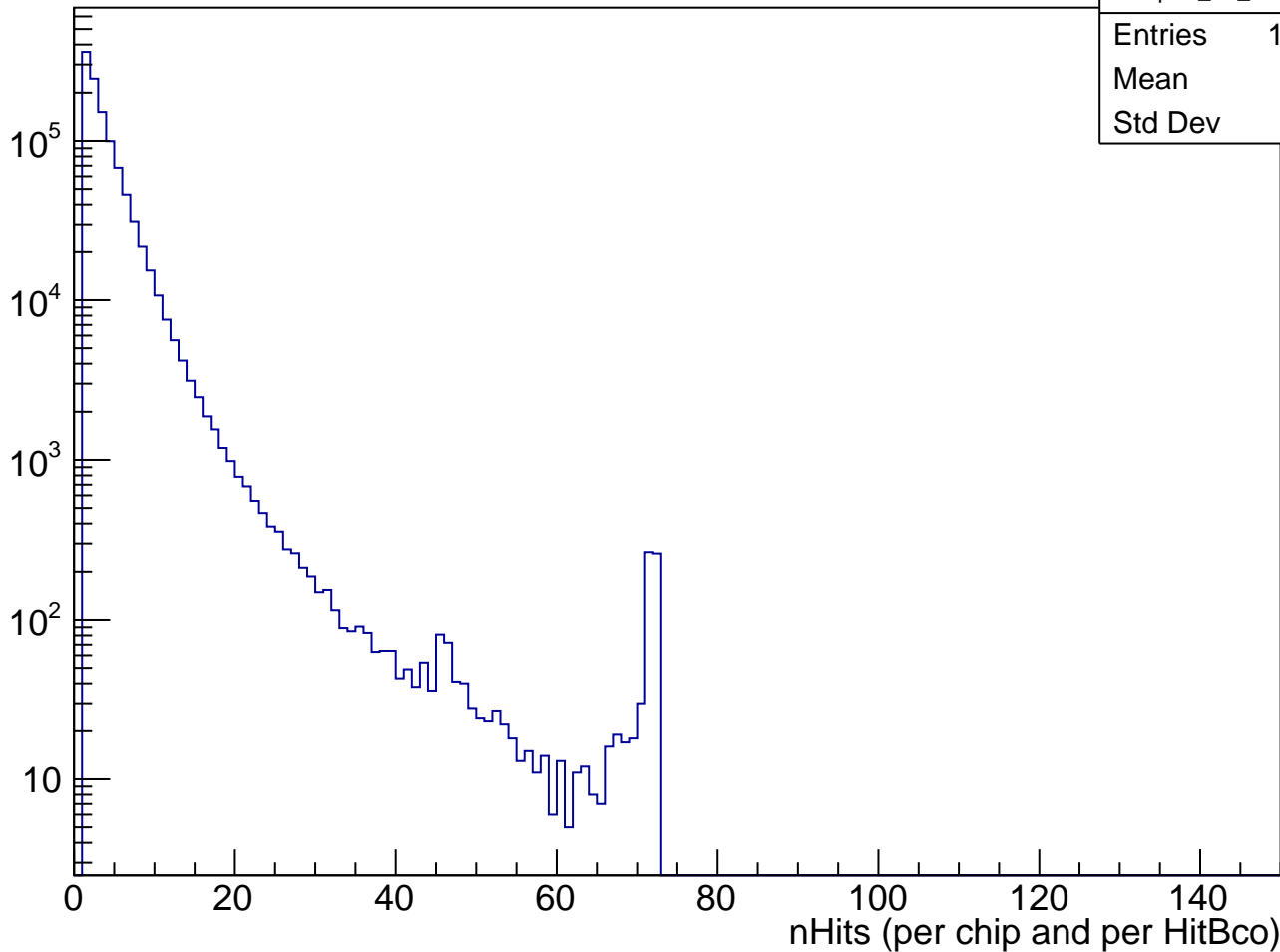
Entries



nChipHit_F1_Fch5_Chip24	
Entries	1086713
Mean	3.246
Std Dev	3.635

nChipHit_F1_Fch5_Chip25

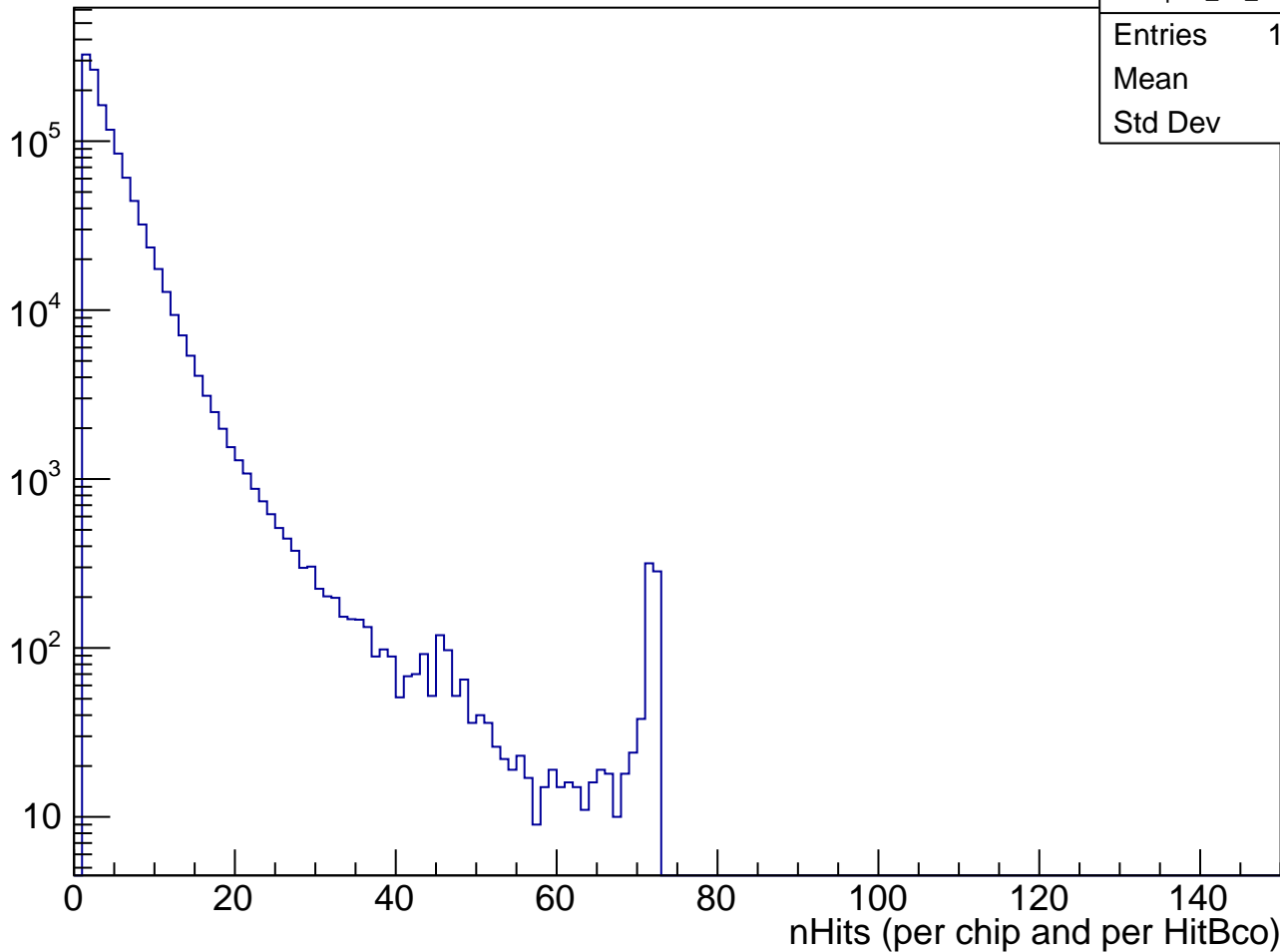
Entries



nChipHit_F1_Fch5_Chip25	
Entries	1083385
Mean	3.272
Std Dev	3.687

nChipHit_F1_Fch5_Chip3

Entries



nChipHit_F1_Fch5_Chip3

Entries 1191029

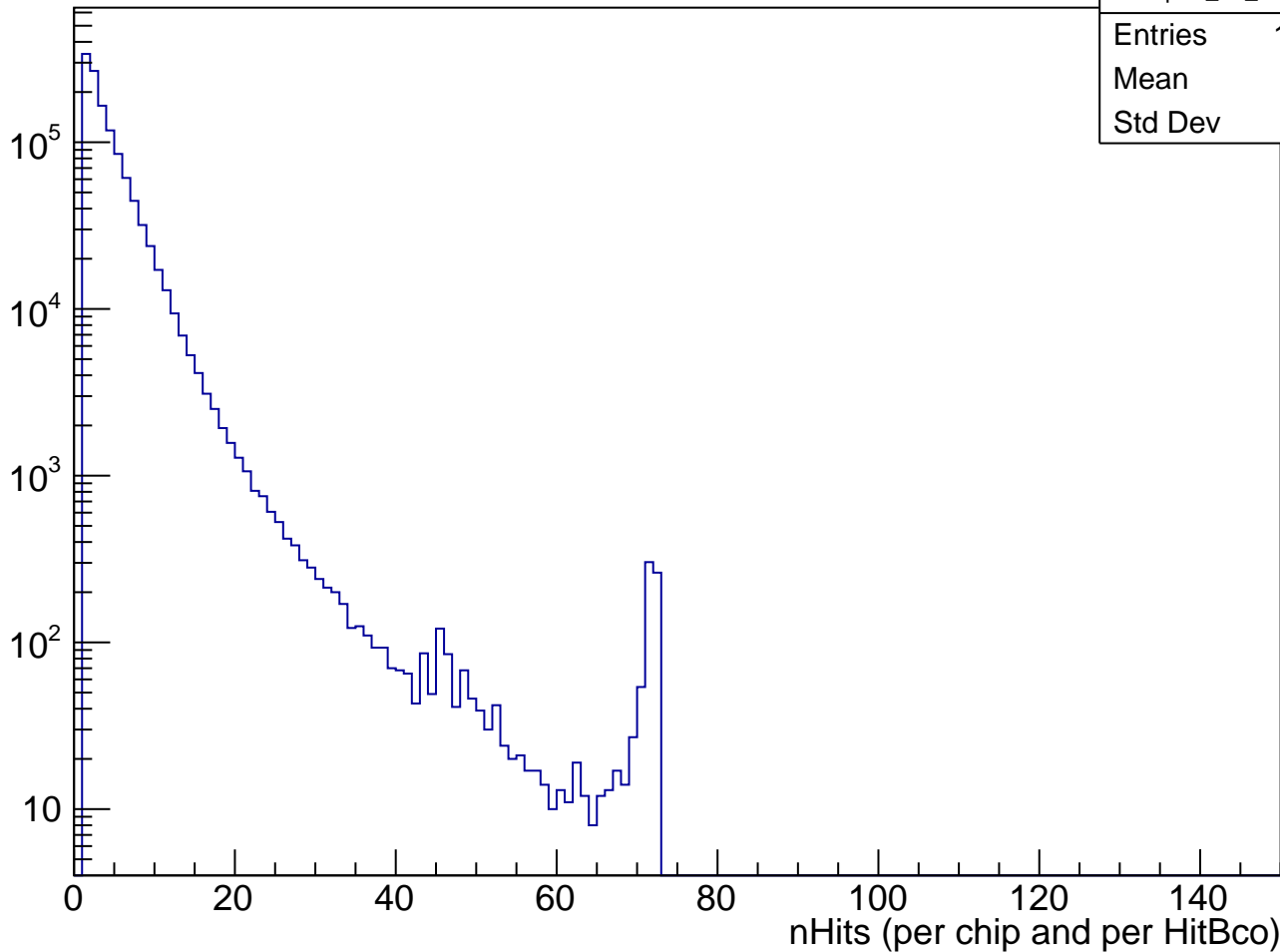
Mean 3.772

Std Dev 4.092

nHits (per chip and per HitBco)

nChipHit_F1_Fch5_Chip4

Entries



nChipHit_F1_Fch5_Chip4

Entries 1210641

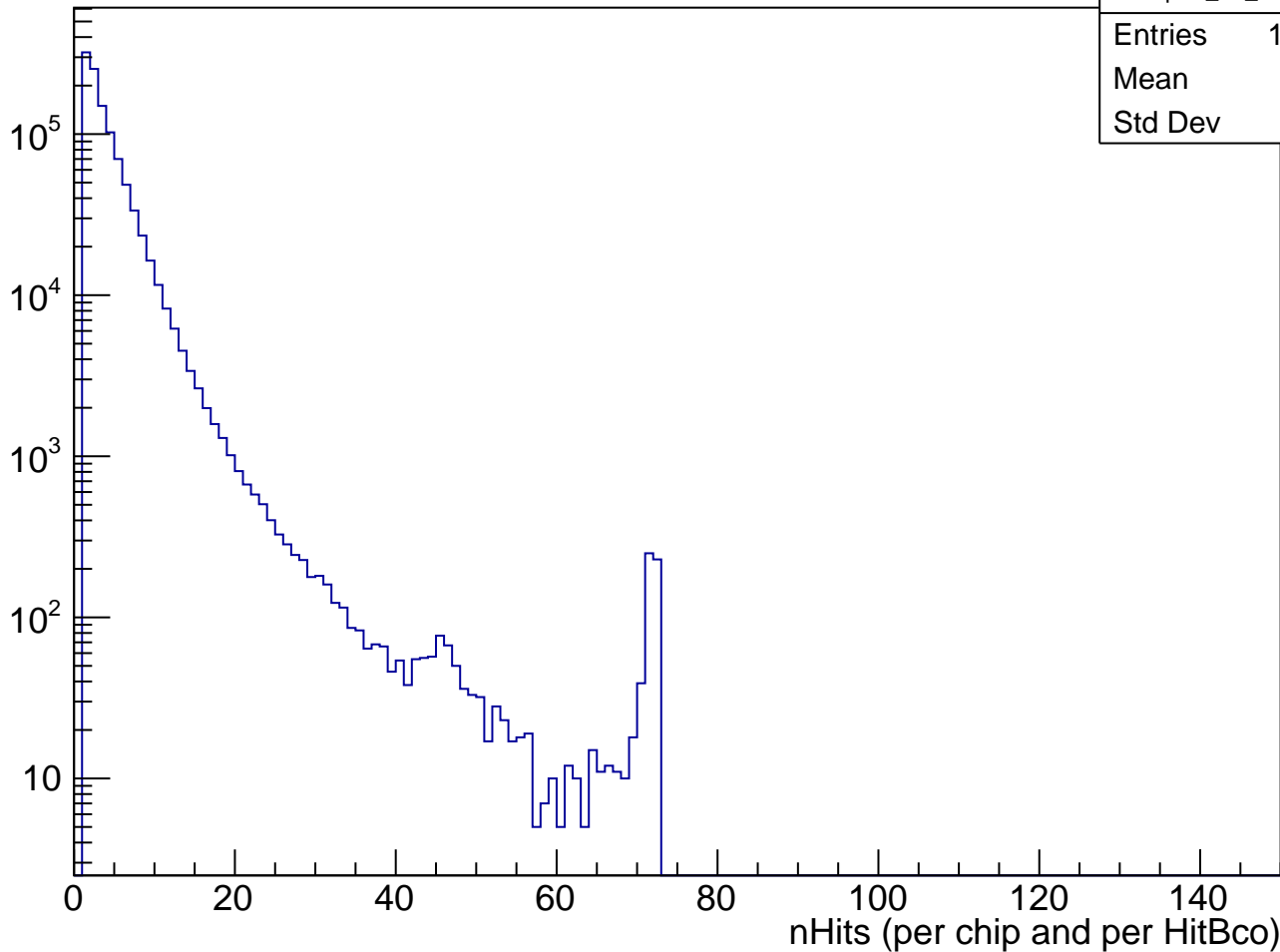
Mean 3.731

Std Dev 4.04

nHits (per chip and per HitBco)

nChipHit_F1_Fch5_Chip5

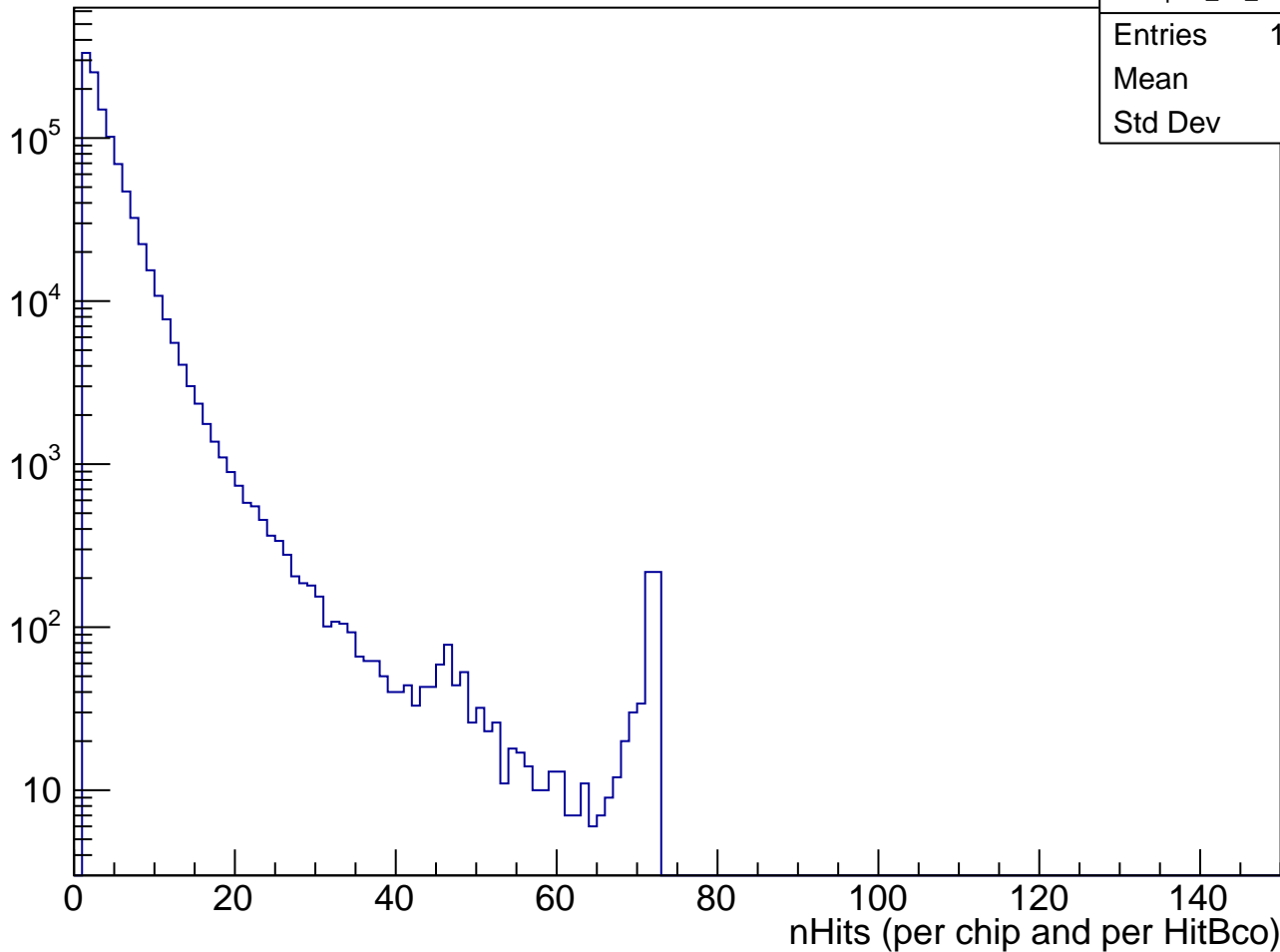
Entries



nChipHit_F1_Fch5_Chip5	
Entries	1068608
Mean	3.402
Std Dev	3.721

nChipHit_F1_Fch5_Chip6

Entries



nChipHit_F1_Fch5_Chip6

Entries 1068353

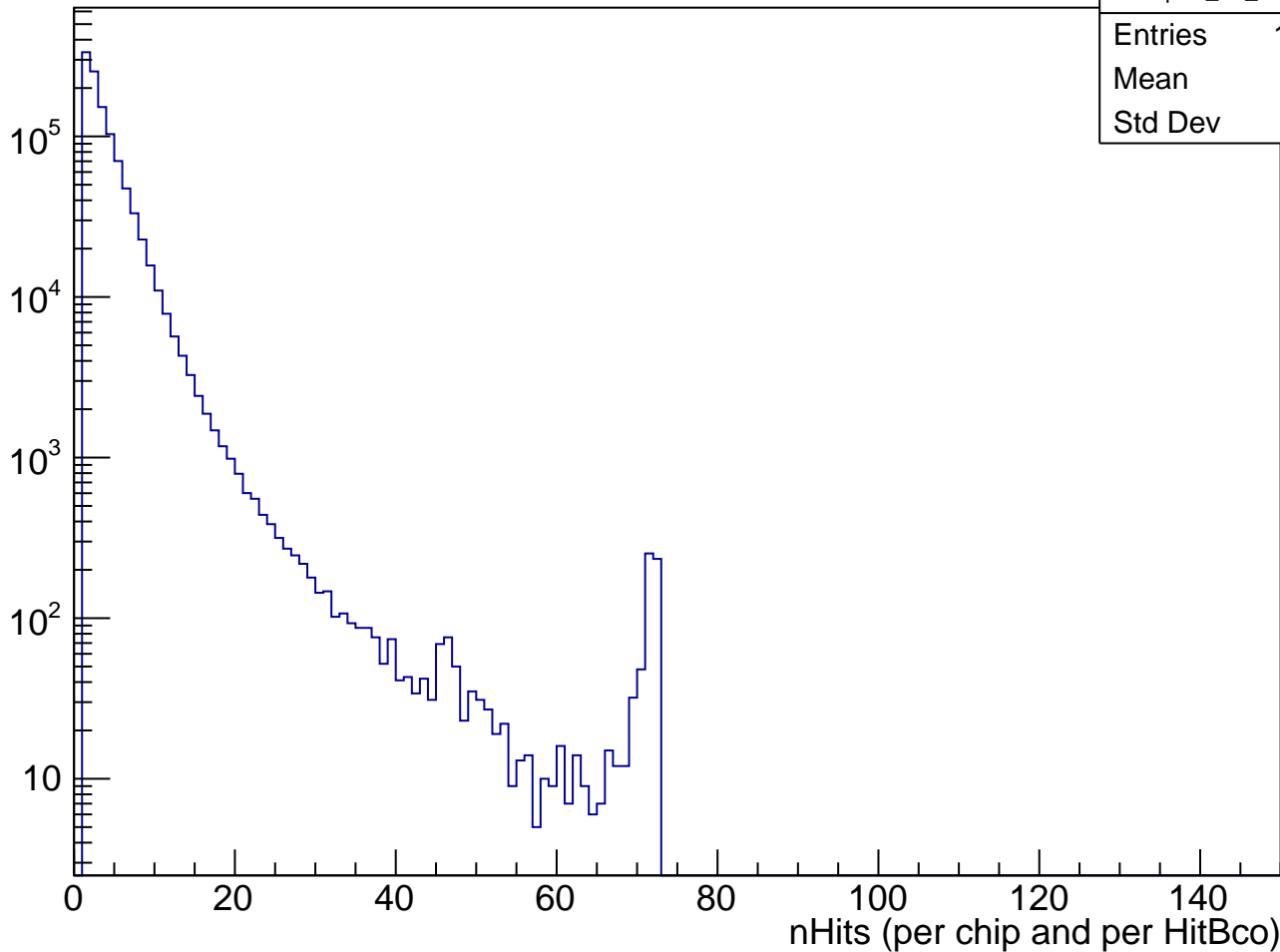
Mean 3.309

Std Dev 3.602

nHits (per chip and per HitBco)

nChipHit_F1_Fch5_Chip7

Entries

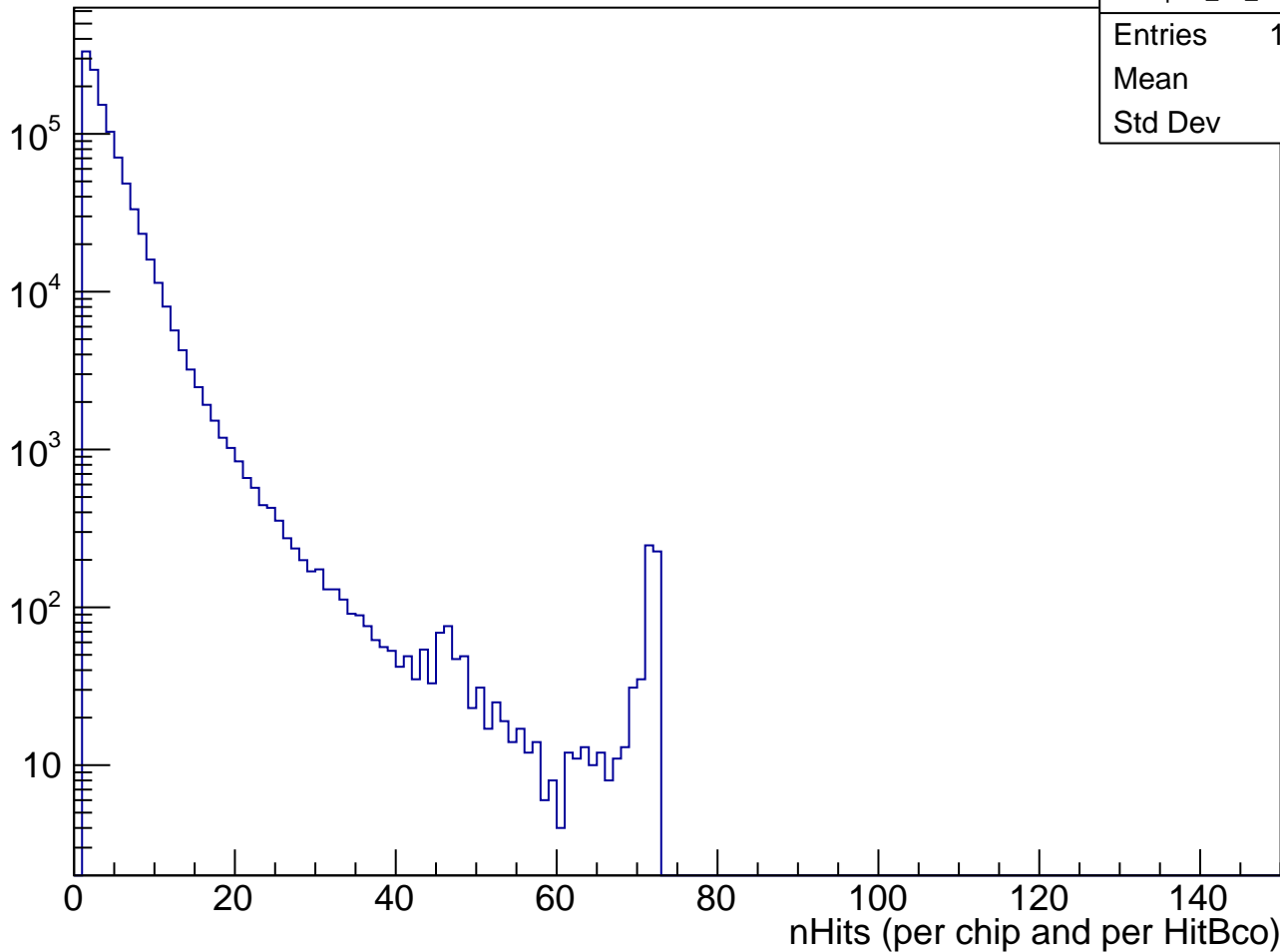


nChipHit_F1_Fch5_Chip7	
Entries	1079261
Mean	3.337
Std Dev	3.666

nHits (per chip and per HitBco)

nChipHit_F1_Fch5_Chip8

Entries



nChipHit_F1_Fch5_Chip8

Entries 1082713

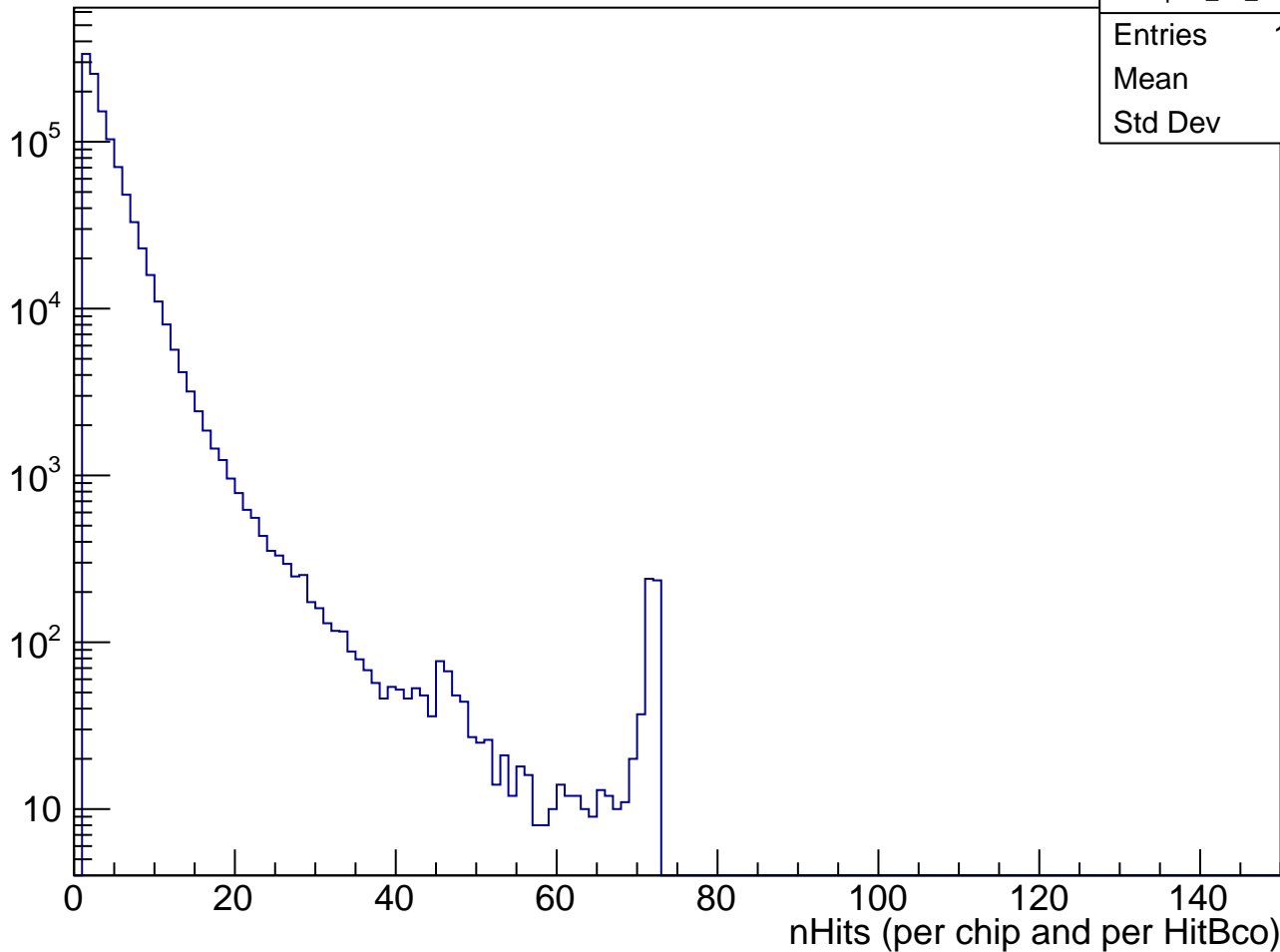
Mean 3.353

Std Dev 3.666

nHits (per chip and per HitBco)

nChipHit_F1_Fch5_Chip9

Entries



nChipHit_F1_Fch5_Chip9

Entries 1084251

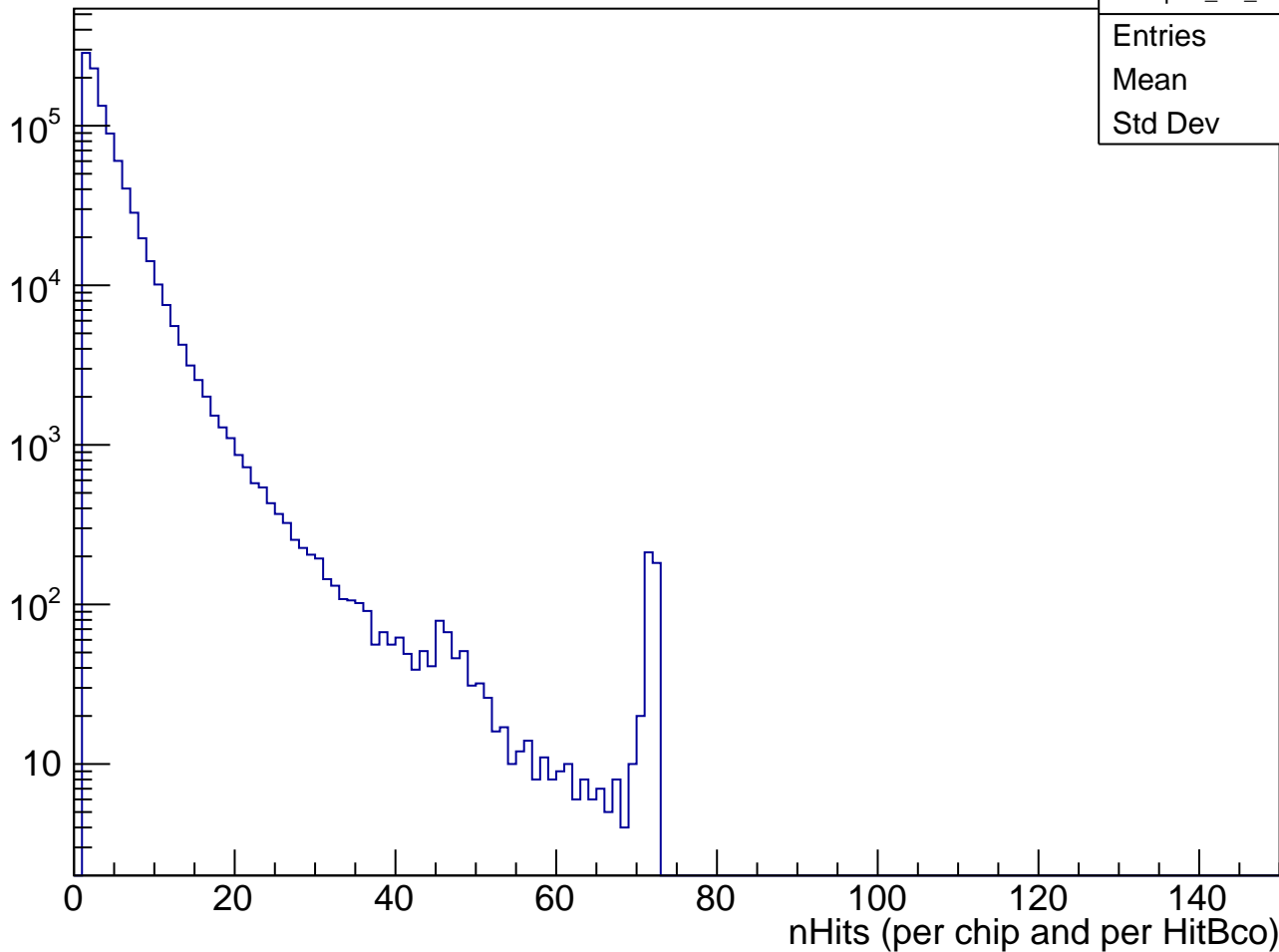
Mean 3.333

Std Dev 3.648

nHits (per chip and per HitBco)

nChipHit_F1_Fch6_Chip0

Entries



nChipHit_F1_Fch6_Chip0

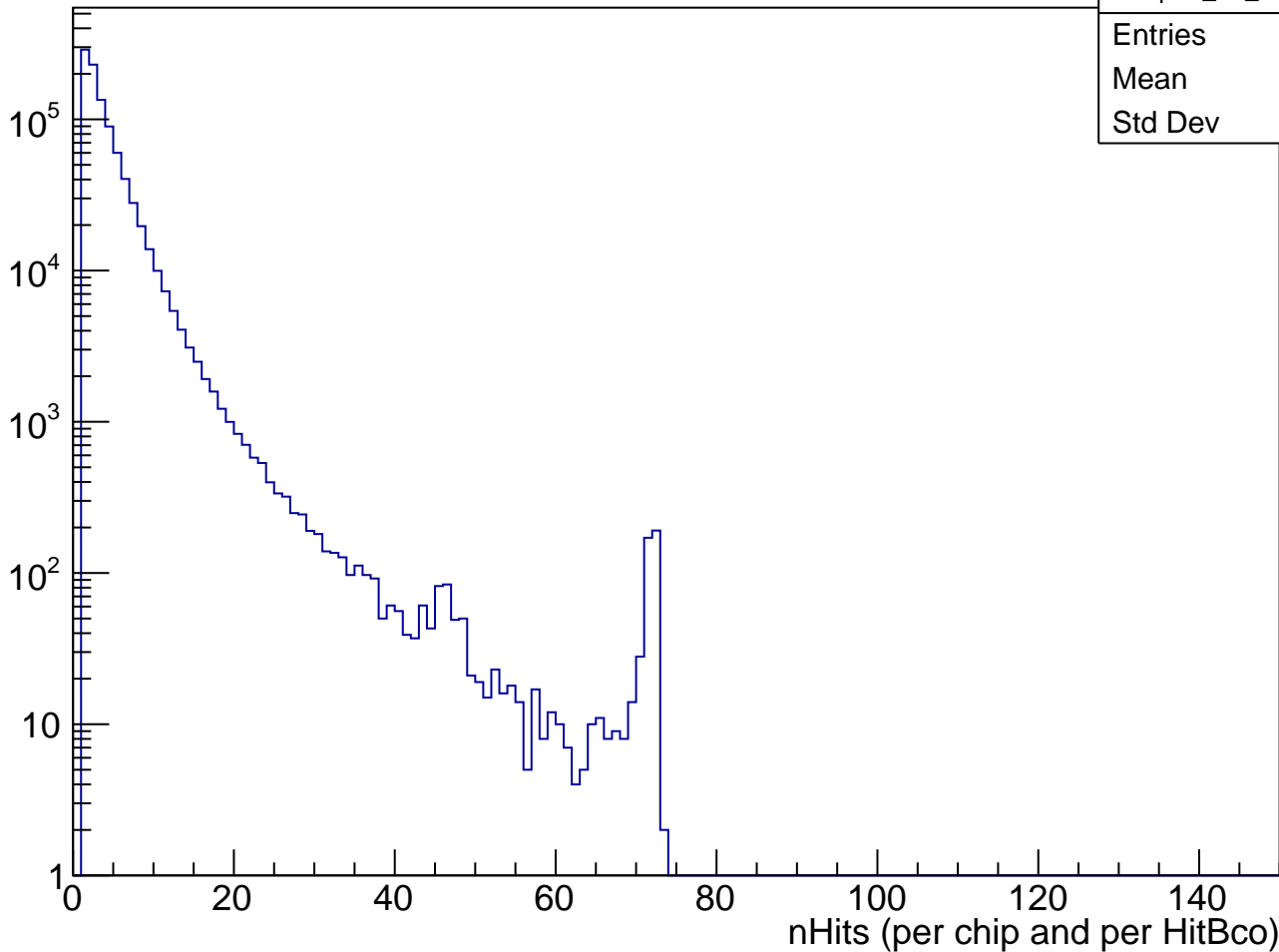
Entries 946389

Mean 3.408

Std Dev 3.781

nChipHit_F1_Fch6_Chip1

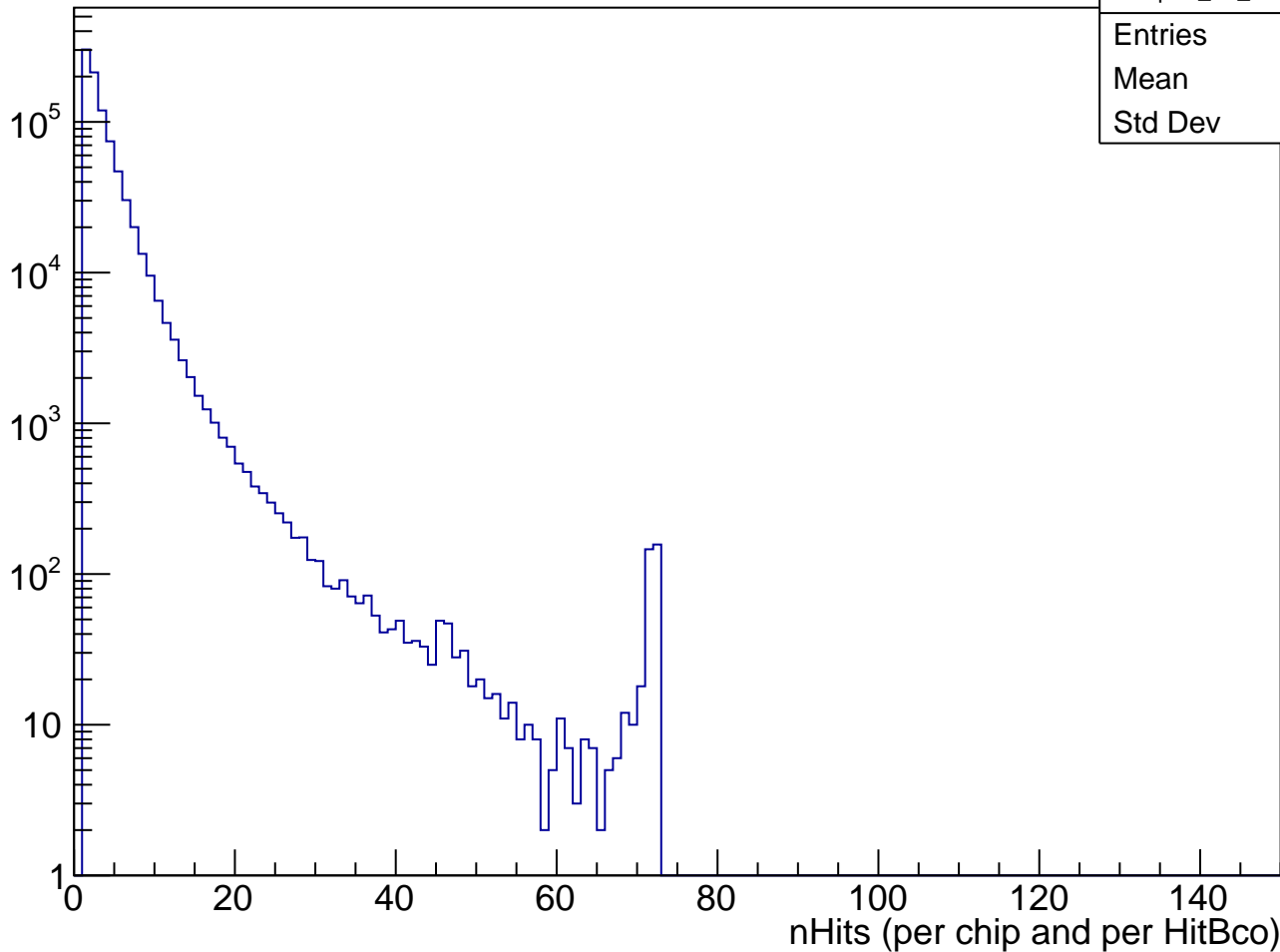
Entries



nChipHit_F1_Fch6_Chip1	
Entries	949958
Mean	3.381
Std Dev	3.753

nChipHit_F1_Fch6_Chip10

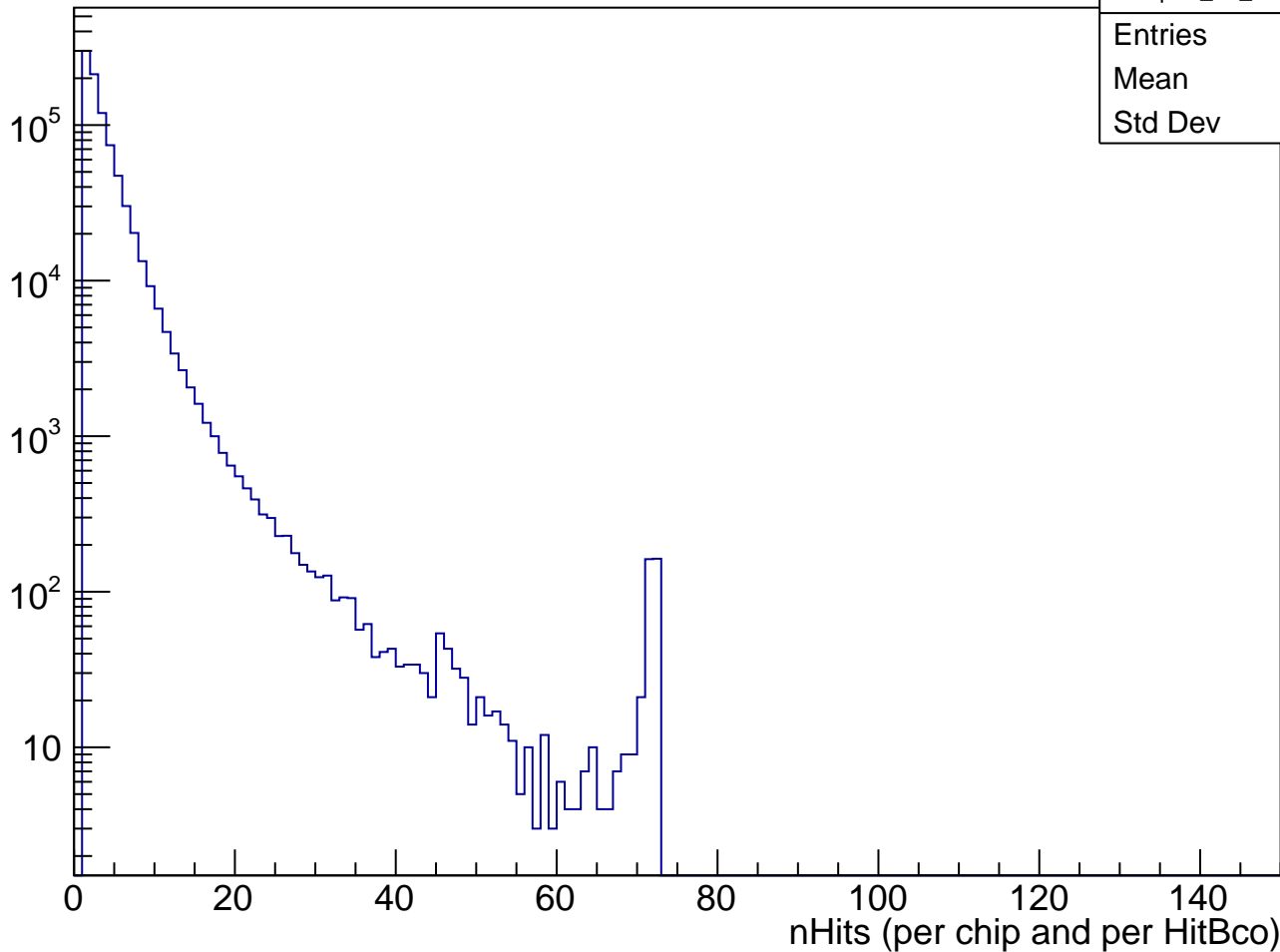
Entries



nChipHit_F1_Fch6_Chip10	
Entries	858487
Mean	3.011
Std Dev	3.401

nChipHit_F1_Fch6_Chip11

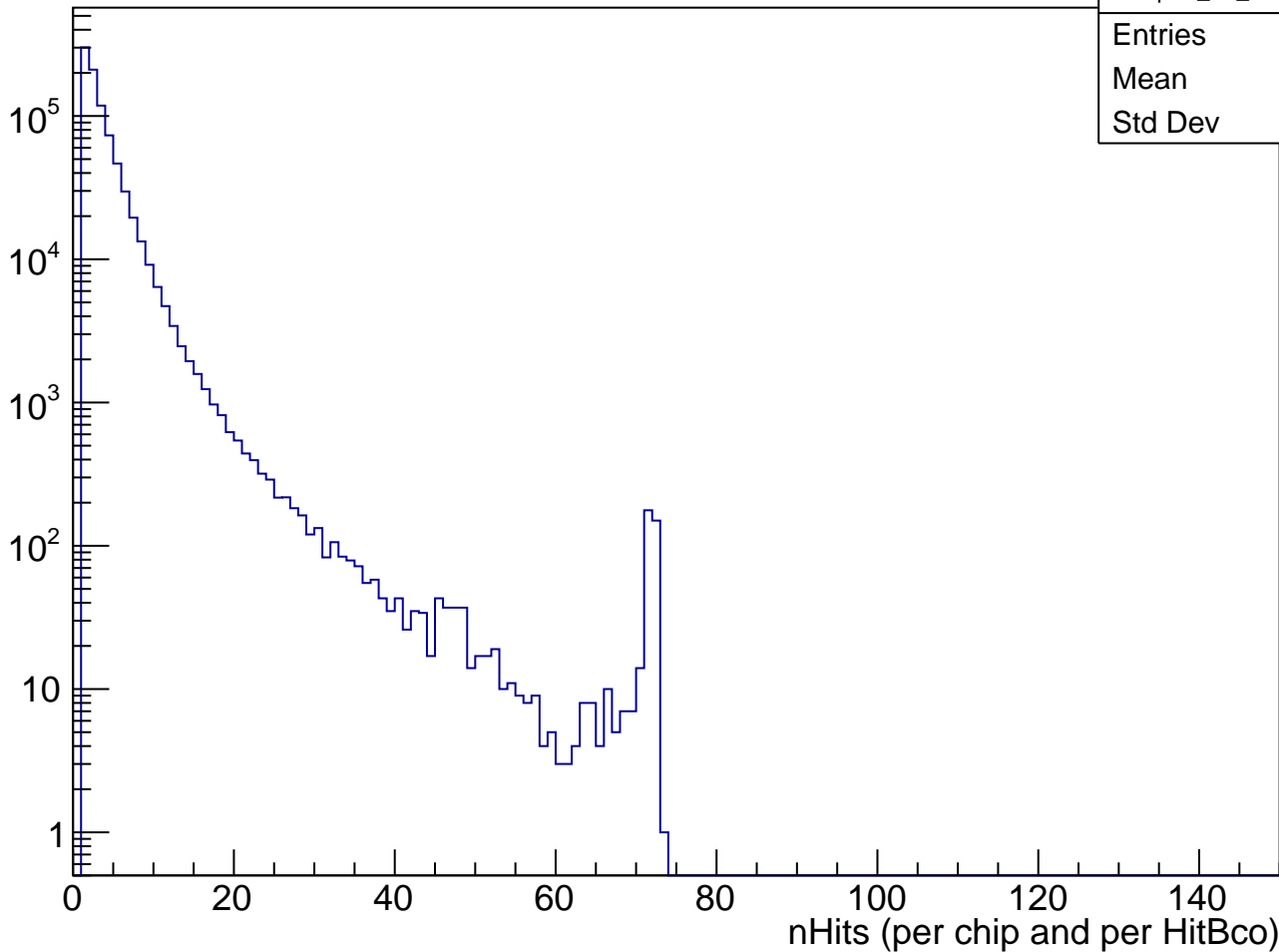
Entries



nChipHit_F1_Fch6_Chip11	
Entries	855592
Mean	3.016
Std Dev	3.412

nChipHit_F1_Fch6_Chip12

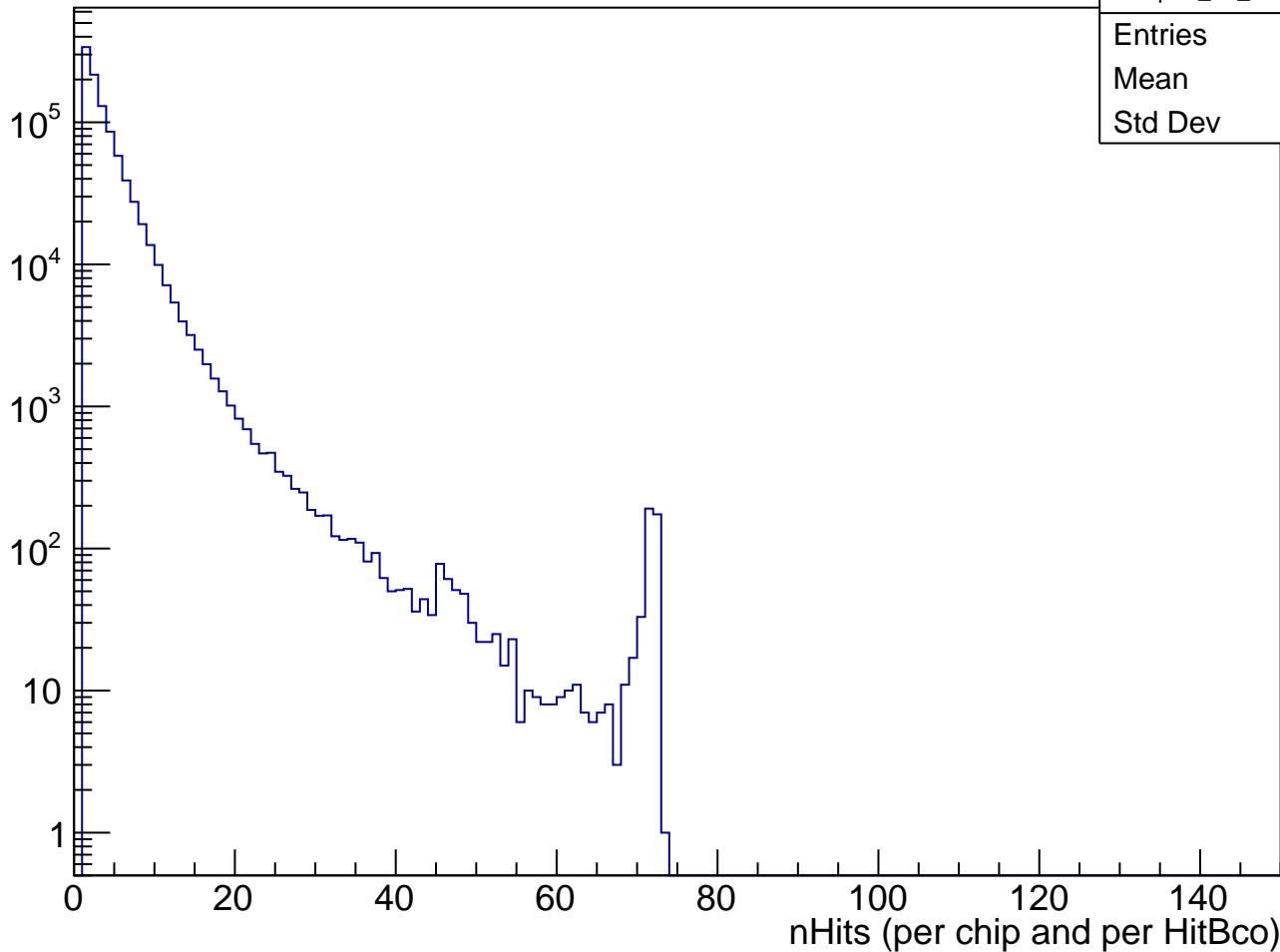
Entries



nChipHit_F1_Fch6_Chip12	
Entries	850186
Mean	3
Std Dev	3.4

nChipHit_F1_Fch6_Chip13

Entries

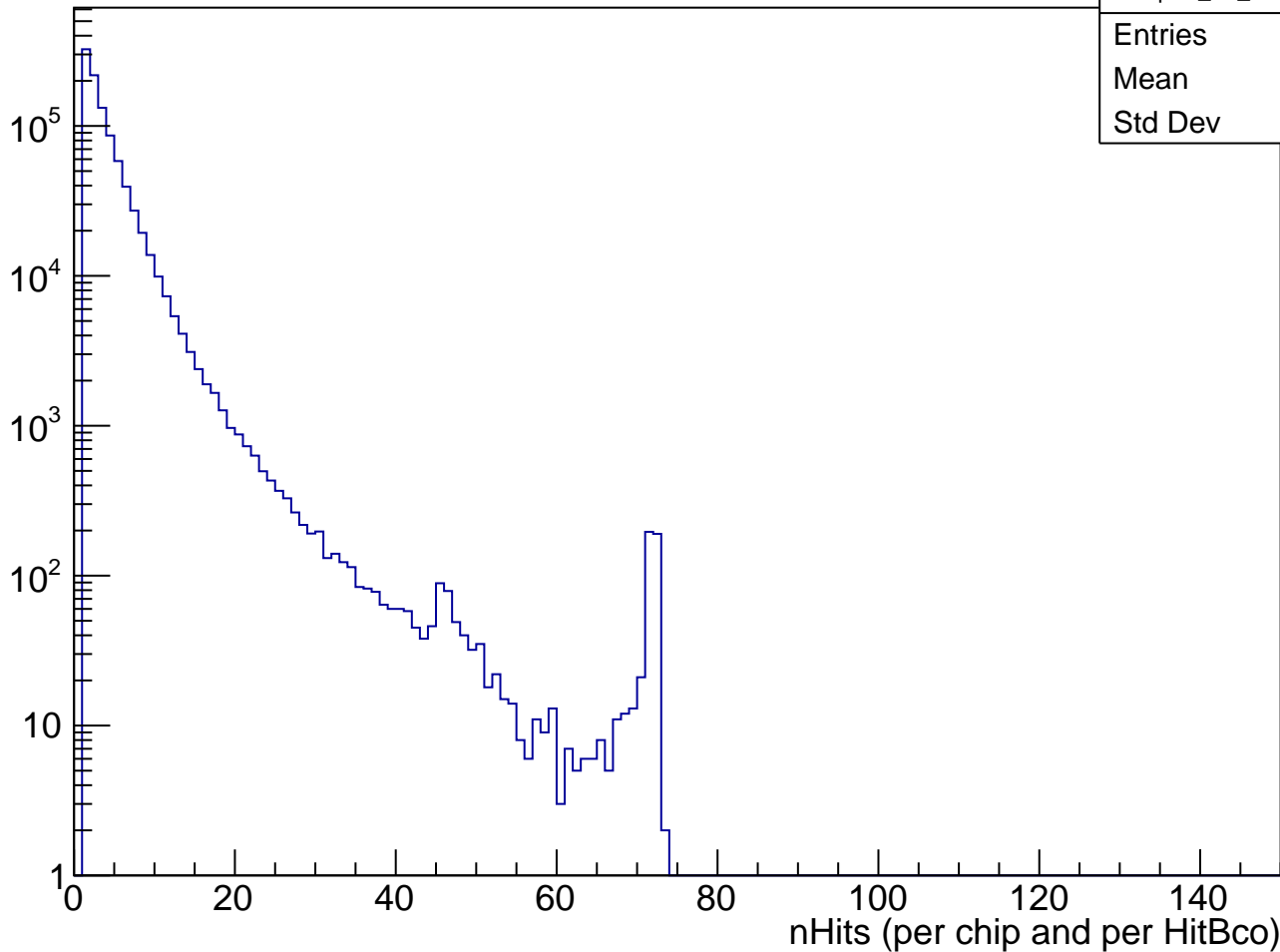


nChipHit_F1_Fch6_Chip13	
Entries	972650
Mean	3.266
Std Dev	3.734

nHits (per chip and per HitBco)

nChipHit_F1_Fch6_Chip14

Entries

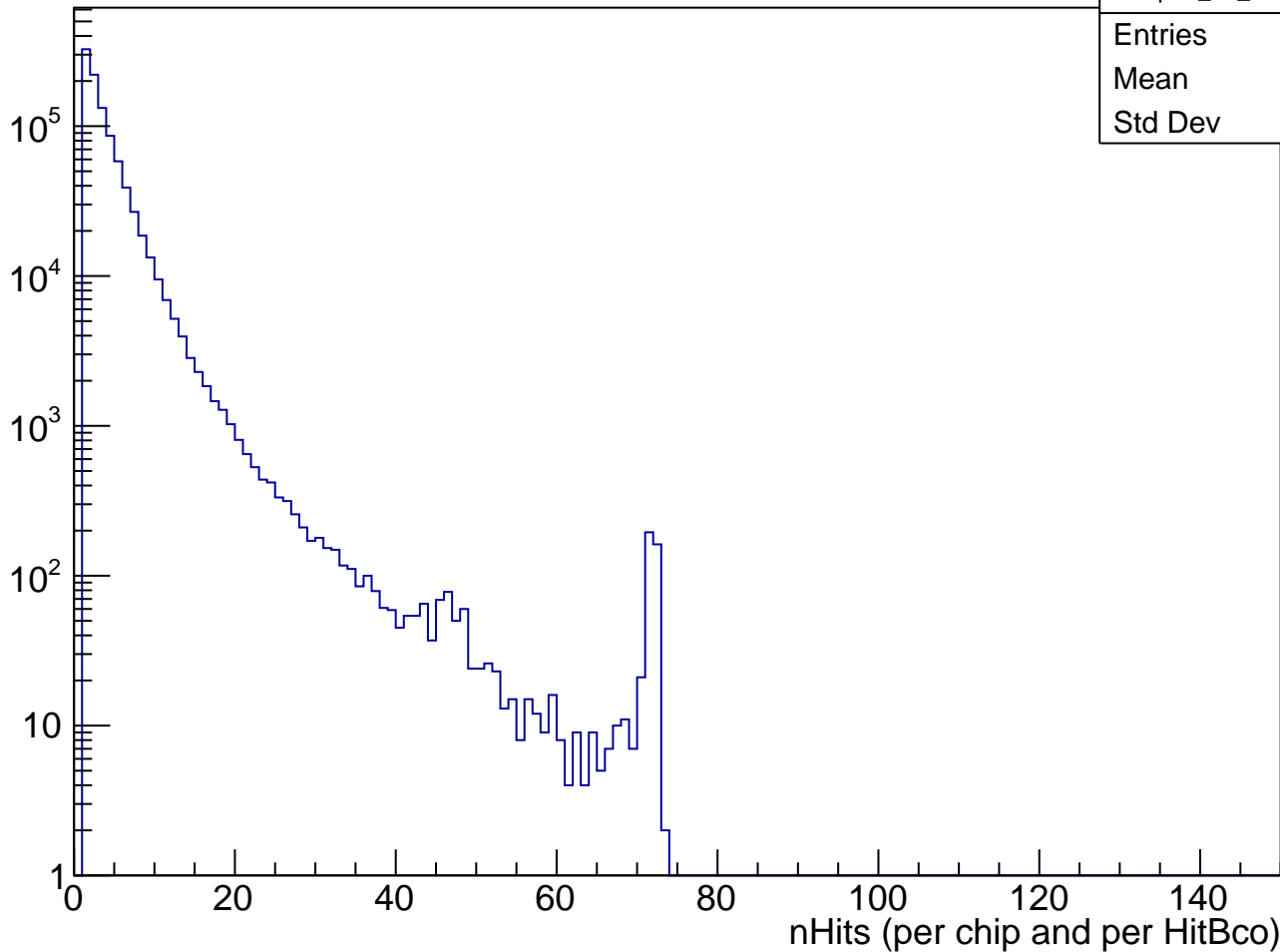


nChipHit_F1_Fch6_Chip14	
Entries	964049
Mean	3.302
Std Dev	3.758

nHits (per chip and per HitBco)

nChipHit_F1_Fch6_Chip15

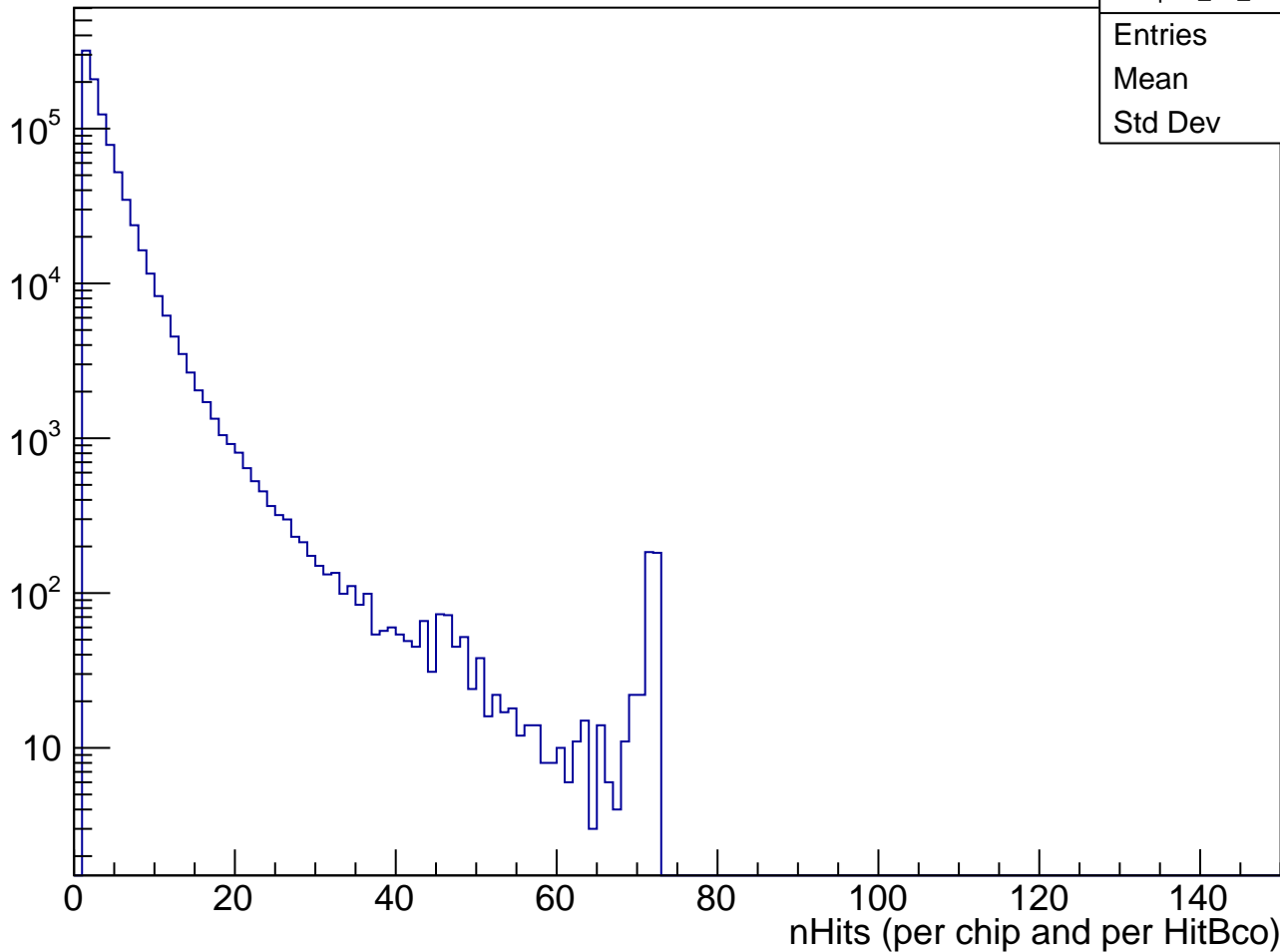
Entries



nChipHit_F1_Fch6_Chip15	
Entries	963362
Mean	3.261
Std Dev	3.7

nChipHit_F1_Fch6_Chip16

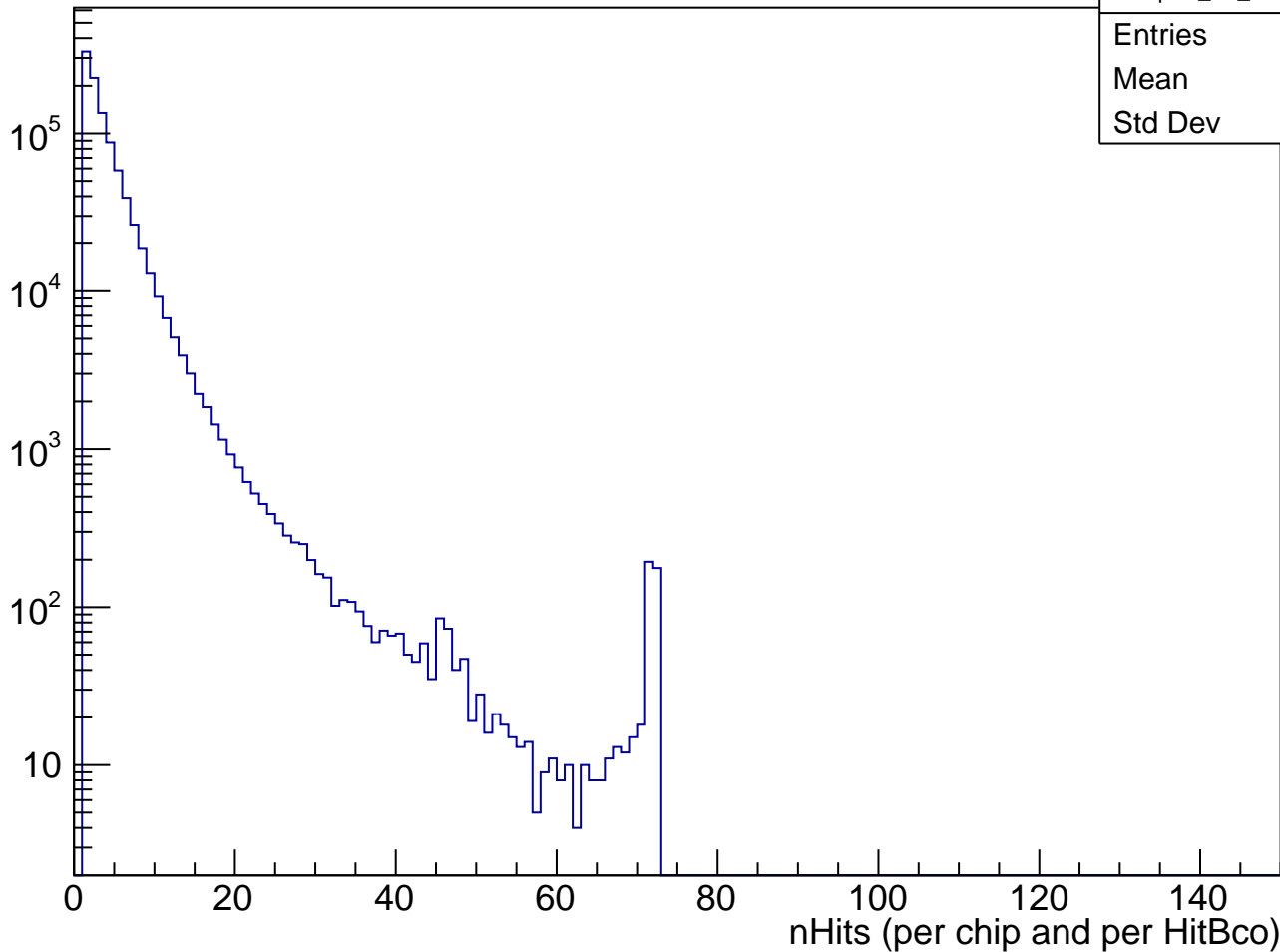
Entries



nChipHit_F1_Fch6_Chip16	
Entries	906288
Mean	3.187
Std Dev	3.717

nChipHit_F1_Fch6_Chip17

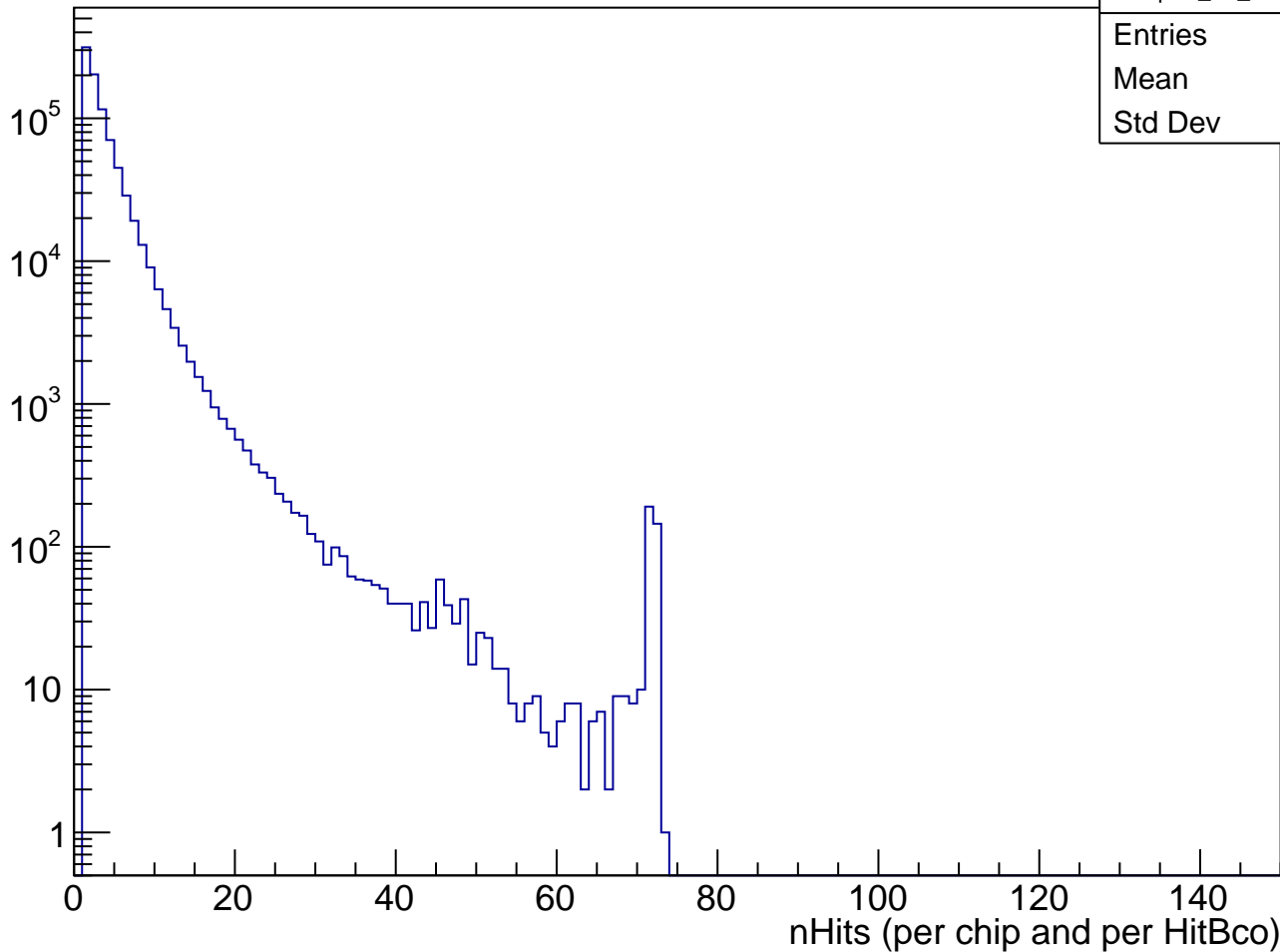
Entries



nChipHit_F1_Fch6_Chip17	
Entries	973336
Mean	3.235
Std Dev	3.67

nChipHit_F1_Fch6_Chip18

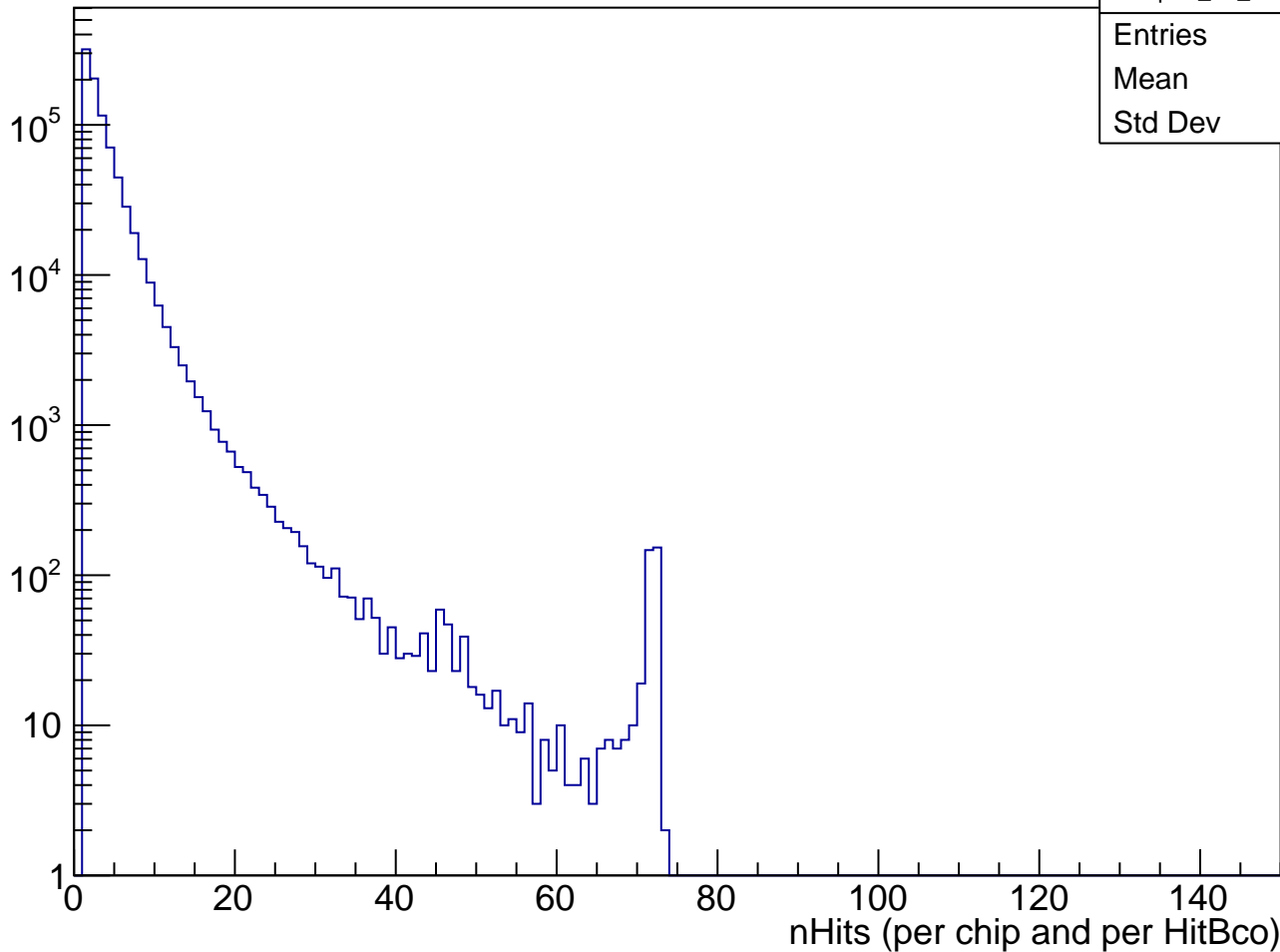
Entries



nChipHit_F1_Fch6_Chip18	
Entries	846776
Mean	2.965
Std Dev	3.424

nChipHit_F1_Fch6_Chip19

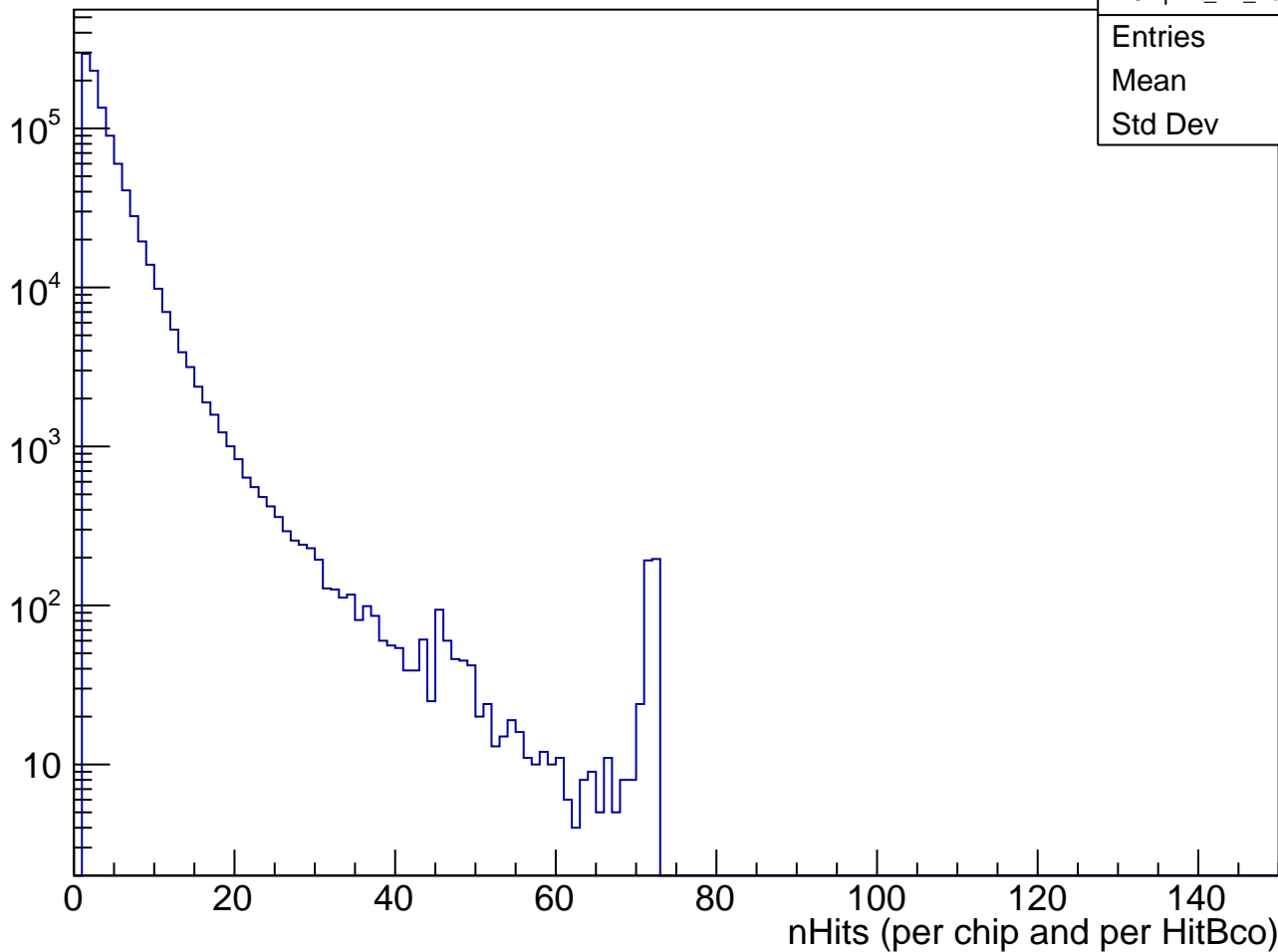
Entries



nChipHit_F1_Fch6_Chip19	
Entries	850387
Mean	2.941
Std Dev	3.384

nChipHit_F1_Fch6_Chip2

Entries



nChipHit_F1_Fch6_Chip2

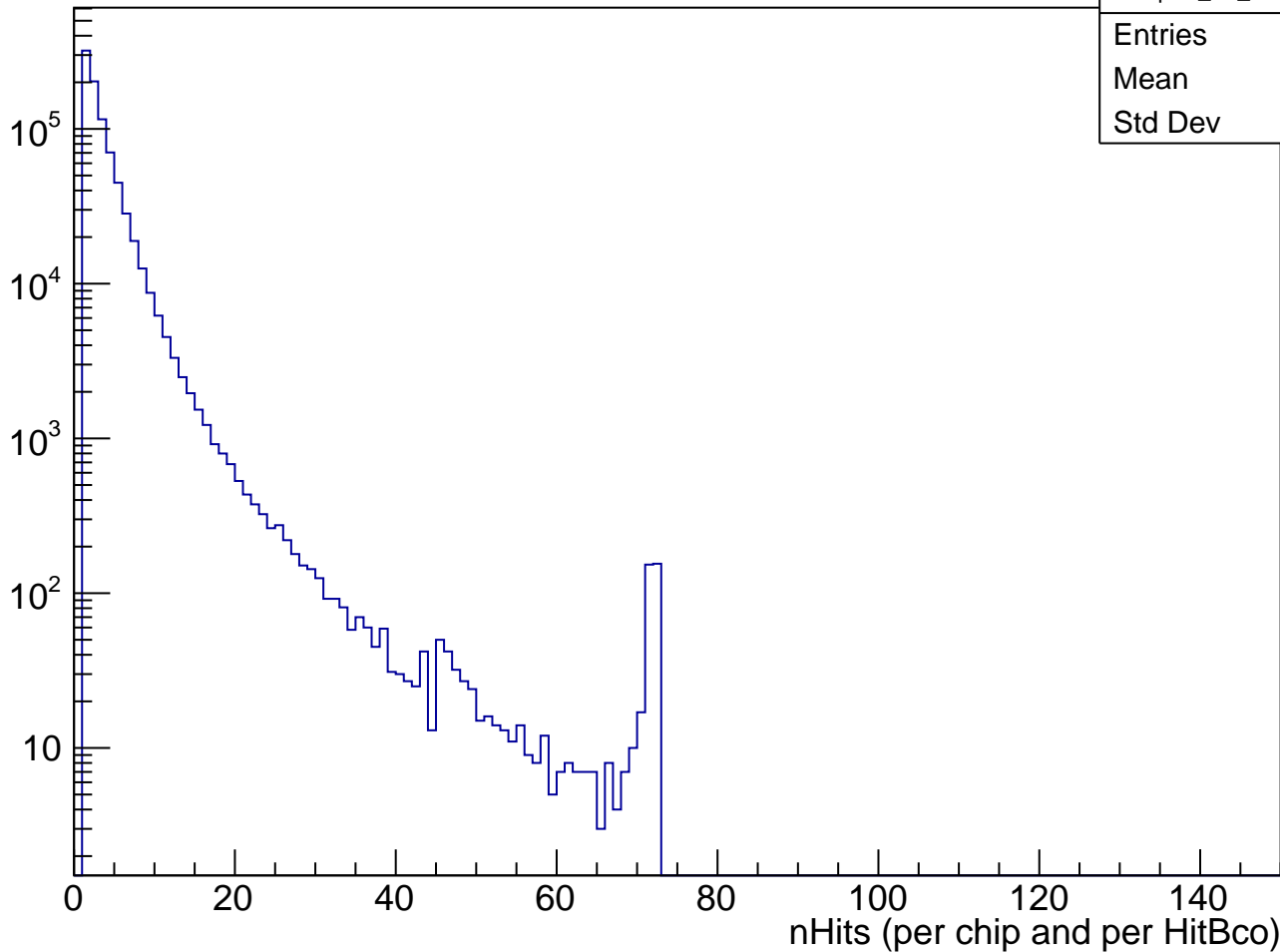
Entries 956452

Mean 3.358

Std Dev 3.735

nChipHit_F1_Fch6_Chip20

Entries

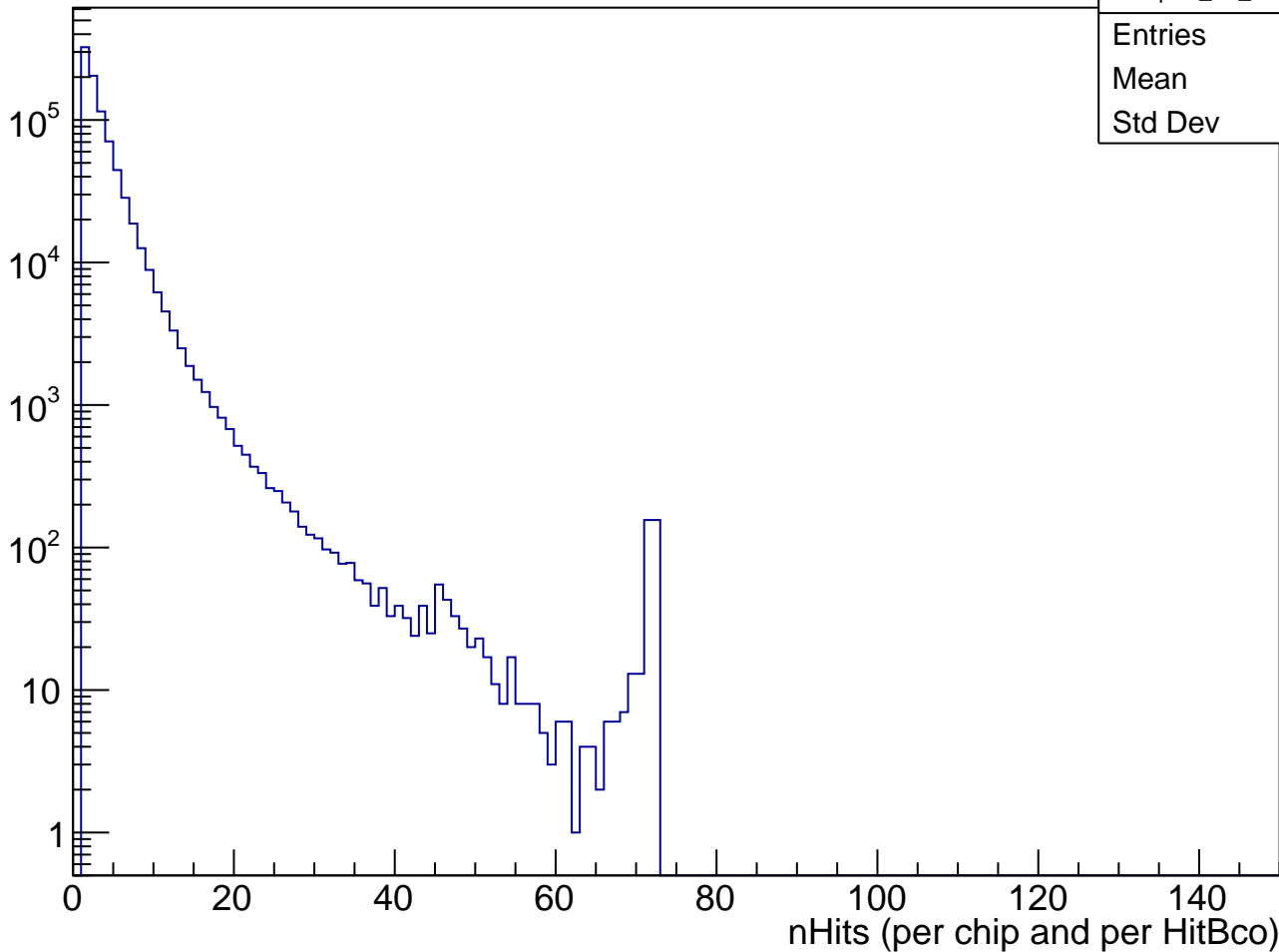


nChipHit_F1_Fch6_Chip20	
Entries	850712
Mean	2.934
Std Dev	3.384

nHits (per chip and per HitBco)

nChipHit_F1_Fch6_Chip21

Entries

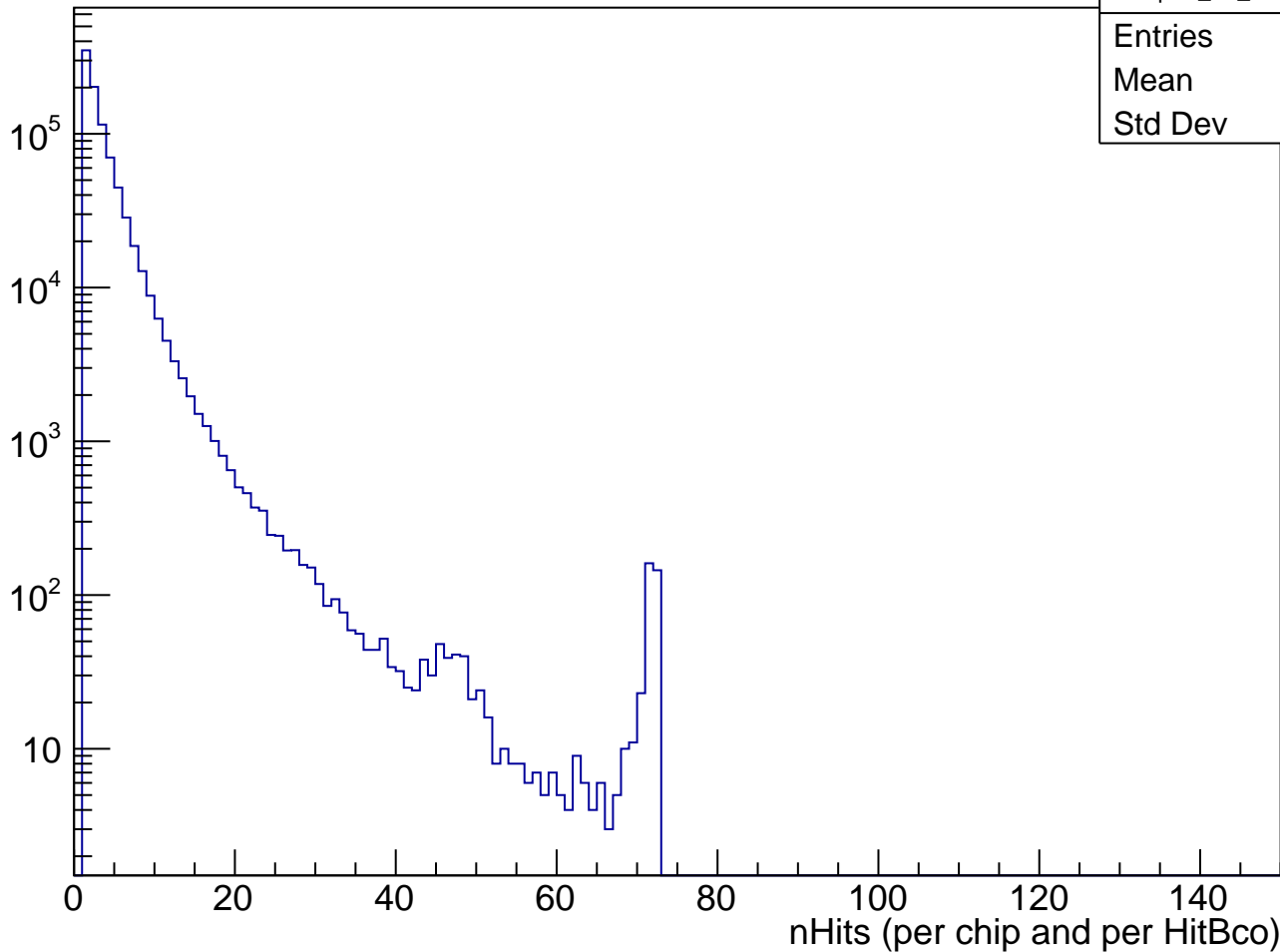


nChipHit_F1_Fch6_Chip21	
Entries	855262
Mean	2.922
Std Dev	3.362

nHits (per chip and per HitBco)

nChipHit_F1_Fch6_Chip22

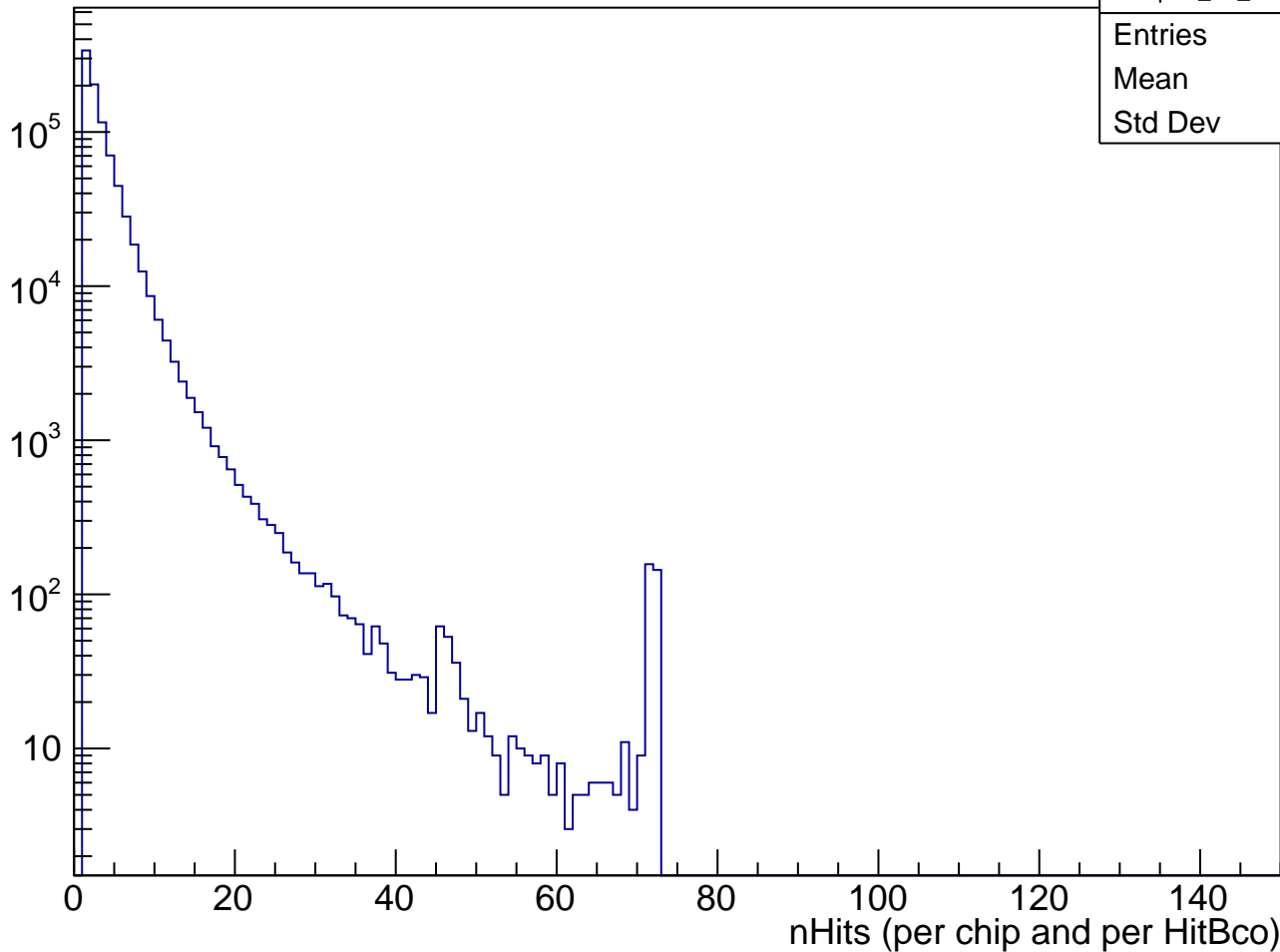
Entries



nChipHit_F1_Fch6_Chip22	
Entries	878251
Mean	2.873
Std Dev	3.343

nChipHit_F1_Fch6_Chip23

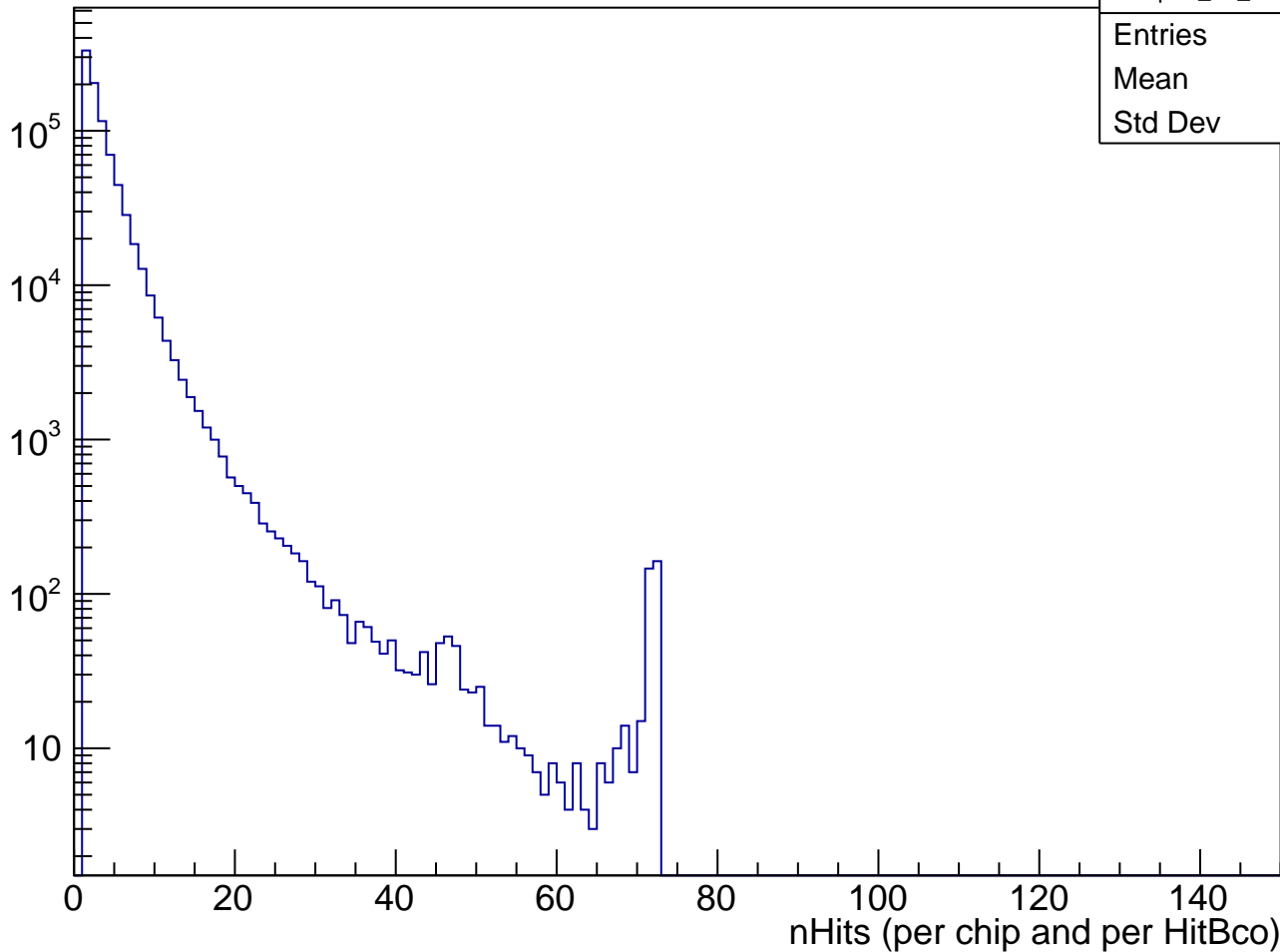
Entries



nChipHit_F1_Fch6_Chip23	
Entries	867967
Mean	2.878
Std Dev	3.313

nChipHit_F1_Fch6_Chip24

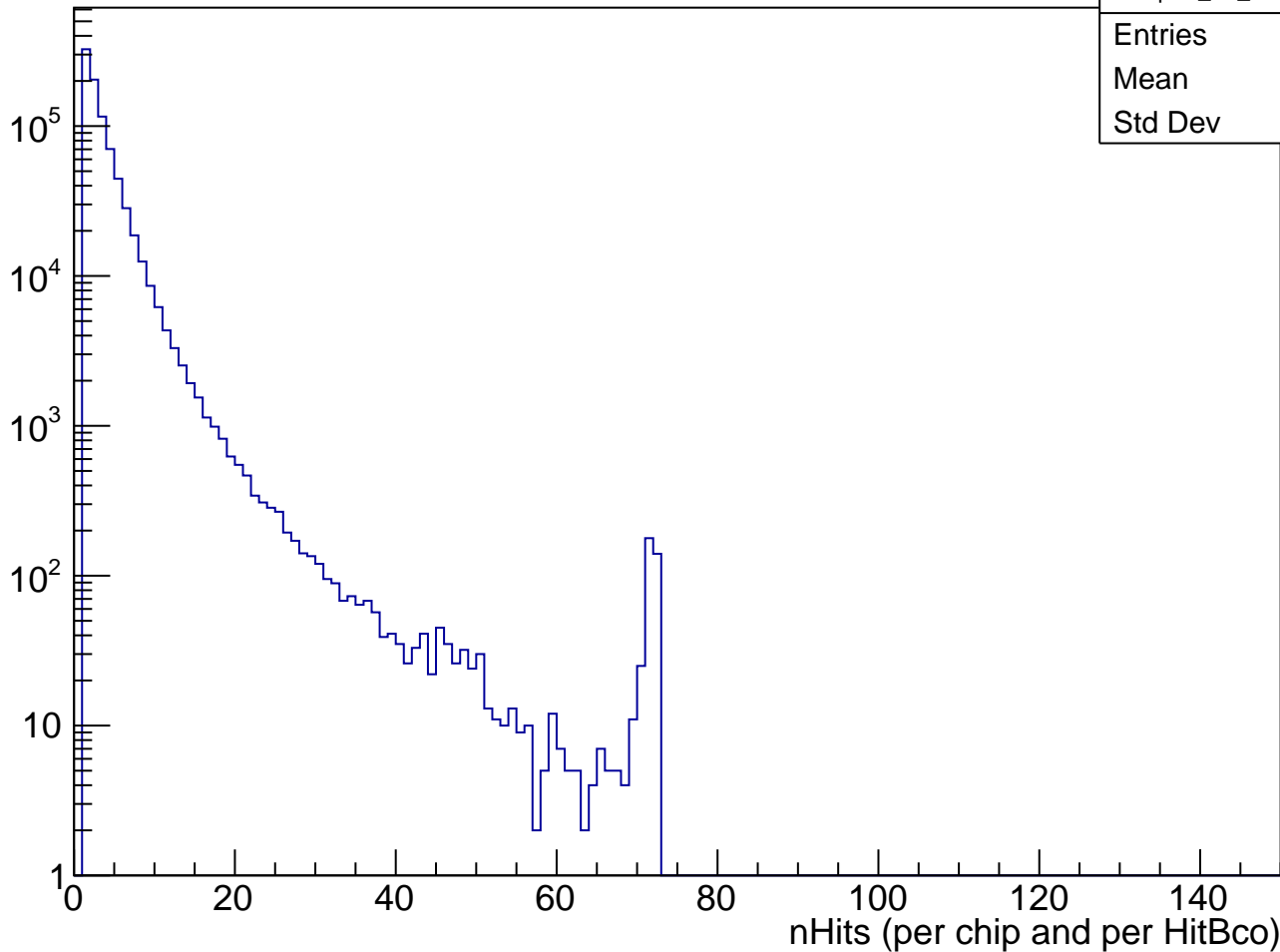
Entries



nChipHit_F1_Fch6_Chip24	
Entries	861386
Mean	2.898
Std Dev	3.357

nChipHit_F1_Fch6_Chip25

Entries

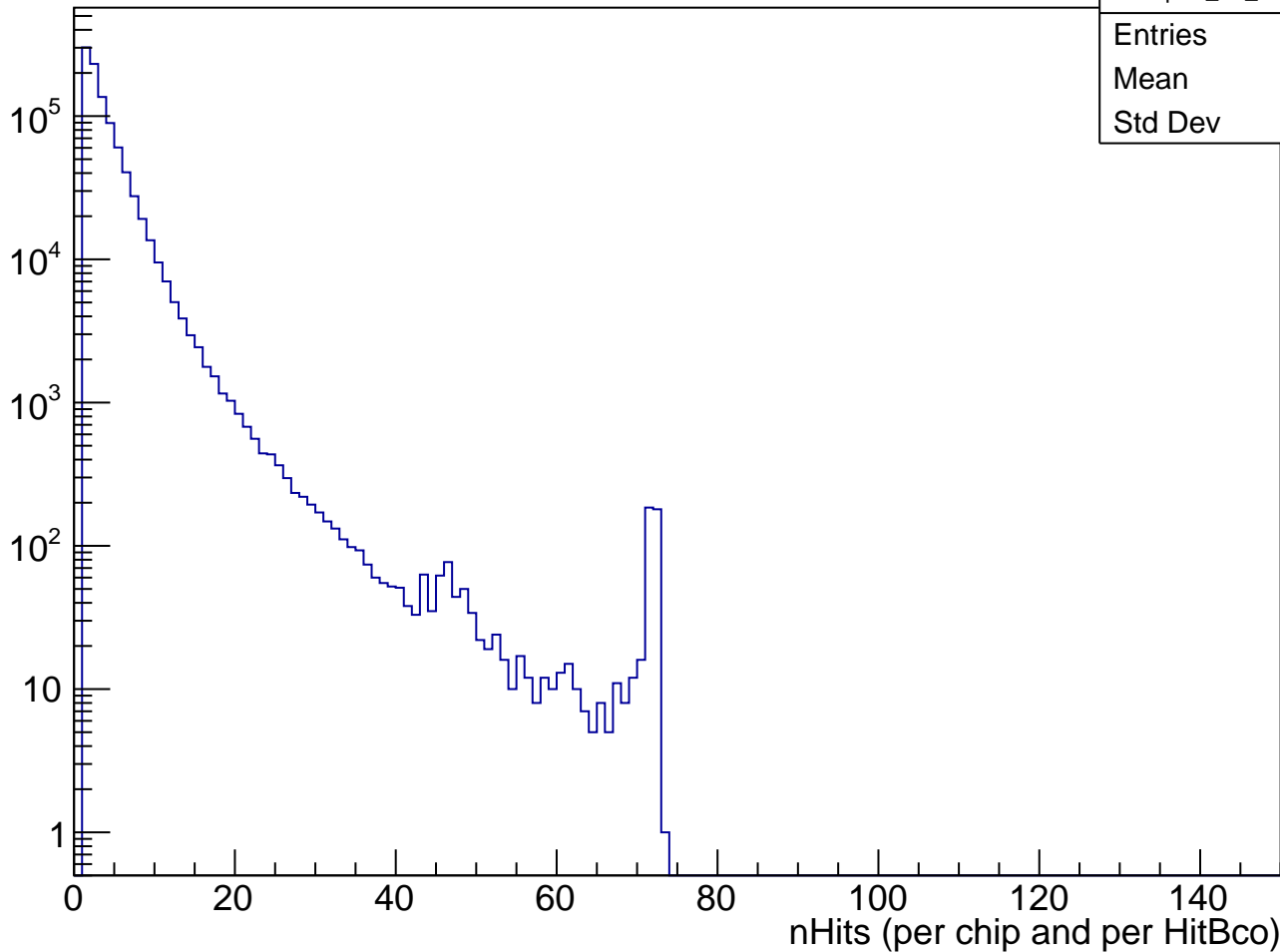


nChipHit_F1_Fch6_Chip25	
Entries	856035
Mean	2.914
Std Dev	3.371

nHits (per chip and per HitBco)

nChipHit_F1_Fch6_Chip3

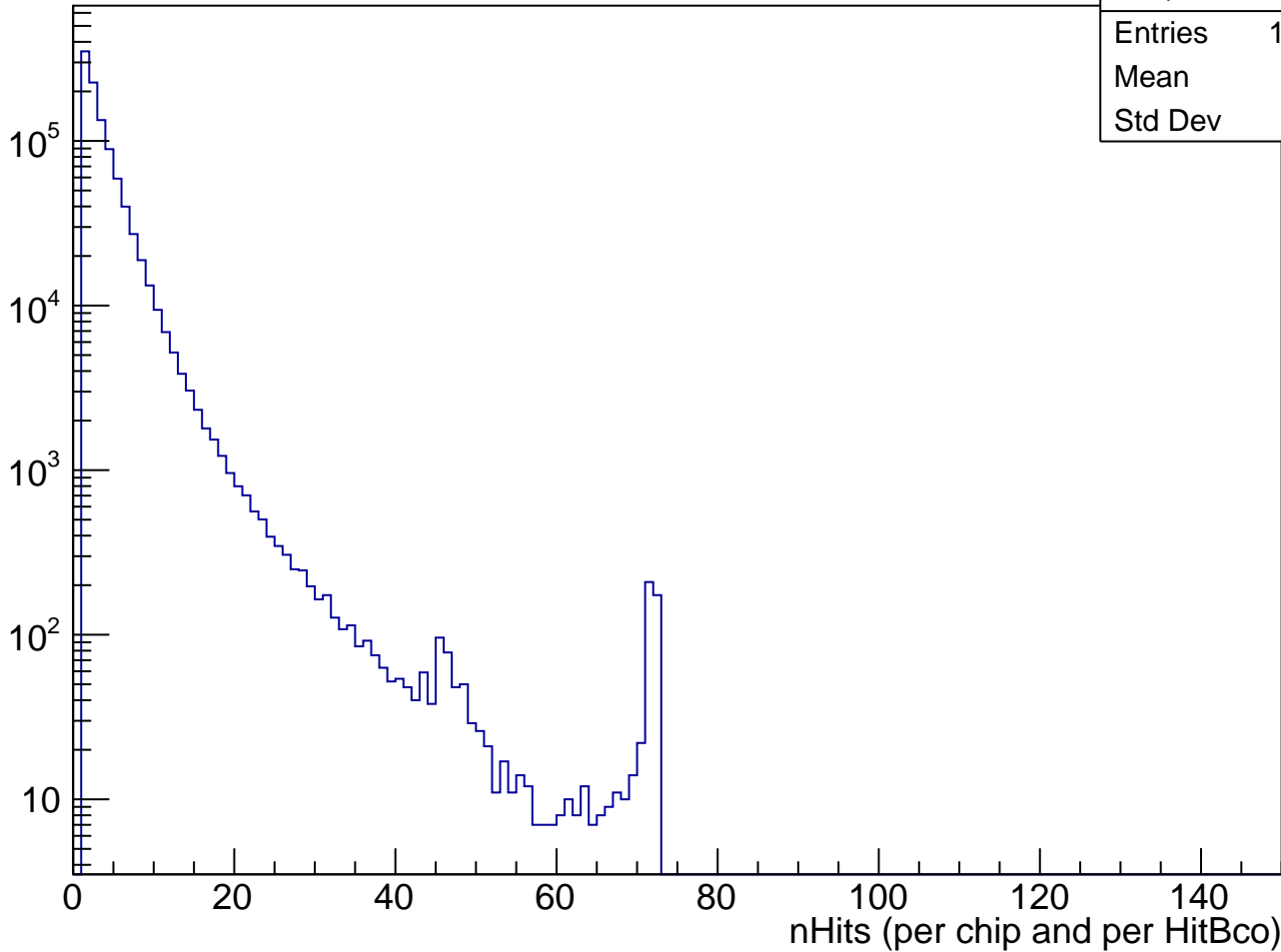
Entries



nChipHit_F1_Fch6_Chip3	
Entries	962288
Mean	3.319
Std Dev	3.683

nChipHit_F1_Fch6_Chip4

Entries



nChipHit_F1_Fch6_Chip4

Entries 1000872

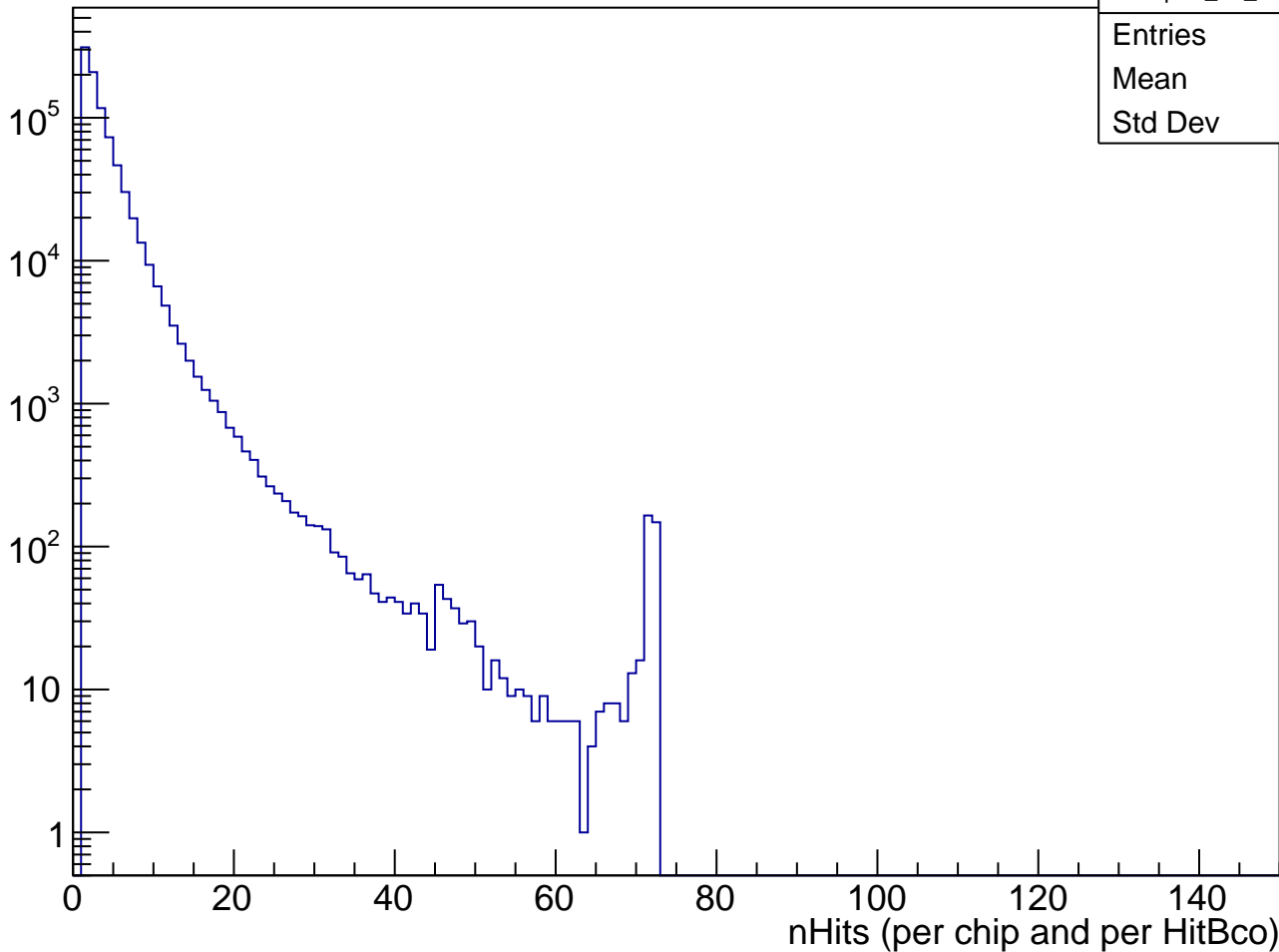
Mean 3.211

Std Dev 3.673

nHits (per chip and per HitBco)

nChipHit_F1_Fch6_Chip5

Entries

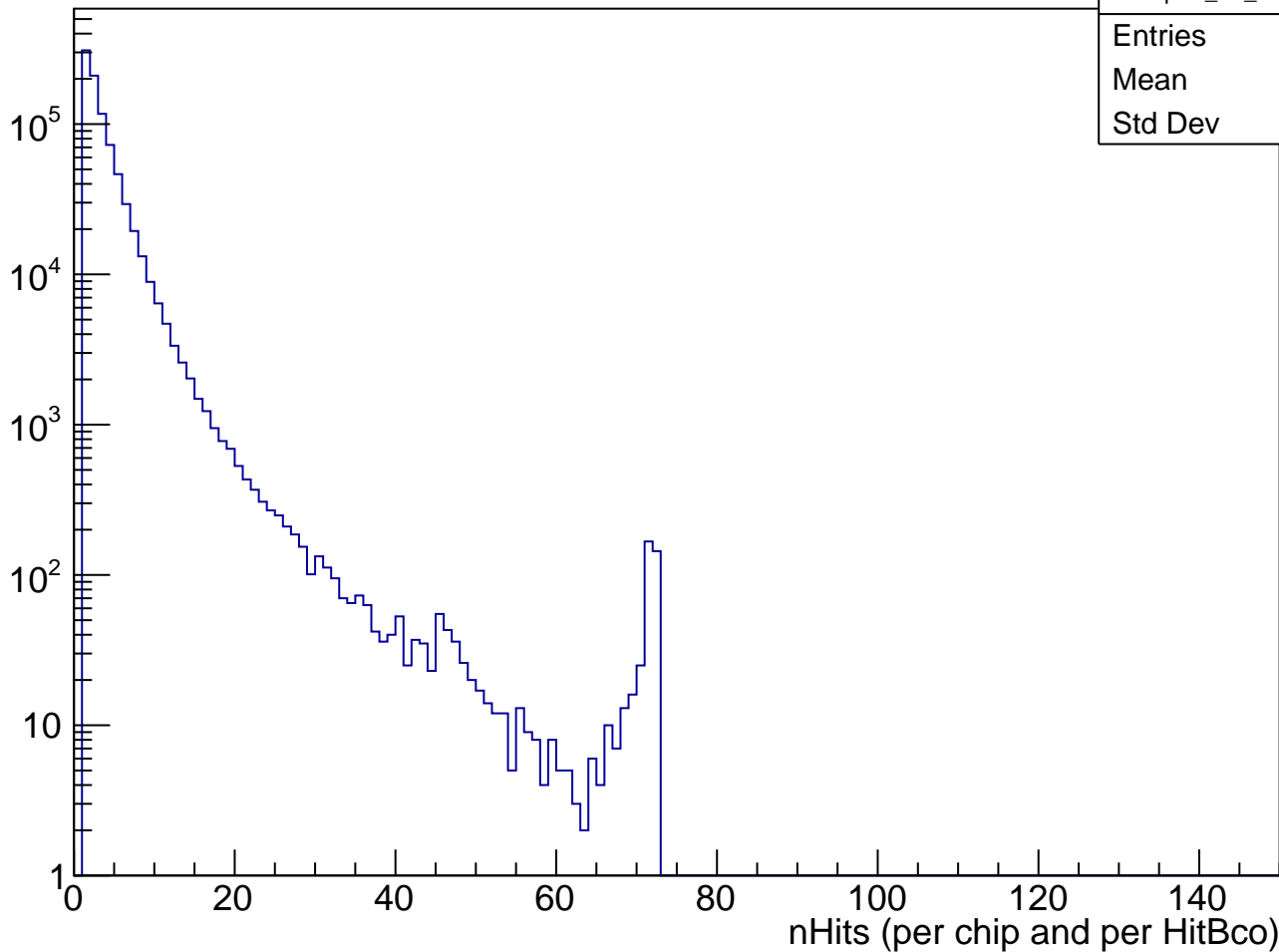


nChipHit_F1_Fch6_Chip5

Entries	858547
Mean	2.995
Std Dev	3.418

nChipHit_F1_Fch6_Chip6

Entries



nChipHit_F1_Fch6_Chip6

Entries 855100

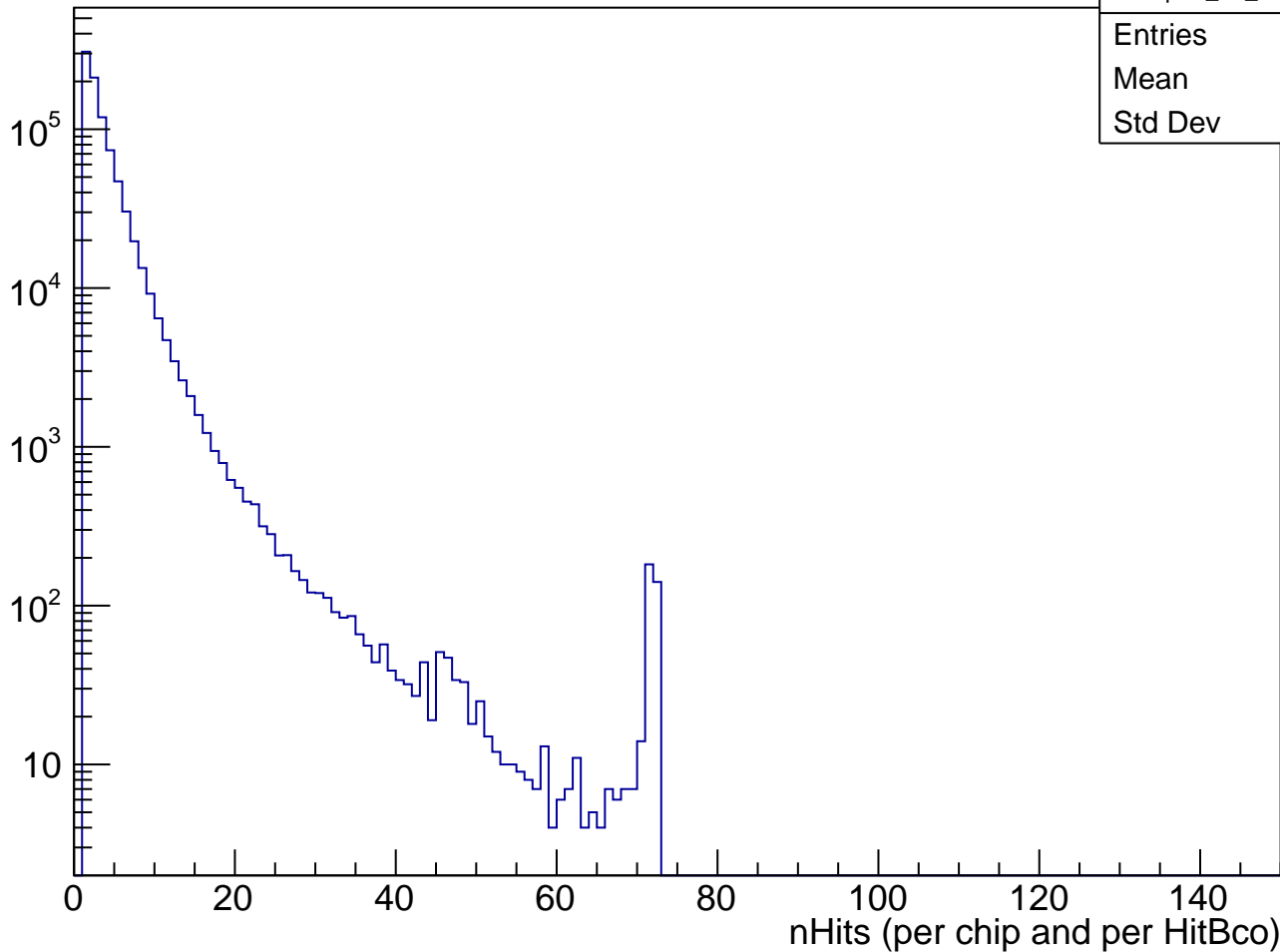
Mean 2.975

Std Dev 3.393

nHits (per chip and per HitBco)

nChipHit_F1_Fch6_Chip7

Entries

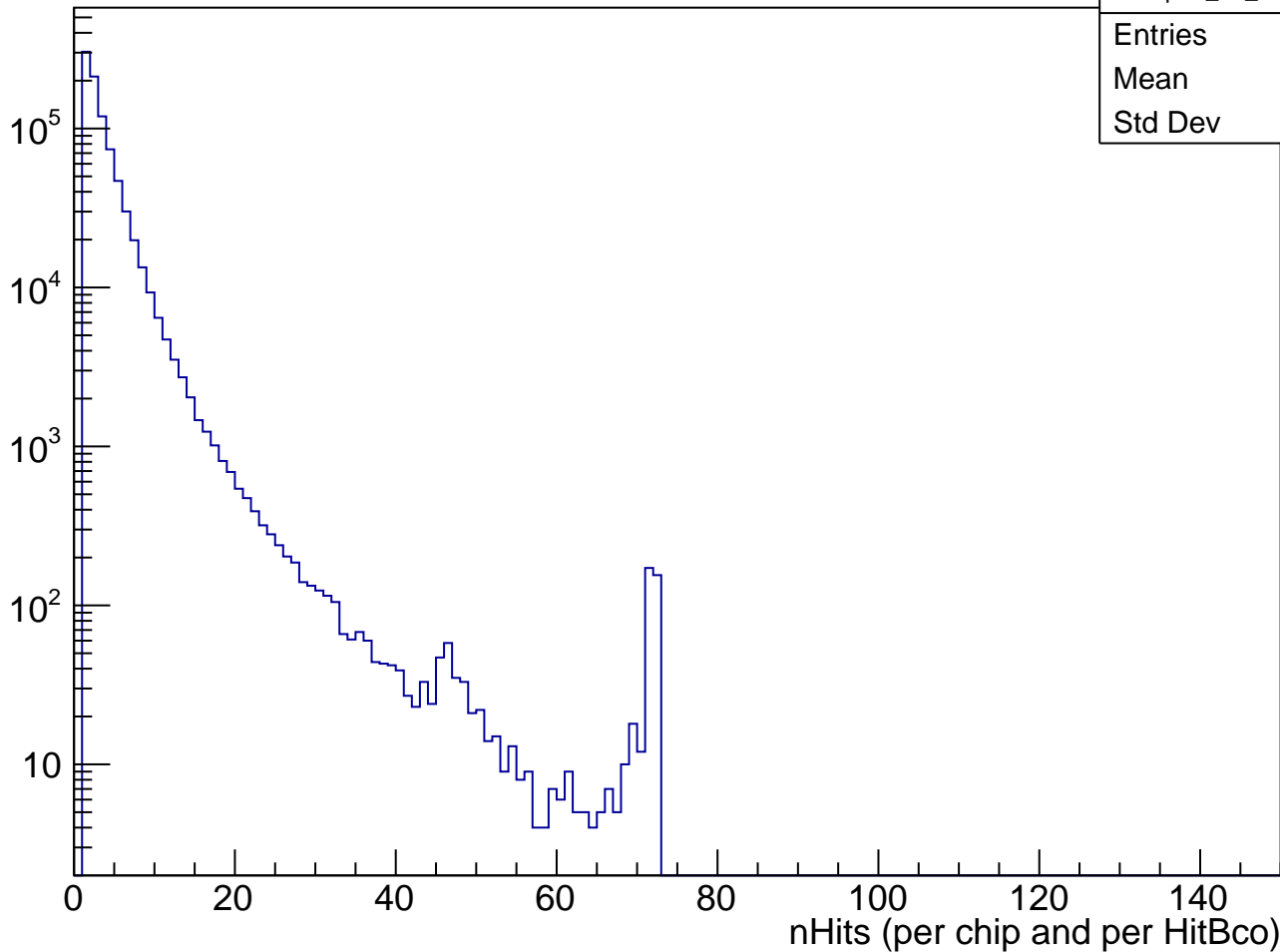


nChipHit_F1_Fch6_Chip7	
Entries	860424
Mean	2.992
Std Dev	3.395

nHits (per chip and per HitBco)

nChipHit_F1_Fch6_Chip8

Entries



nChipHit_F1_Fch6_Chip8

Entries 858003

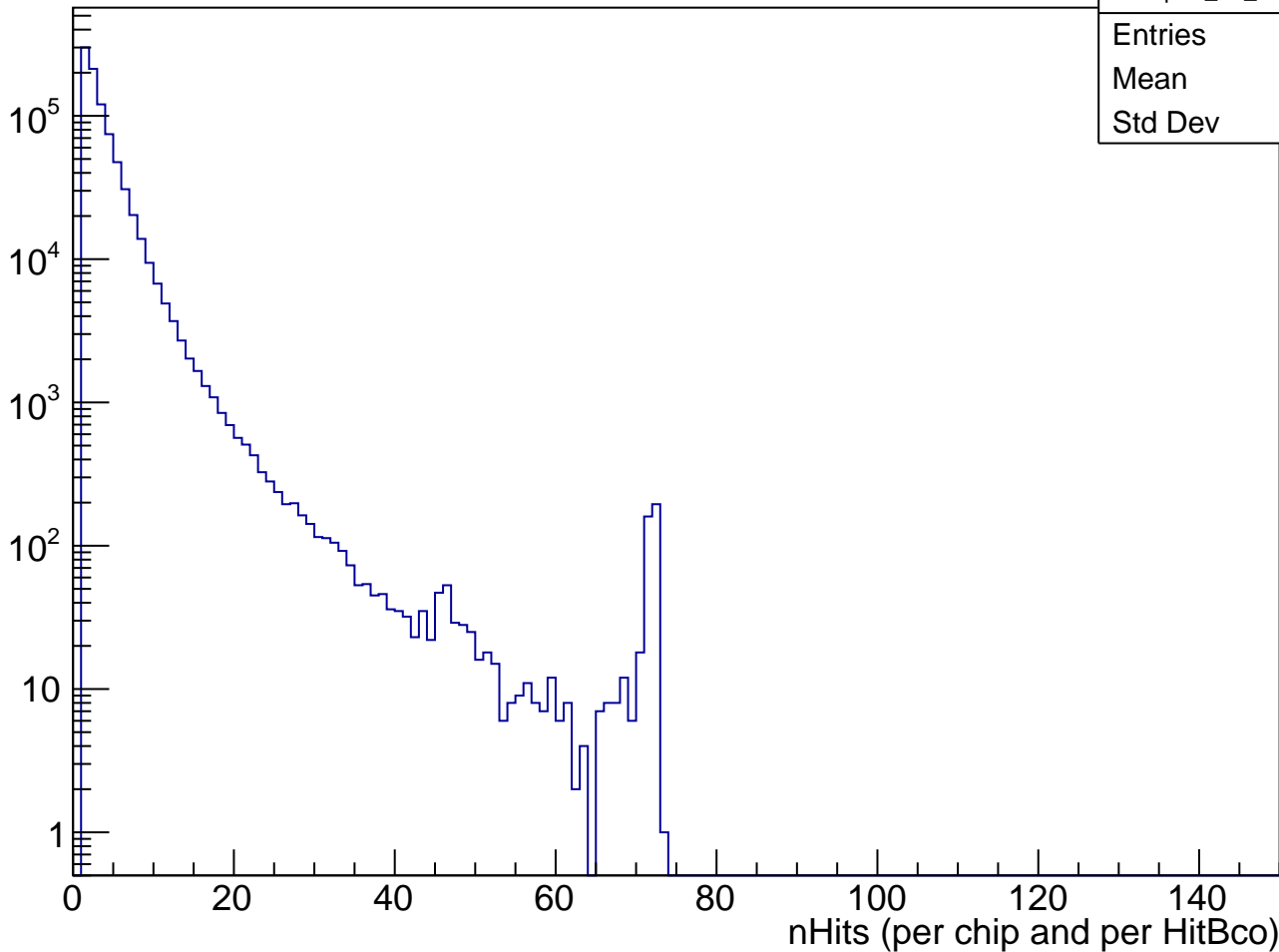
Mean 3.003

Std Dev 3.408

nHits (per chip and per HitBco)

nChipHit_F1_Fch6_Chip9

Entries

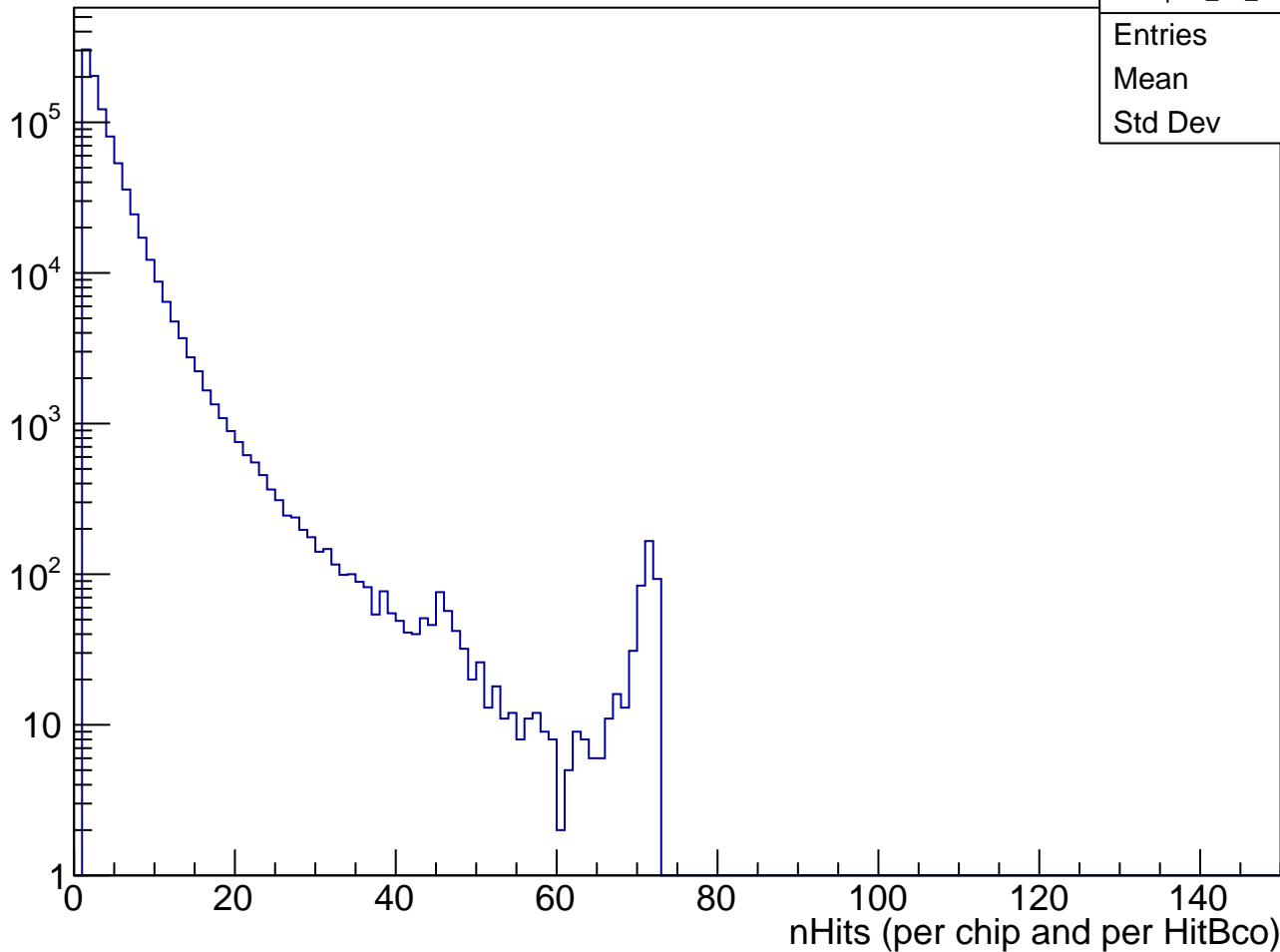


nChipHit_F1_Fch6_Chip9	
Entries	860289
Mean	3.038
Std Dev	3.453

nHits (per chip and per HitBco)

nChipHit_F1_Fch7_Chip0

Entries



nChipHit_F1_Fch7_Chip0

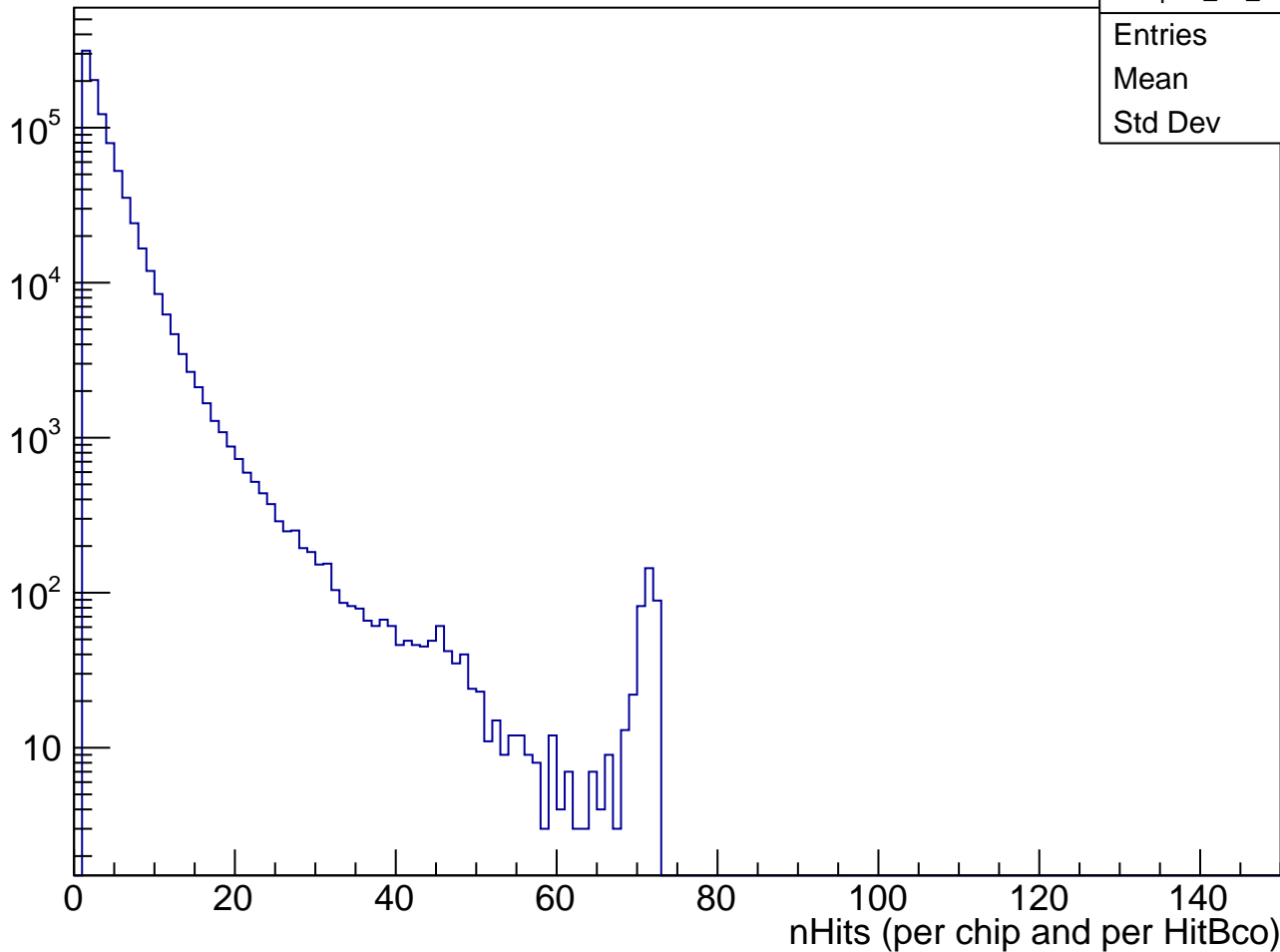
Entries 892922

Mean 3.25

Std Dev 3.689

nChipHit_F1_Fch7_Chip1

Entries

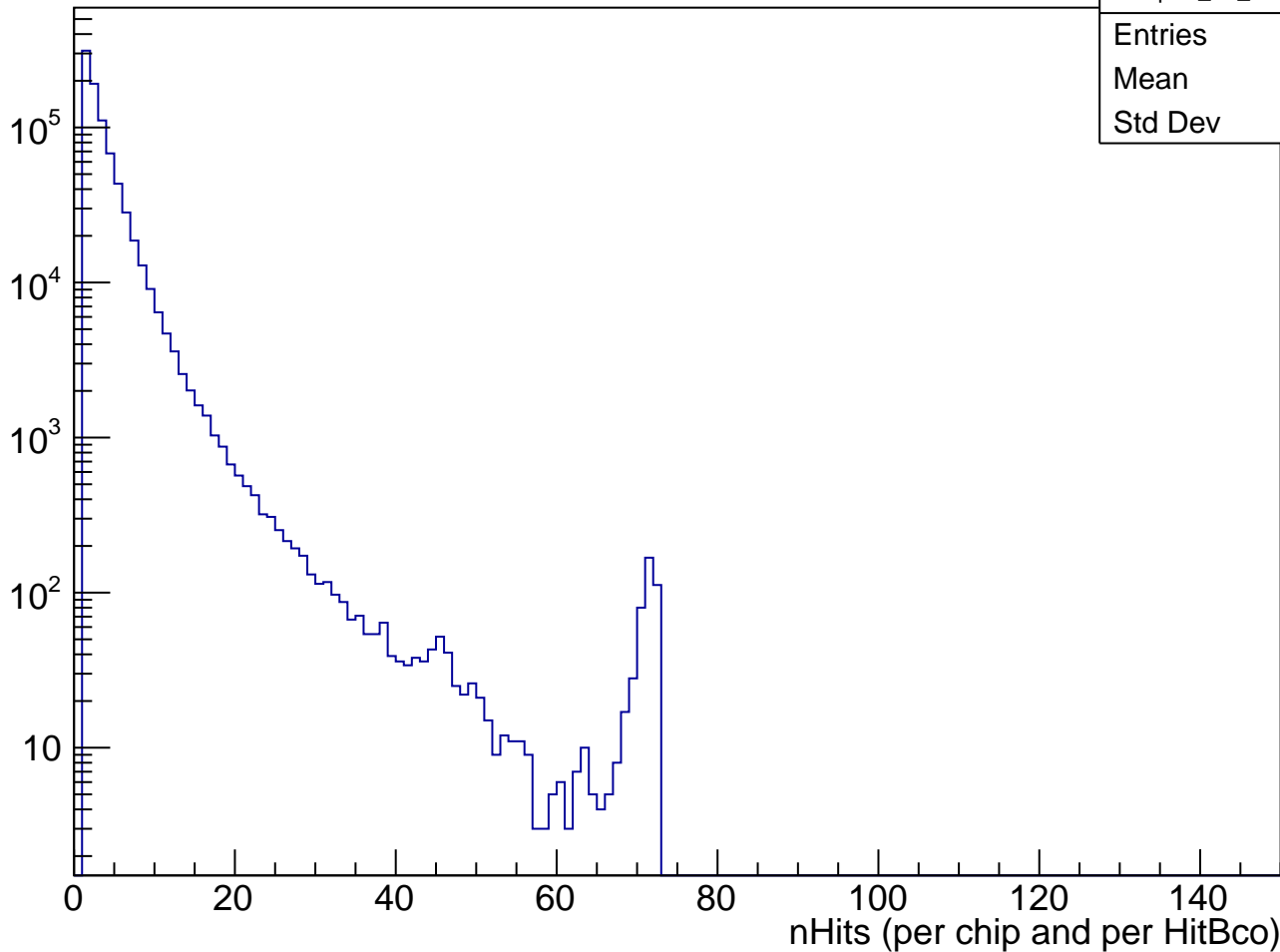


nChipHit_F1_Fch7_Chip1	
Entries	897497
Mean	3.197
Std Dev	3.606

nHits (per chip and per HitBco)

nChipHit_F1_Fch7_Chip10

Entries

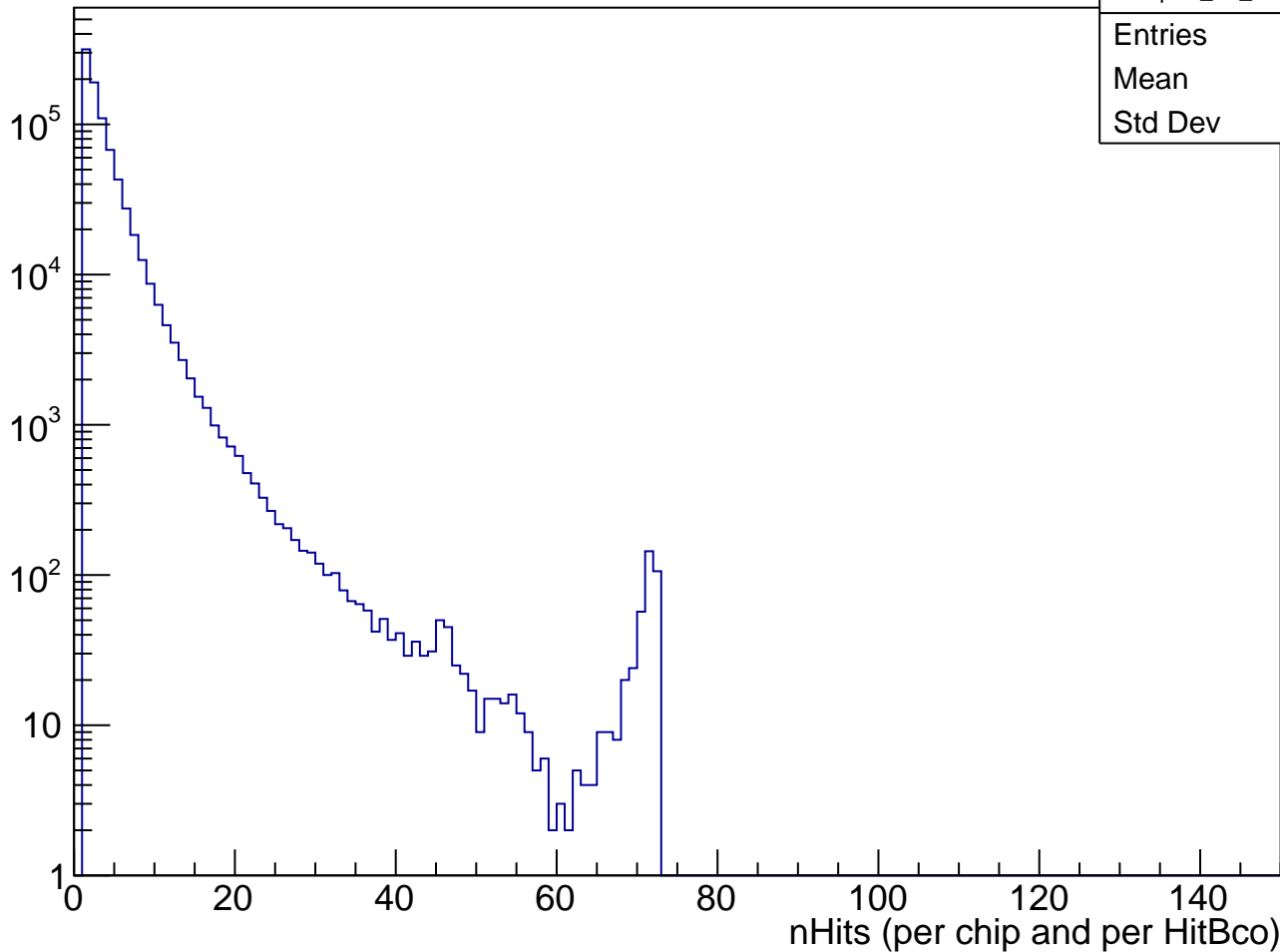


nChipHit_F1_Fch7_Chip10	
Entries	824870
Mean	2.988
Std Dev	3.521

nHits (per chip and per HitBco)

nChipHit_F1_Fch7_Chip11

Entries

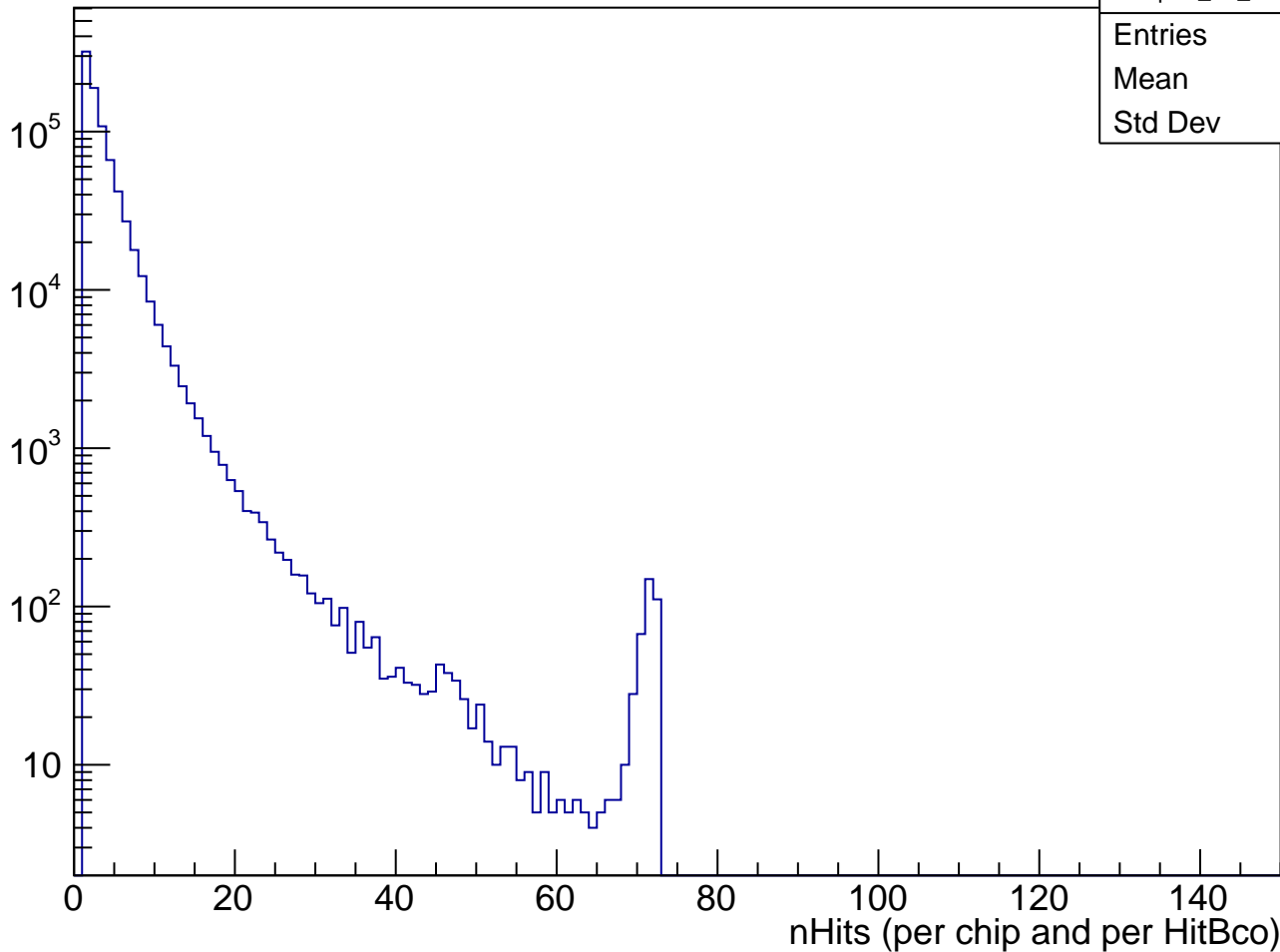


nChipHit_F1_Fch7_Chip11	
Entries	822632
Mean	2.956
Std Dev	3.446

nHits (per chip and per HitBco)

nChipHit_F1_Fch7_Chip12

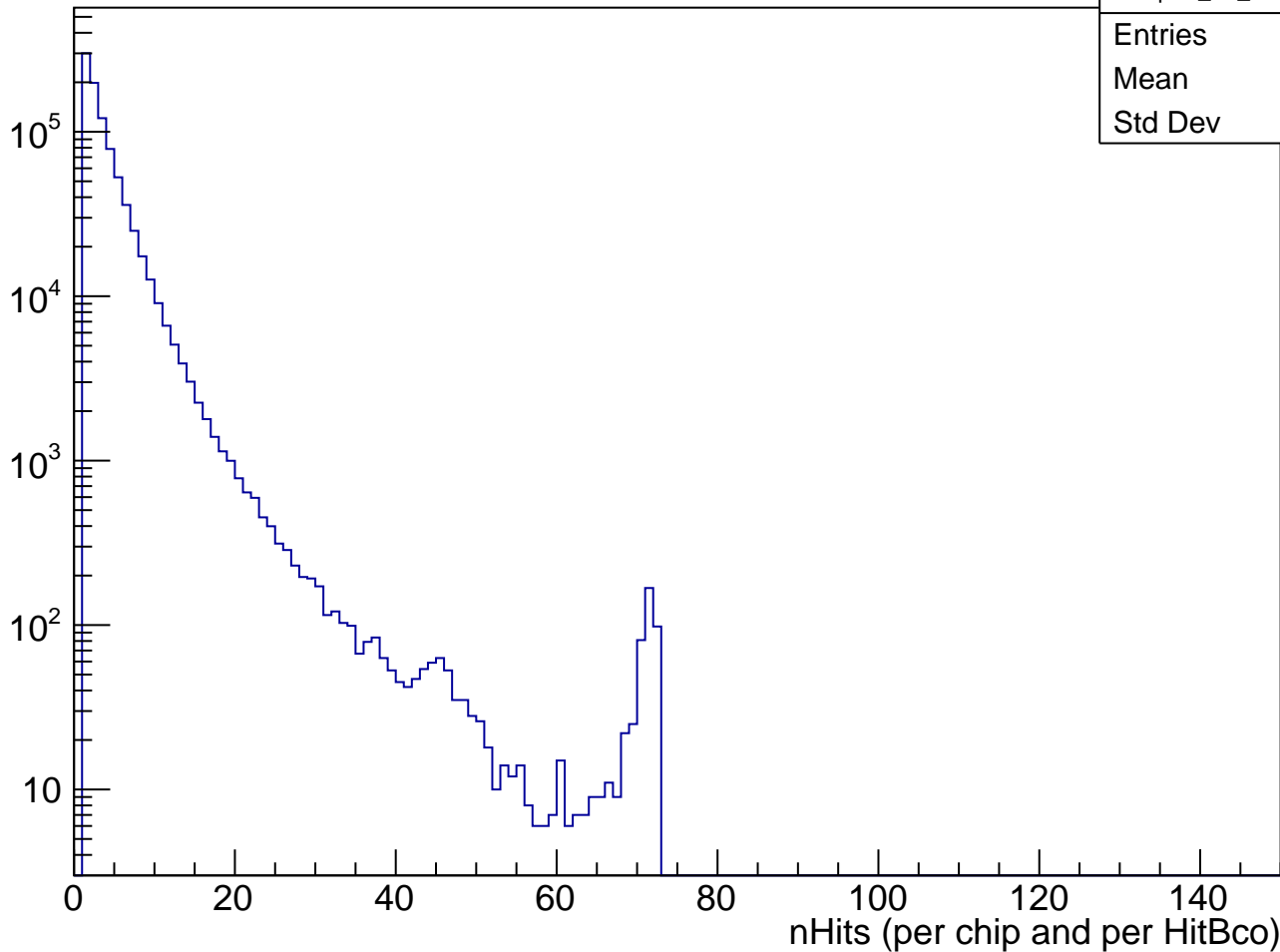
Entries



nChipHit_F1_Fch7_Chip12	
Entries	818015
Mean	2.916
Std Dev	3.427

nChipHit_F1_Fch7_Chip13

Entries

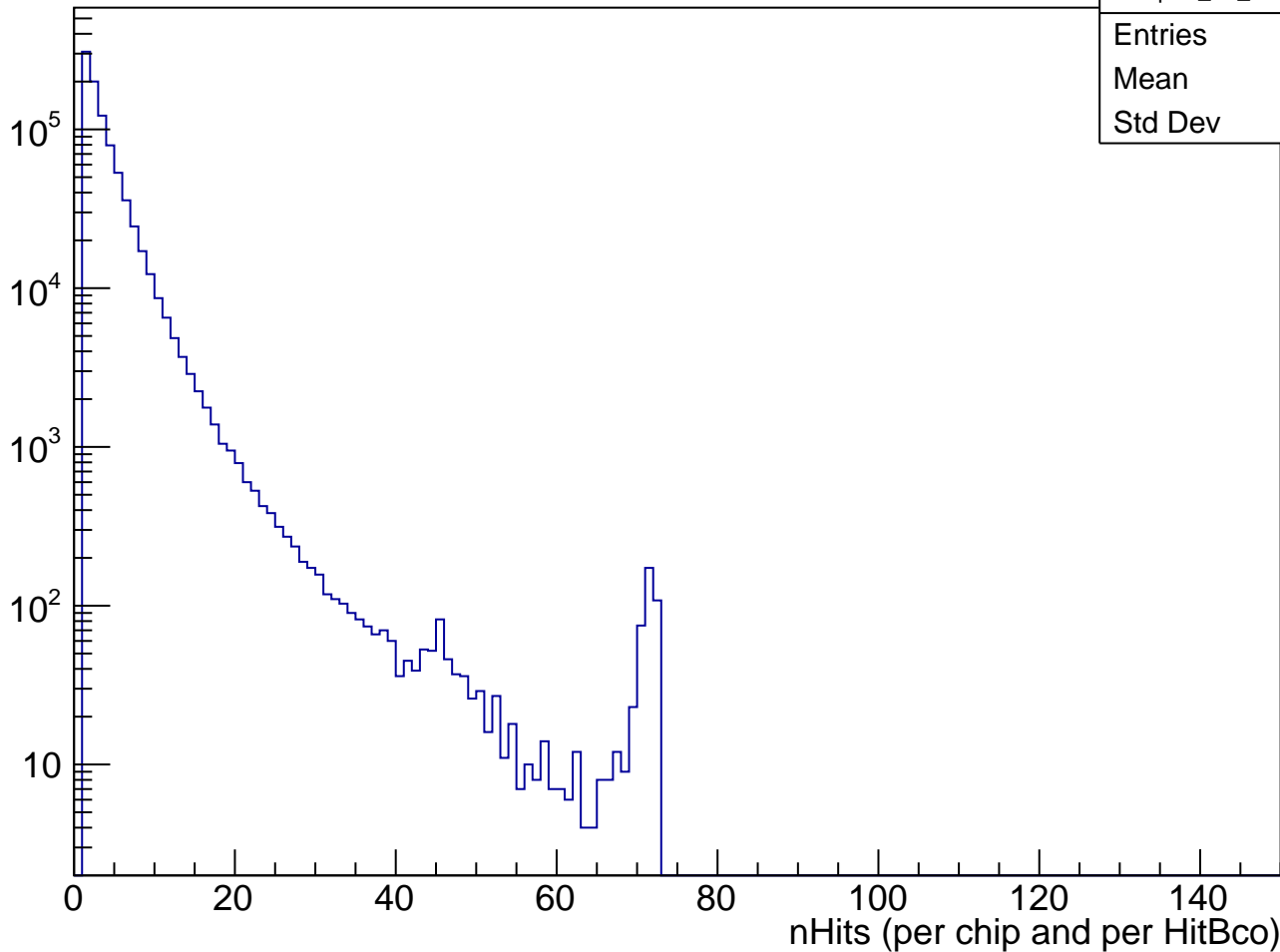


nChipHit_F1_Fch7_Chip13	
Entries	883578
Mean	3.296
Std Dev	3.752

nHits (per chip and per HitBco)

nChipHit_F1_Fch7_Chip14

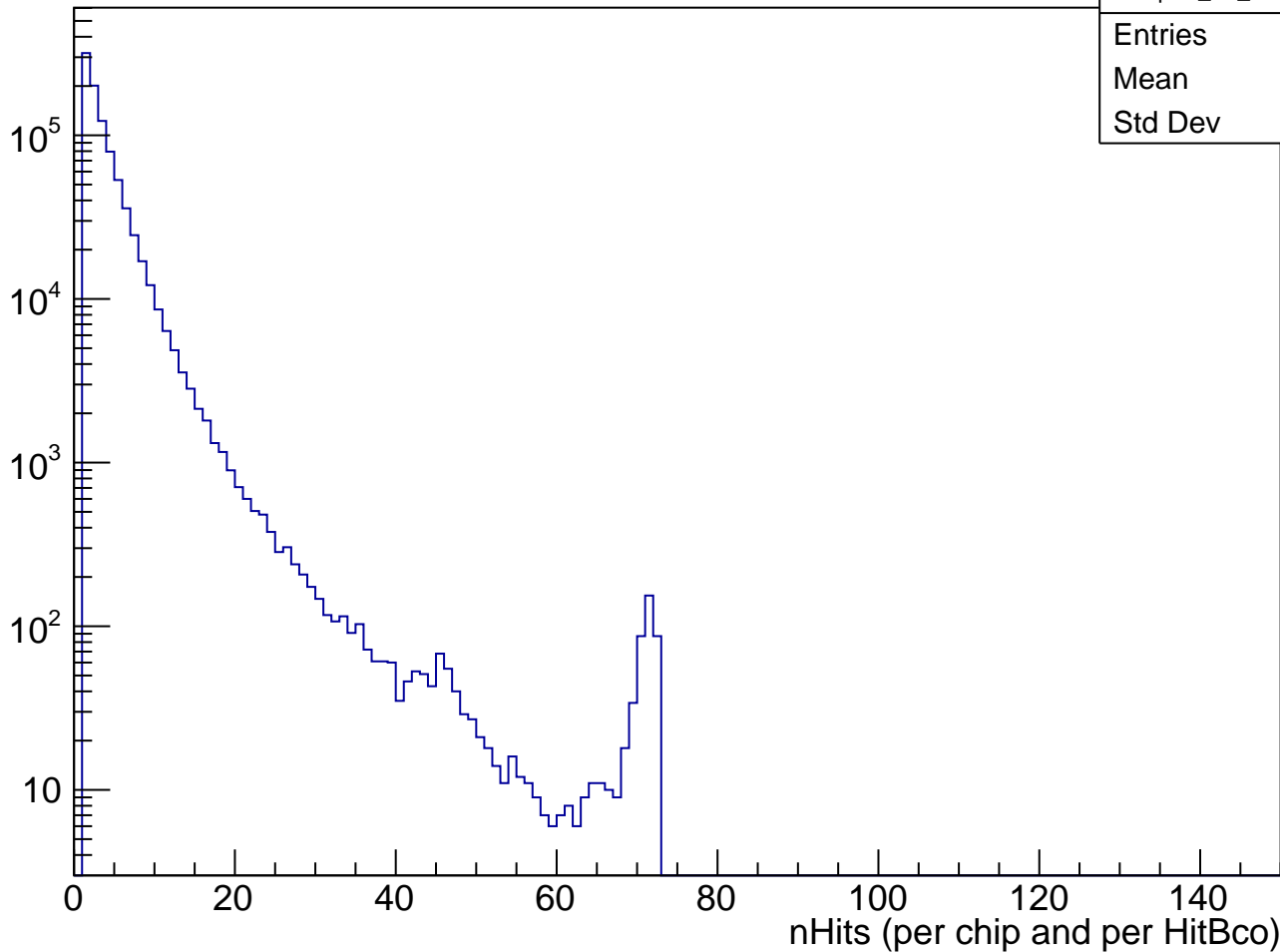
Entries



nChipHit_F1_Fch7_Chip14	
Entries	892813
Mean	3.248
Std Dev	3.703

nChipHit_F1_Fch7_Chip15

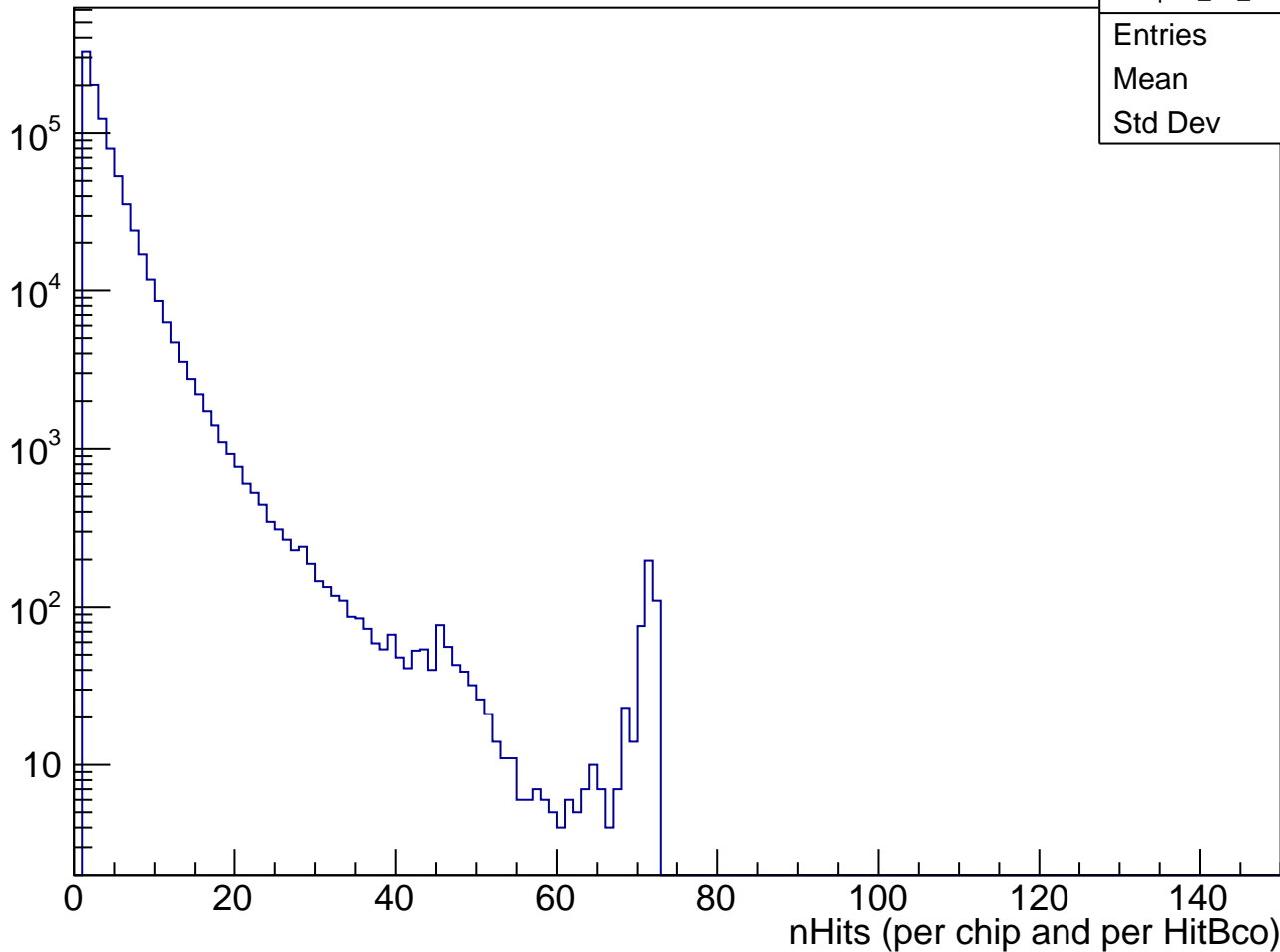
Entries



nChipHit_F1_Fch7_Chip15	
Entries	903187
Mean	3.216
Std Dev	3.672

nChipHit_F1_Fch7_Chip16

Entries

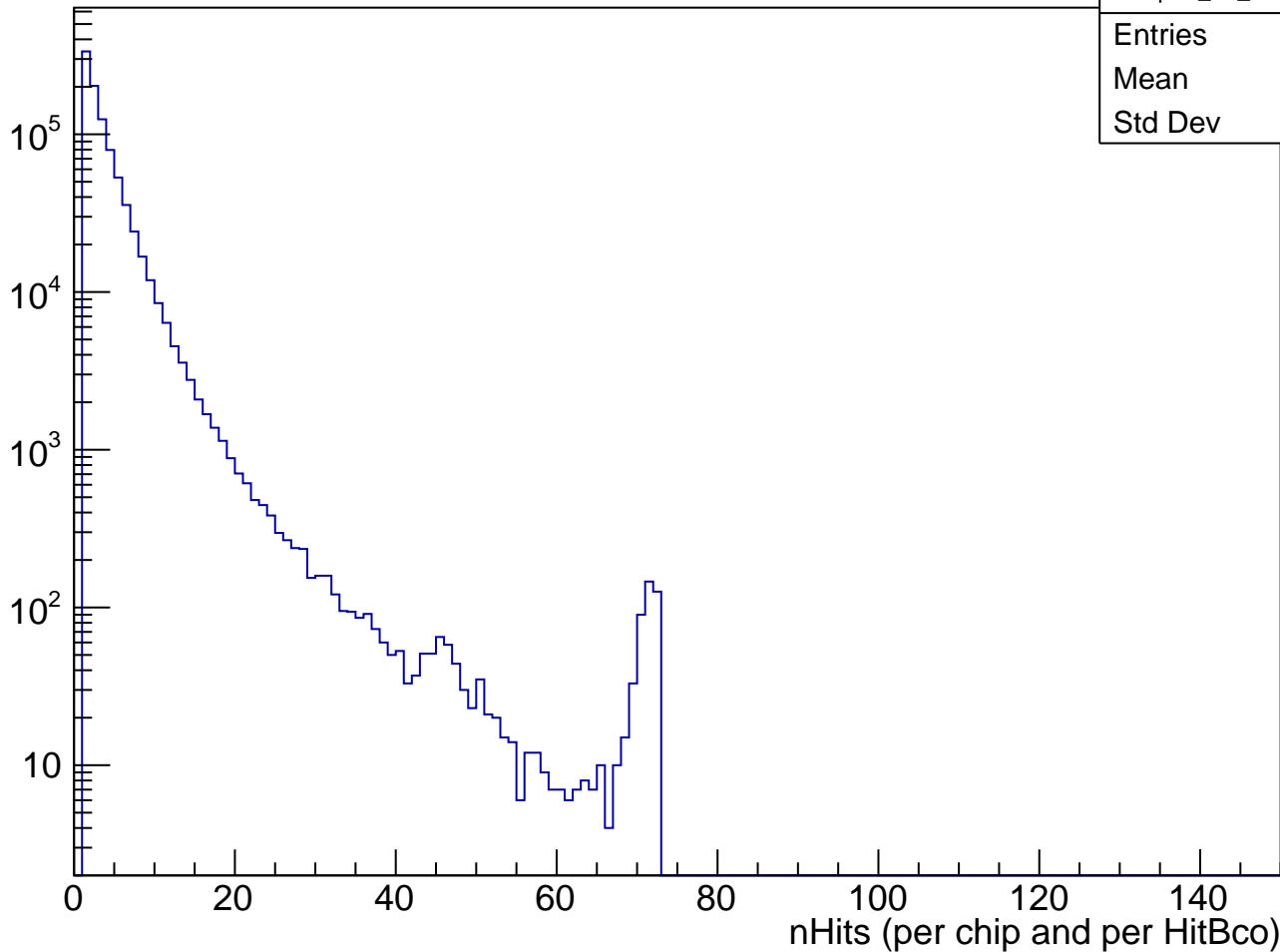


nChipHit_F1_Fch7_Chip16	
Entries	912487
Mean	3.19
Std Dev	3.677

nHits (per chip and per HitBco)

nChipHit_F1_Fch7_Chip17

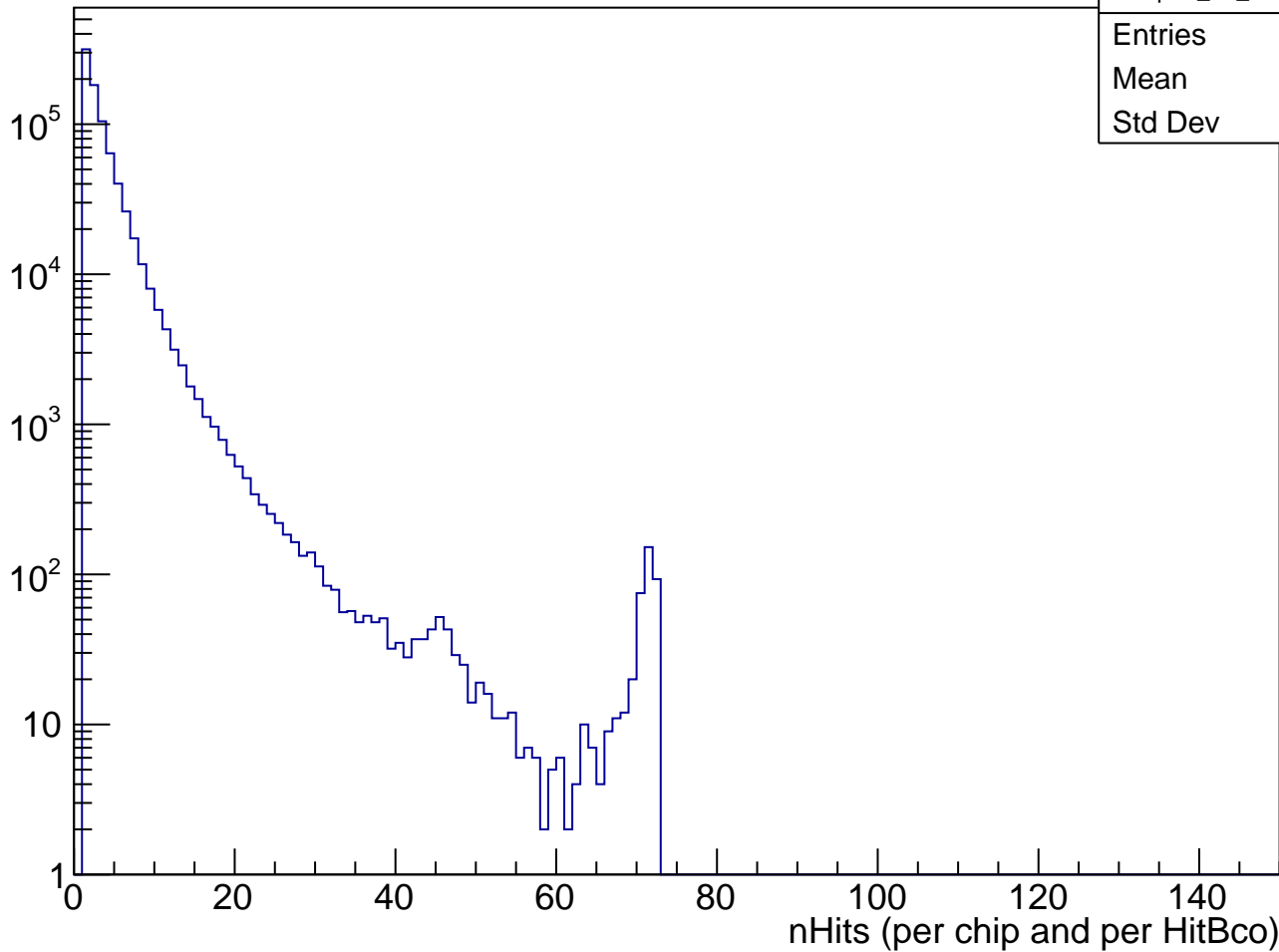
Entries



nChipHit_F1_Fch7_Chip17	
Entries	922382
Mean	3.164
Std Dev	3.66

nChipHit_F1_Fch7_Chip18

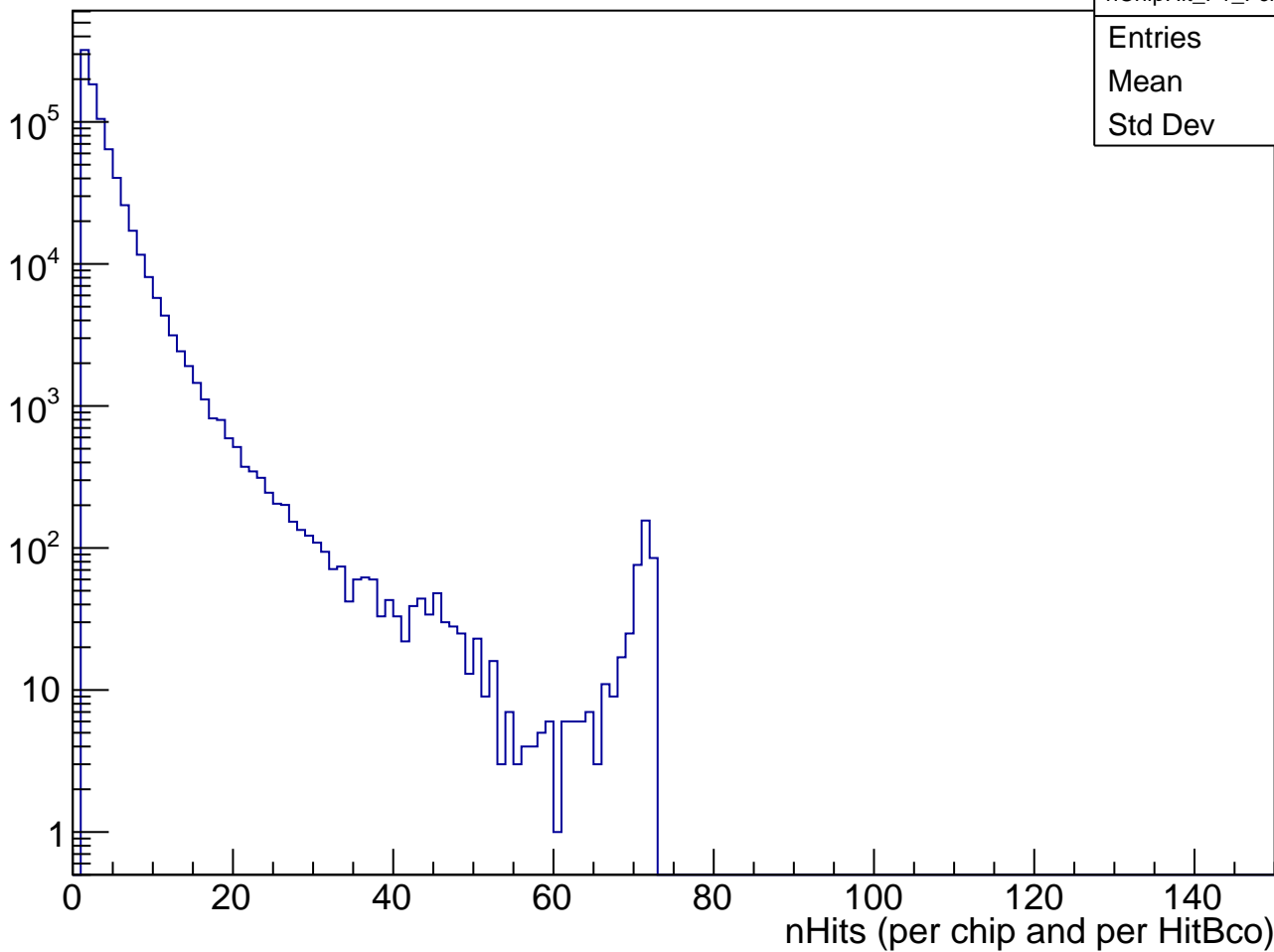
Entries



nChipHit_F1_Fch7_Chip18	
Entries	796589
Mean	2.899
Std Dev	3.419

nChipHit_F1_Fch7_Chip19

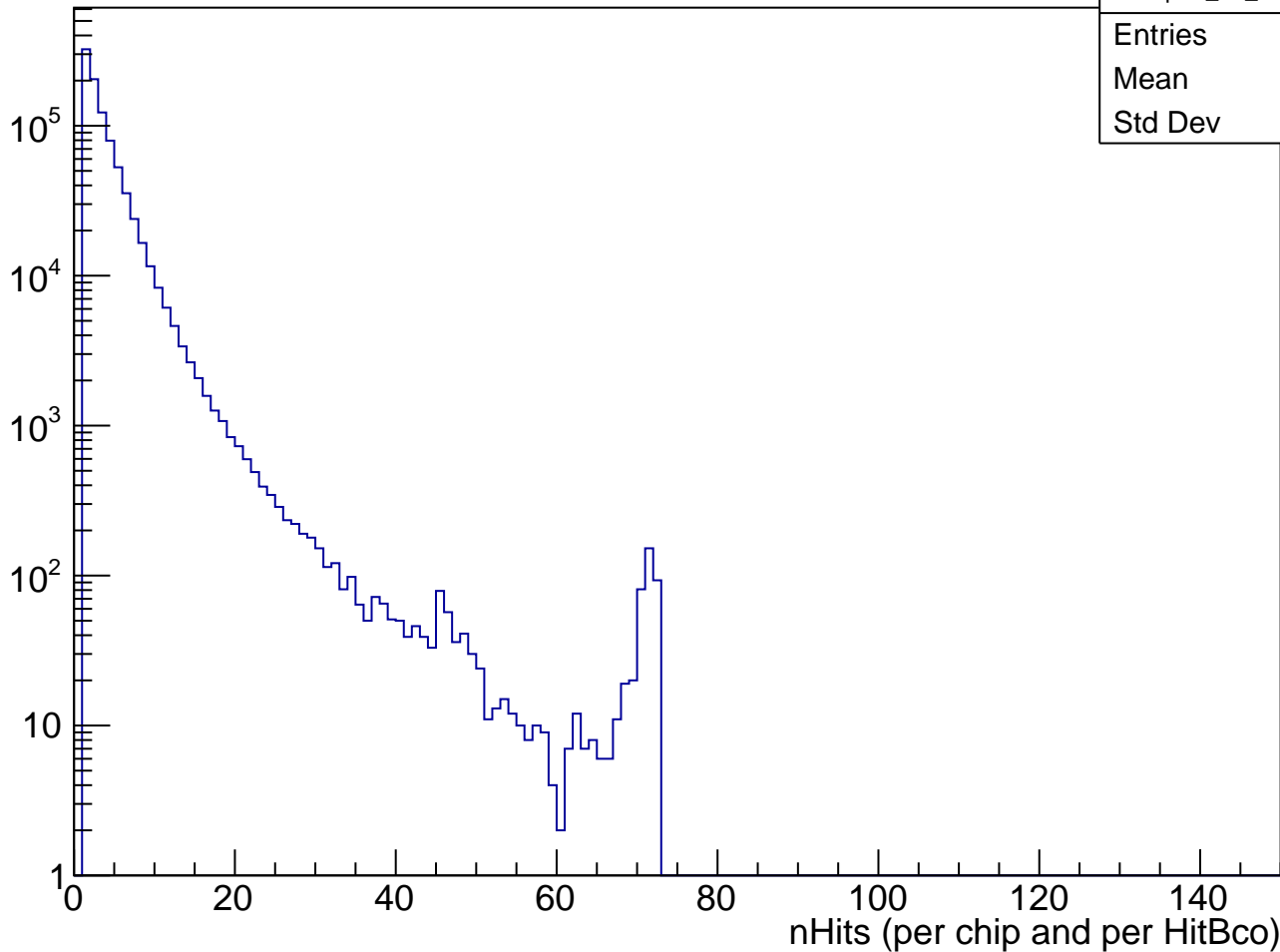
Entries



nChipHit_F1_Fch7_Chip19	
Entries	803596
Mean	2.878
Std Dev	3.387

nChipHit_F1_Fch7_Chip2

Entries



nChipHit_F1_Fch7_Chip2

Entries 907816

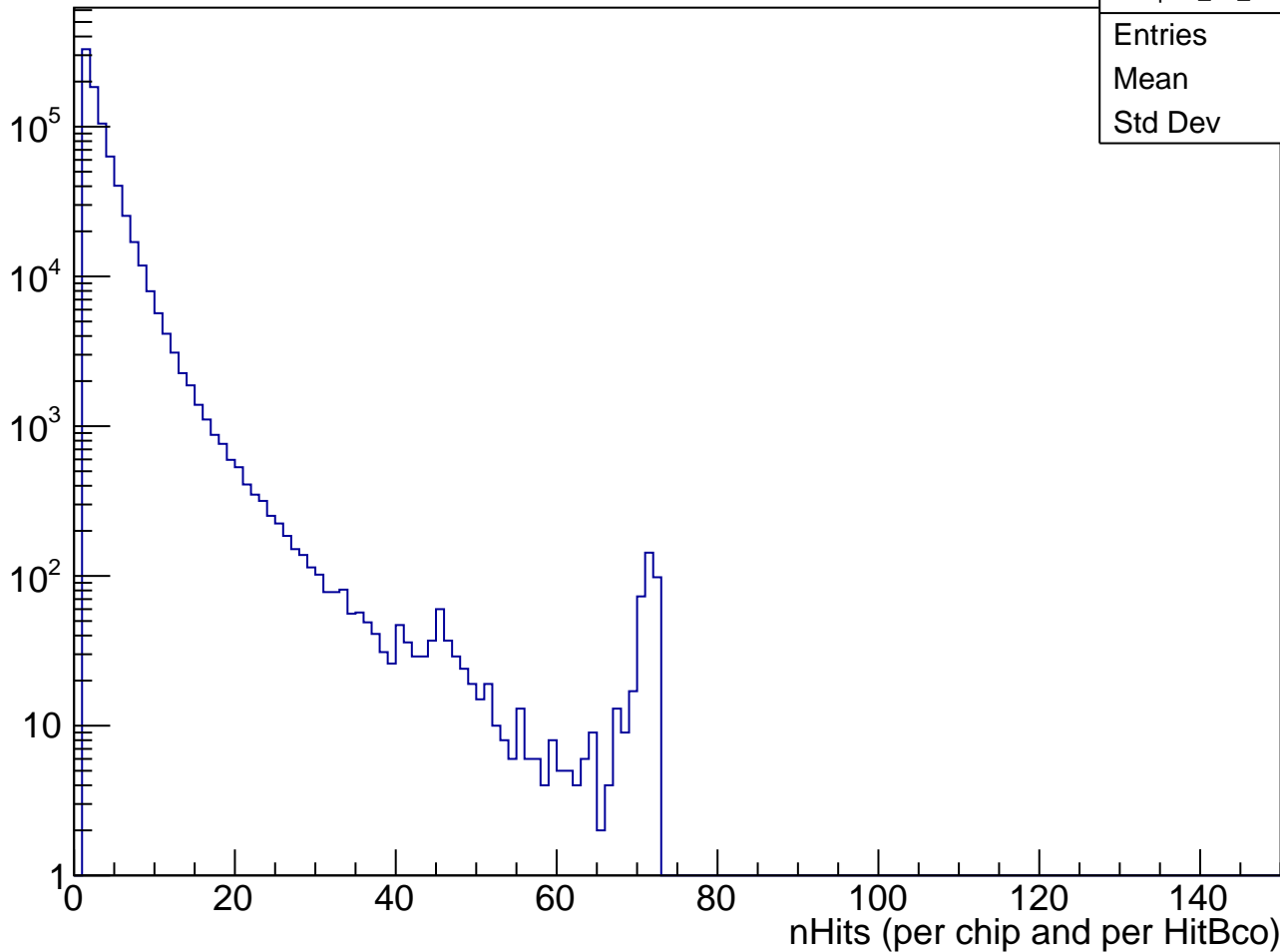
Mean 3.16

Std Dev 3.59

nHits (per chip and per HitBco)

nChipHit_F1_Fch7_Chip20

Entries

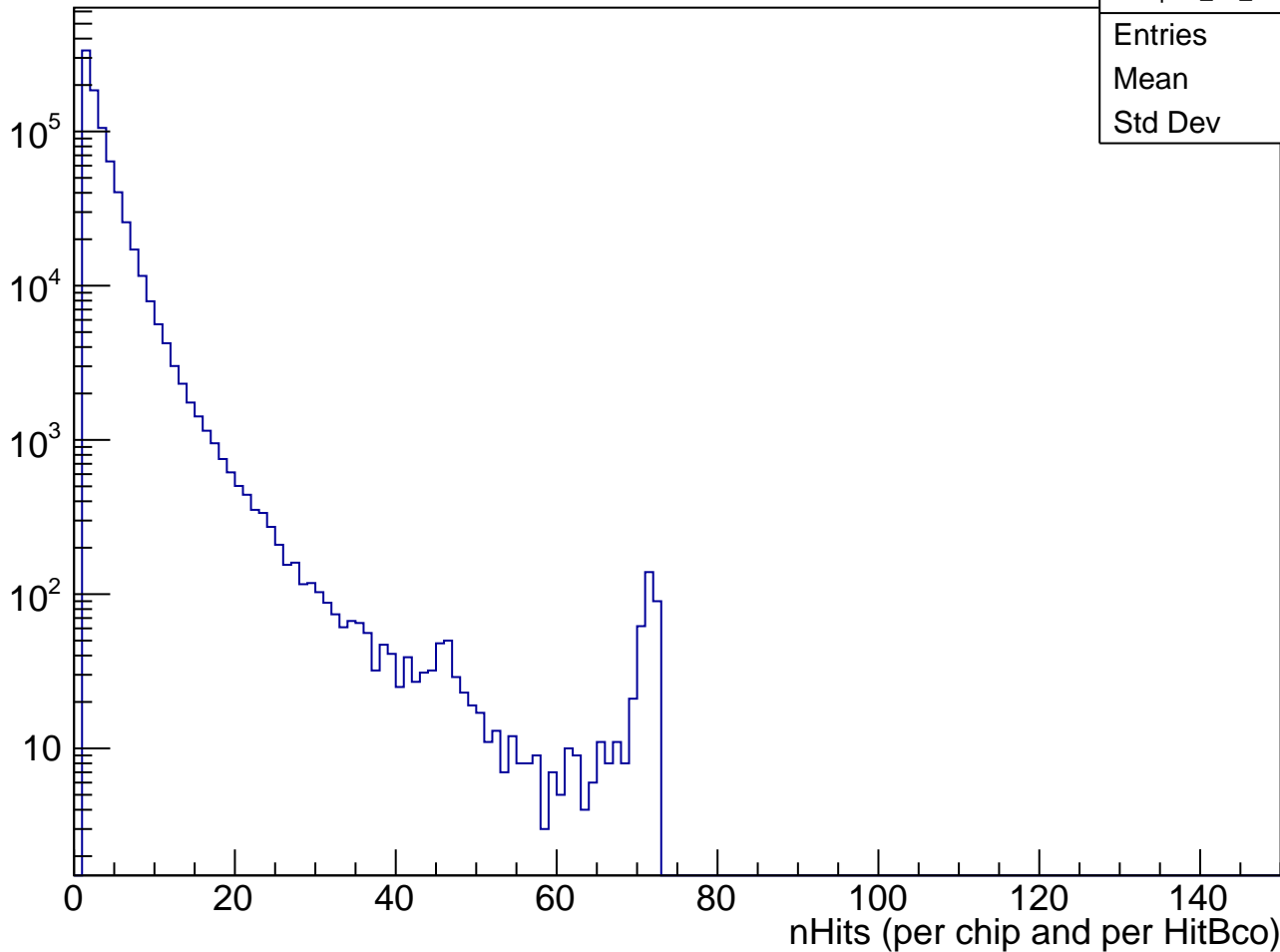


nChipHit_F1_Fch7_Chip20	
Entries	809196
Mean	2.85
Std Dev	3.365

nHits (per chip and per HitBco)

nChipHit_F1_Fch7_Chip21

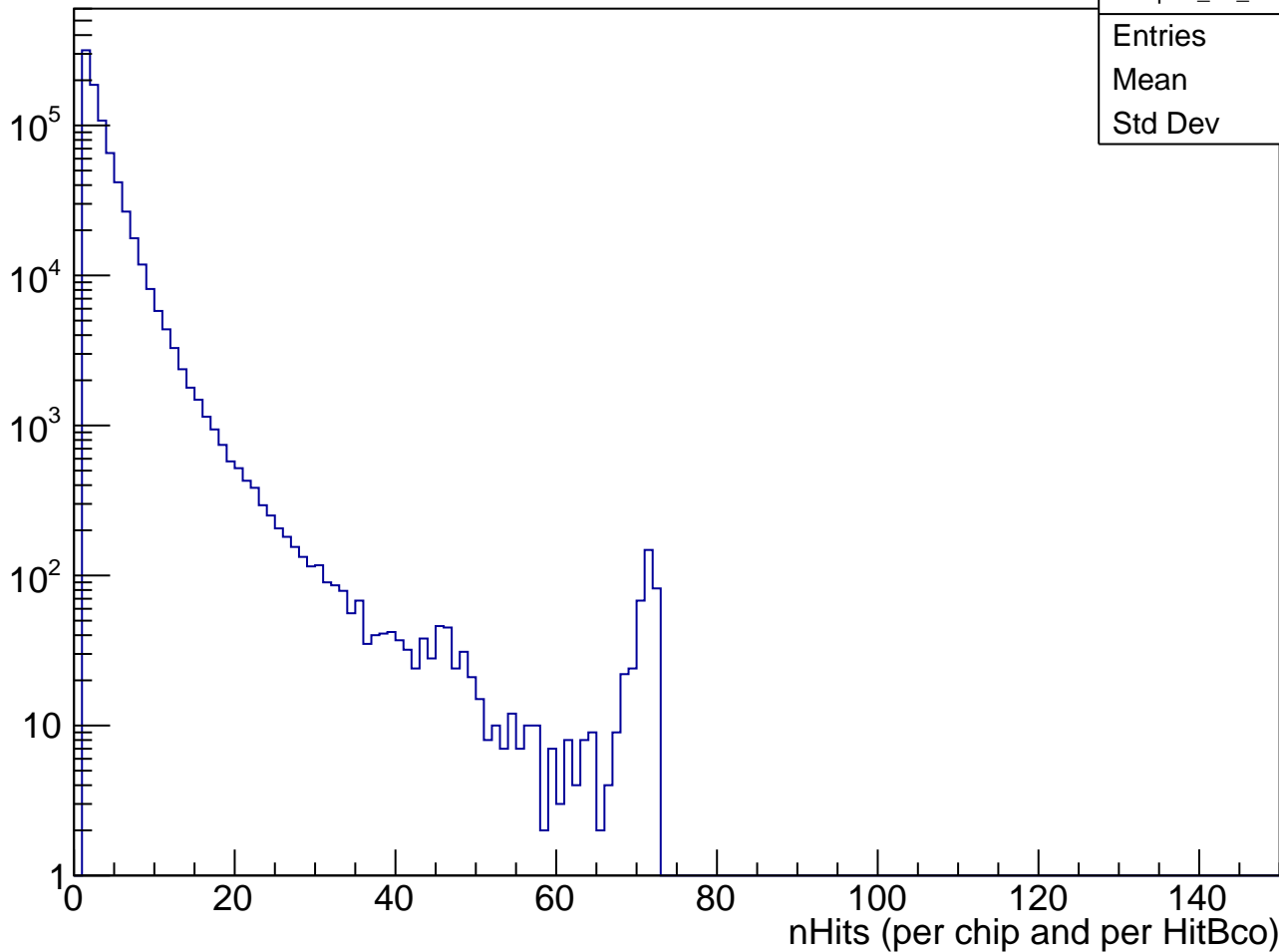
Entries



nChipHit_F1_Fch7_Chip21	
Entries	817995
Mean	2.837
Std Dev	3.347

nChipHit_F1_Fch7_Chip22

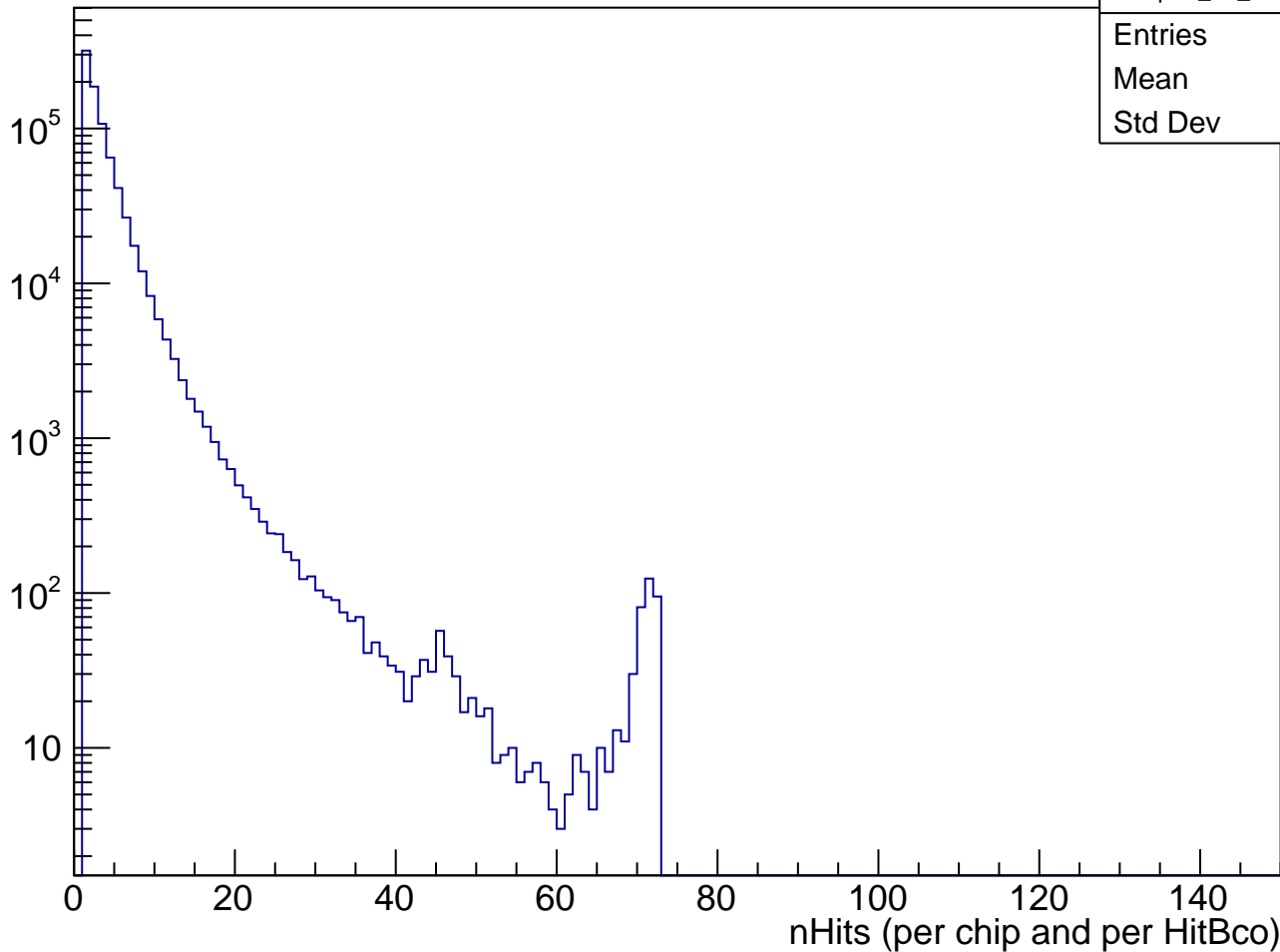
Entries



nChipHit_F1_Fch7_Chip22	
Entries	809083
Mean	2.901
Std Dev	3.377

nChipHit_F1_Fch7_Chip23

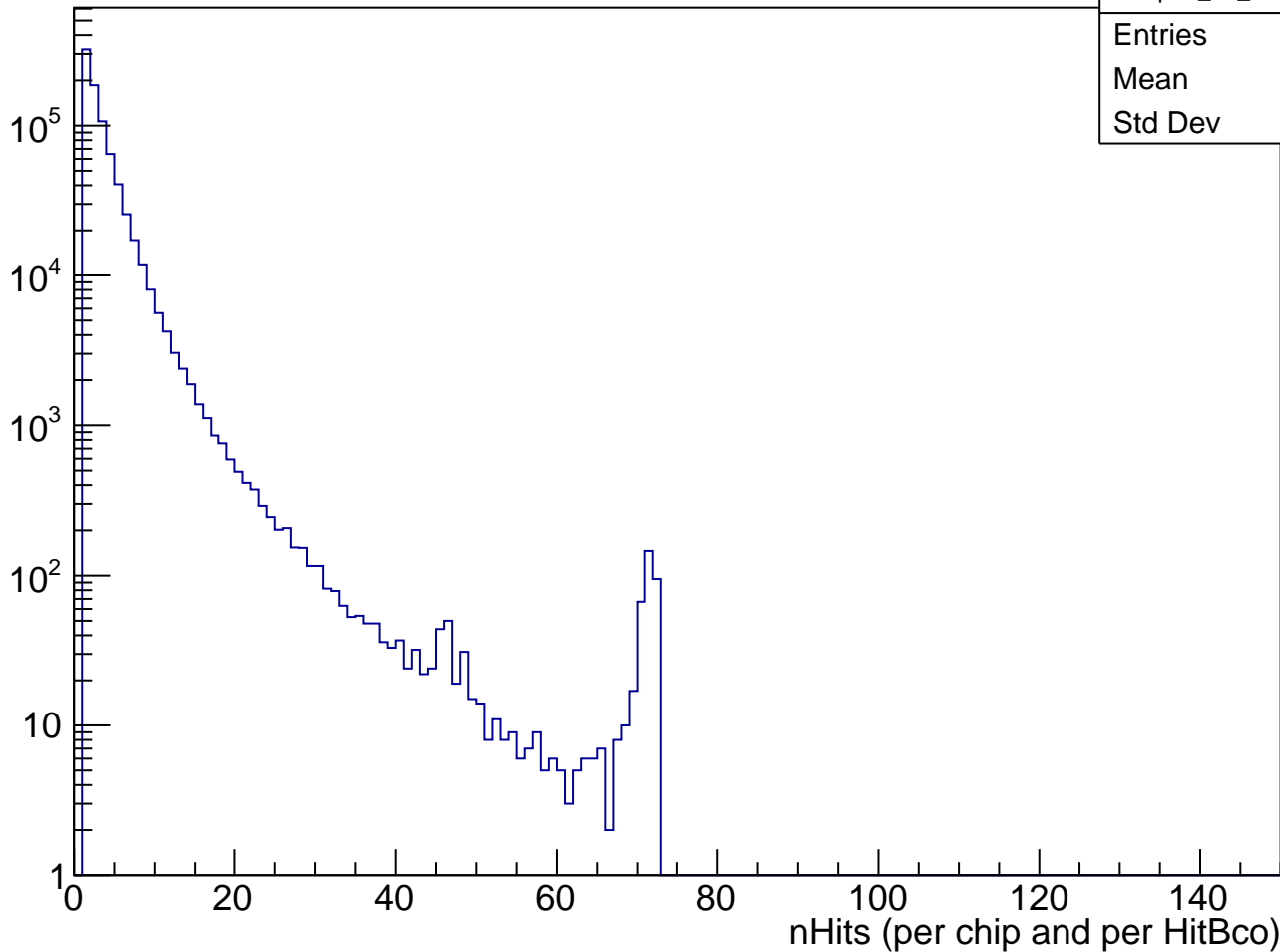
Entries



nChipHit_F1_Fch7_Chip23	
Entries	809189
Mean	2.897
Std Dev	3.385

nChipHit_F1_Fch7_Chip24

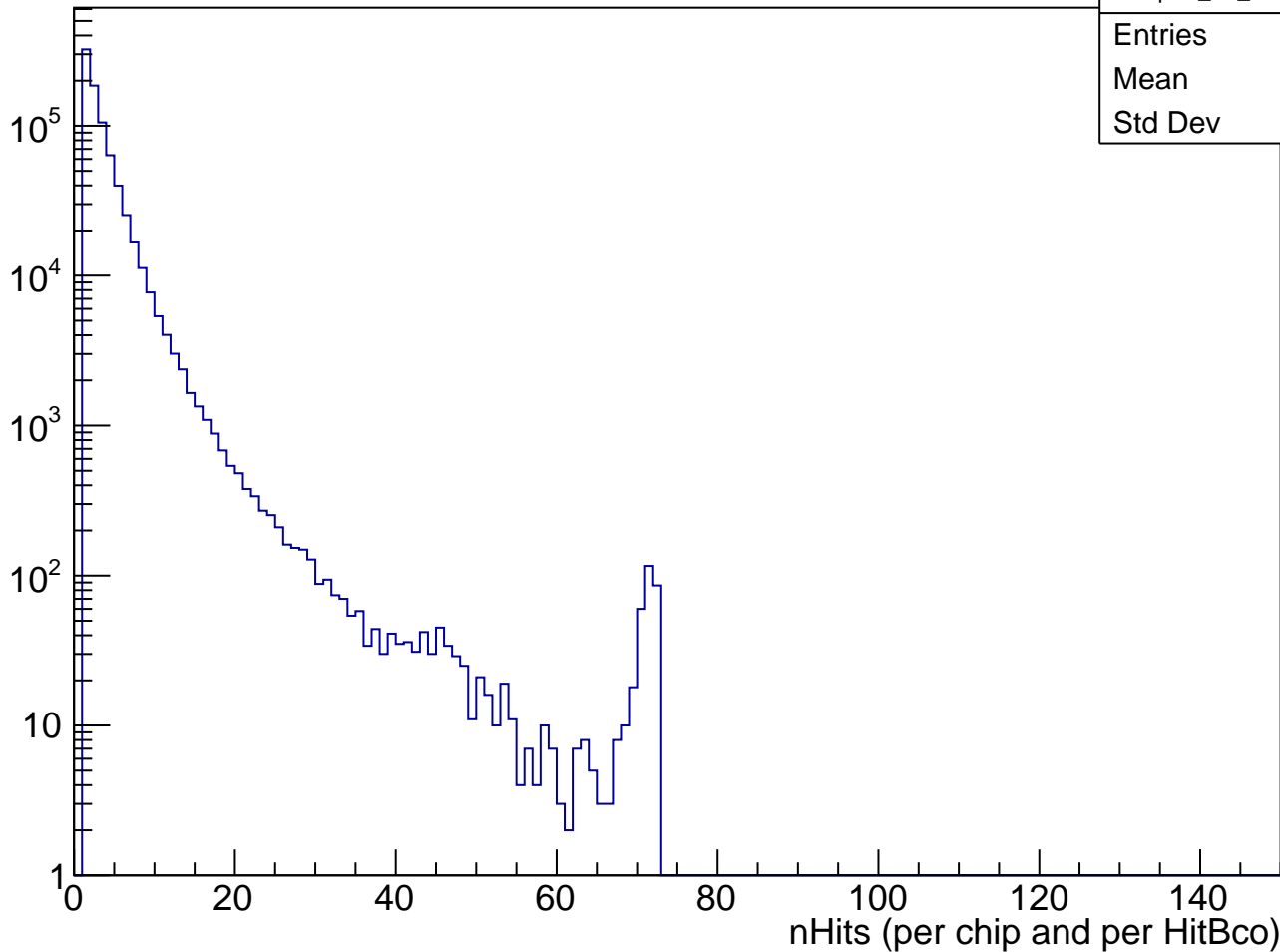
Entries



nChipHit_F1_Fch7_Chip24	
Entries	807855
Mean	2.865
Std Dev	3.339

nChipHit_F1_Fch7_Chip25

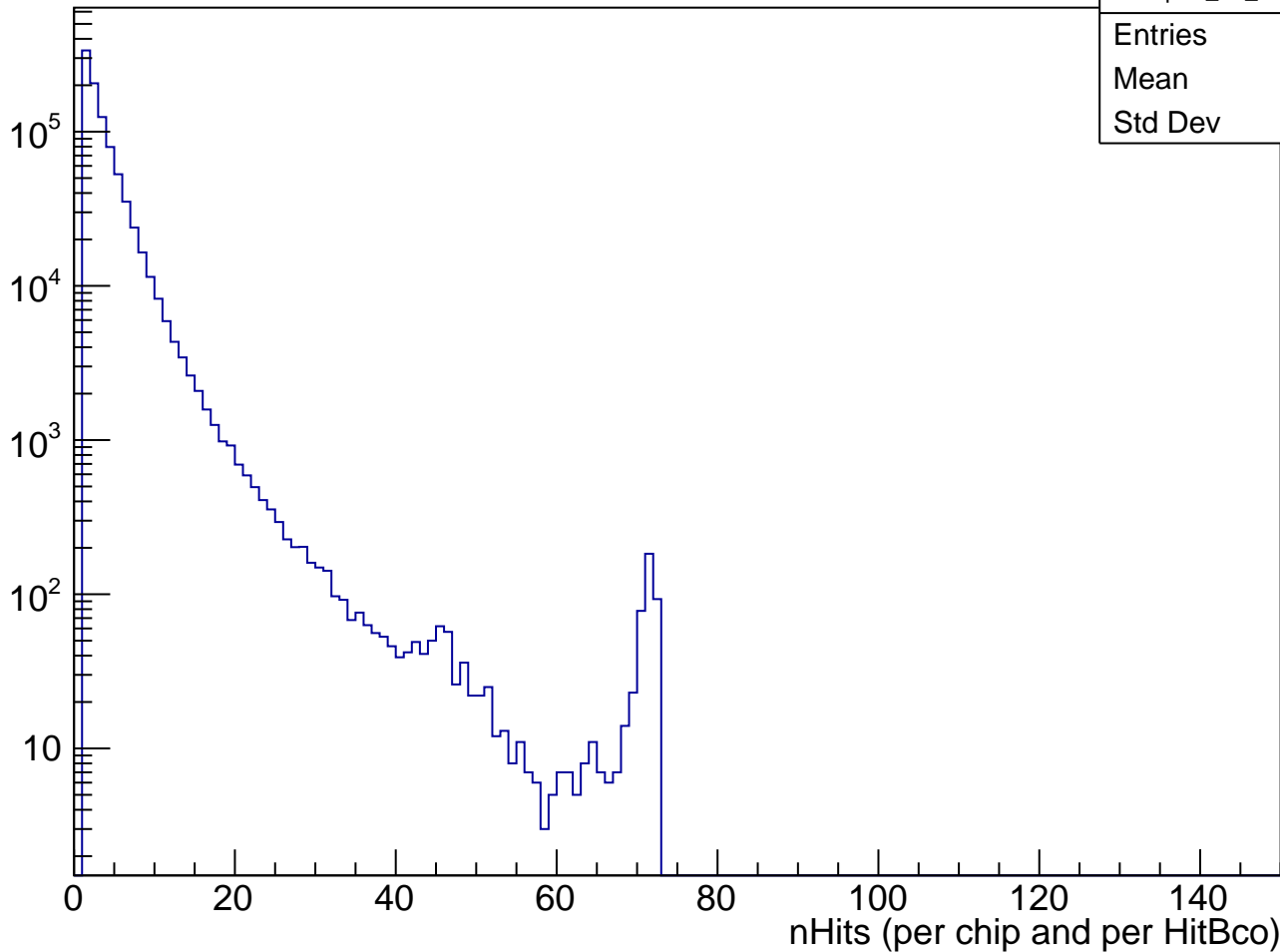
Entries



nChipHit_F1_Fch7_Chip25	
Entries	803188
Mean	2.833
Std Dev	3.287

nChipHit_F1_Fch7_Chip3

Entries



nChipHit_F1_Fch7_Chip3

Entries 923066

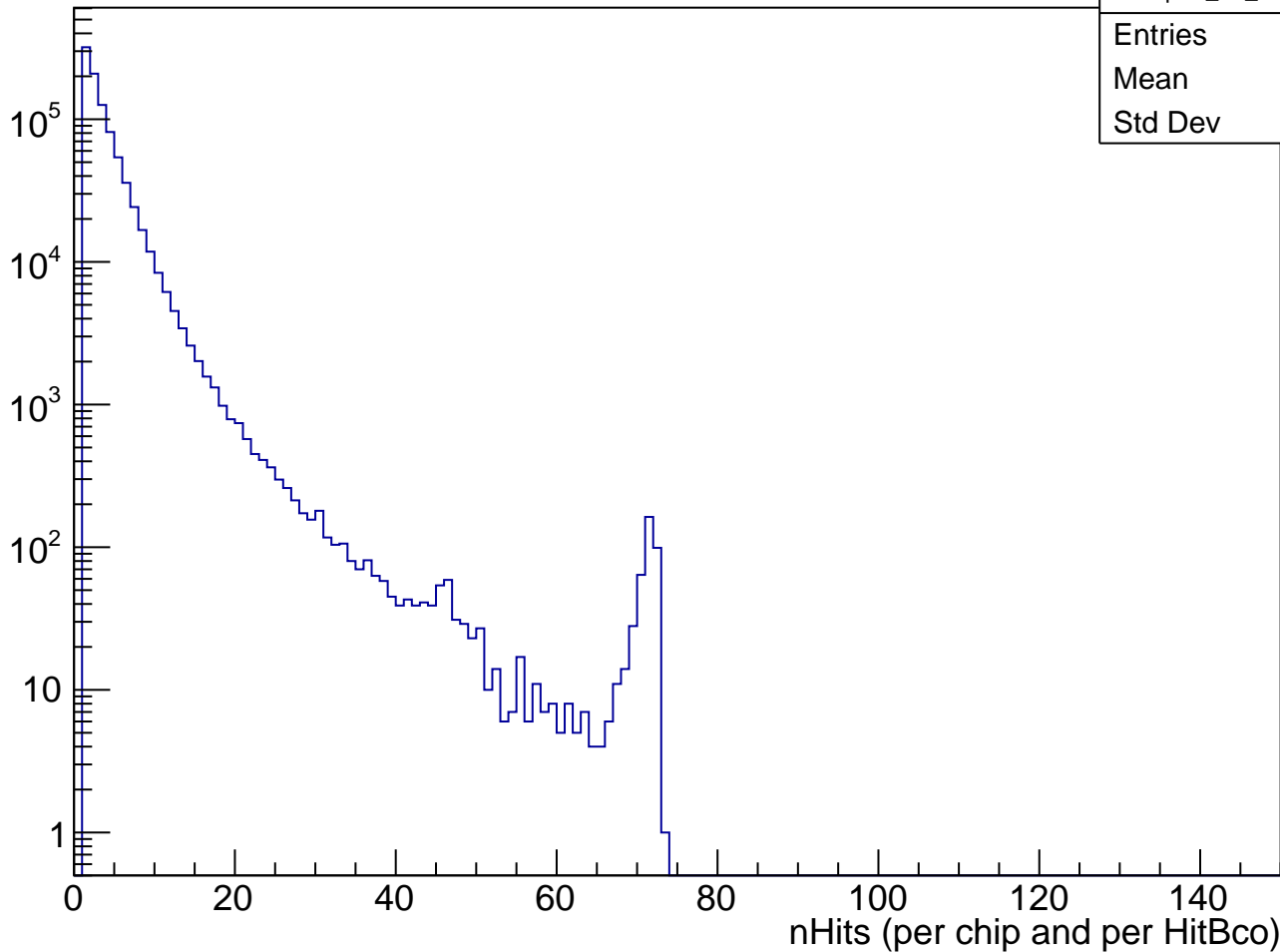
Mean 3.119

Std Dev 3.563

nHits (per chip and per HitBco)

nChipHit_F1_Fch7_Chip4

Entries



nChipHit_F1_Fch7_Chip4

Entries 915702

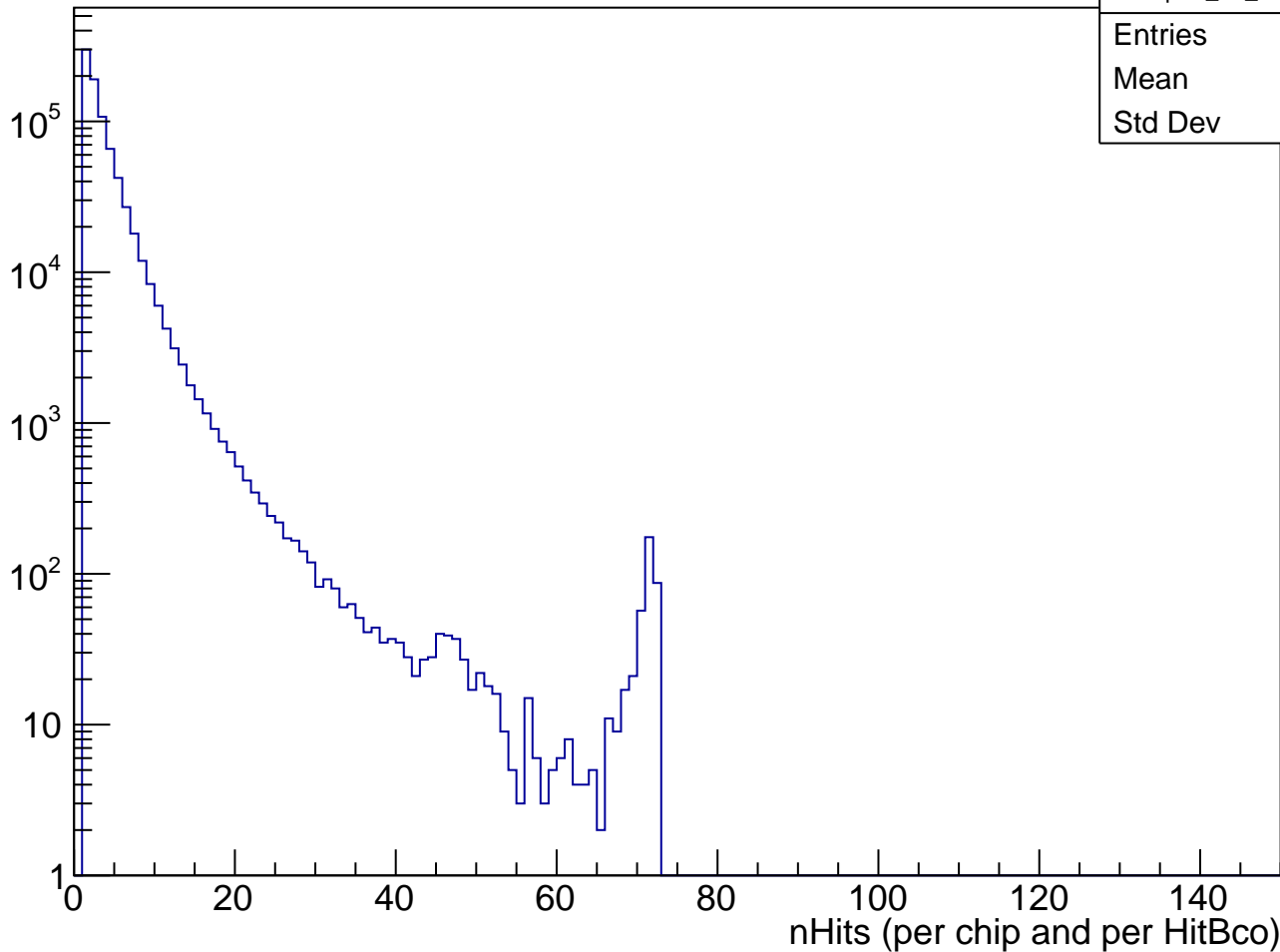
Mean 3.167

Std Dev 3.556

nHits (per chip and per HitBco)

nChipHit_F1_Fch7_Chip5

Entries



nChipHit_F1_Fch7_Chip5

Entries 796947

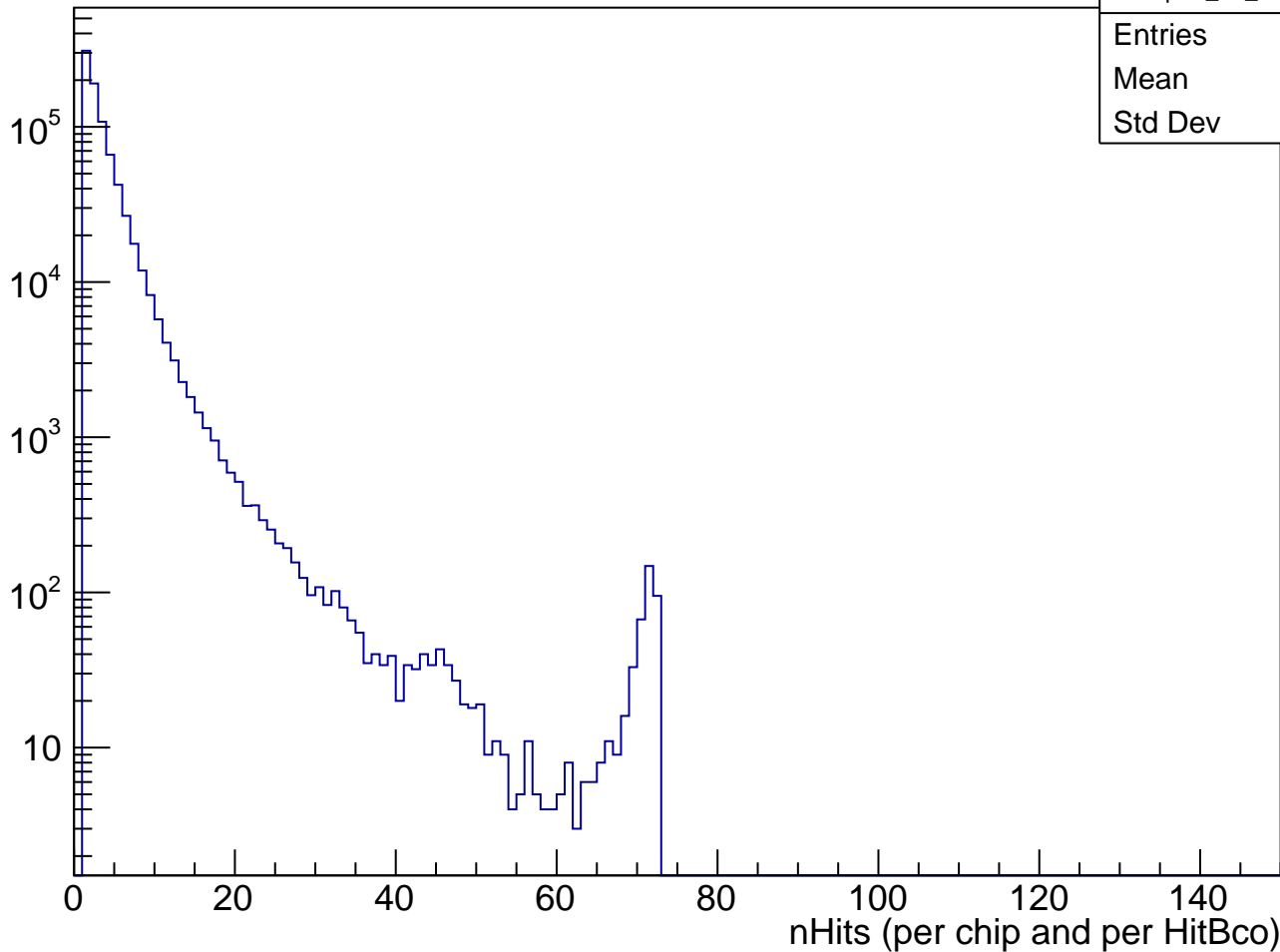
Mean 2.942

Std Dev 3.395

nHits (per chip and per HitBco)

nChipHit_F1_Fch7_Chip6

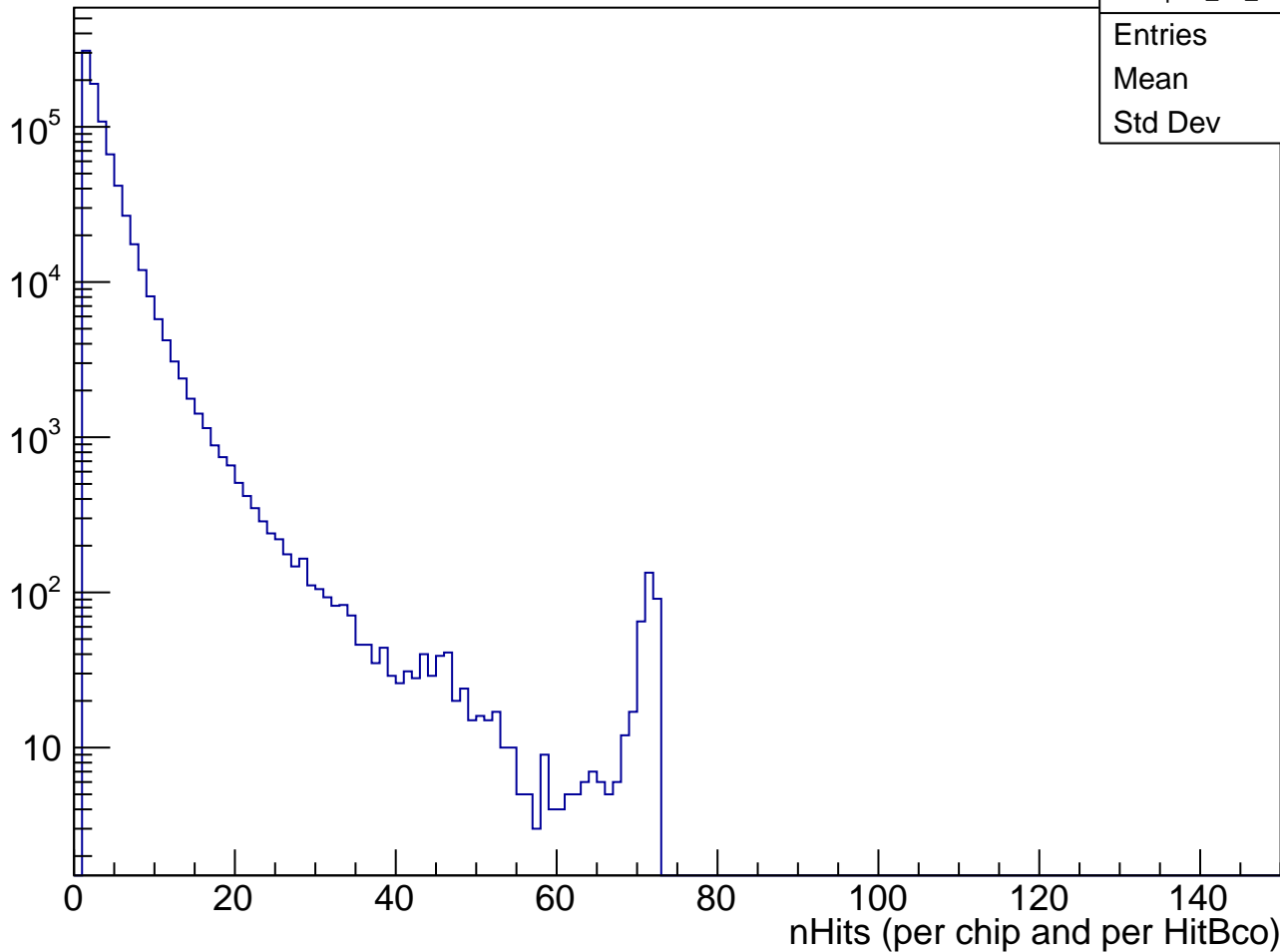
Entries



nChipHit_F1_Fch7_Chip6	
Entries	806615
Mean	2.908
Std Dev	3.368

nChipHit_F1_Fch7_Chip7

Entries

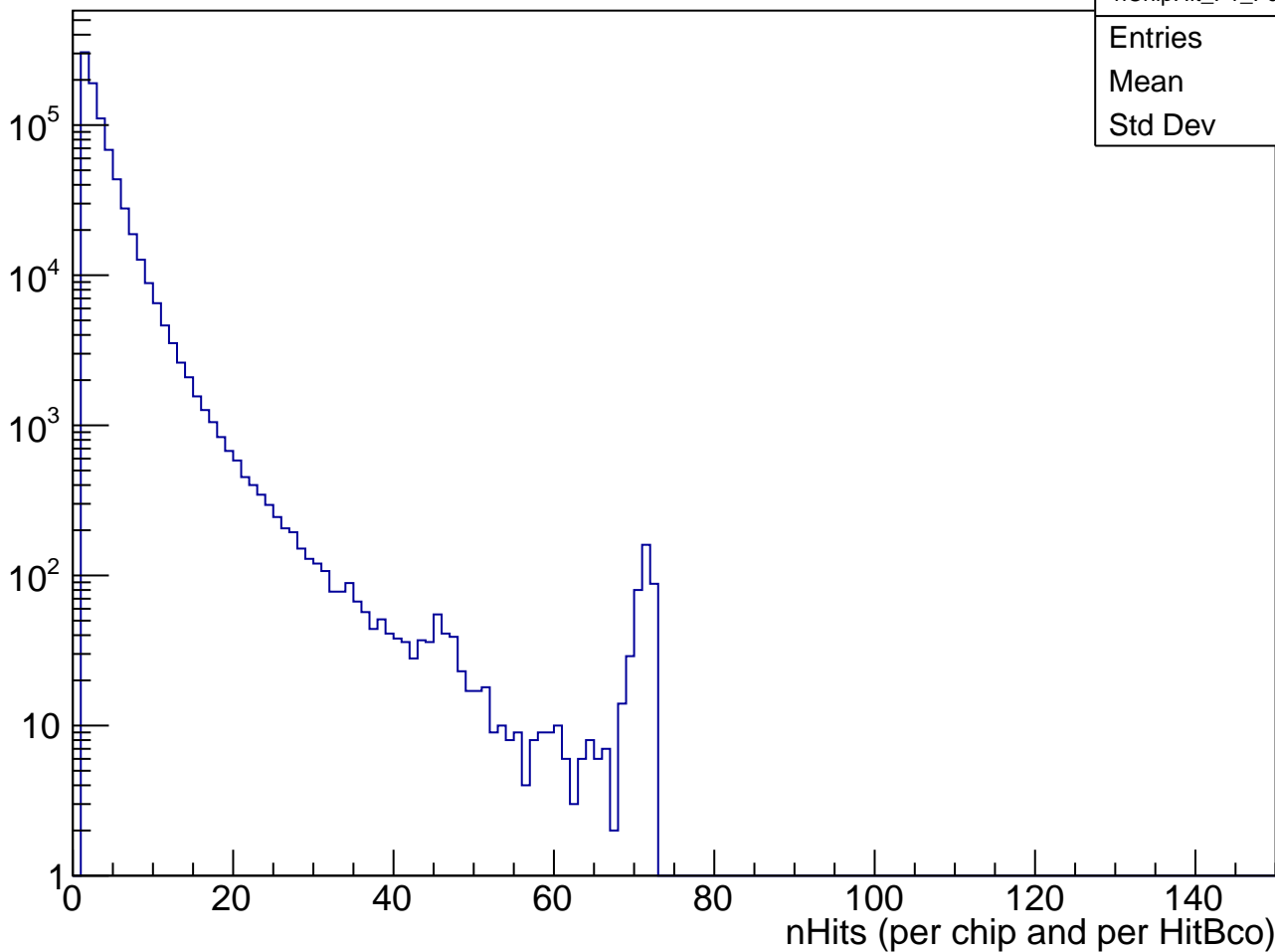


nChipHit_F1_Fch7_Chip7	
Entries	805534
Mean	2.907
Std Dev	3.338

nHits (per chip and per HitBco)

nChipHit_F1_Fch7_Chip8

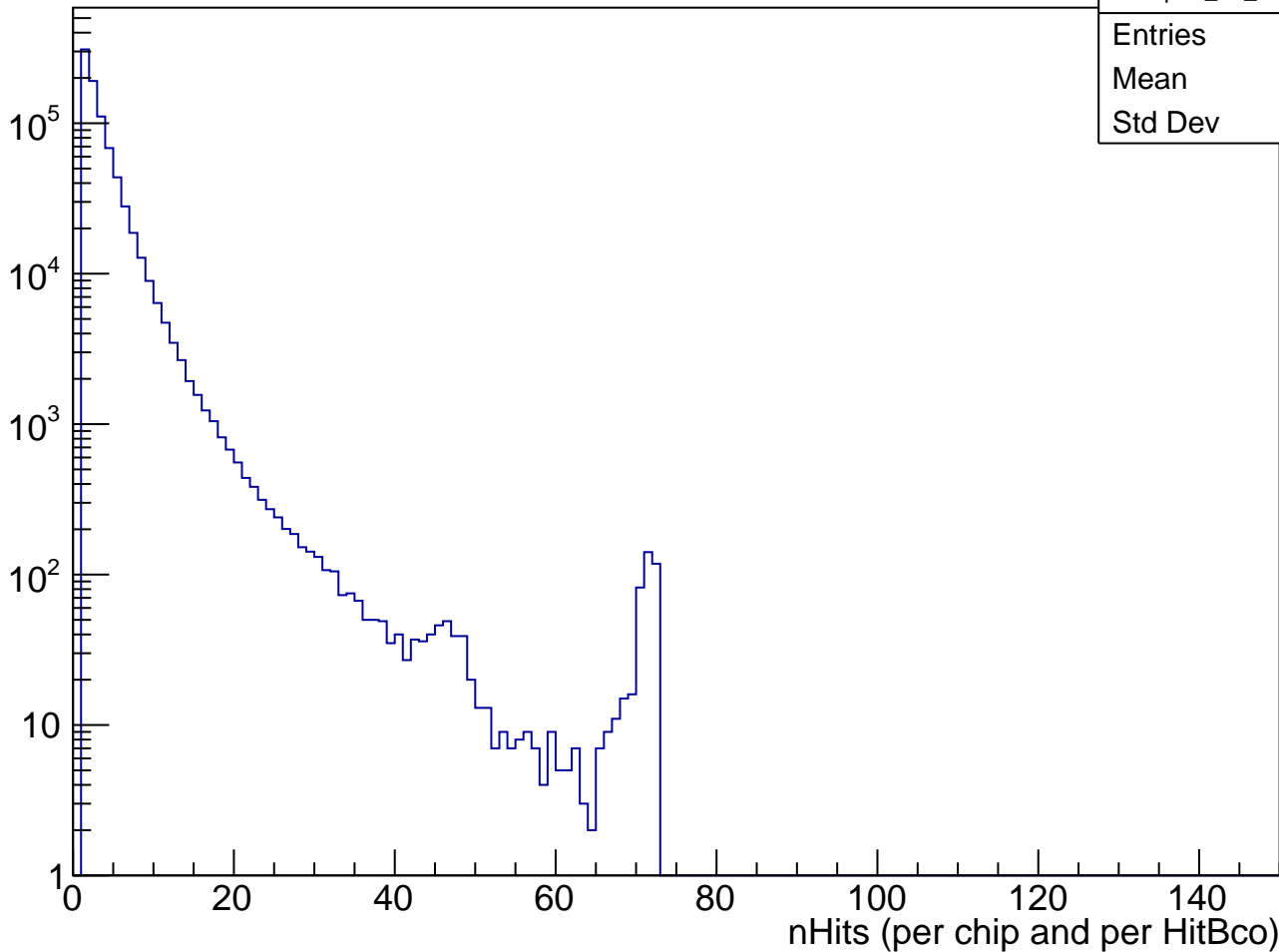
Entries



nChipHit_F1_Fch7_Chip8	
Entries	815653
Mean	2.996
Std Dev	3.49

nChipHit_F1_Fch7_Chip9

Entries



nChipHit_F1_Fch7_Chip9

Entries 820933

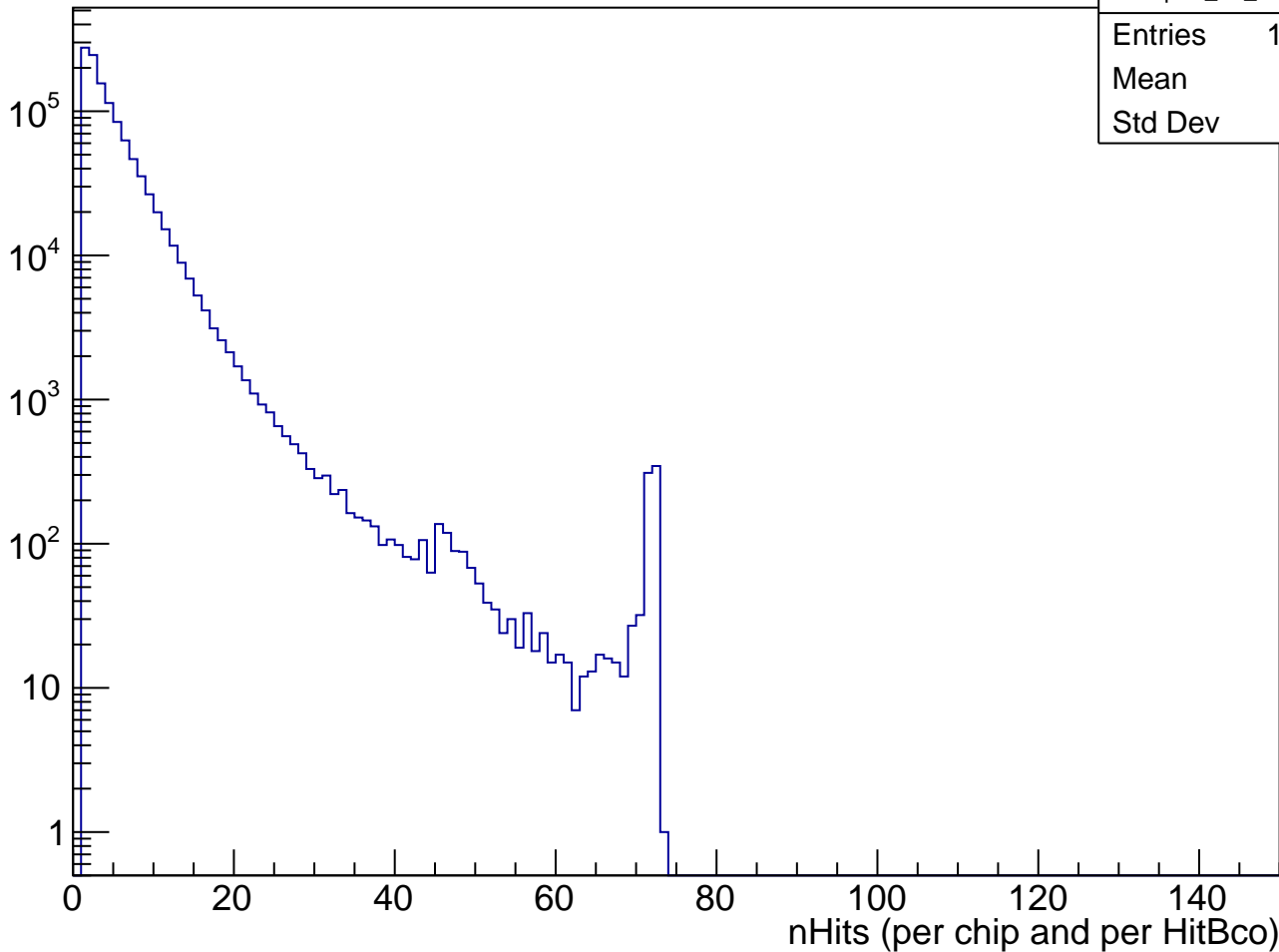
Mean 2.982

Std Dev 3.476

nHits (per chip and per HitBco)

nChipHit_F1_Fch8_Chip0

Entries



nChipHit_F1_Fch8_Chip0

Entries 1139338

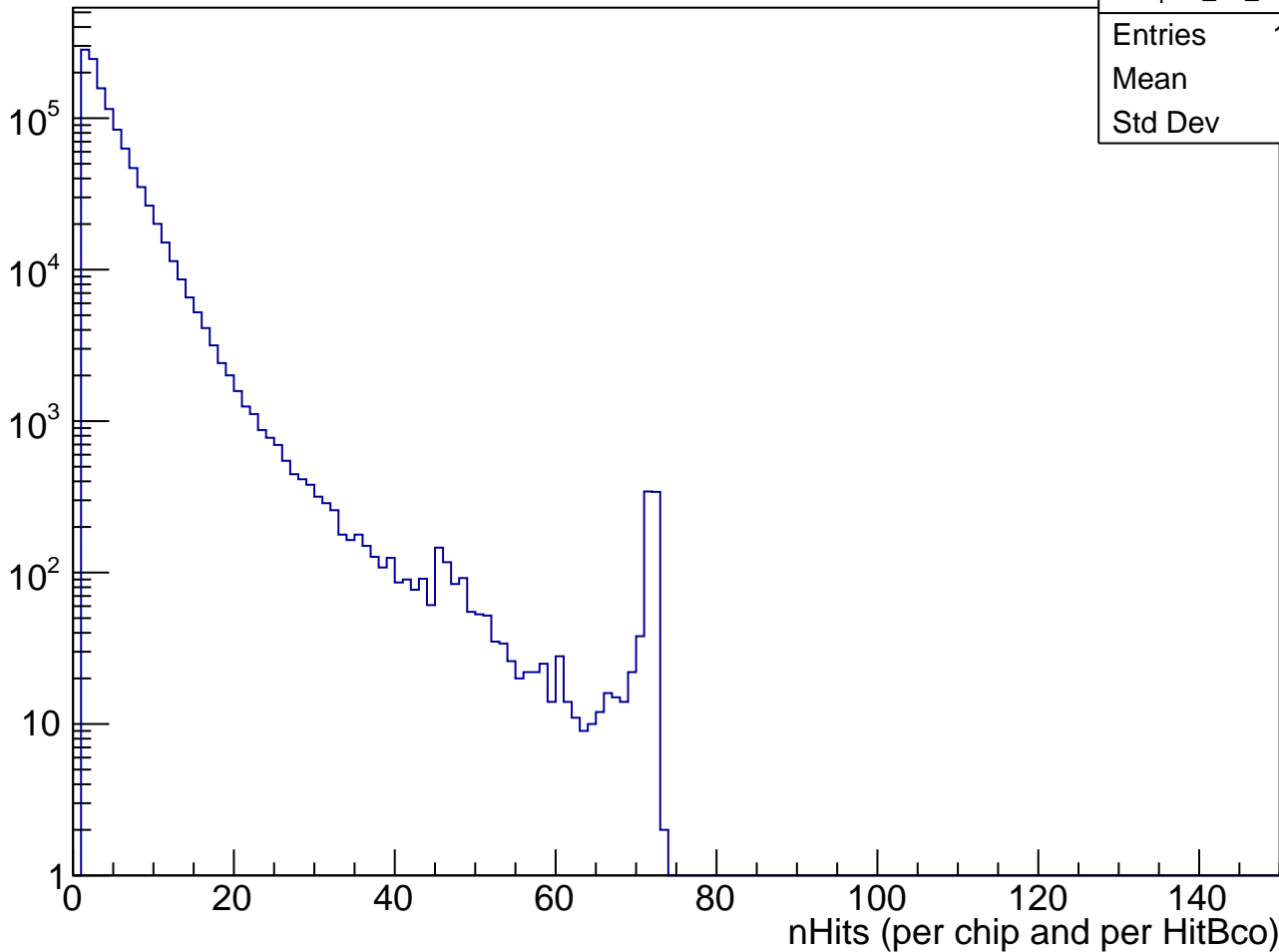
Mean 4.136

Std Dev 4.455

nHits (per chip and per HitBco)

nChipHit_F1_Fch8_Chip1

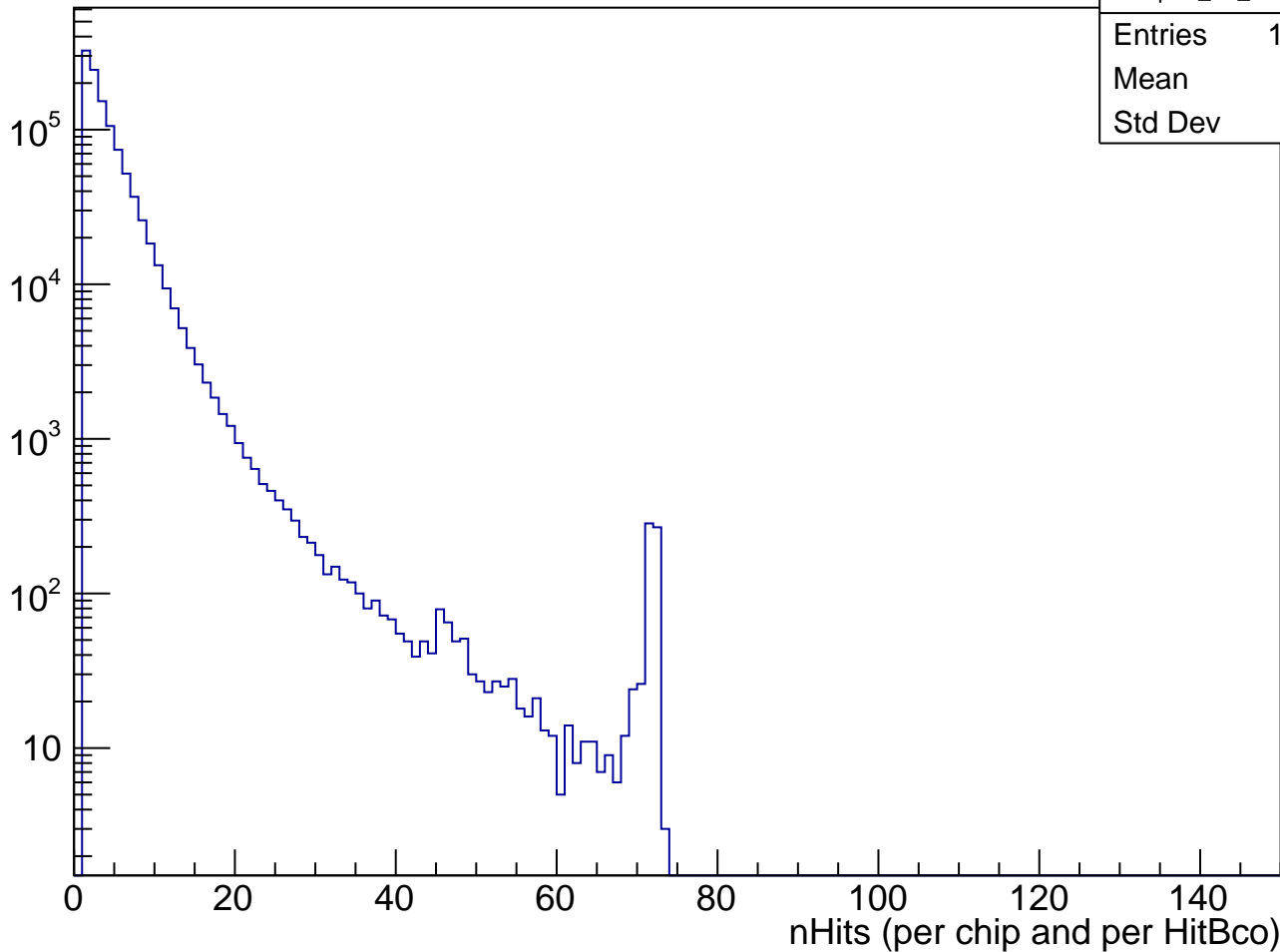
Entries



nChipHit_F1_Fch8_Chip1	
Entries	1148091
Mean	4.101
Std Dev	4.442

nChipHit_F1_Fch8_Chip10

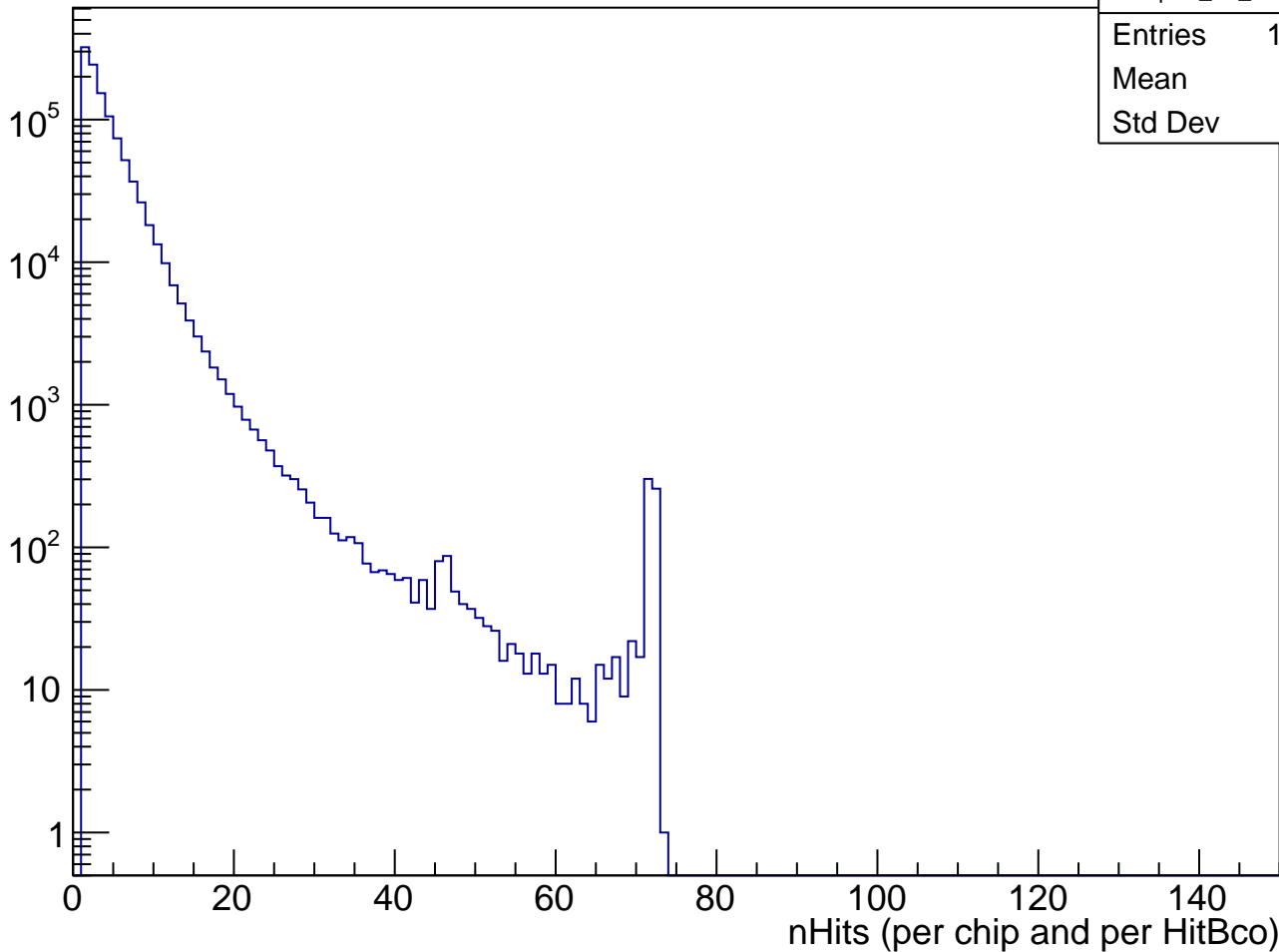
Entries



nChipHit_F1_Fch8_Chip10	
Entries	1090017
Mean	3.524
Std Dev	3.86

nChipHit_F1_Fch8_Chip11

Entries



nChipHit_F1_Fch8_Chip11

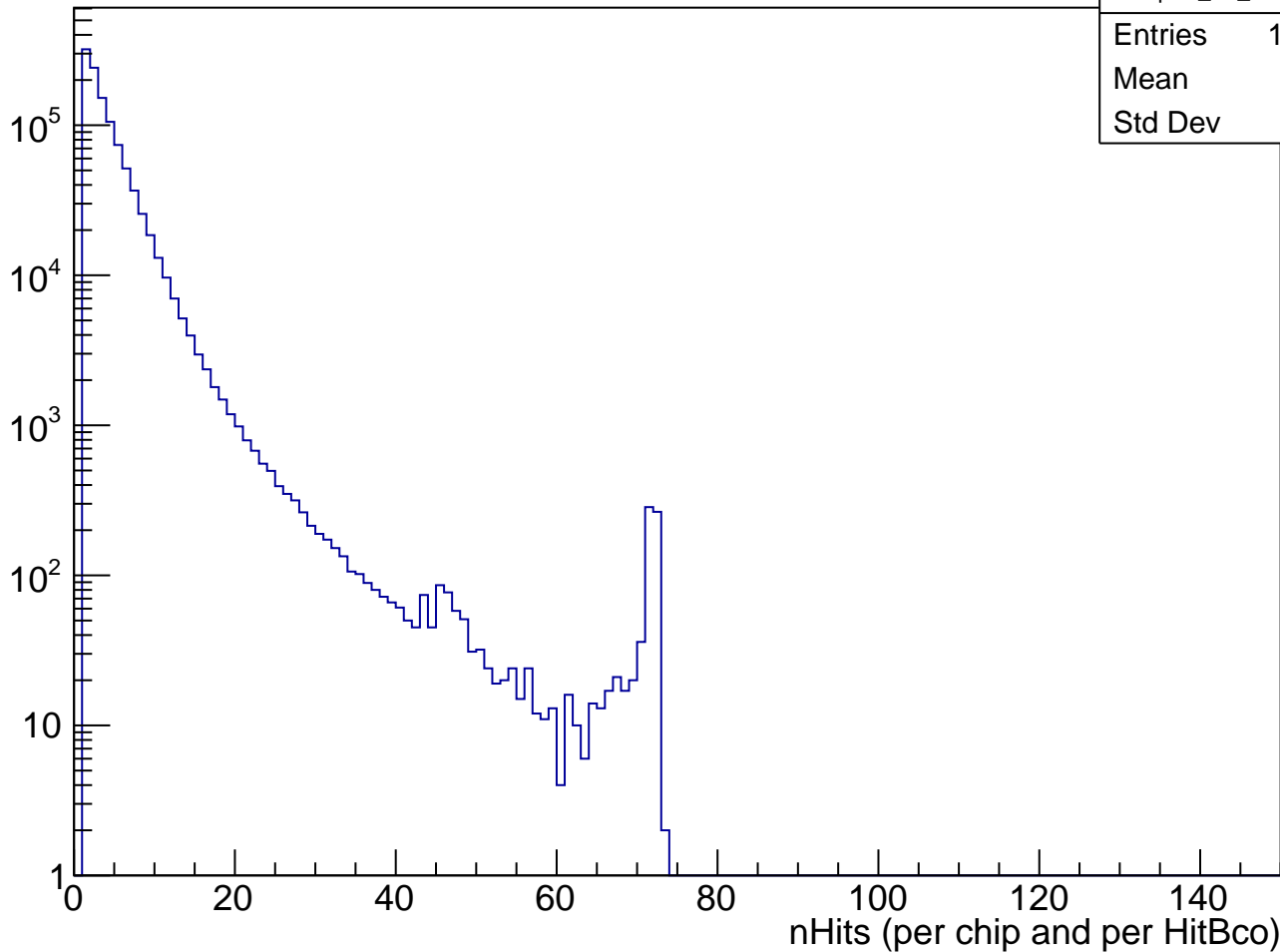
Entries 1086884

Mean 3.537

Std Dev 3.873

nChipHit_F1_Fch8_Chip12

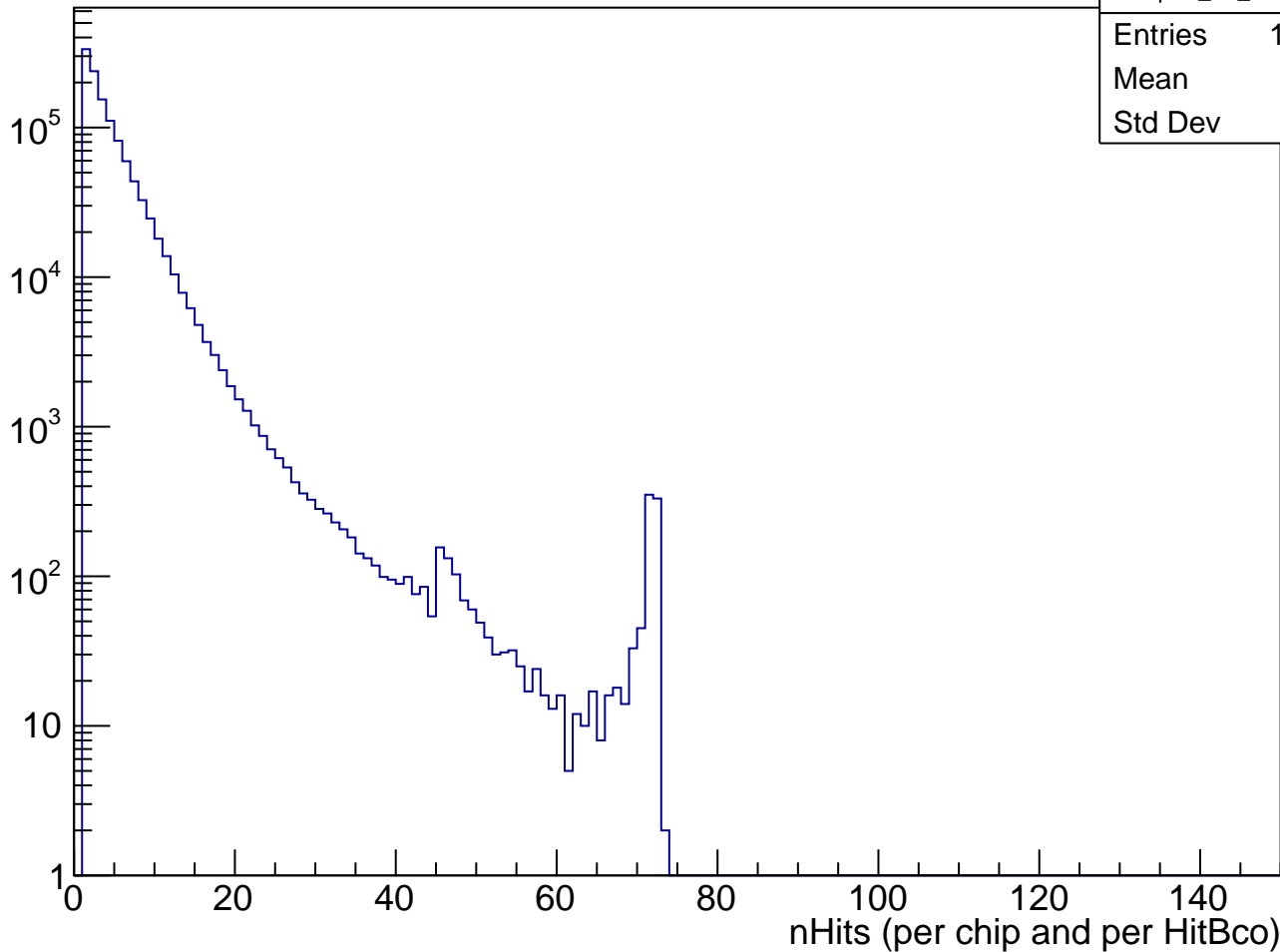
Entries



nChipHit_F1_Fch8_Chip12	
Entries	1081989
Mean	3.545
Std Dev	3.913

nChipHit_F1_Fch8_Chip13

Entries

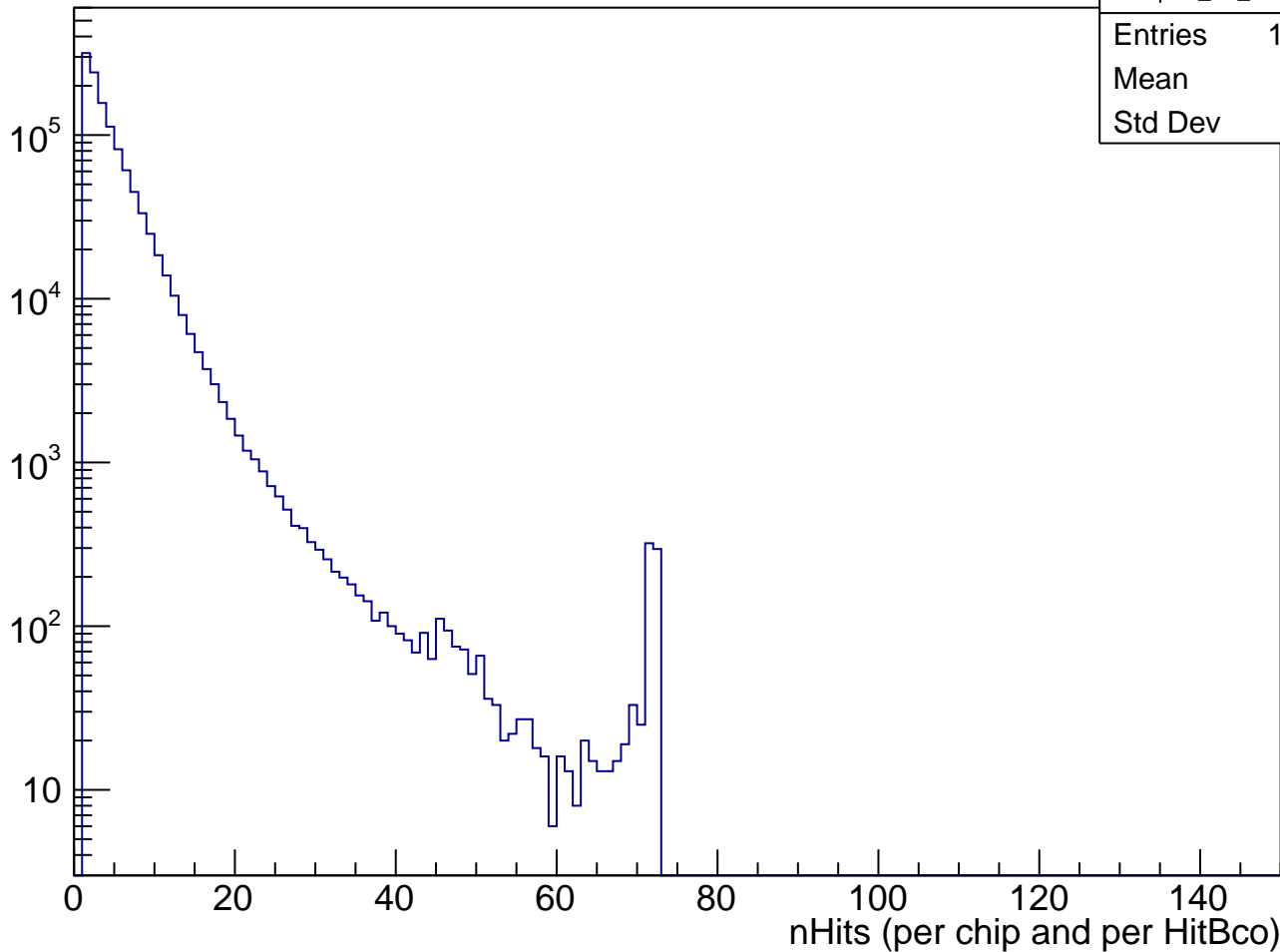


nChipHit_F1_Fch8_Chip13	
Entries	1163260
Mean	3.896
Std Dev	4.365

nHits (per chip and per HitBco)

nChipHit_F1_Fch8_Chip14

Entries

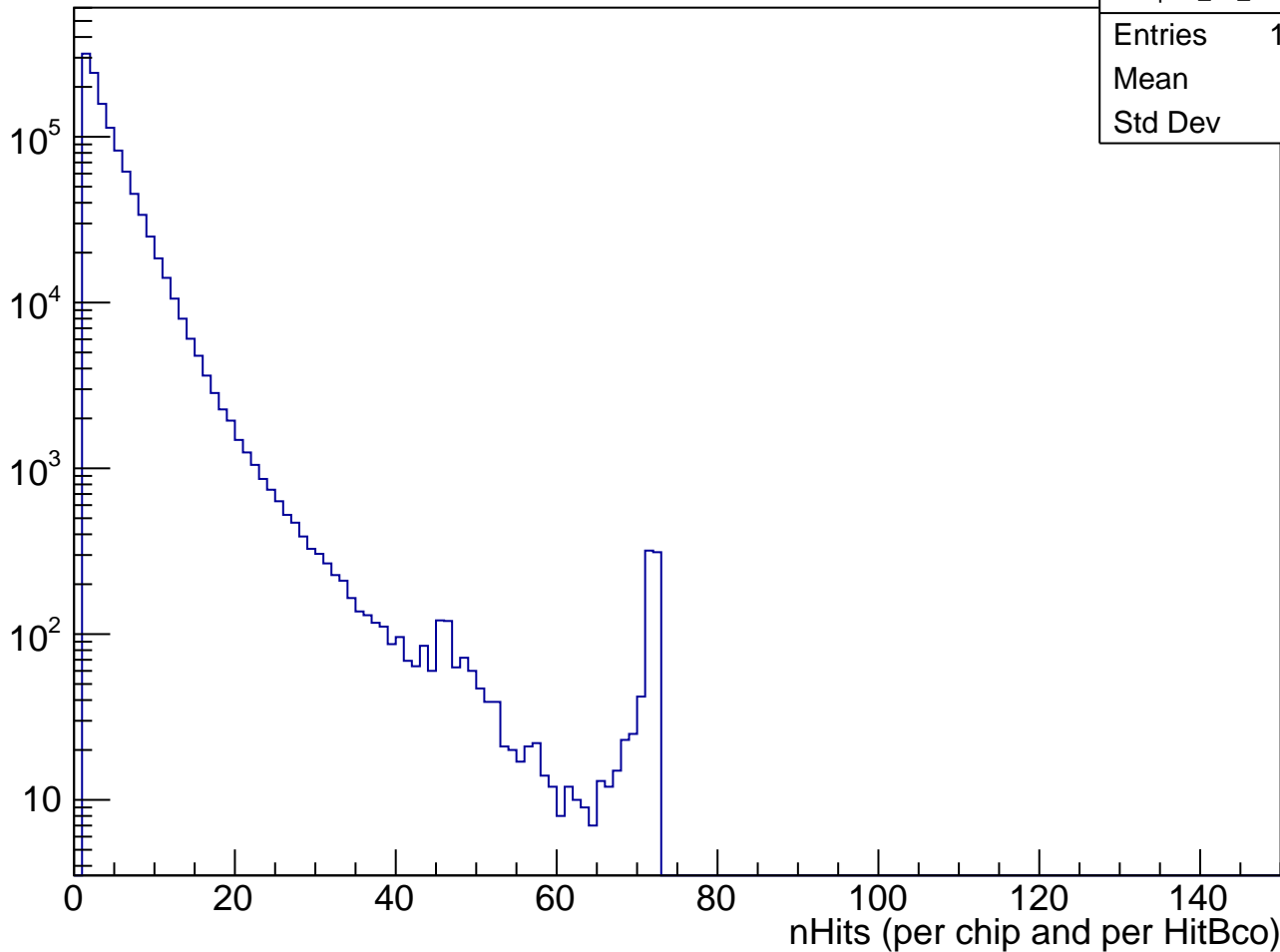


nChipHit_F1_Fch8_Chip14	
Entries	1157382
Mean	3.933
Std Dev	4.309

nHits (per chip and per HitBco)

nChipHit_F1_Fch8_Chip15

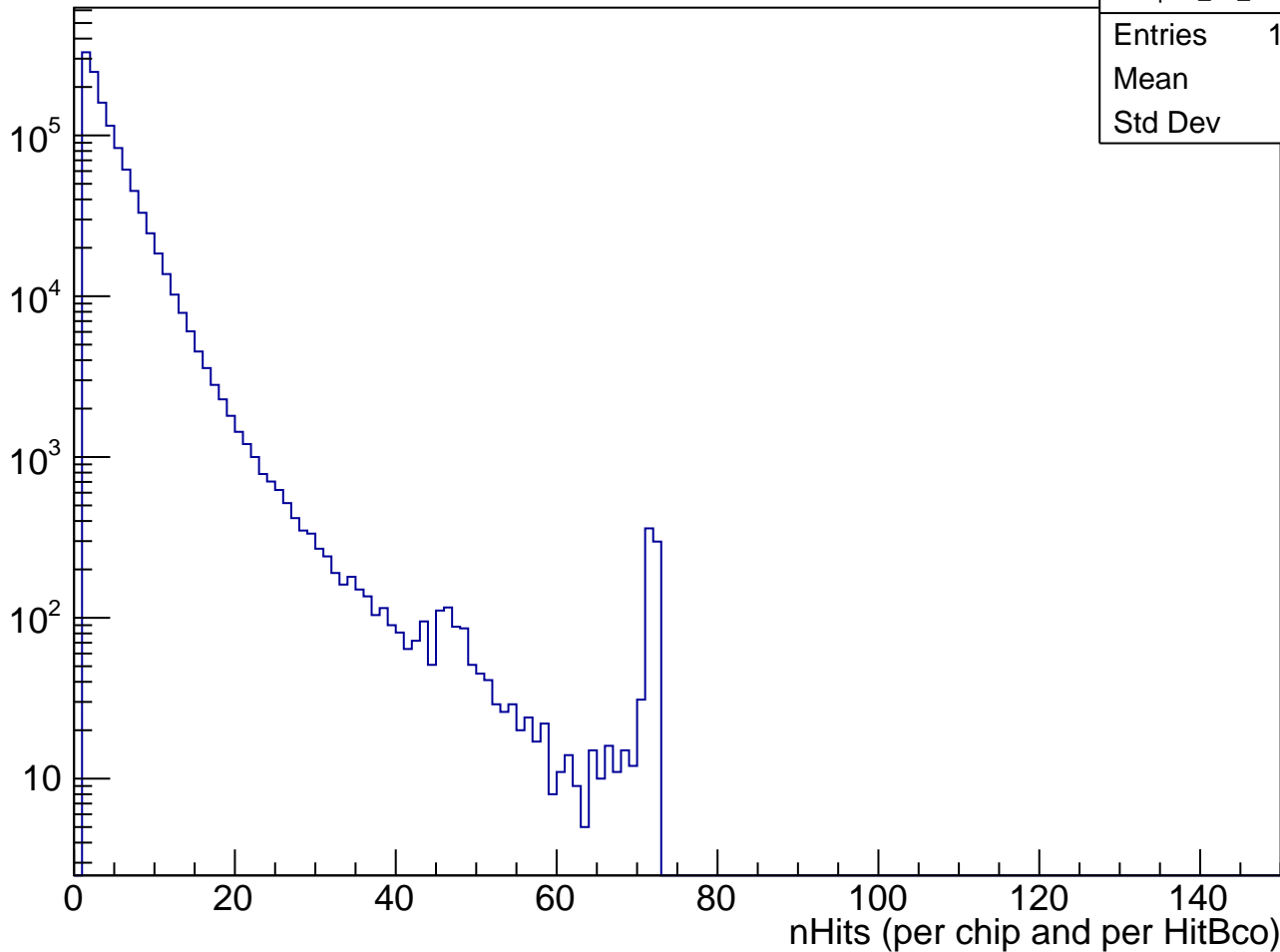
Entries



nChipHit_F1_Fch8_Chip15	
Entries	1164230
Mean	3.935
Std Dev	4.304

nChipHit_F1_Fch8_Chip16

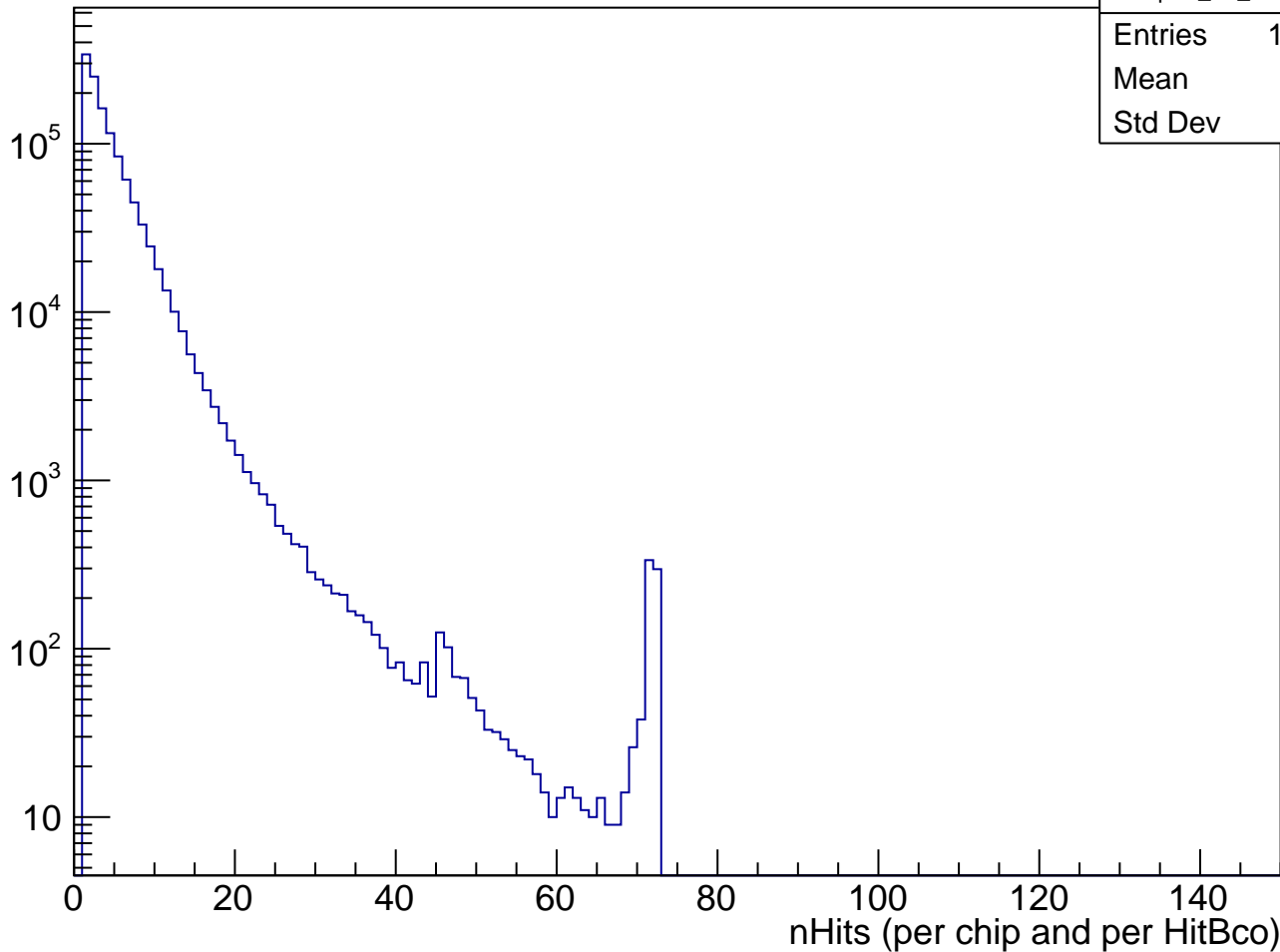
Entries



nChipHit_F1_Fch8_Chip16	
Entries	1181497
Mean	3.874
Std Dev	4.251

nChipHit_F1_Fch8_Chip17

Entries

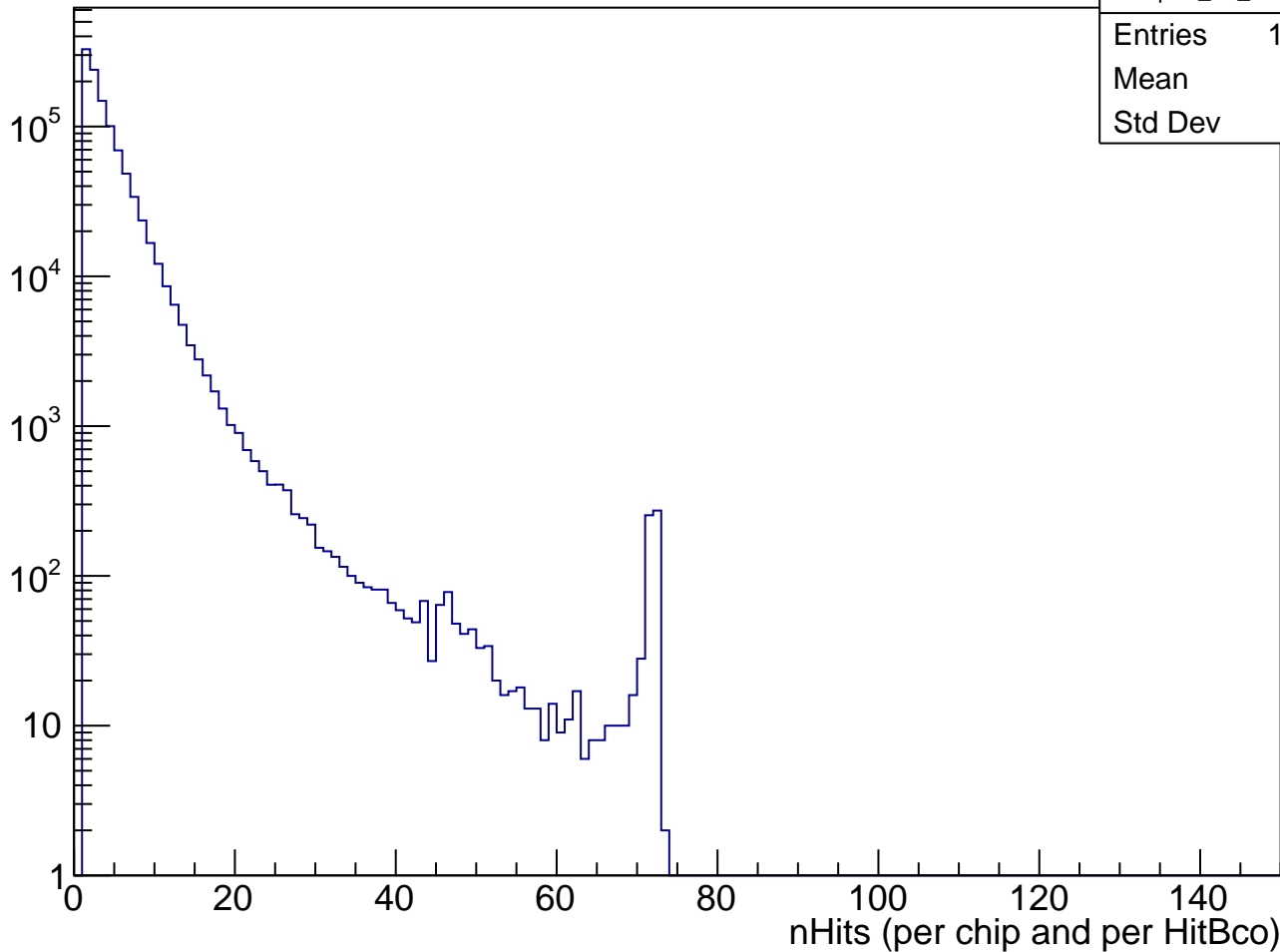


nChipHit_F1_Fch8_Chip17	
Entries	1195186
Mean	3.821
Std Dev	4.197

nHits (per chip and per HitBco)

nChipHit_F1_Fch8_Chip18

Entries

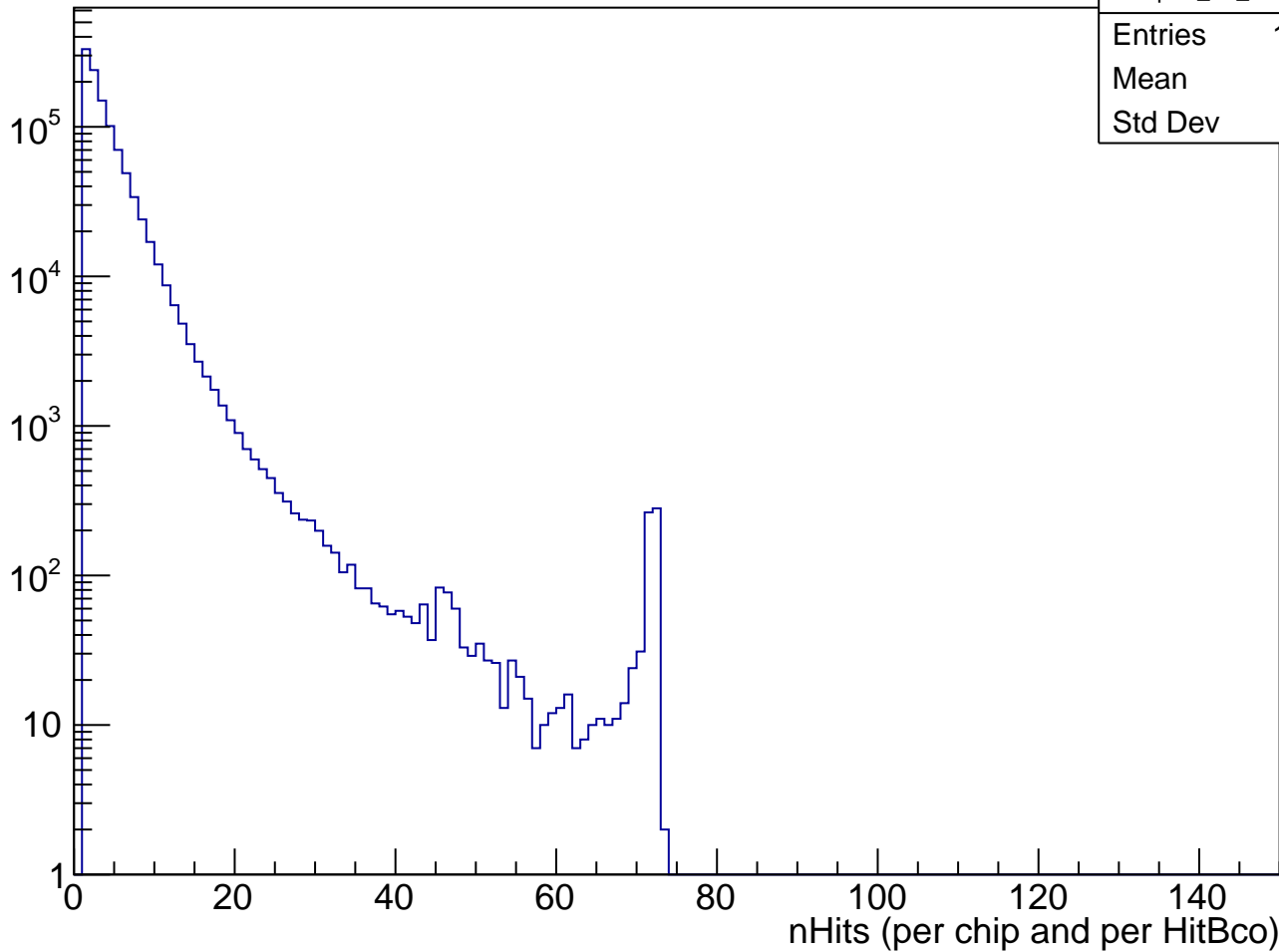


nChipHit_F1_Fch8_Chip18	
Entries	1059978
Mean	3.436
Std Dev	3.815

nHits (per chip and per HitBco)

nChipHit_F1_Fch8_Chip19

Entries

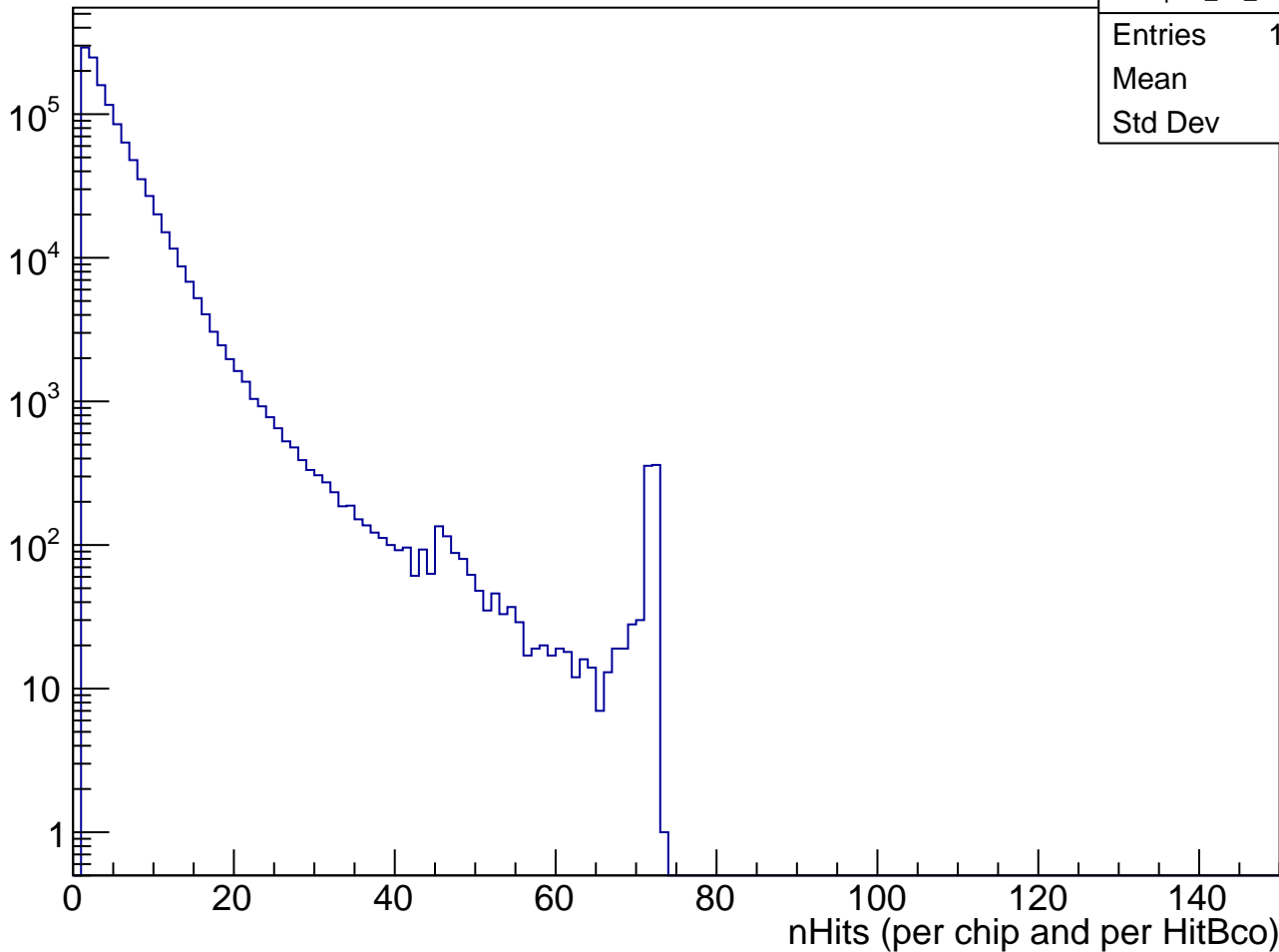


nChipHit_F1_Fch8_Chip19	
Entries	1067351
Mean	3.44
Std Dev	3.833

nHits (per chip and per HitBco)

nChipHit_F1_Fch8_Chip2

Entries



nChipHit_F1_Fch8_Chip2

Entries 1164300

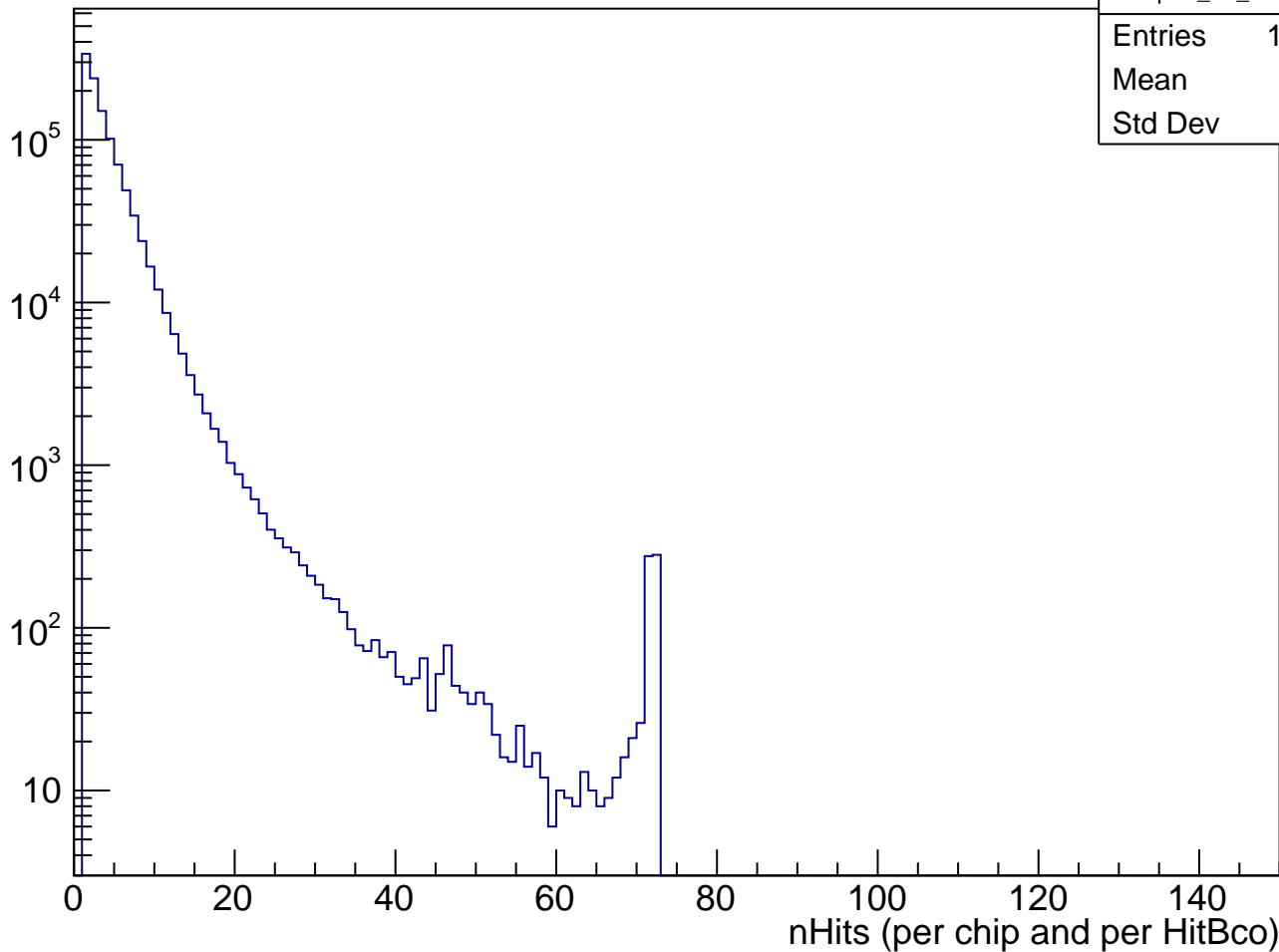
Mean 4.083

Std Dev 4.428

nHits (per chip and per HitBco)

nChipHit_F1_Fch8_Chip20

Entries

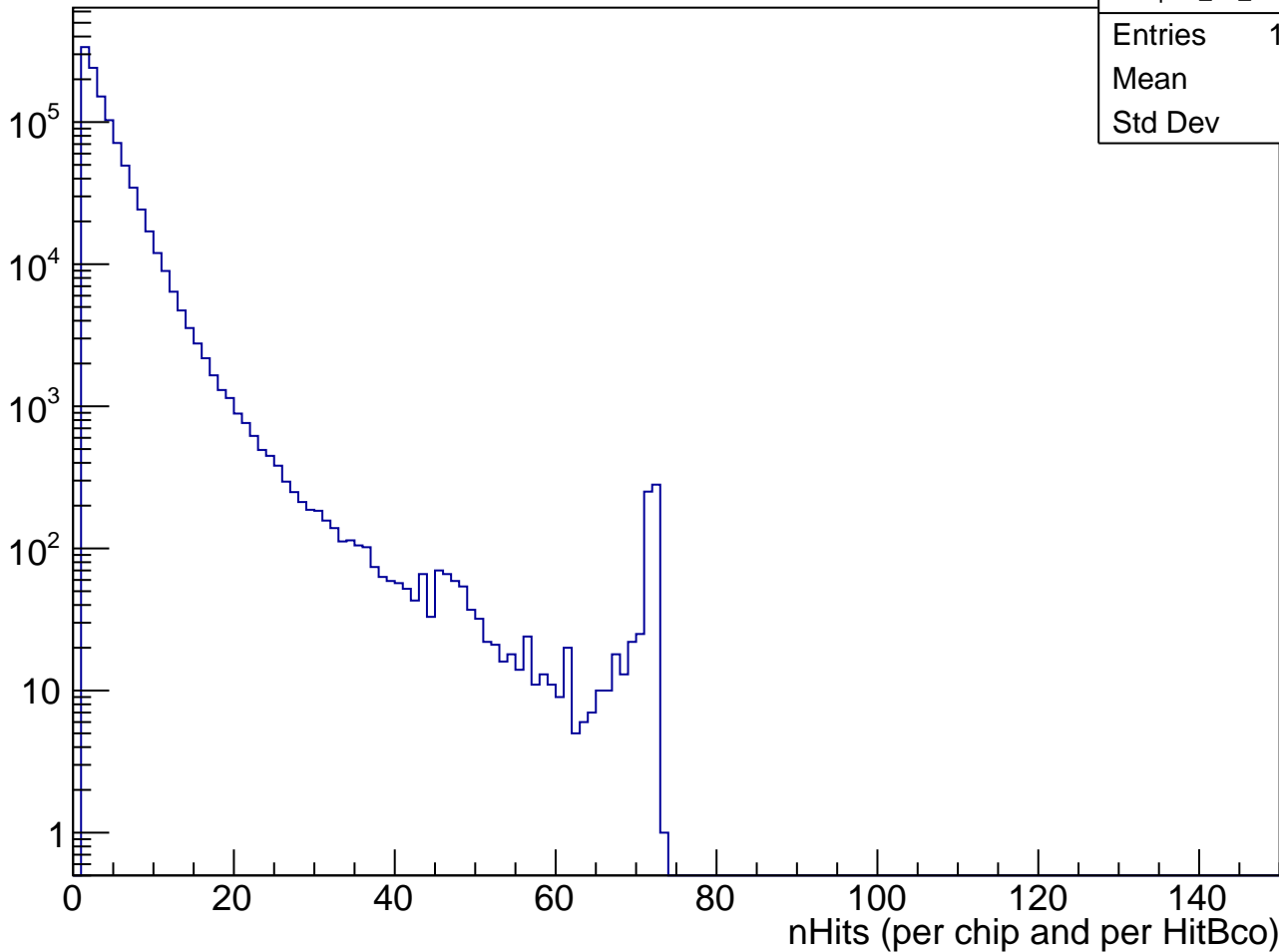


nChipHit_F1_Fch8_Chip20	
Entries	1074464
Mean	3.42
Std Dev	3.813

nHits (per chip and per HitBco)

nChipHit_F1_Fch8_Chip21

Entries



nChipHit_F1_Fch8_Chip21

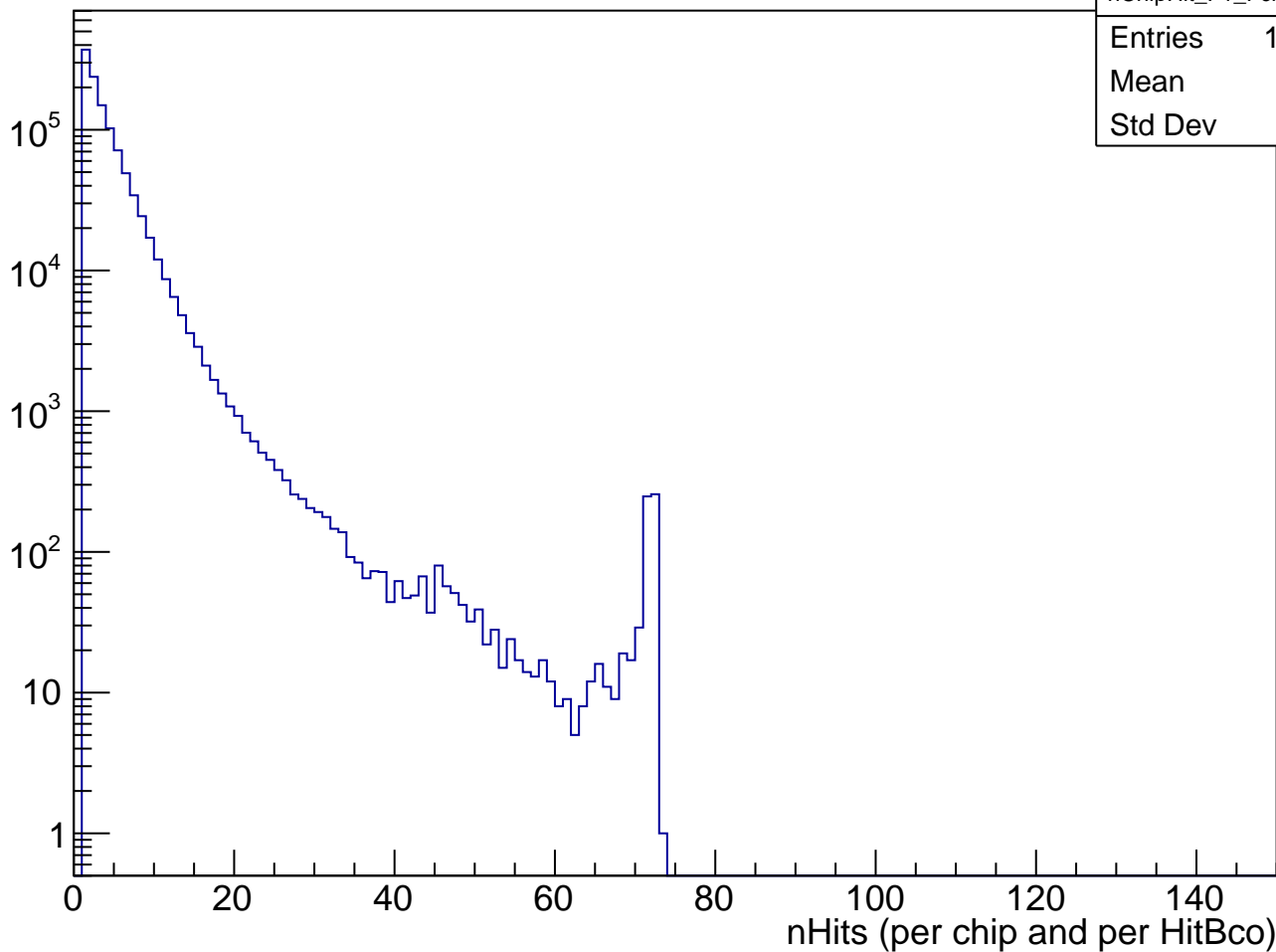
Entries 1080540

Mean 3.43

Std Dev 3.804

nChipHit_F1_Fch8_Chip22

Entries

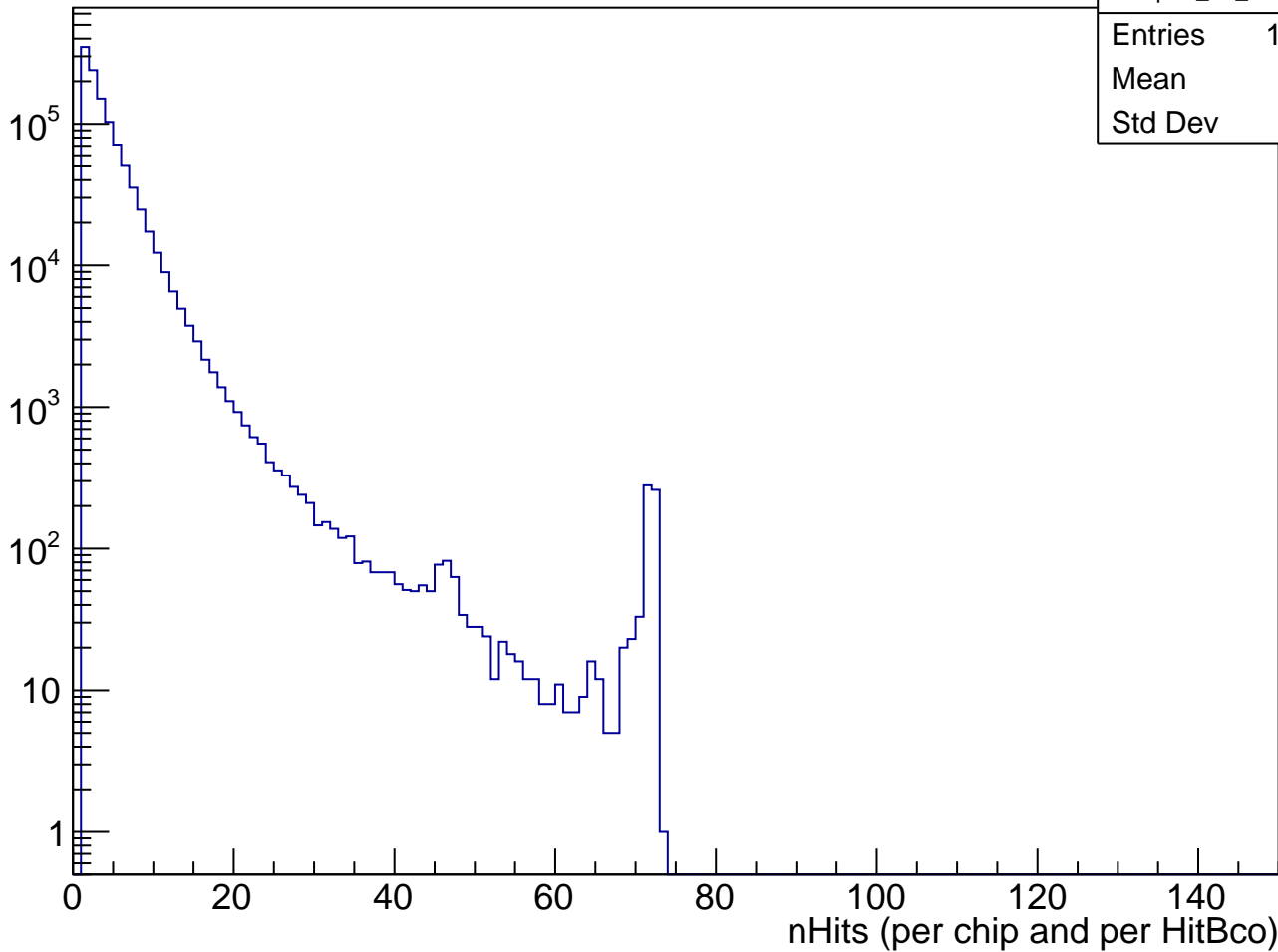


nChipHit_F1_Fch8_Chip22	
Entries	1108452
Mean	3.358
Std Dev	3.764

nHits (per chip and per HitBco)

nChipHit_F1_Fch8_Chip23

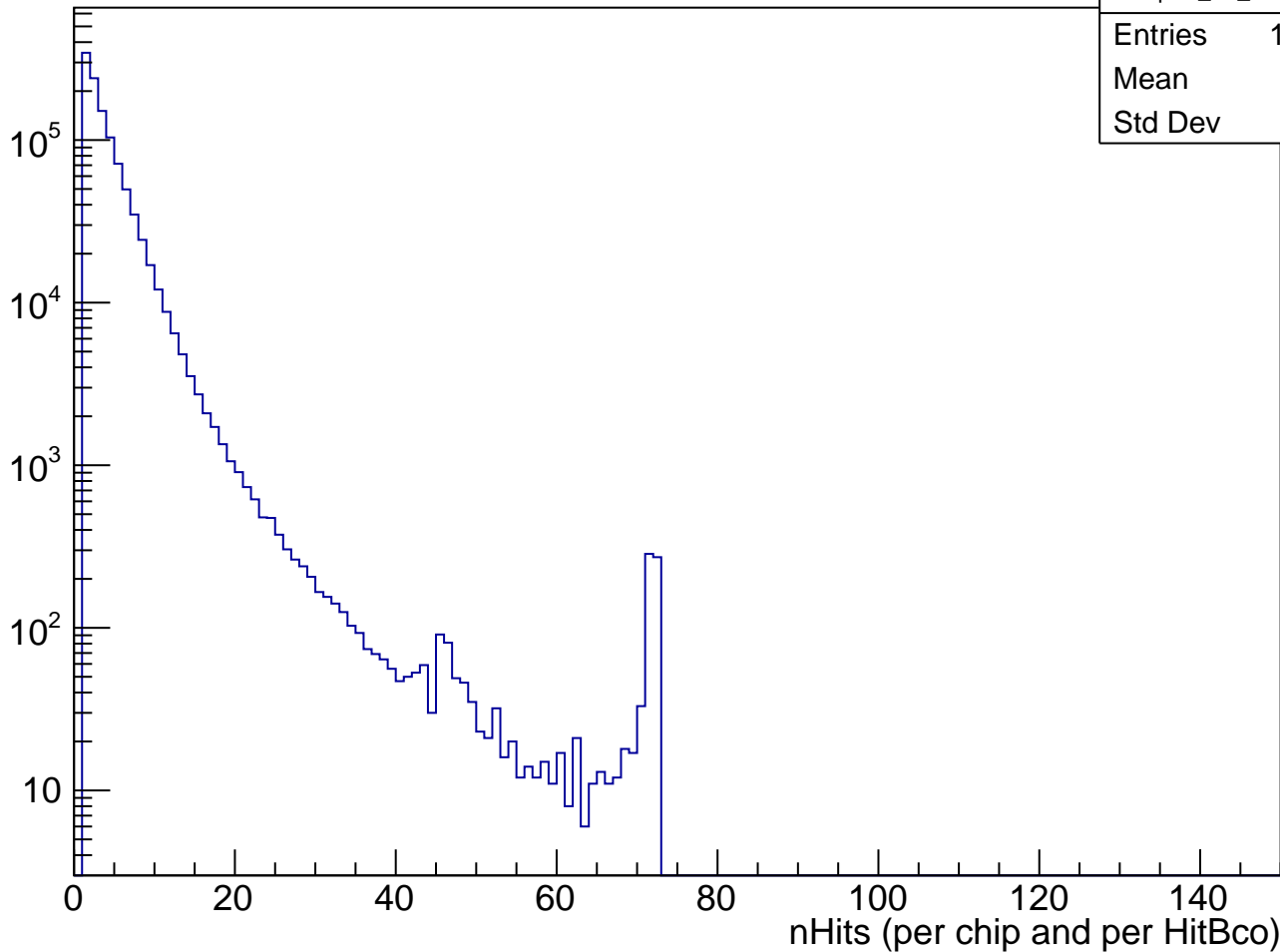
Entries



nChipHit_F1_Fch8_Chip23	
Entries	1094622
Mean	3.424
Std Dev	3.806

nChipHit_F1_Fch8_Chip24

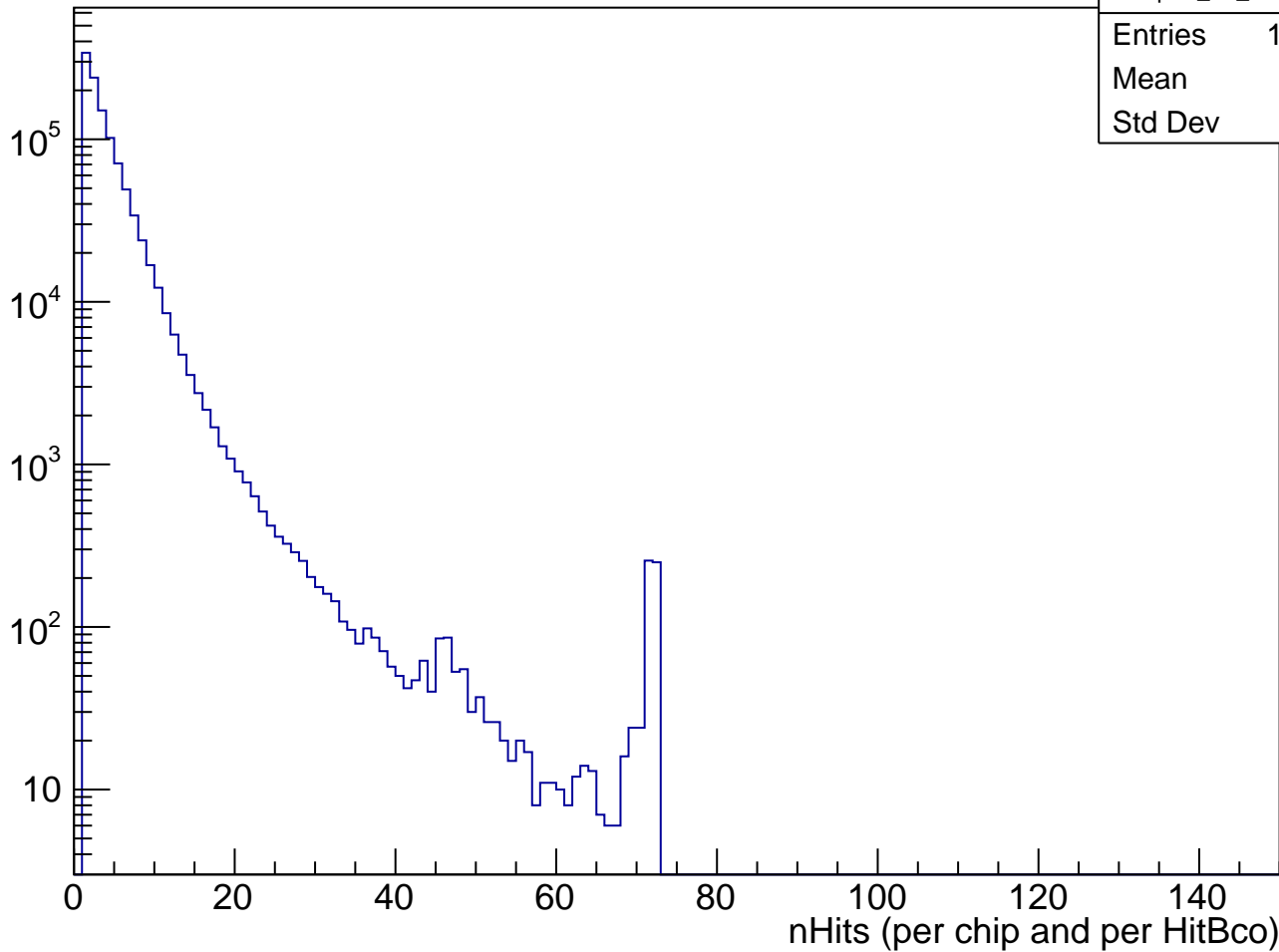
Entries



nChipHit_F1_Fch8_Chip24	
Entries	1088040
Mean	3.419
Std Dev	3.814

nChipHit_F1_Fch8_Chip25

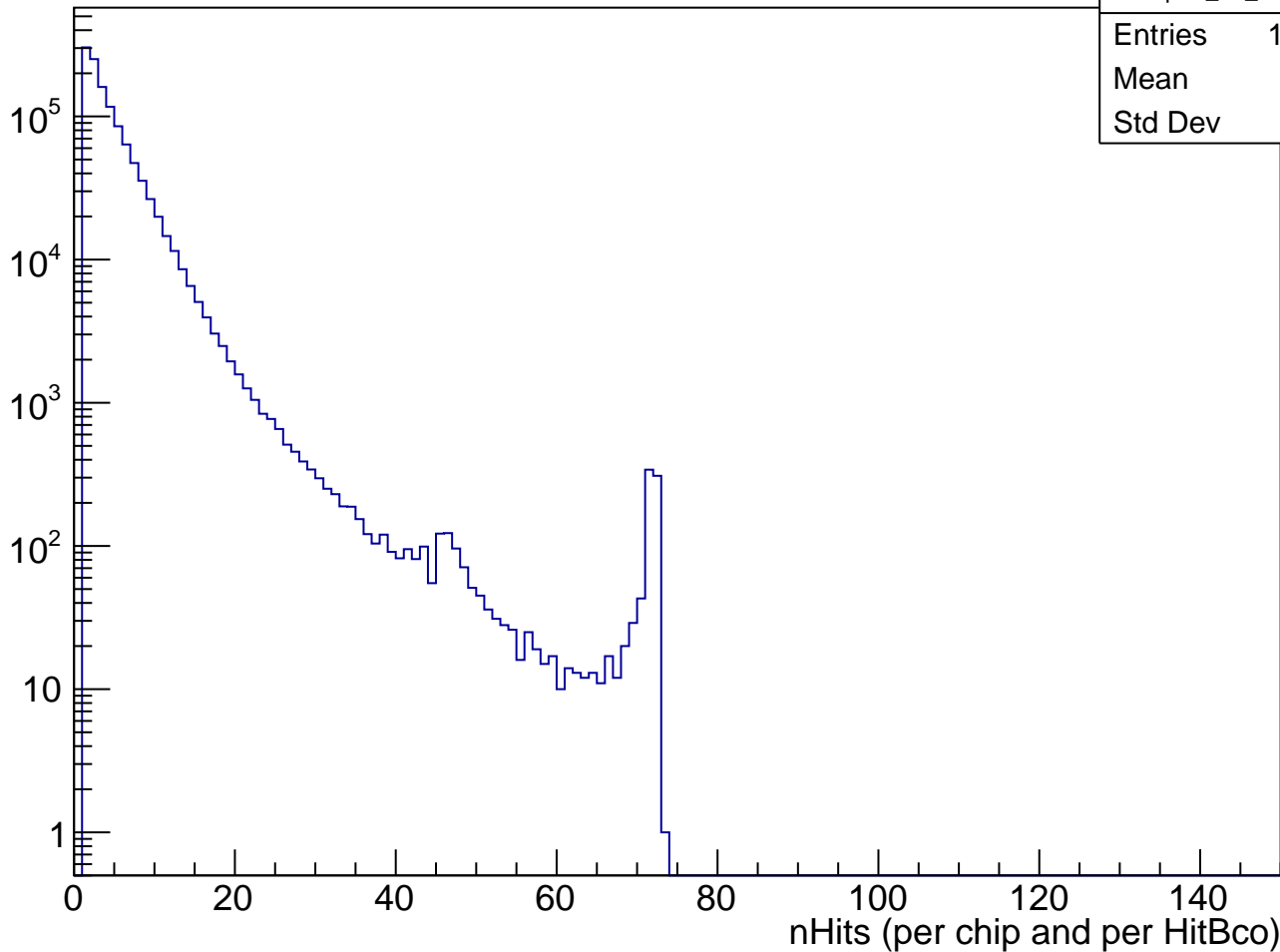
Entries



nChipHit_F1_Fch8_Chip25	
Entries	1078804
Mean	3.416
Std Dev	3.794

nChipHit_F1_Fch8_Chip3

Entries



nChipHit_F1_Fch8_Chip3

Entries 1179428

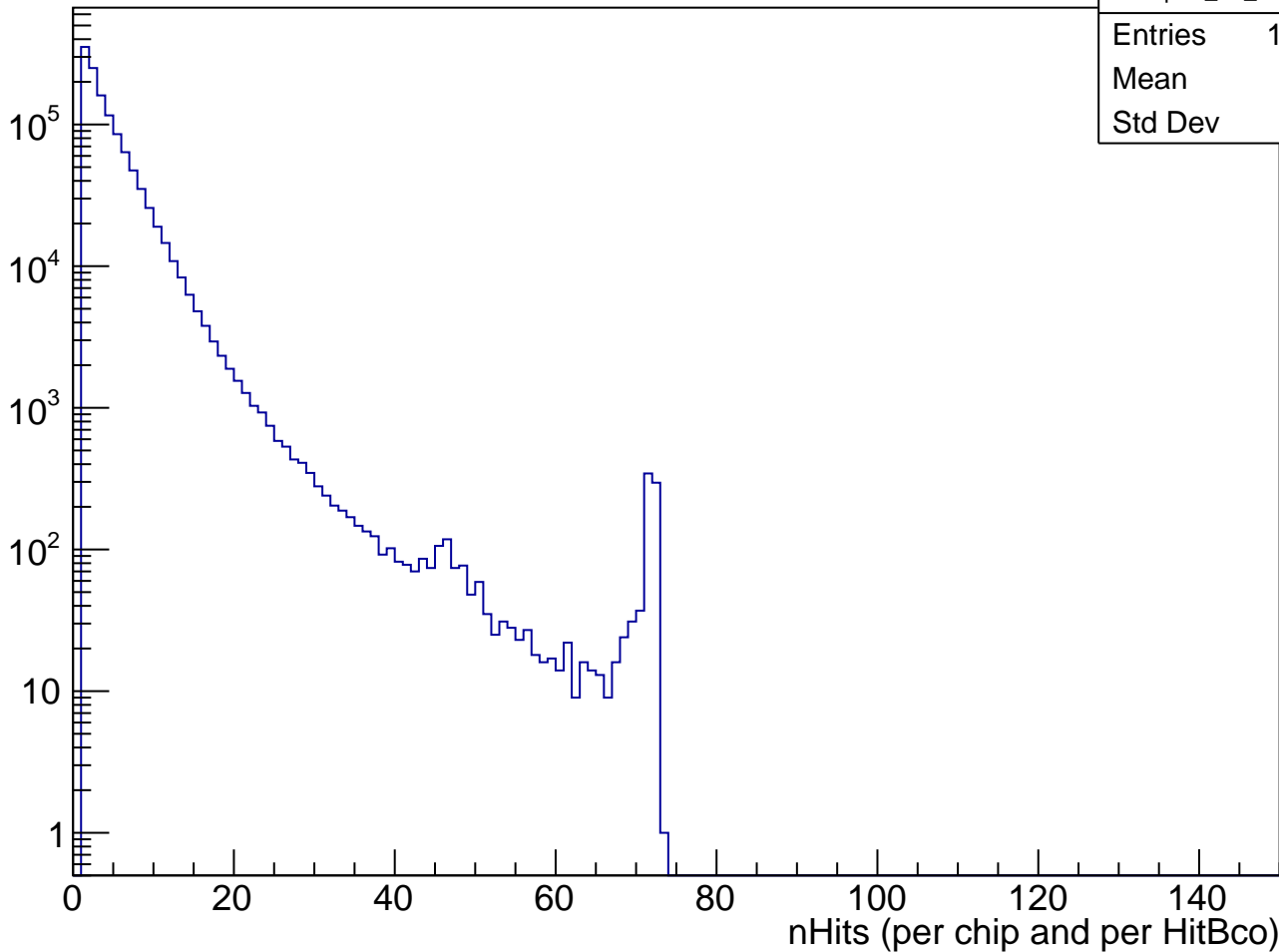
Mean 4.02

Std Dev 4.346

nHits (per chip and per HitBco)

nChipHit_F1_Fch8_Chip4

Entries



nChipHit_F1_Fch8_Chip4

Entries 1222877

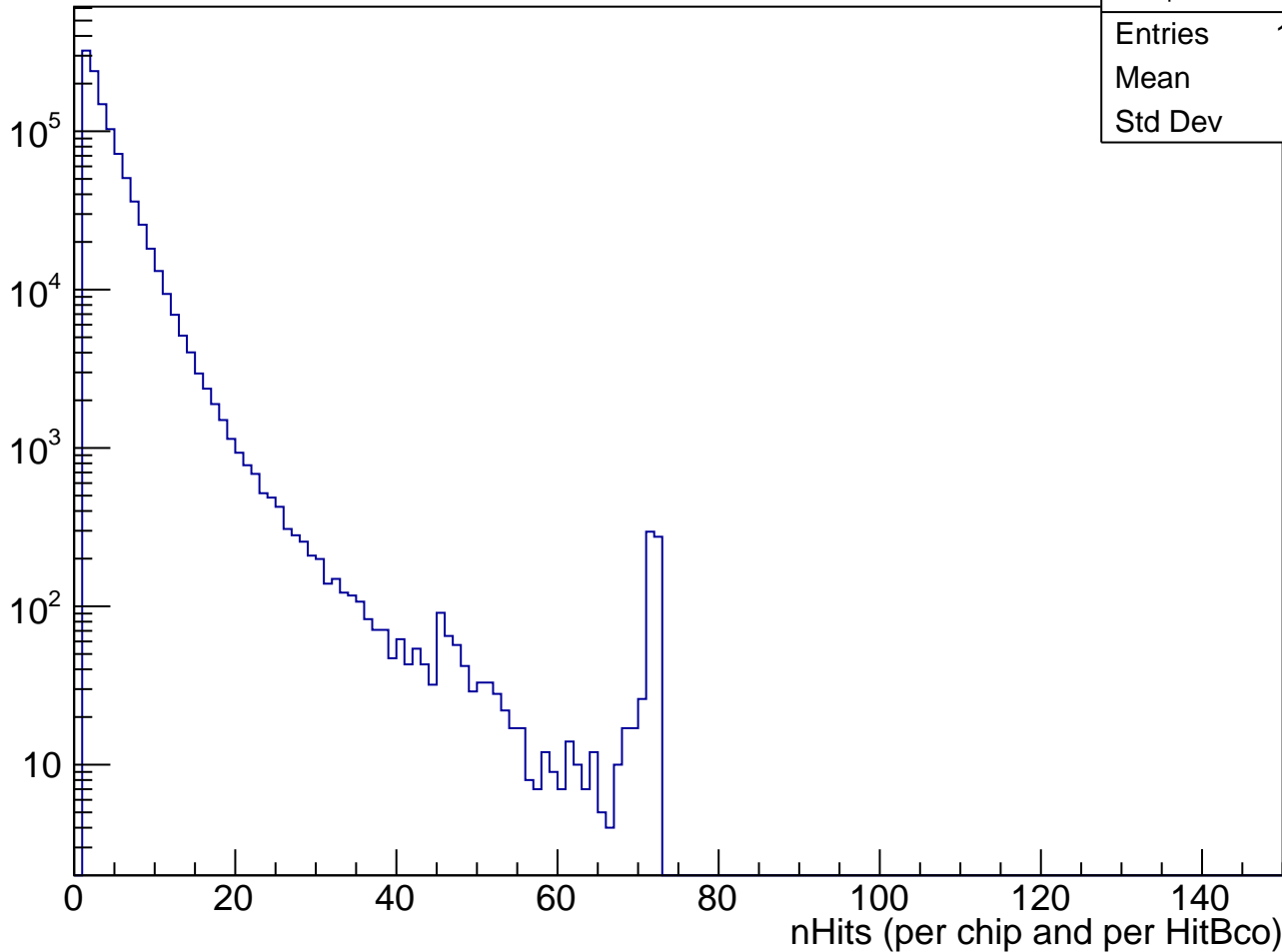
Mean 3.874

Std Dev 4.272

nHits (per chip and per HitBco)

nChipHit_F1_Fch8_Chip5

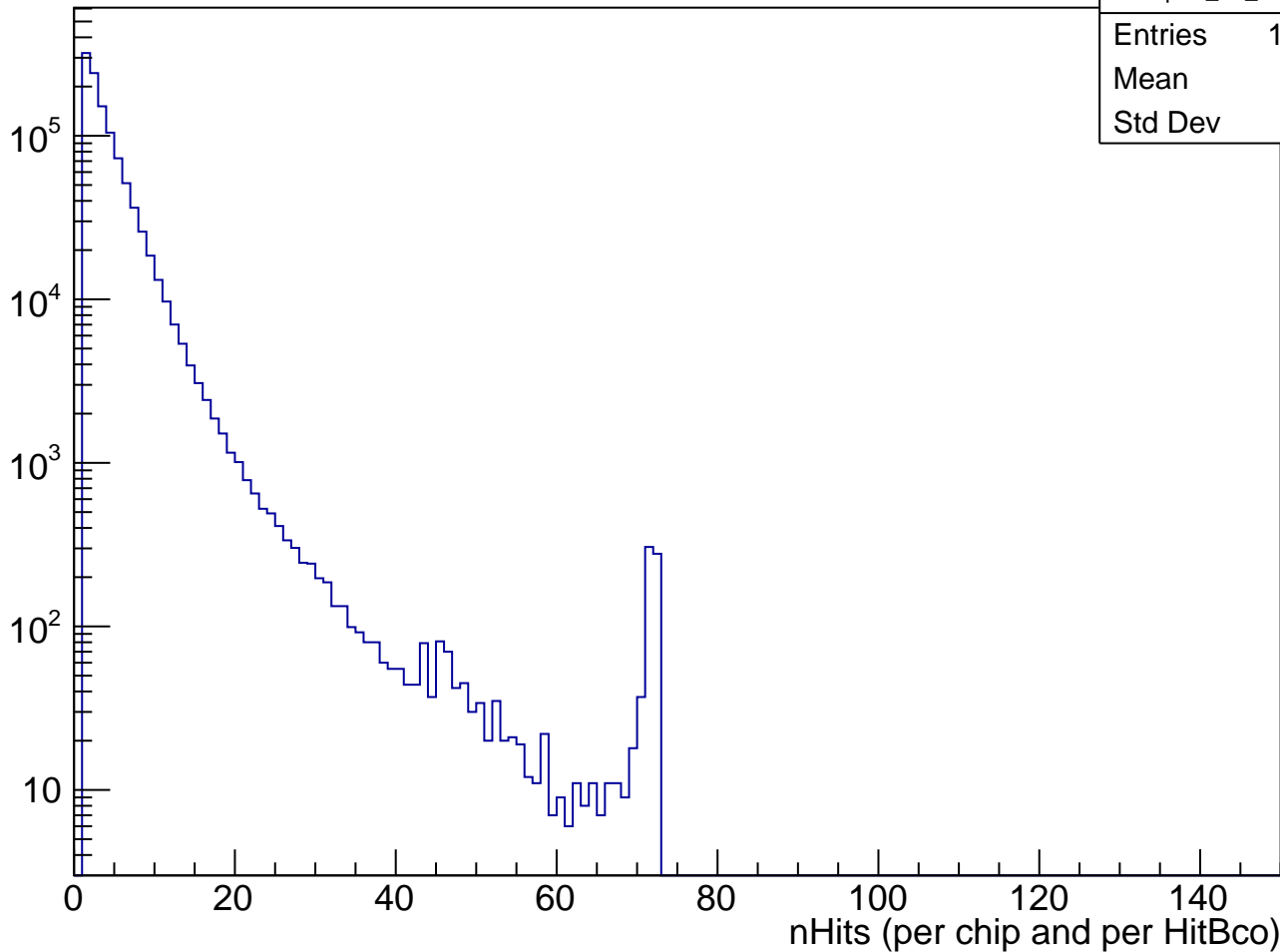
Entries



nChipHit_F1_Fch8_Chip5	
Entries	1072391
Mean	3.525
Std Dev	3.885

nChipHit_F1_Fch8_Chip6

Entries

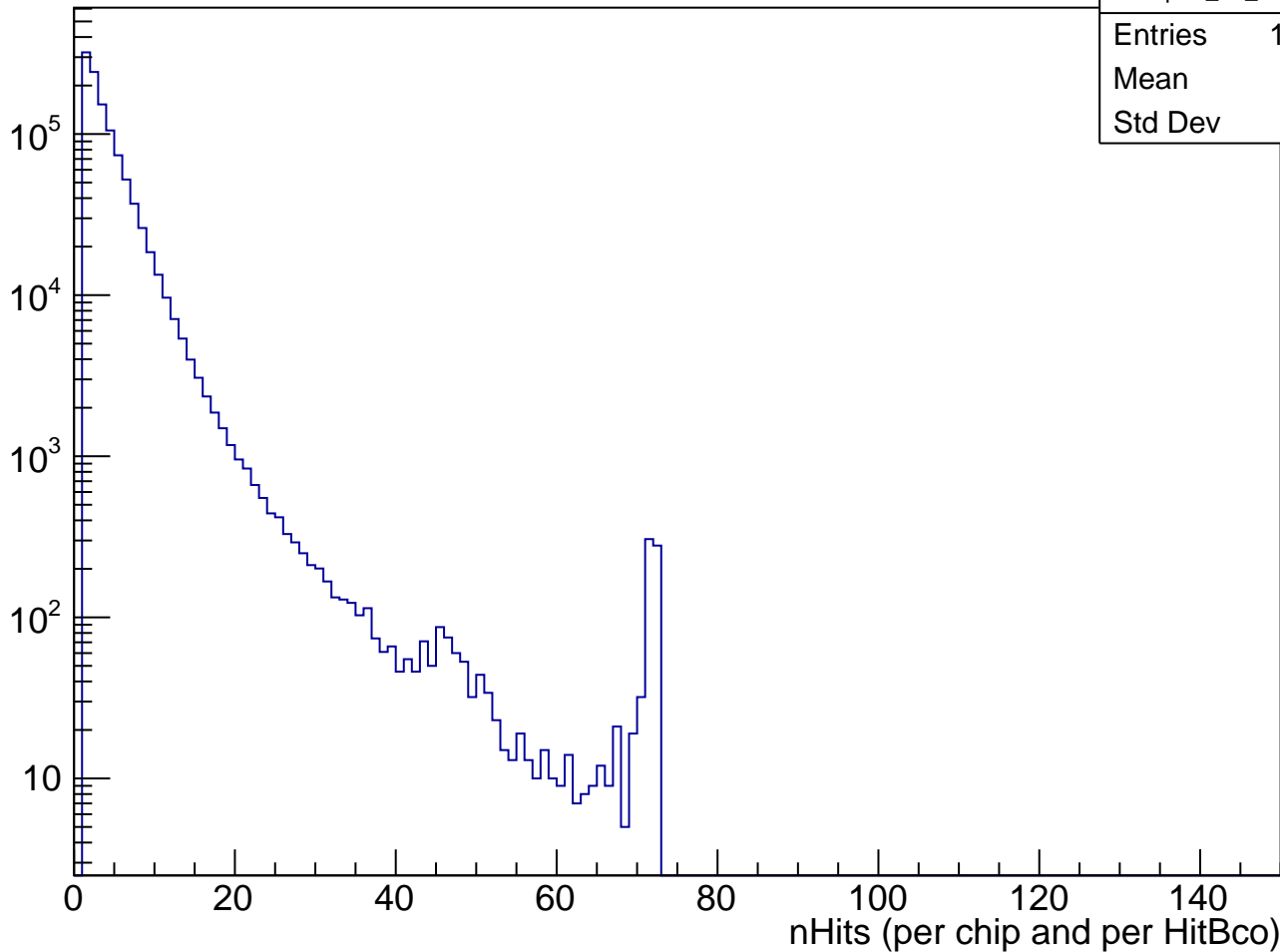


nChipHit_F1_Fch8_Chip6	
Entries	1080035
Mean	3.544
Std Dev	3.906

nHits (per chip and per HitBco)

nChipHit_F1_Fch8_Chip7

Entries

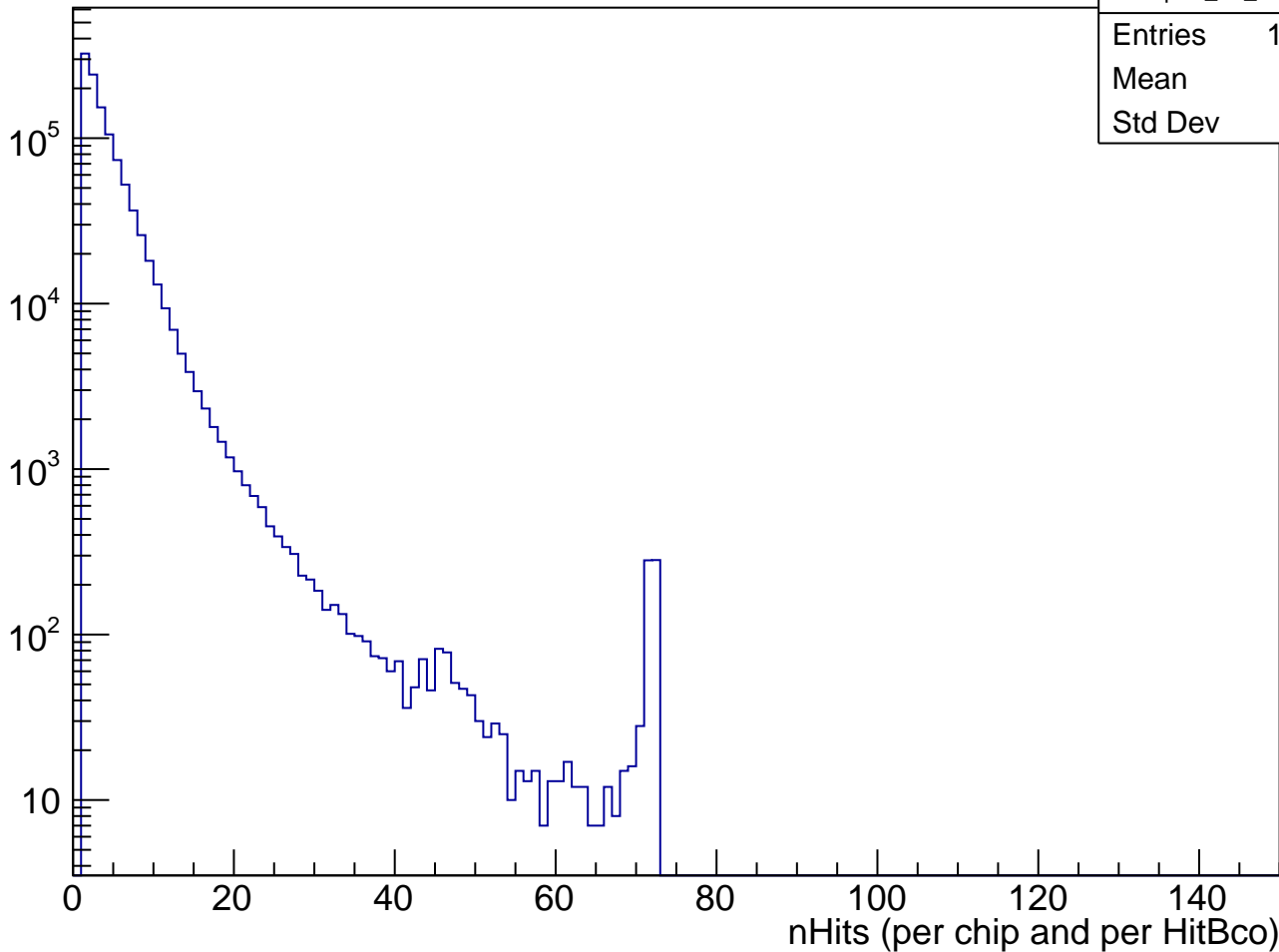


nChipHit_F1_Fch8_Chip7	
Entries	1086670
Mean	3.551
Std Dev	3.913

nHits (per chip and per HitBco)

nChipHit_F1_Fch8_Chip8

Entries



nChipHit_F1_Fch8_Chip8

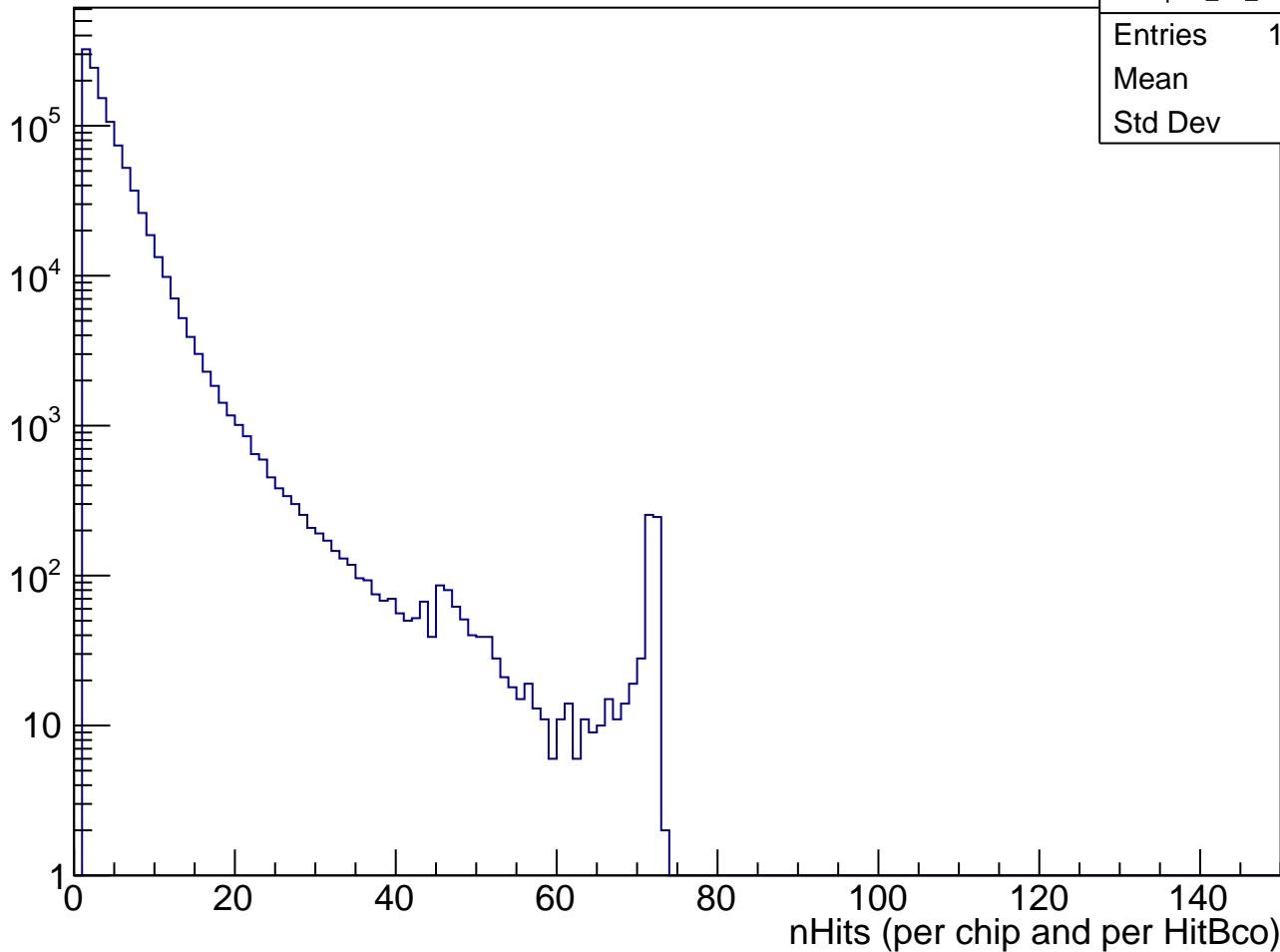
Entries 1088189

Mean 3.524

Std Dev 3.874

nChipHit_F1_Fch8_Chip9

Entries



nChipHit_F1_Fch8_Chip9

Entries 1091519

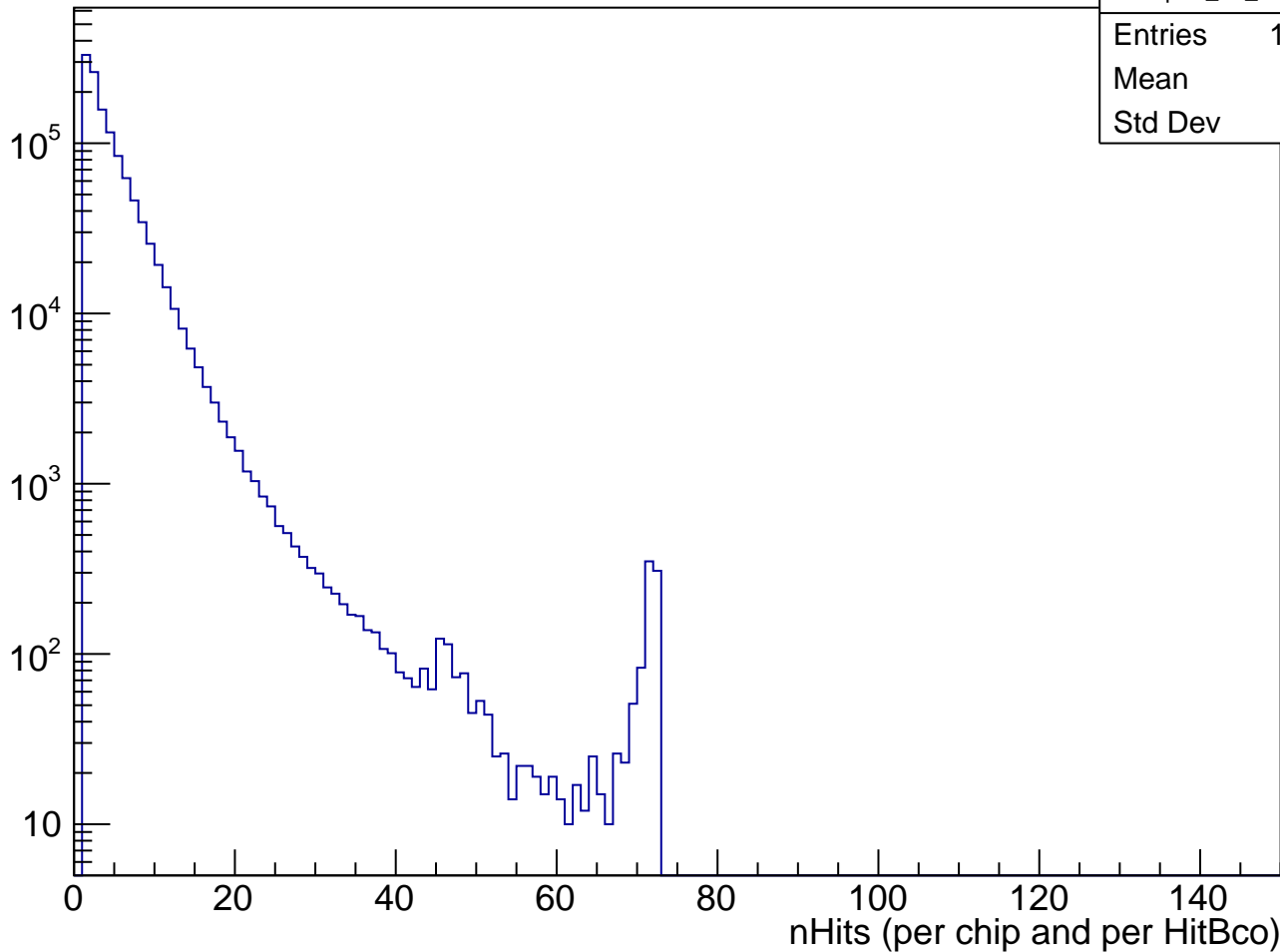
Mean 3.538

Std Dev 3.864

nHits (per chip and per HitBco)

nChipHit_F1_Fch9_Chip0

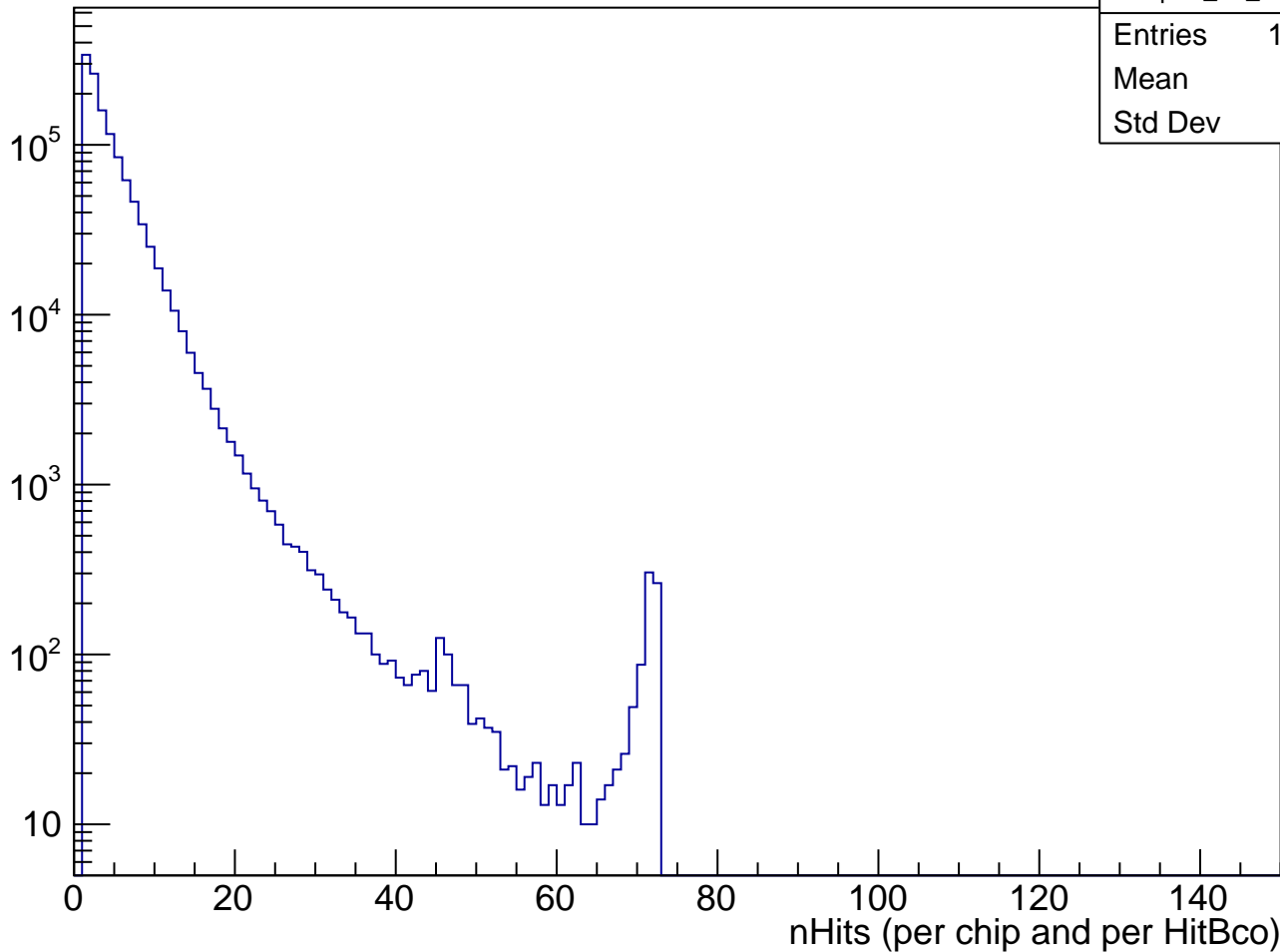
Entries



nChipHit_F1_Fch9_Chip0	
Entries	1204050
Mean	3.899
Std Dev	4.314

nChipHit_F1_Fch9_Chip1

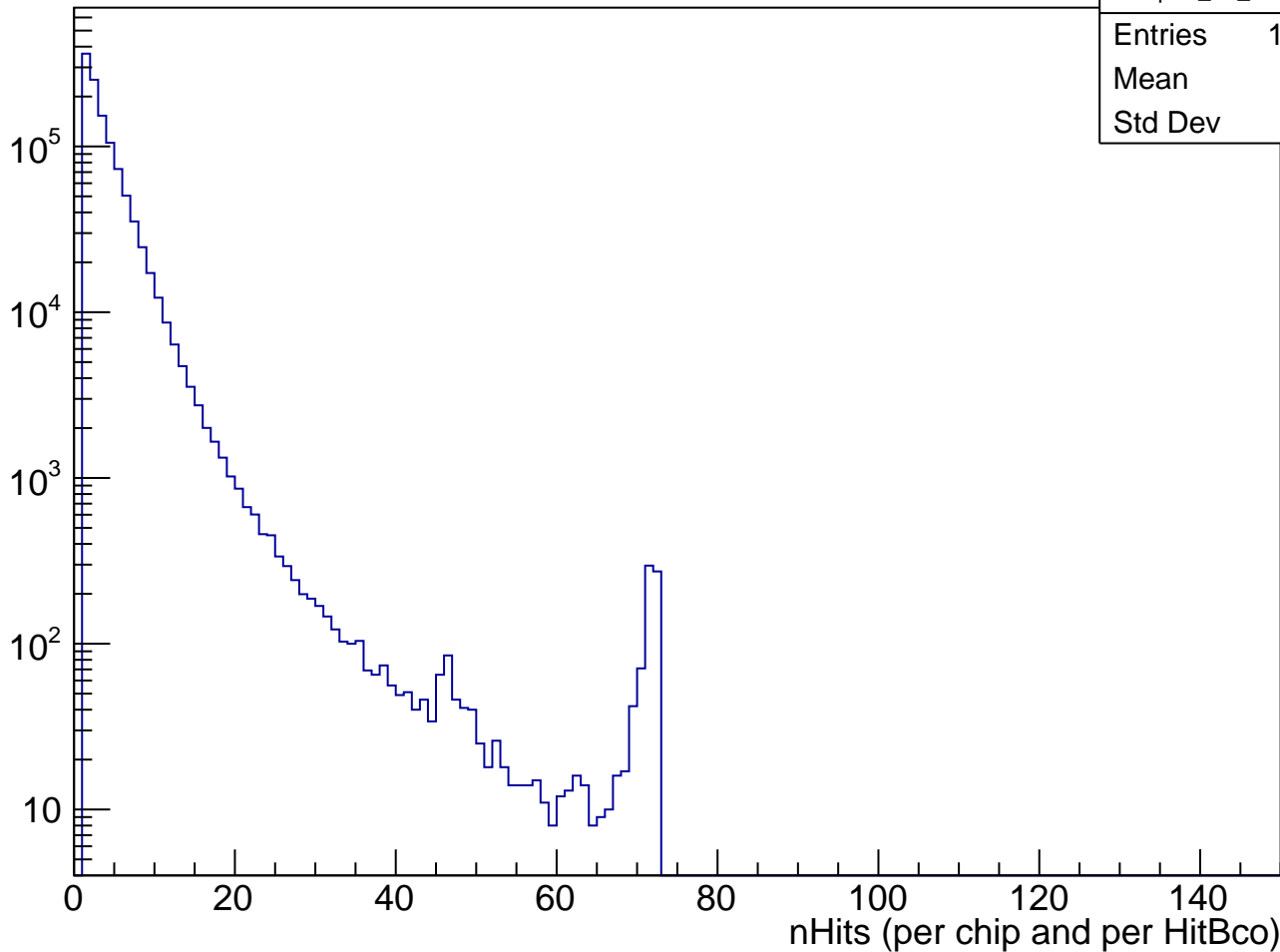
Entries



nChipHit_F1_Fch9_Chip1	
Entries	1211576
Mean	3.84
Std Dev	4.212

nChipHit_F1_Fch9_Chip10

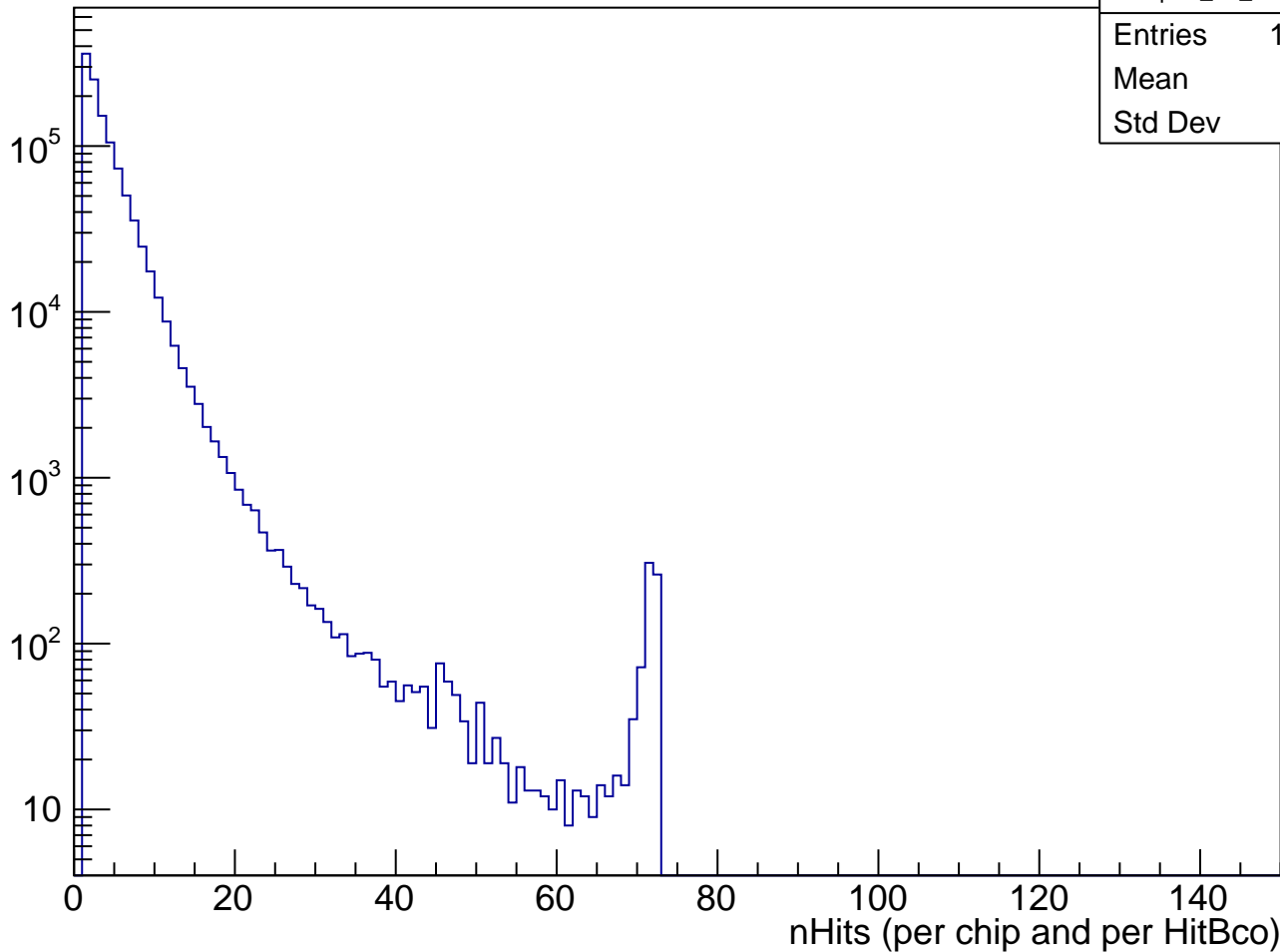
Entries



nChipHit_F1_Fch9_Chip10	
Entries	1125834
Mean	3.363
Std Dev	3.77

nChipHit_F1_Fch9_Chip11

Entries

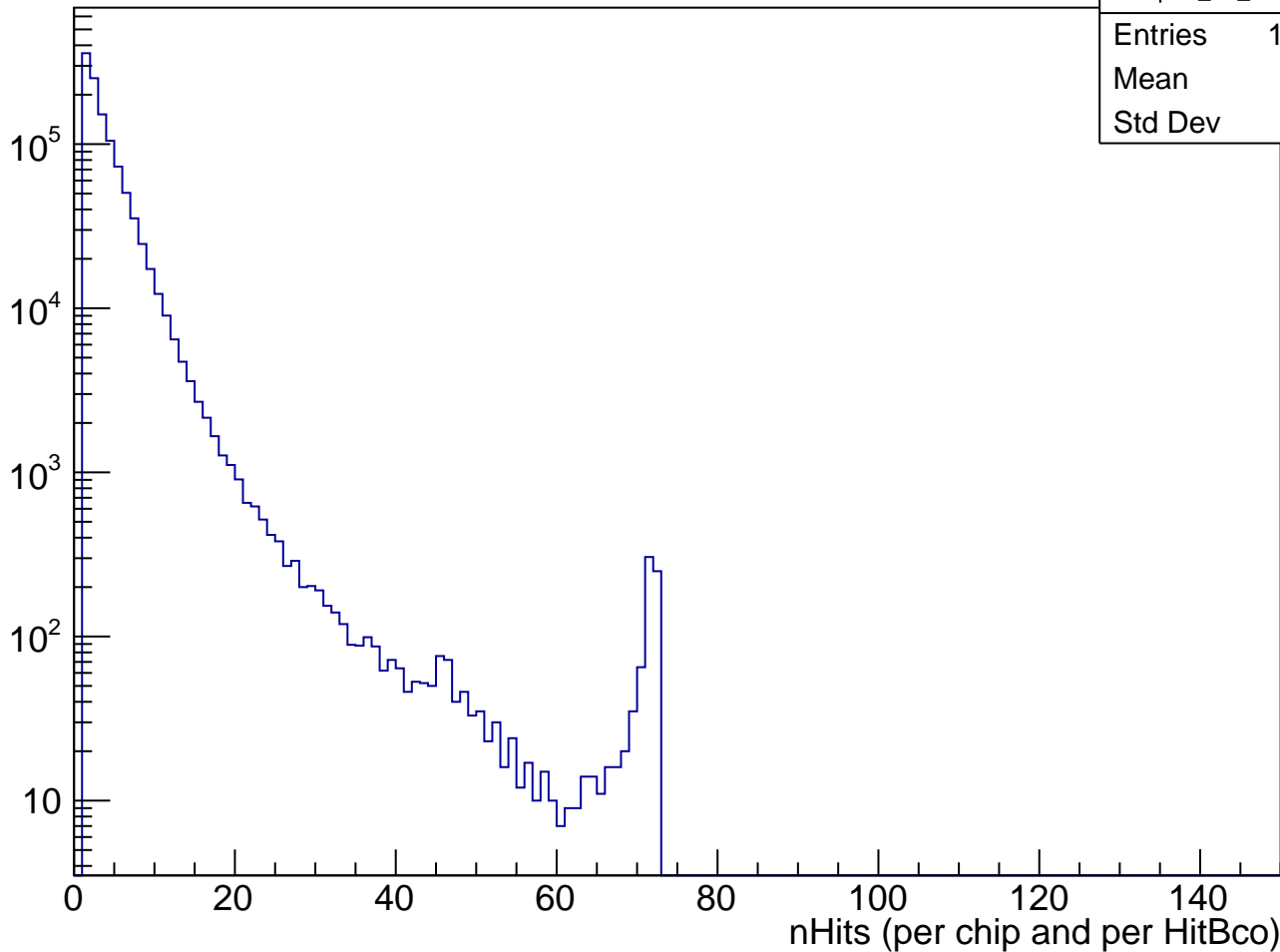


nChipHit_F1_Fch9_Chip11	
Entries	1122220
Mean	3.369
Std Dev	3.767

nHits (per chip and per HitBco)

nChipHit_F1_Fch9_Chip12

Entries

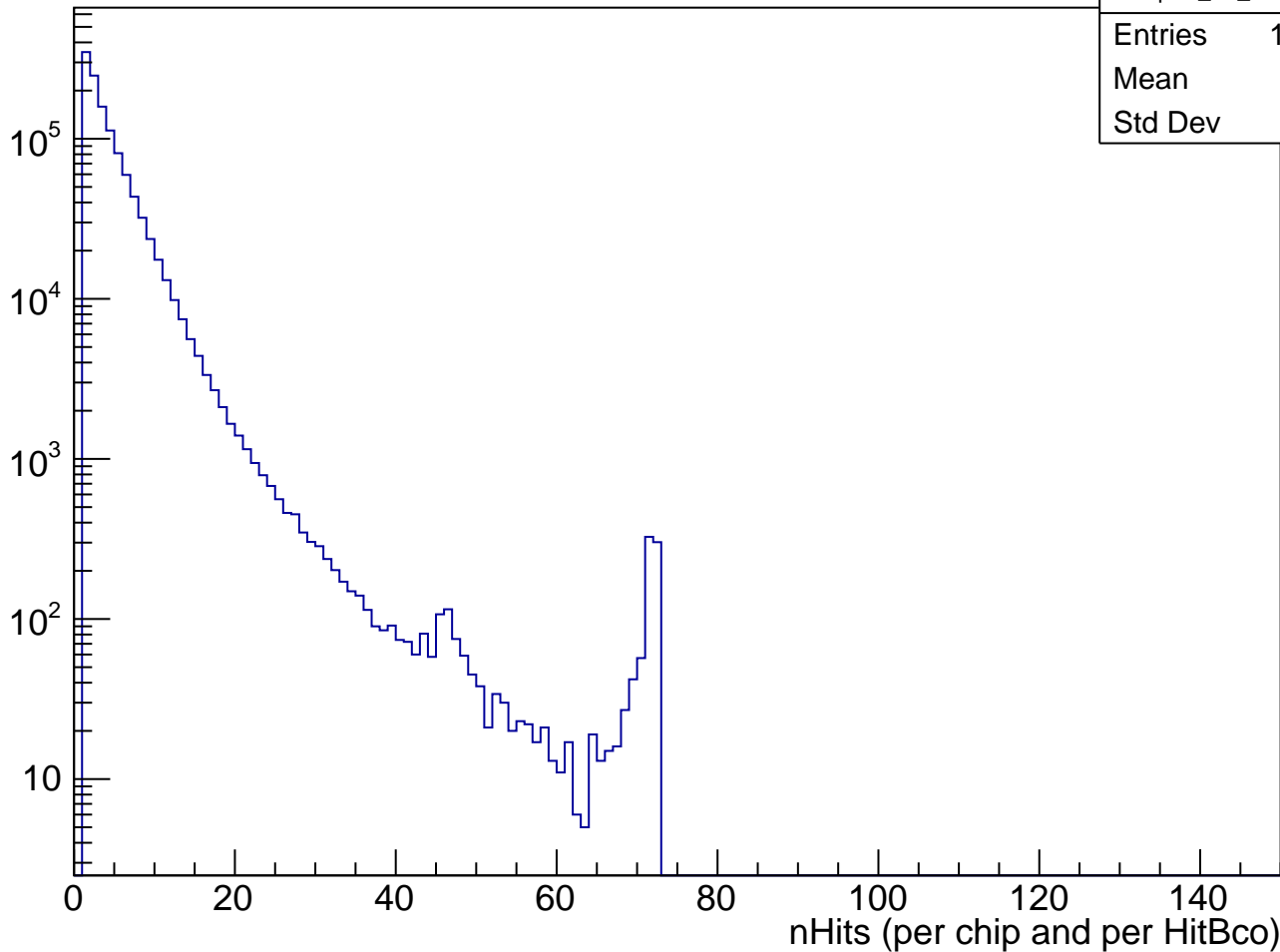


nChipHit_F1_Fch9_Chip12	
Entries	1119506
Mean	3.386
Std Dev	3.803

nHits (per chip and per HitBco)

nChipHit_F1_Fch9_Chip13

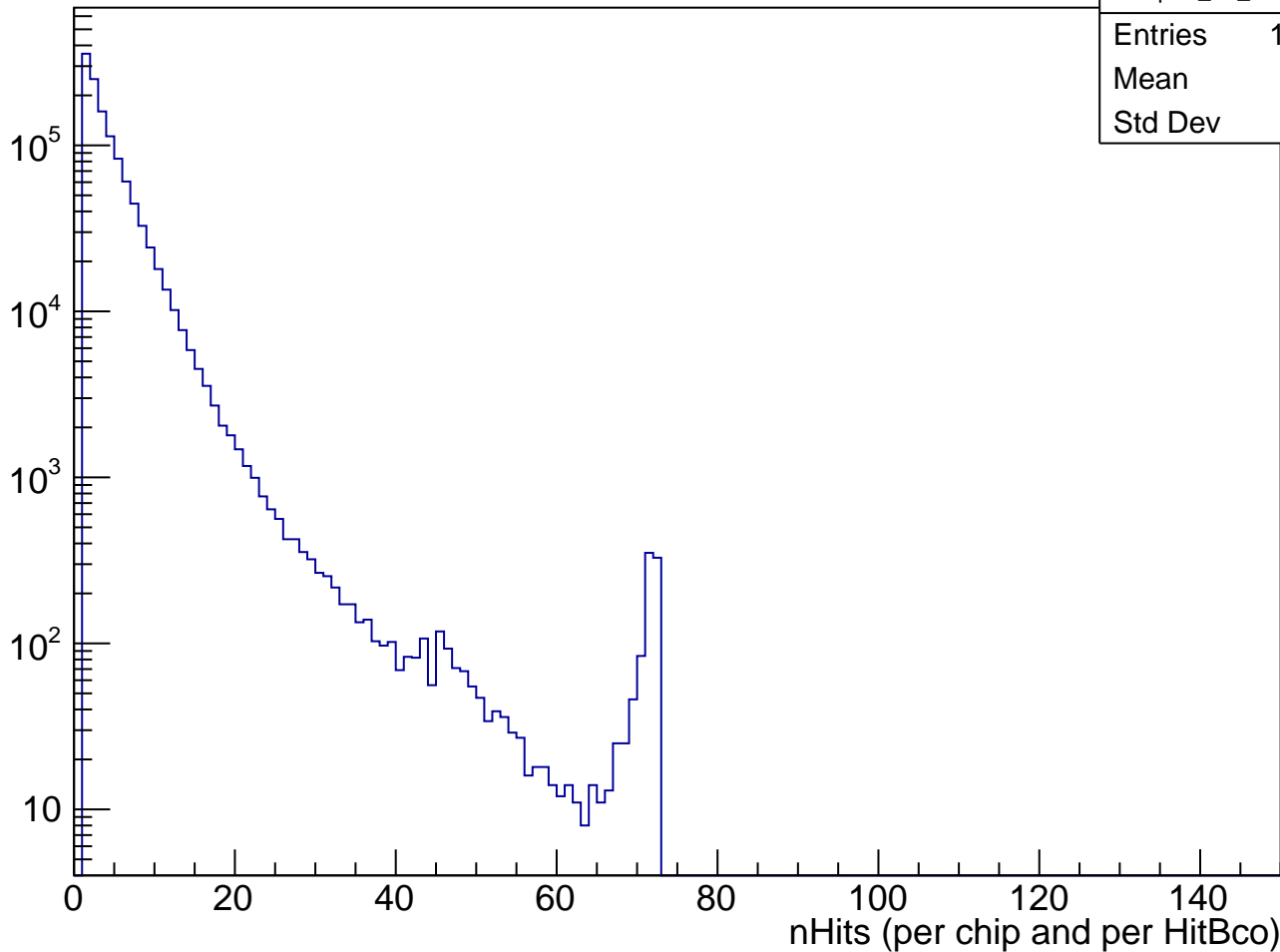
Entries



nChipHit_F1_Fch9_Chip13	
Entries	1185583
Mean	3.777
Std Dev	4.204

nChipHit_F1_Fch9_Chip14

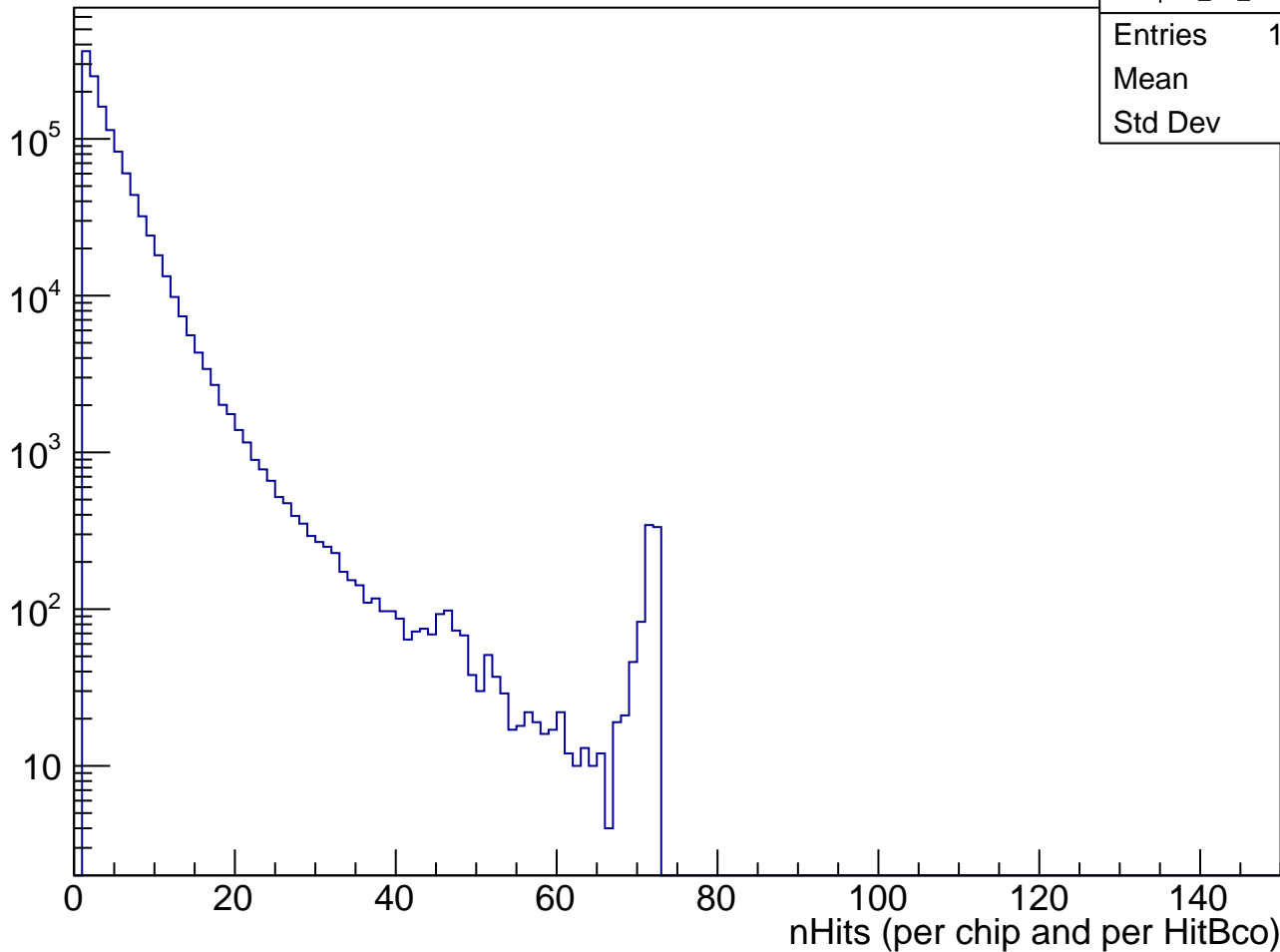
Entries



nChipHit_F1_Fch9_Chip14	
Entries	1206483
Mean	3.791
Std Dev	4.262

nChipHit_F1_Fch9_Chip15

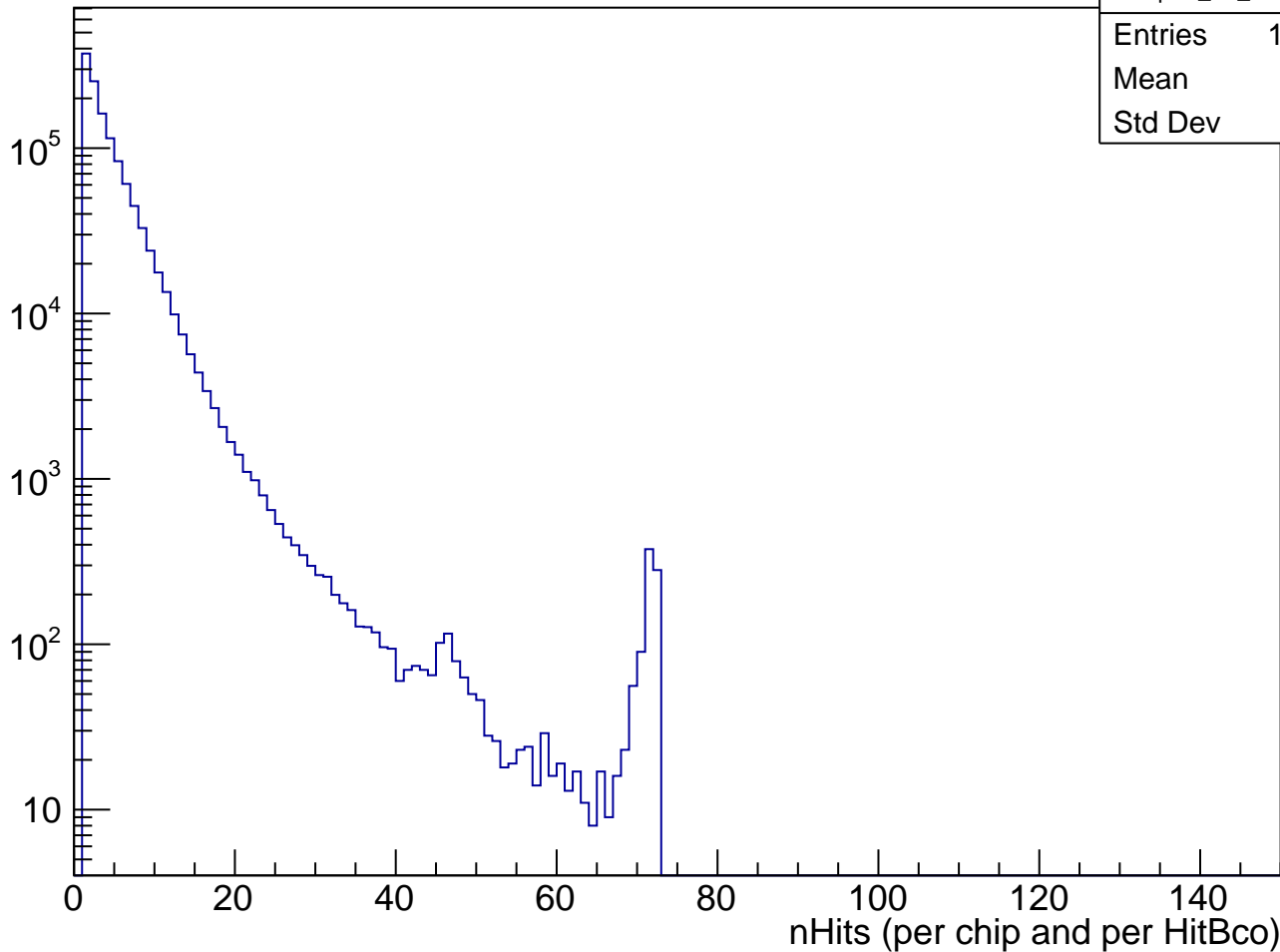
Entries



nChipHit_F1_Fch9_Chip15	
Entries	1210566
Mean	3.75
Std Dev	4.211

nChipHit_F1_Fch9_Chip16

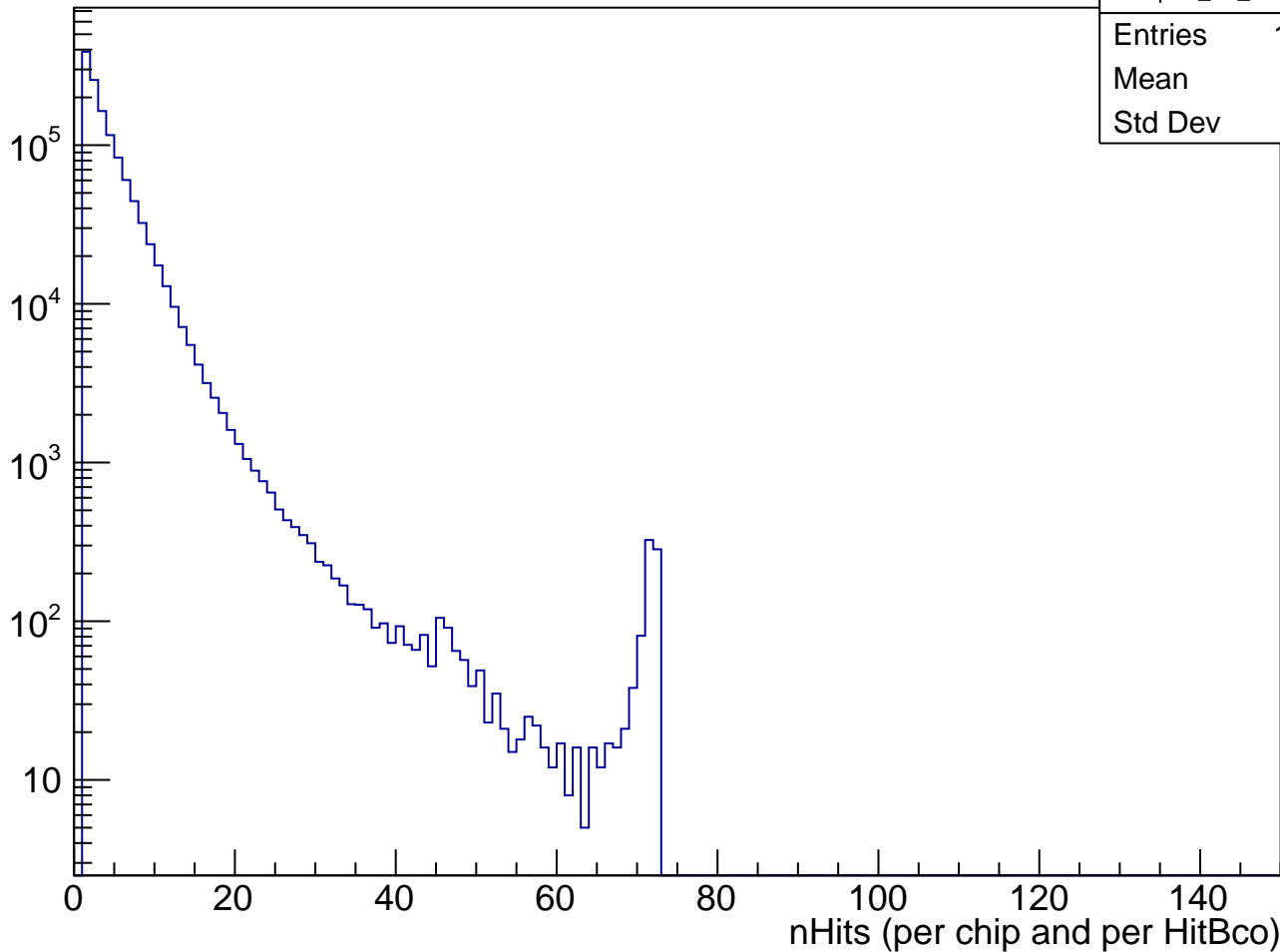
Entries



nChipHit_F1_Fch9_Chip16	
Entries	1228337
Mean	3.729
Std Dev	4.194

nChipHit_F1_Fch9_Chip17

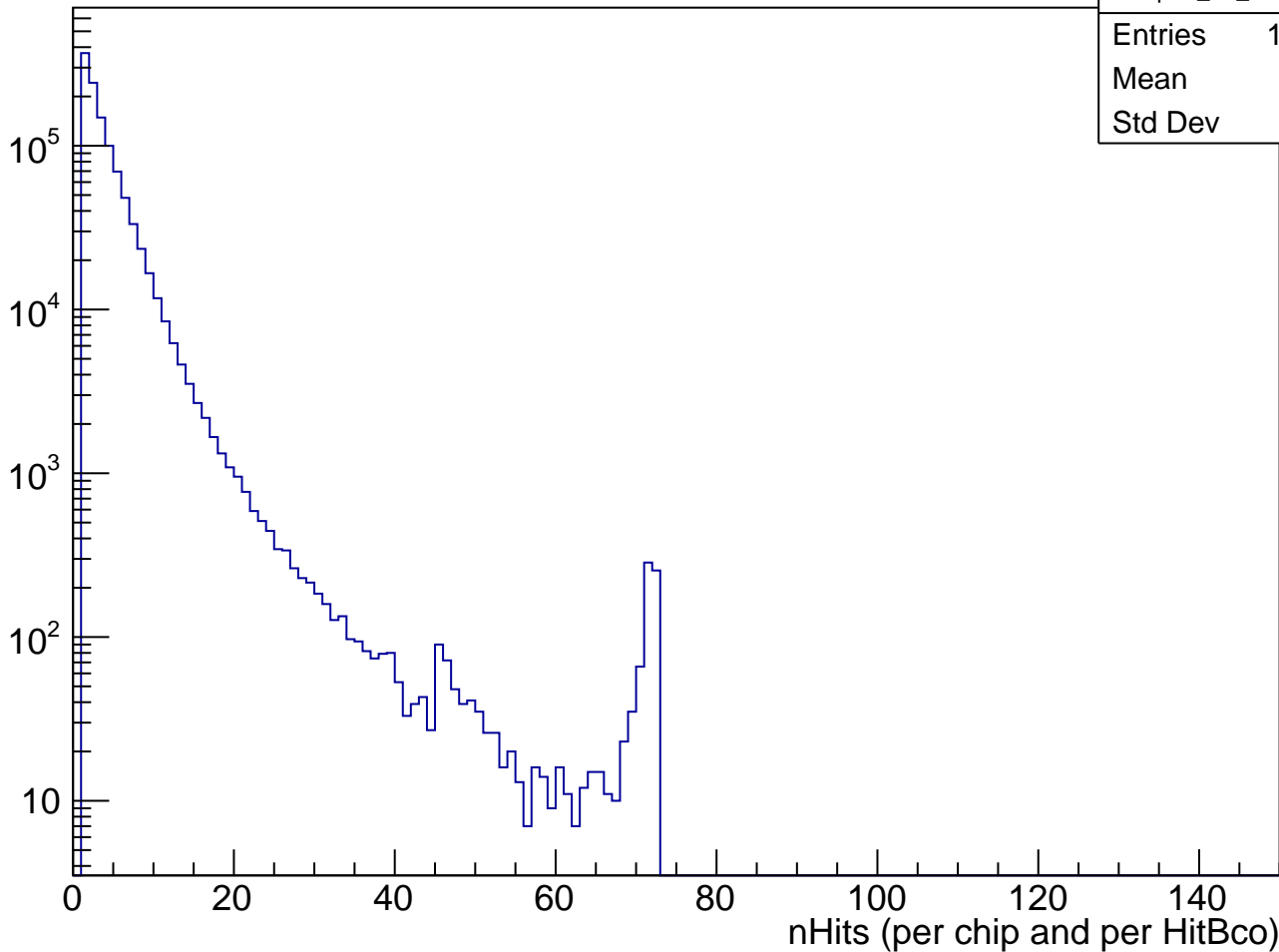
Entries



nChipHit_F1_Fch9_Chip17	
Entries	1245731
Mean	3.655
Std Dev	4.086

nChipHit_F1_Fch9_Chip18

Entries

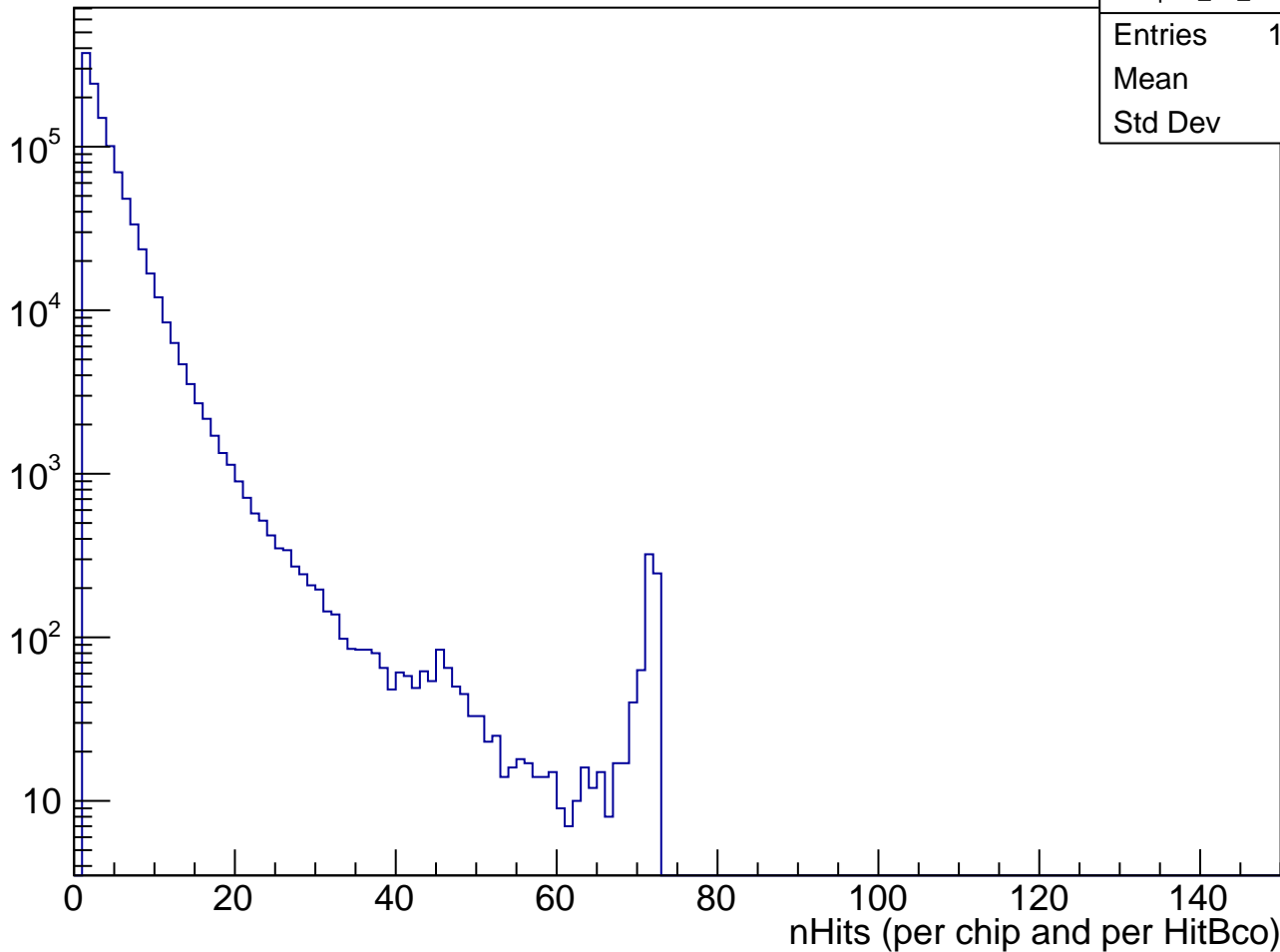


nChipHit_F1_Fch9_Chip18	
Entries	1100939
Mean	3.341
Std Dev	3.812

nHits (per chip and per HitBco)

nChipHit_F1_Fch9_Chip19

Entries

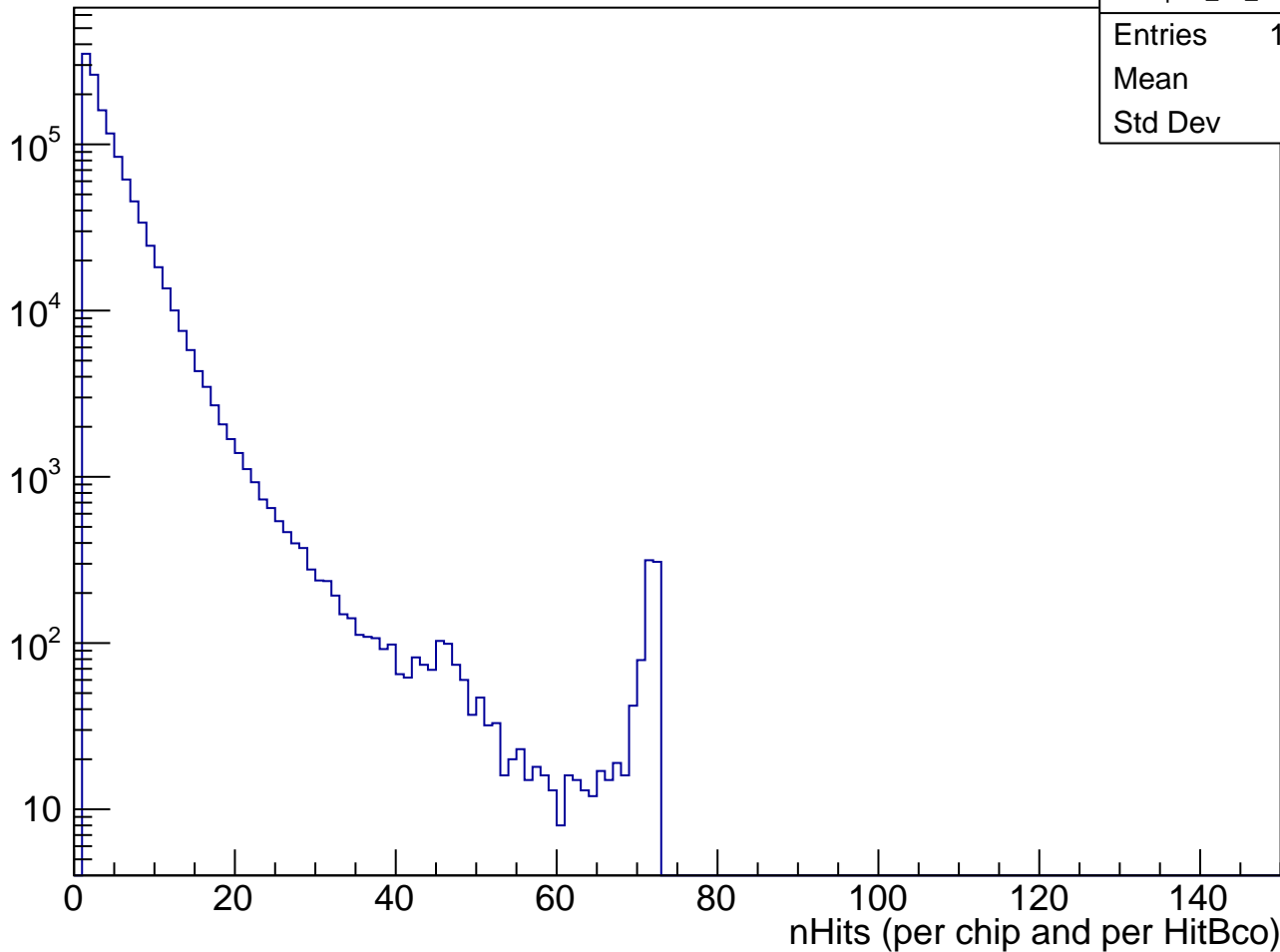


nChipHit_F1_Fch9_Chip19	
Entries	1109832
Mean	3.335
Std Dev	3.82

nHits (per chip and per HitBco)

nChipHit_F1_Fch9_Chip2

Entries



nChipHit_F1_Fch9_Chip2

Entries 1218403

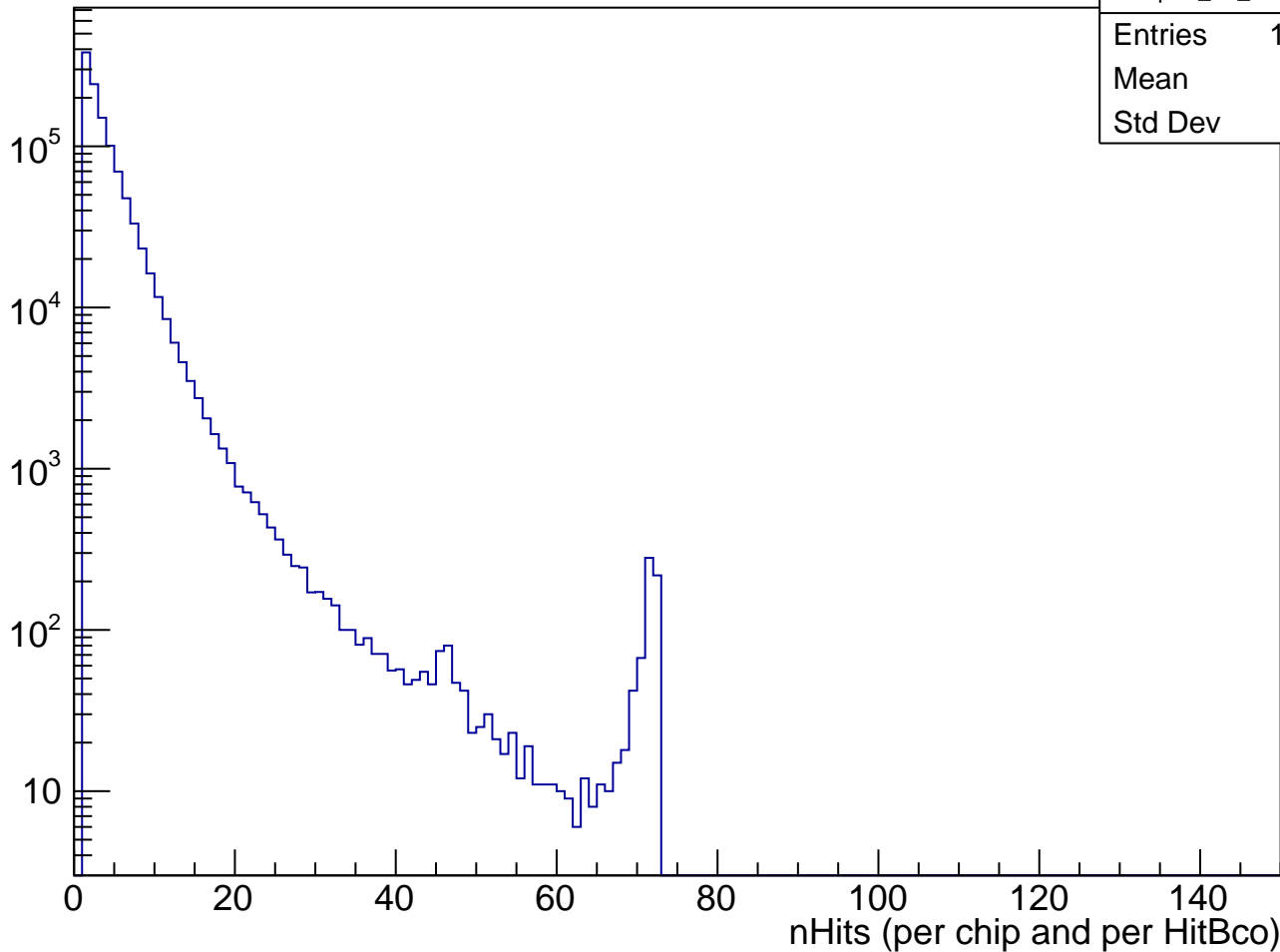
Mean 3.776

Std Dev 4.159

nHits (per chip and per HitBco)

nChipHit_F1_Fch9_Chip20

Entries

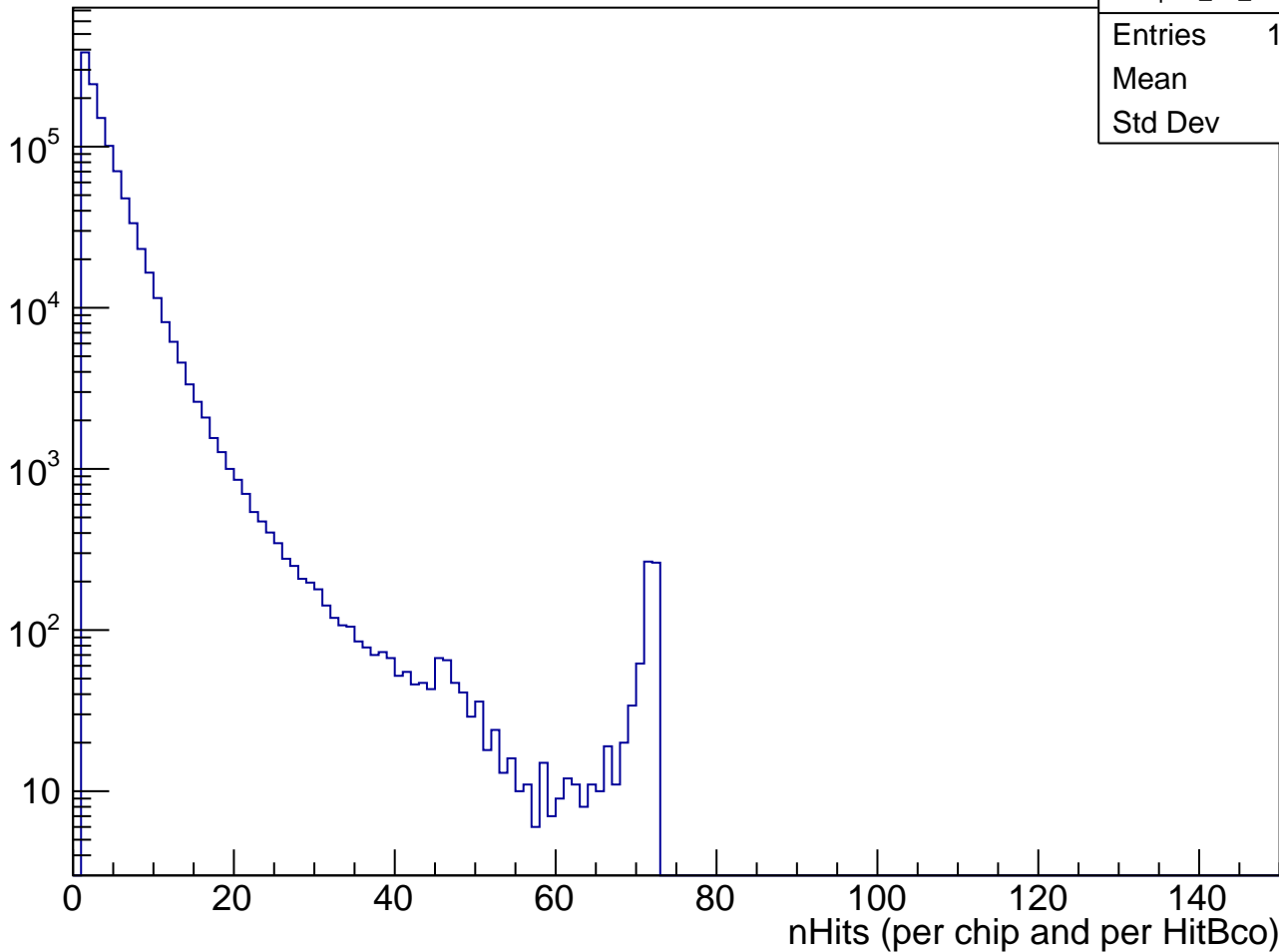


nChipHit_F1_Fch9_Chip20	
Entries	1116340
Mean	3.293
Std Dev	3.743

nHits (per chip and per HitBco)

nChipHit_F1_Fch9_Chip21

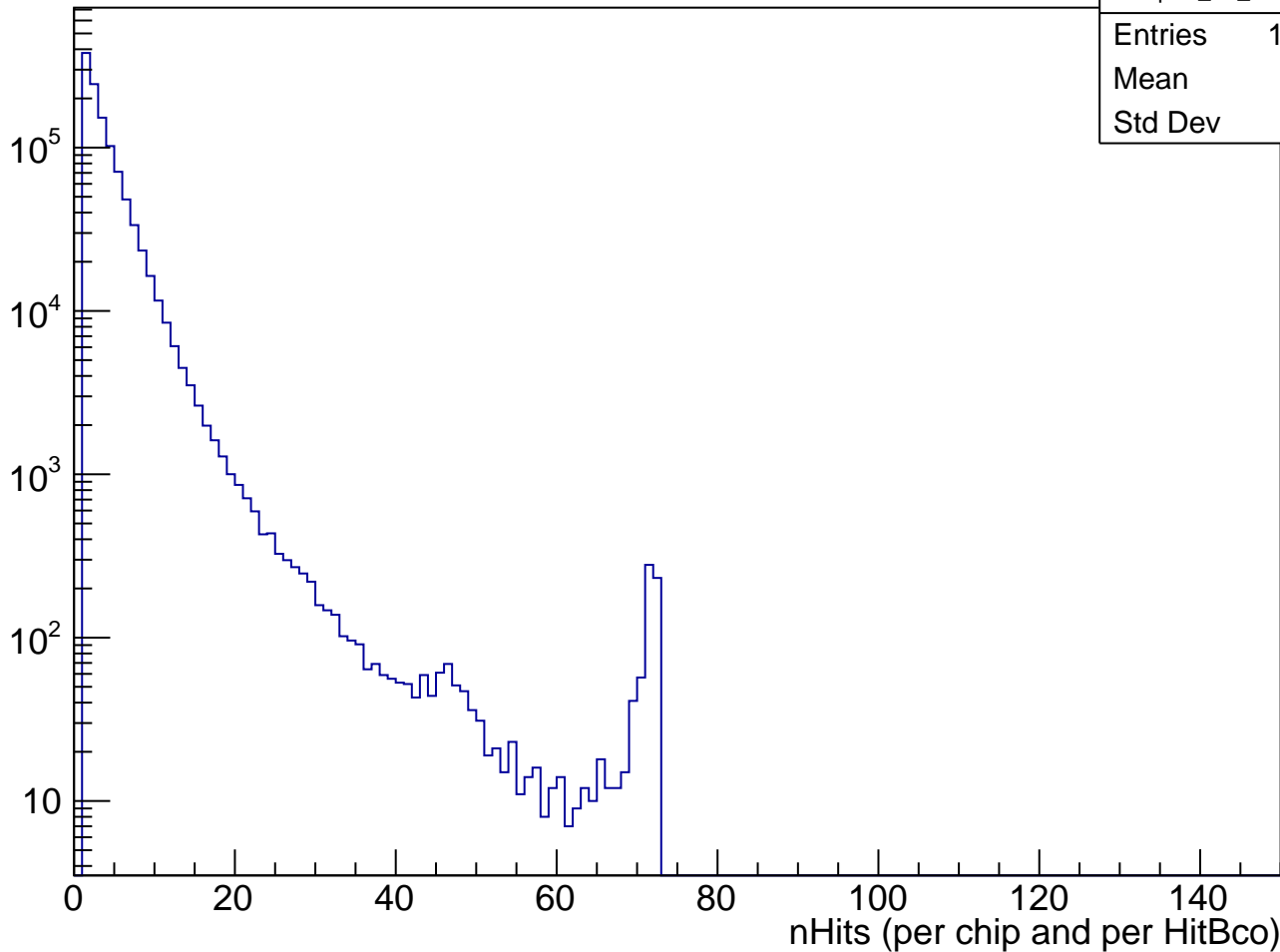
Entries



nChipHit_F1_Fch9_Chip21	
Entries	1120758
Mean	3.28
Std Dev	3.722

nChipHit_F1_Fch9_Chip22

Entries

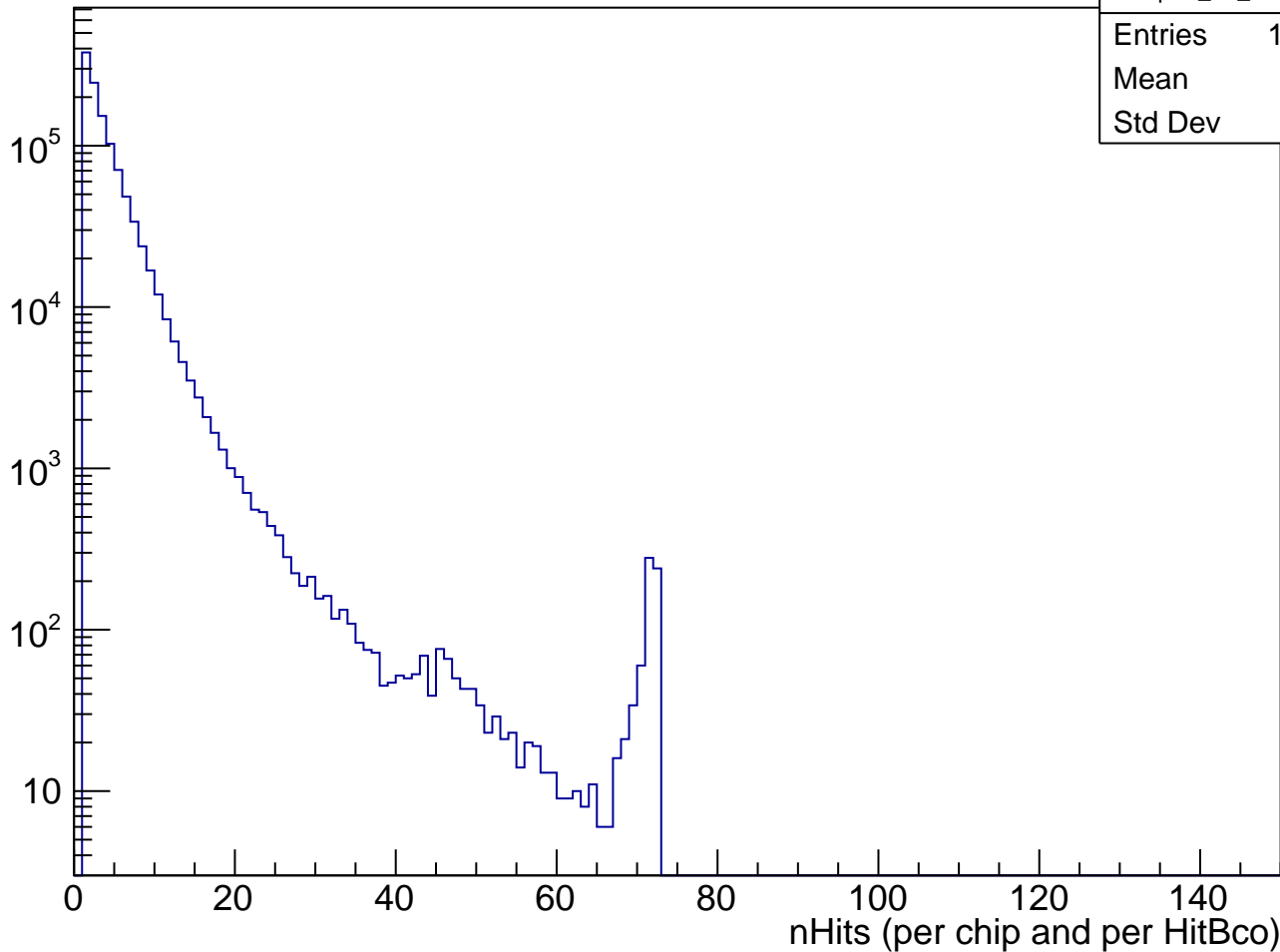


nChipHit_F1_Fch9_Chip22	
Entries	1120958
Mean	3.298
Std Dev	3.726

nHits (per chip and per HitBco)

nChipHit_F1_Fch9_Chip23

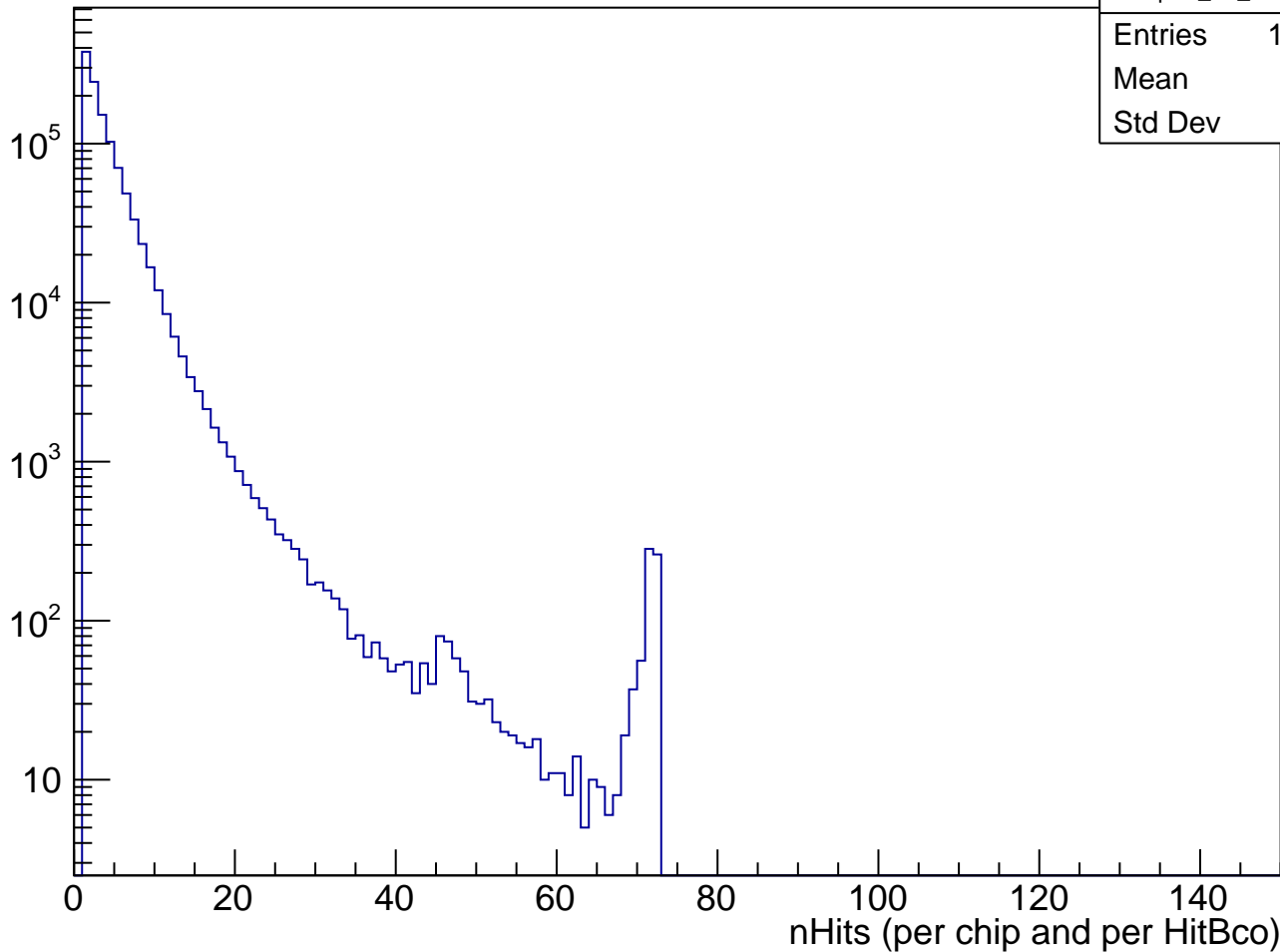
Entries



nChipHit_F1_Fch9_Chip23	
Entries	1123735
Mean	3.312
Std Dev	3.746

nChipHit_F1_Fch9_Chip24

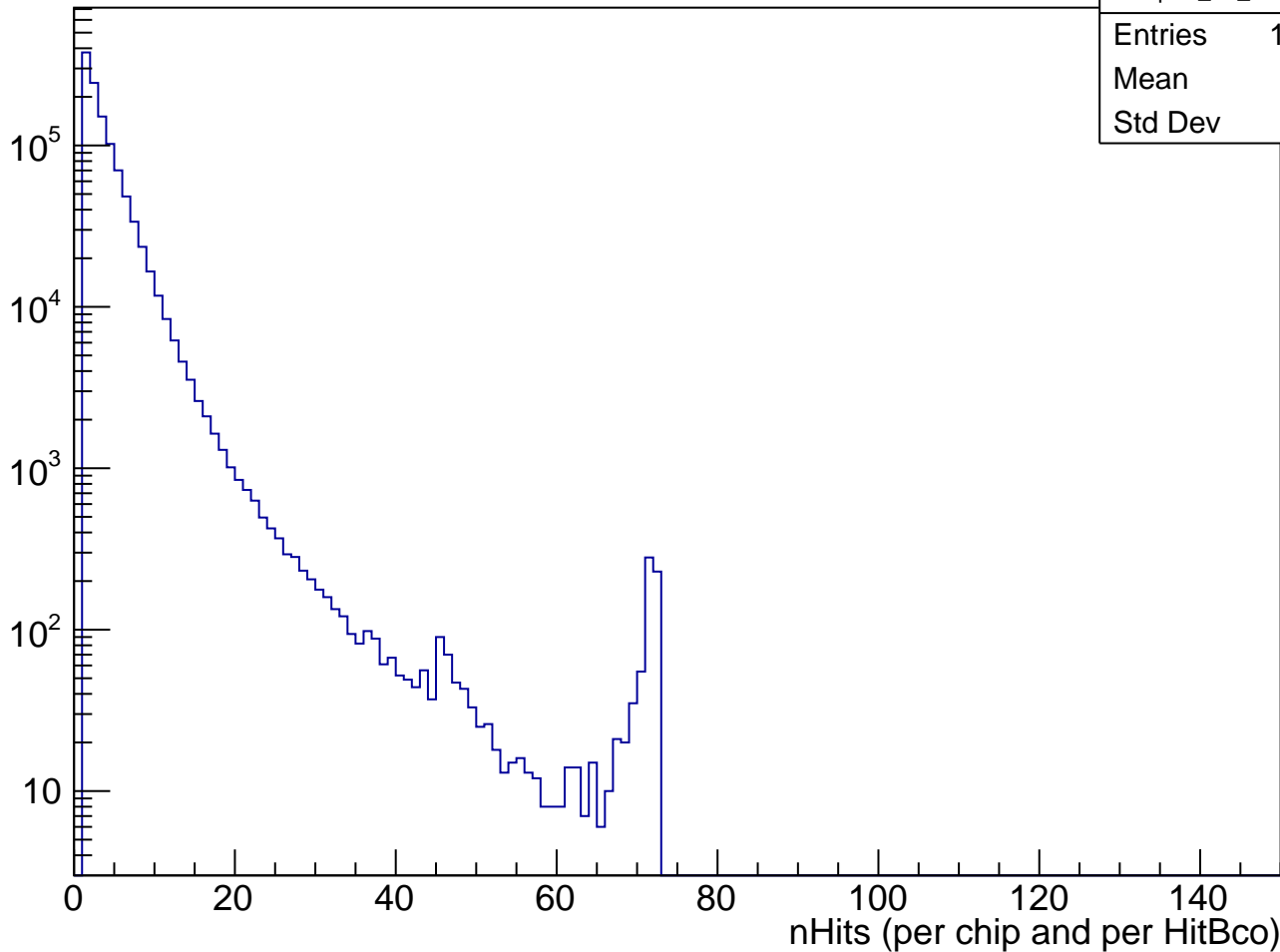
Entries



nChipHit_F1_Fch9_Chip24	
Entries	1120057
Mean	3.313
Std Dev	3.758

nChipHit_F1_Fch9_Chip25

Entries

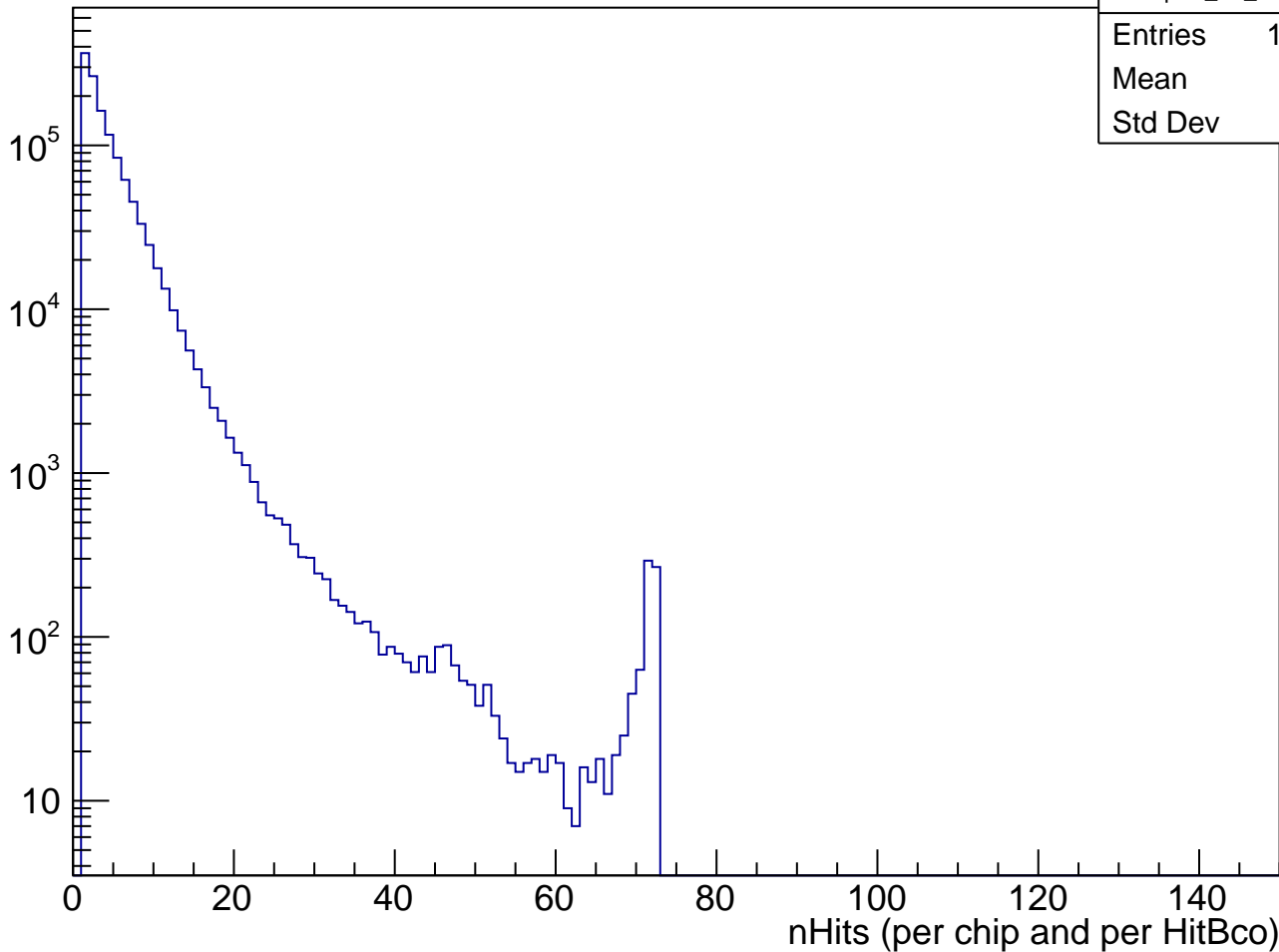


nChipHit_F1_Fch9_Chip25	
Entries	1116510
Mean	3.313
Std Dev	3.749

nHits (per chip and per HitBco)

nChipHit_F1_Fch9_Chip3

Entries



nChipHit_F1_Fch9_Chip3

Entries 1235299

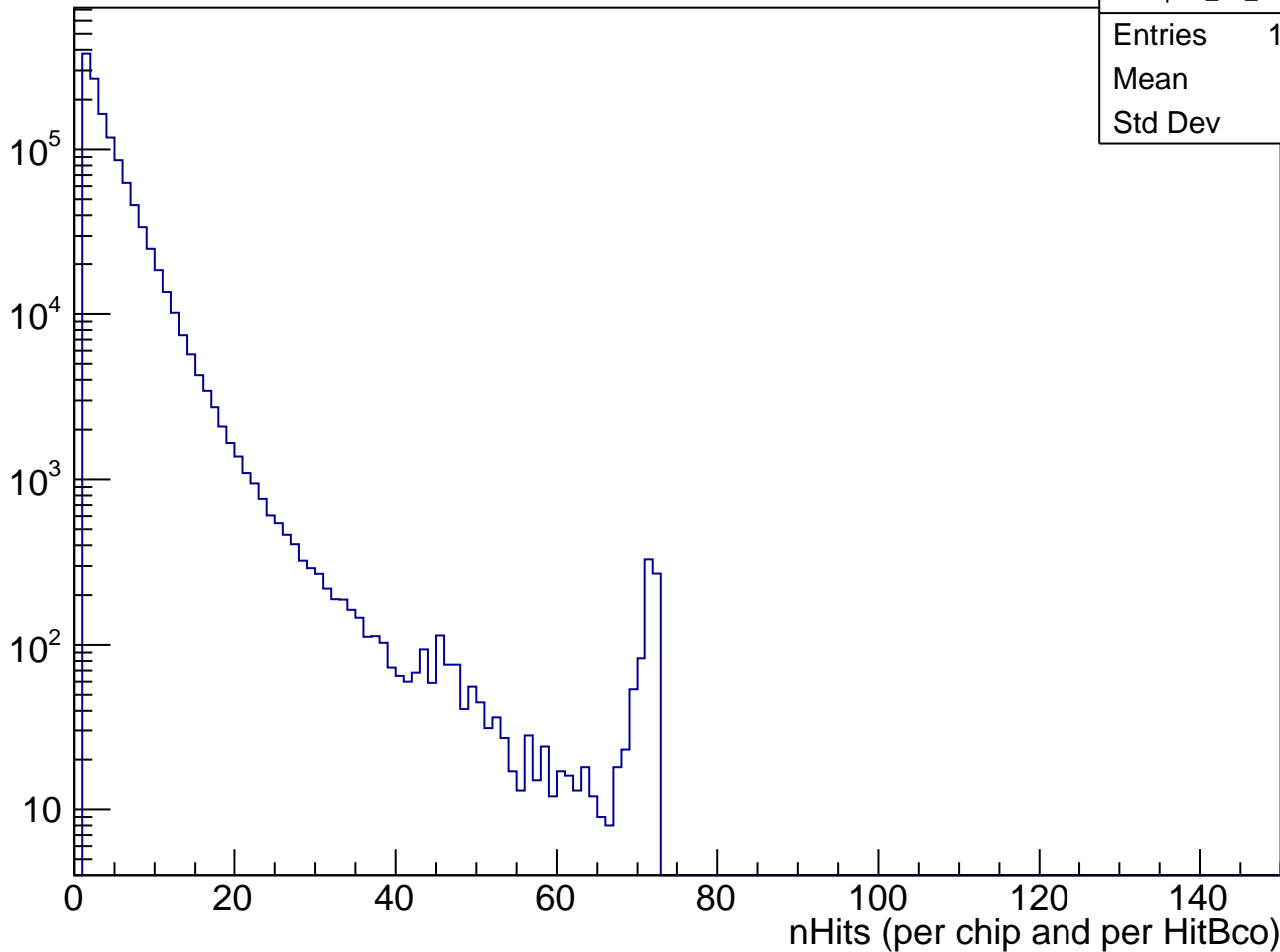
Mean 3.714

Std Dev 4.078

nHits (per chip and per HitBco)

nChipHit_F1_Fch9_Chip4

Entries



nChipHit_F1_Fch9_Chip4

Entries 1262375

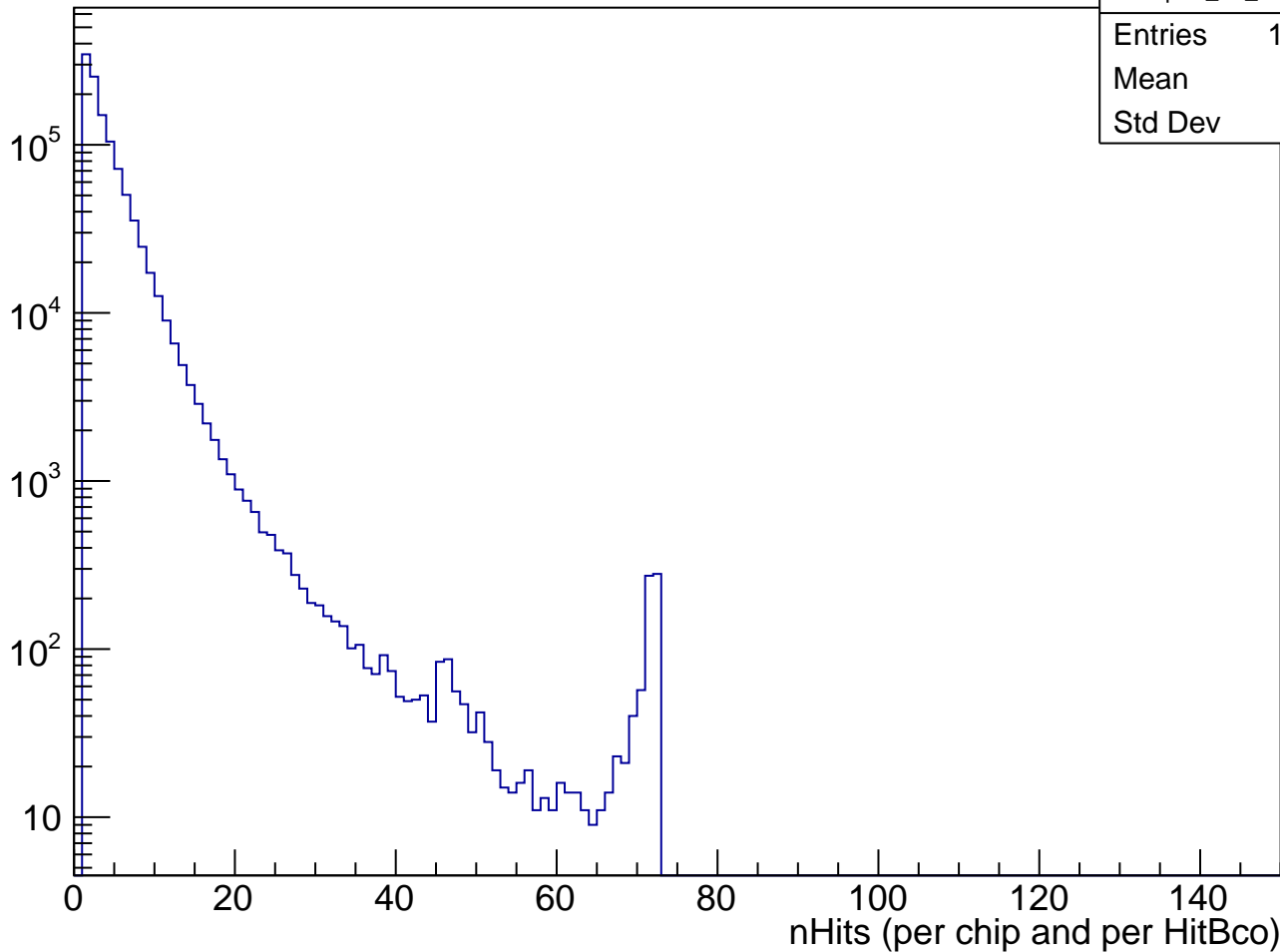
Mean 3.711

Std Dev 4.113

nHits (per chip and per HitBco)

nChipHit_F1_Fch9_Chip5

Entries



nChipHit_F1_Fch9_Chip5

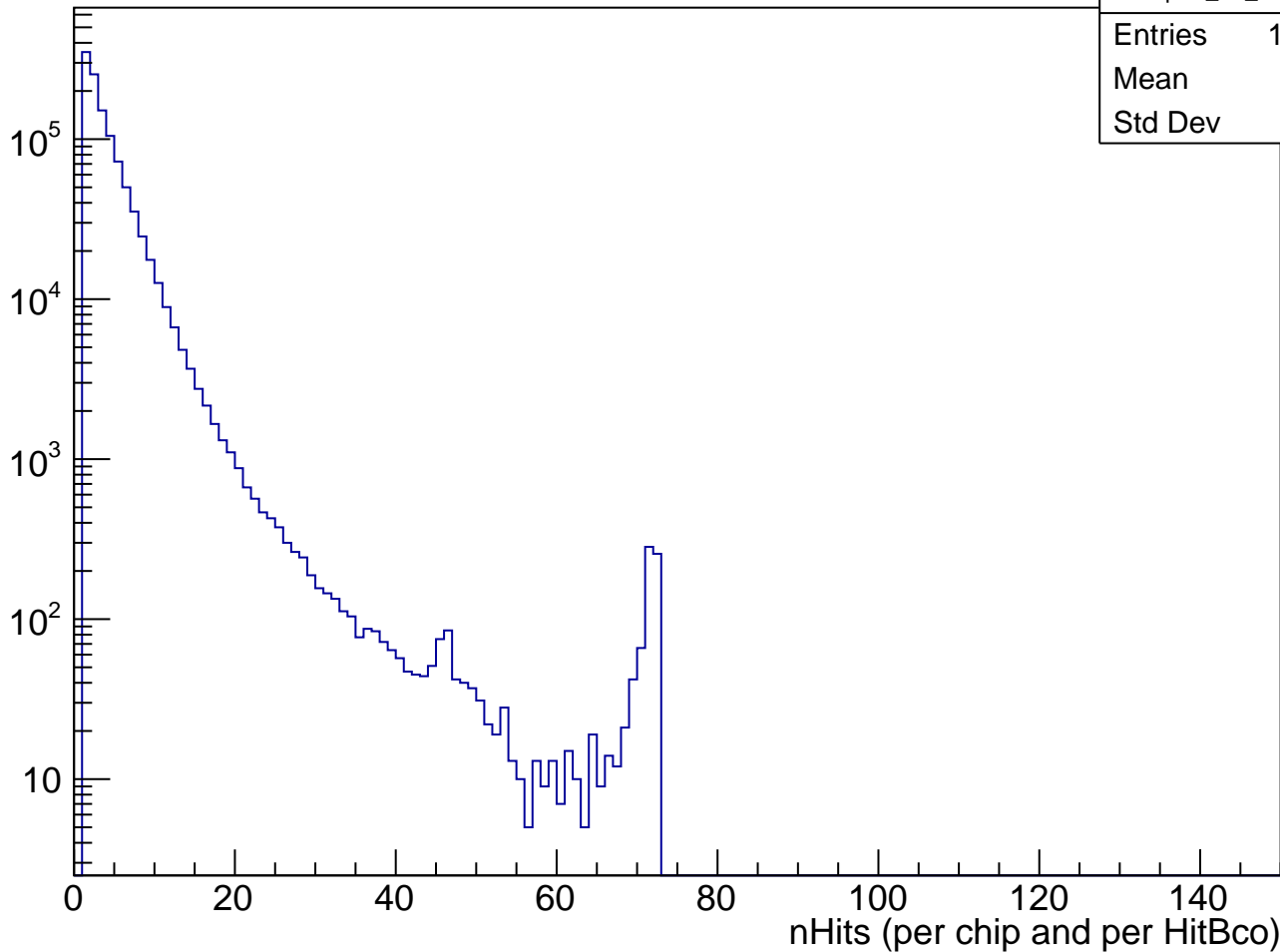
Entries 1107819

Mean 3.427

Std Dev 3.855

nChipHit_F1_Fch9_Chip6

Entries



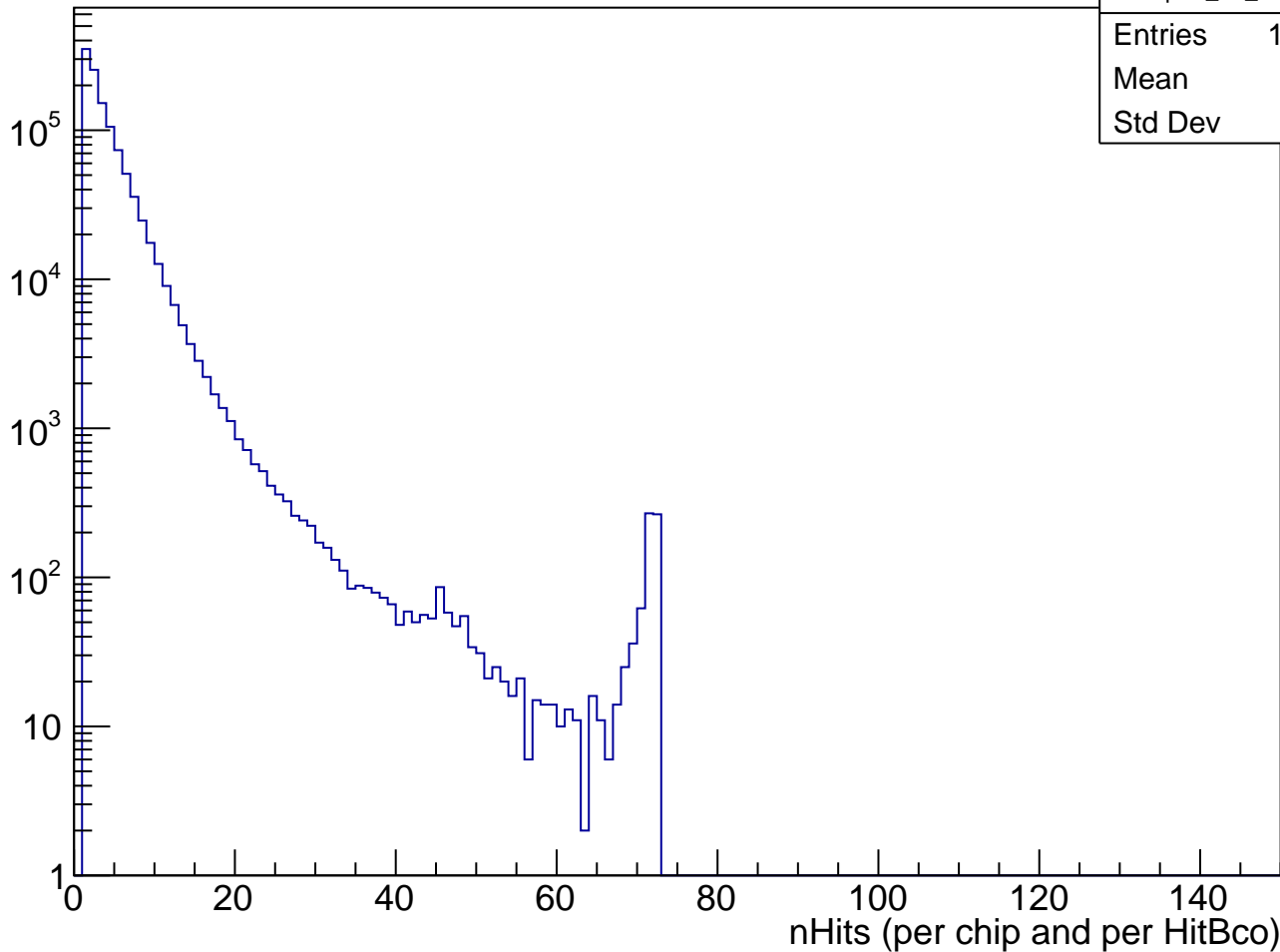
nChipHit_F1_Fch9_Chip6

Entries	1113012
Mean	3.401
Std Dev	3.792

nHits (per chip and per HitBco)

nChipHit_F1_Fch9_Chip7

Entries



nChipHit_F1_Fch9_Chip7

Entries 1118522

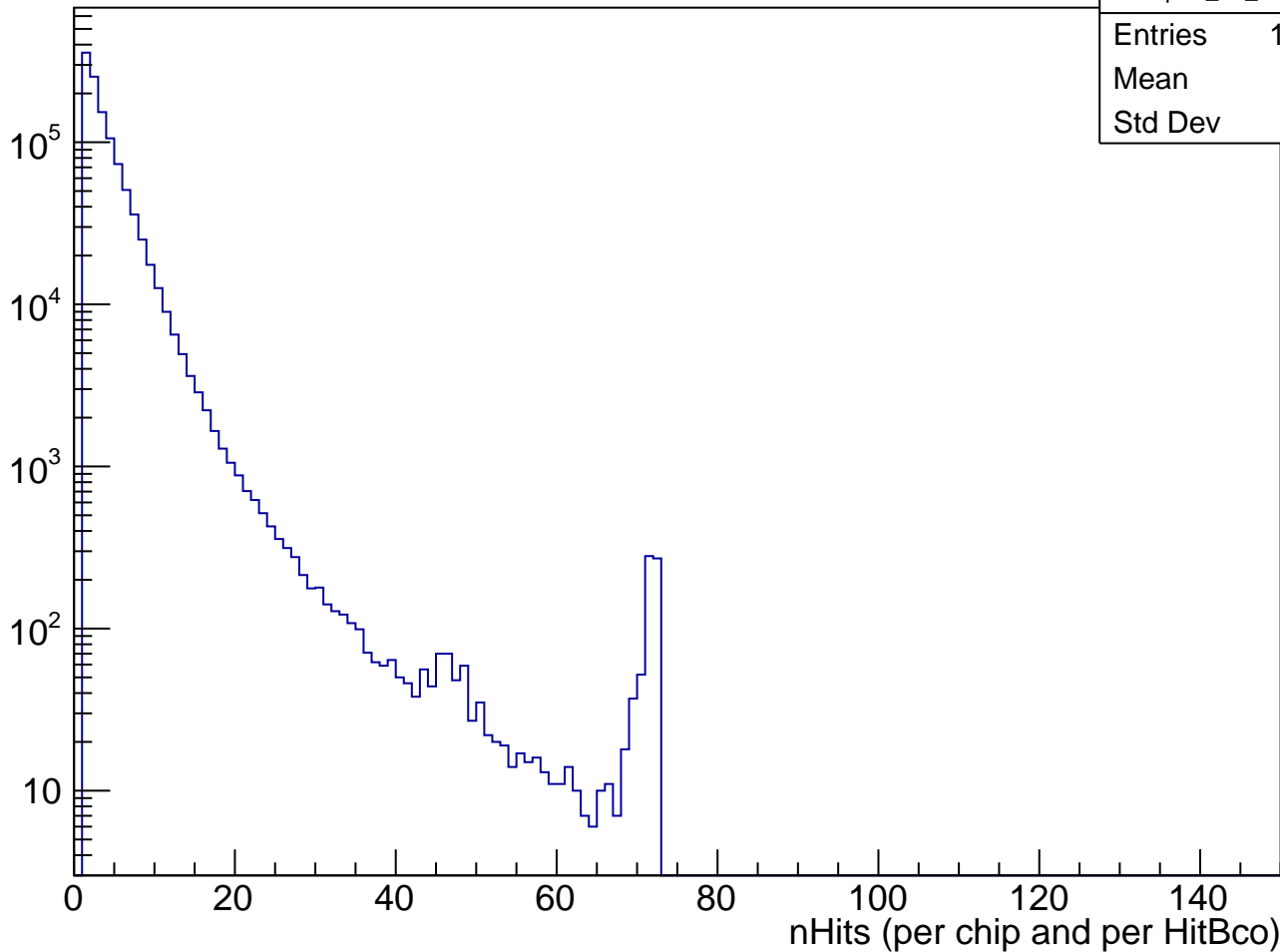
Mean 3.413

Std Dev 3.8

nHits (per chip and per HitBco)

nChipHit_F1_Fch9_Chip8

Entries



nChipHit_F1_Fch9_Chip8

Entries 1124960

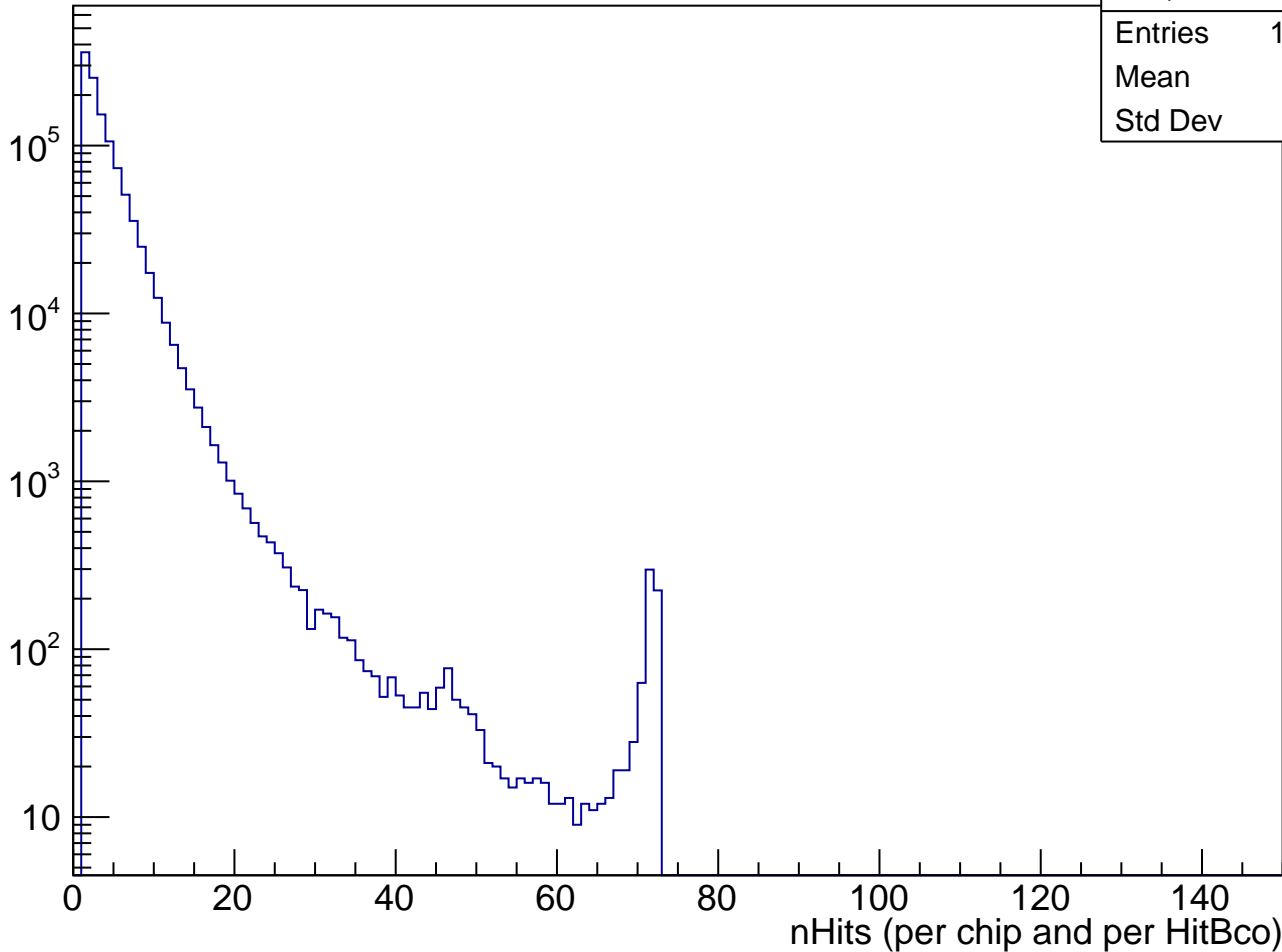
Mean 3.394

Std Dev 3.773

nHits (per chip and per HitBco)

nChipHit_F1_Fch9_Chip9

Entries



nChipHit_F1_Fch9_Chip9

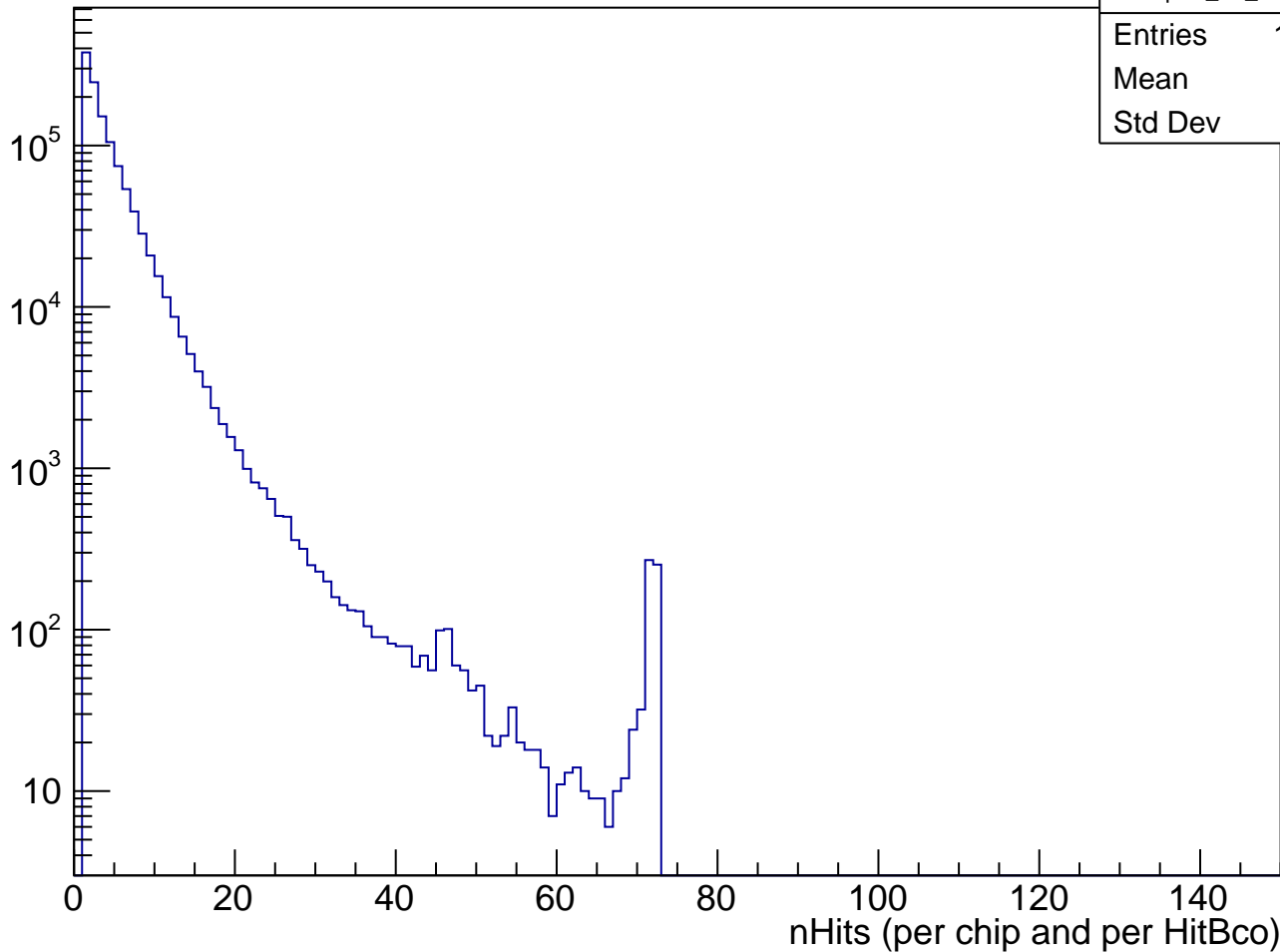
Entries 1126174

Mean 3.376

Std Dev 3.753

nChipHit_F2_Fch0_Chip0

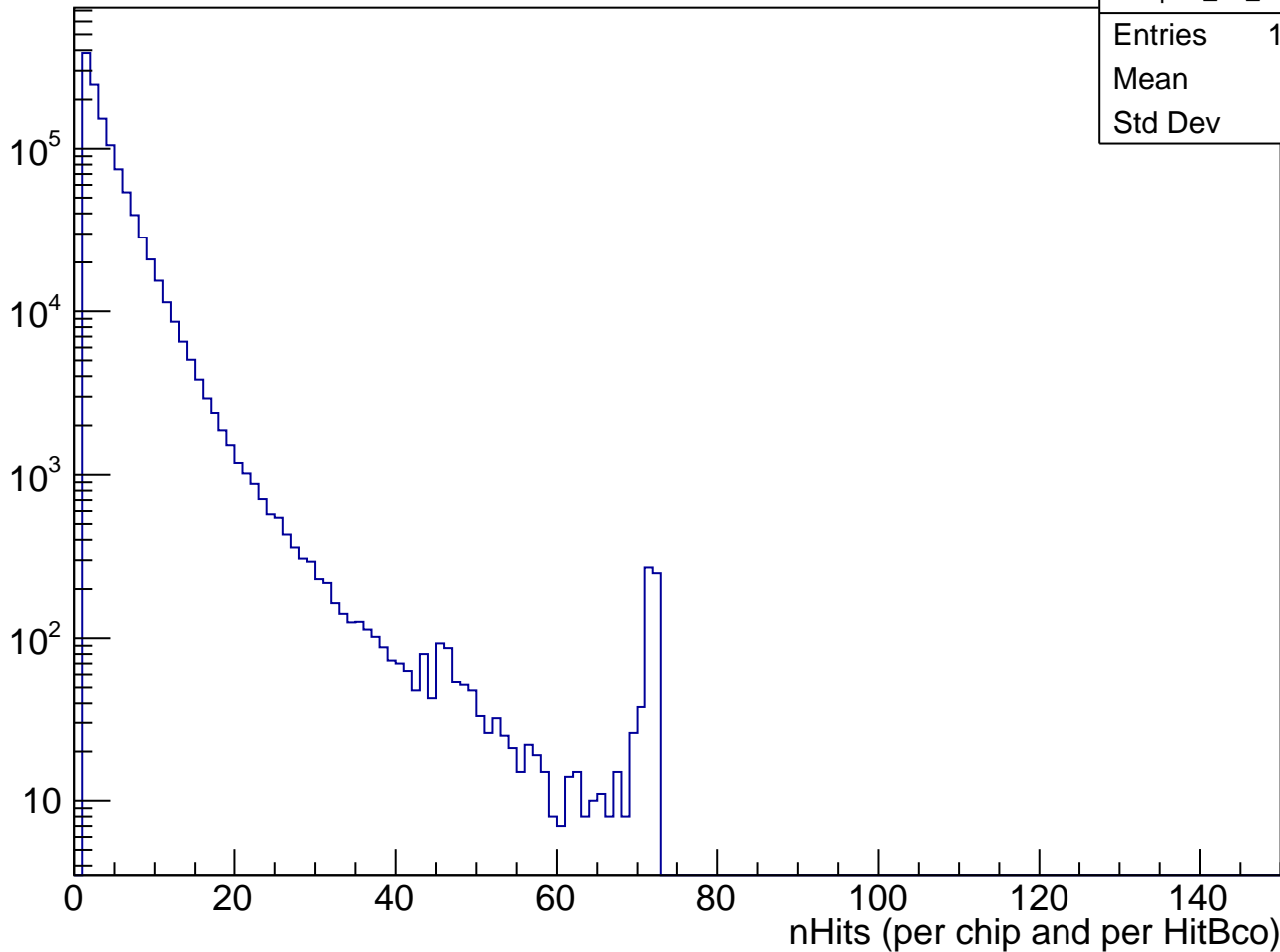
Entries



nChipHit_F2_Fch0_Chip0	
Entries	1167011
Mean	3.573
Std Dev	4.031

nChipHit_F2_Fch0_Chip1

Entries

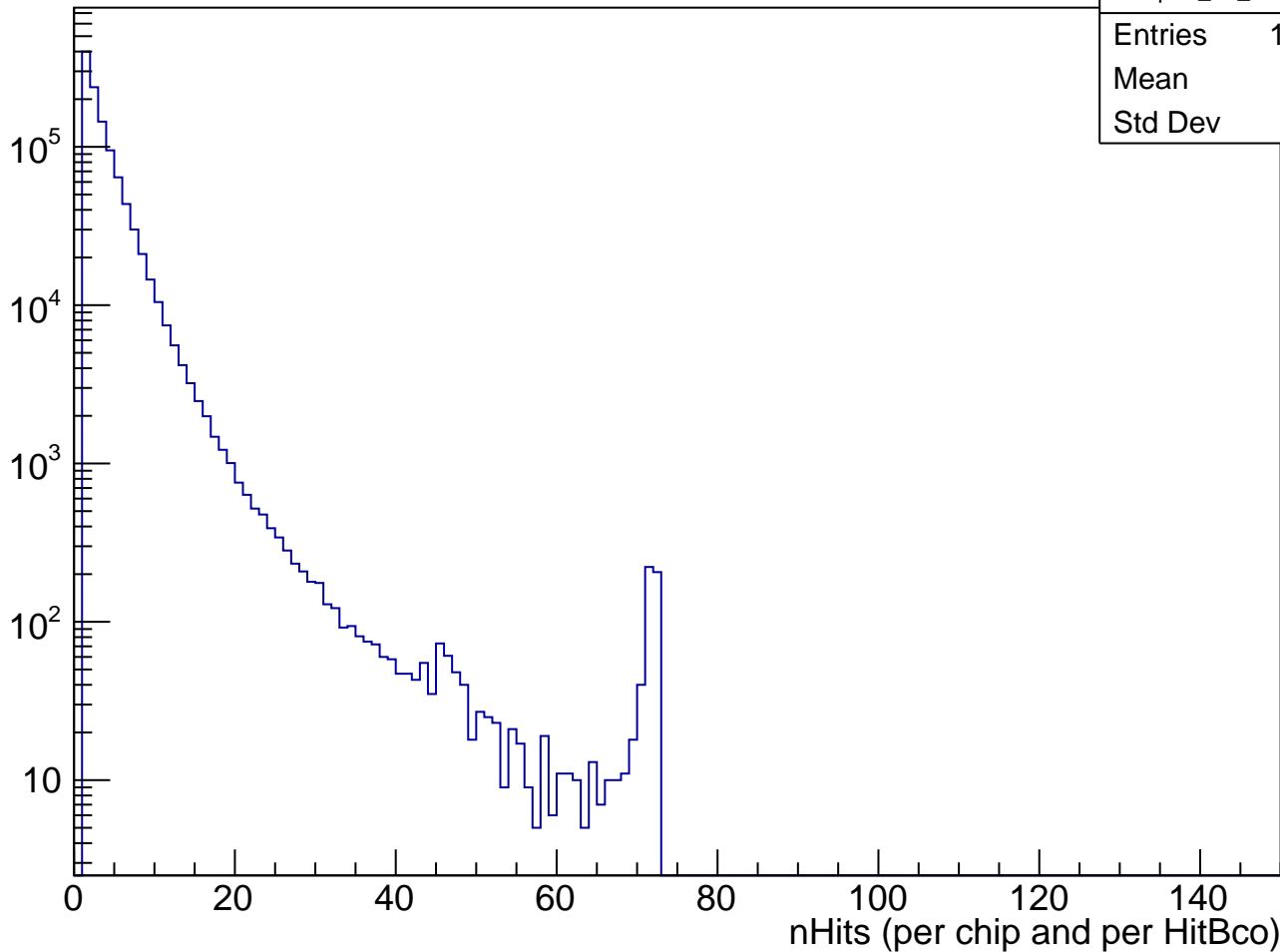


nChipHit_F2_Fch0_Chip1	
Entries	1175169
Mean	3.547
Std Dev	4.002

nHits (per chip and per HitBco)

nChipHit_F2_Fch0_Chip10

Entries

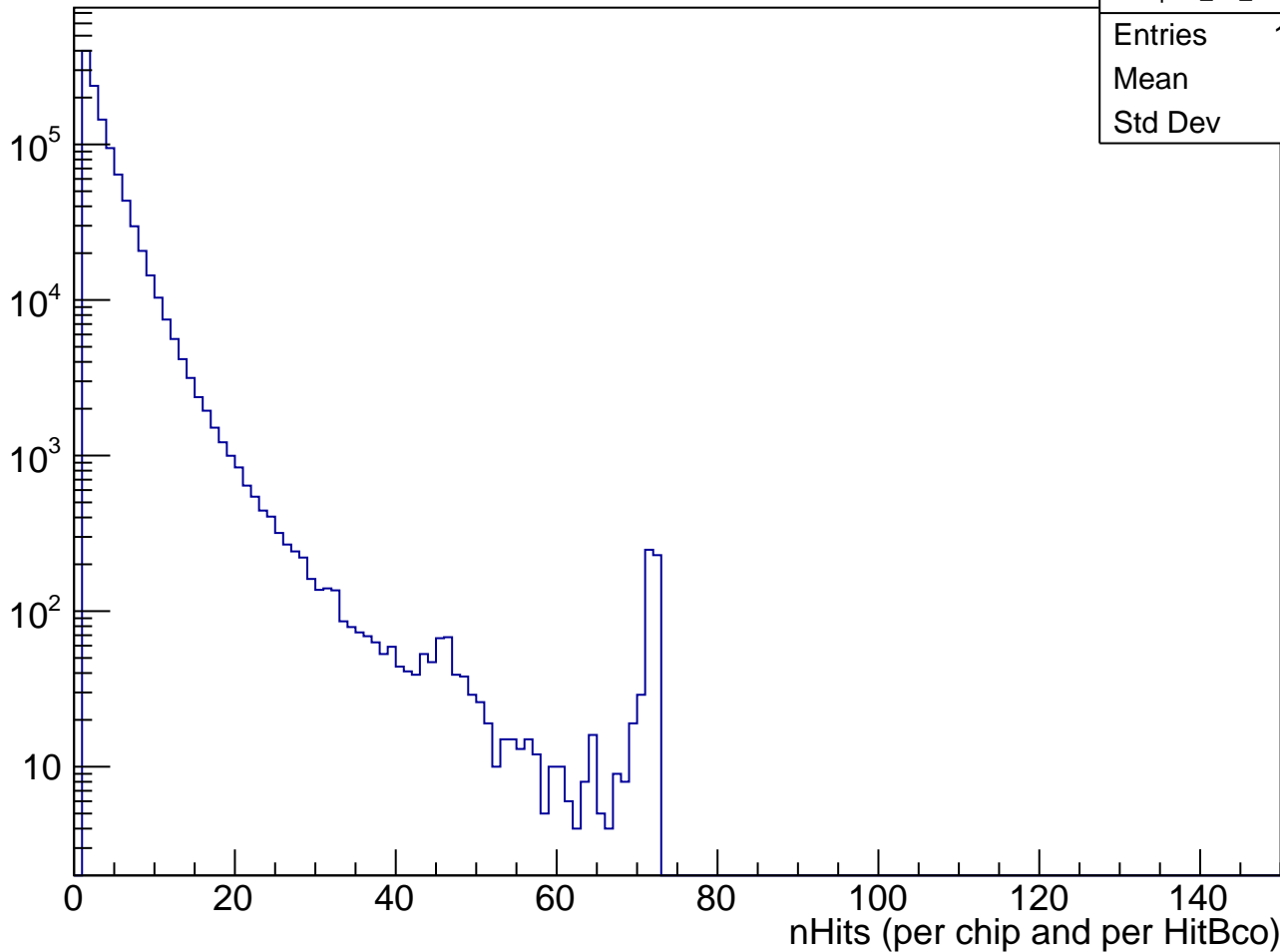


nChipHit_F2_Fch0_Chip10	
Entries	1095620
Mean	3.161
Std Dev	3.603

nHits (per chip and per HitBco)

nChipHit_F2_Fch0_Chip11

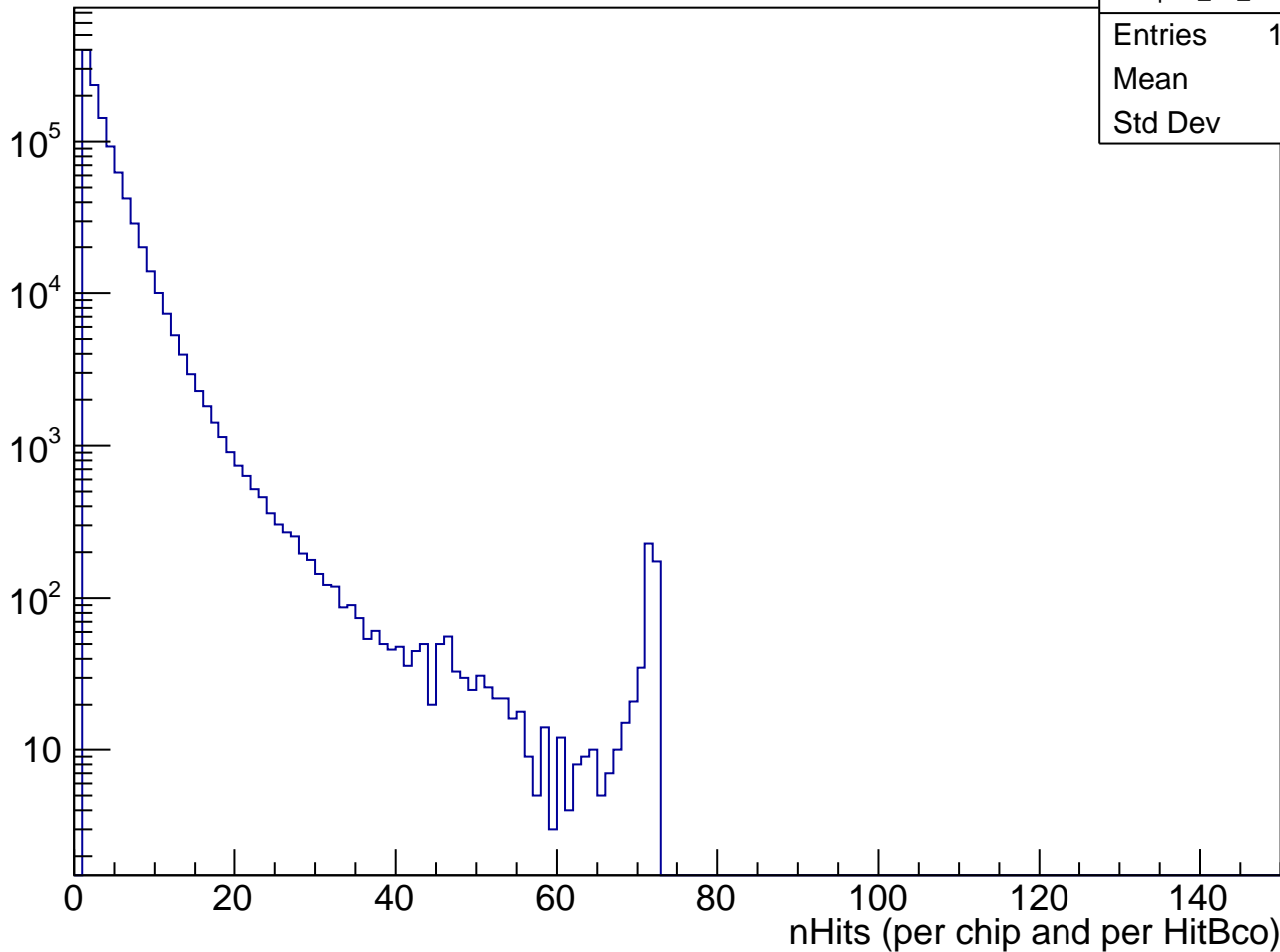
Entries



nChipHit_F2_Fch0_Chip11	
Entries	1093861
Mean	3.155
Std Dev	3.602

nChipHit_F2_Fch0_Chip12

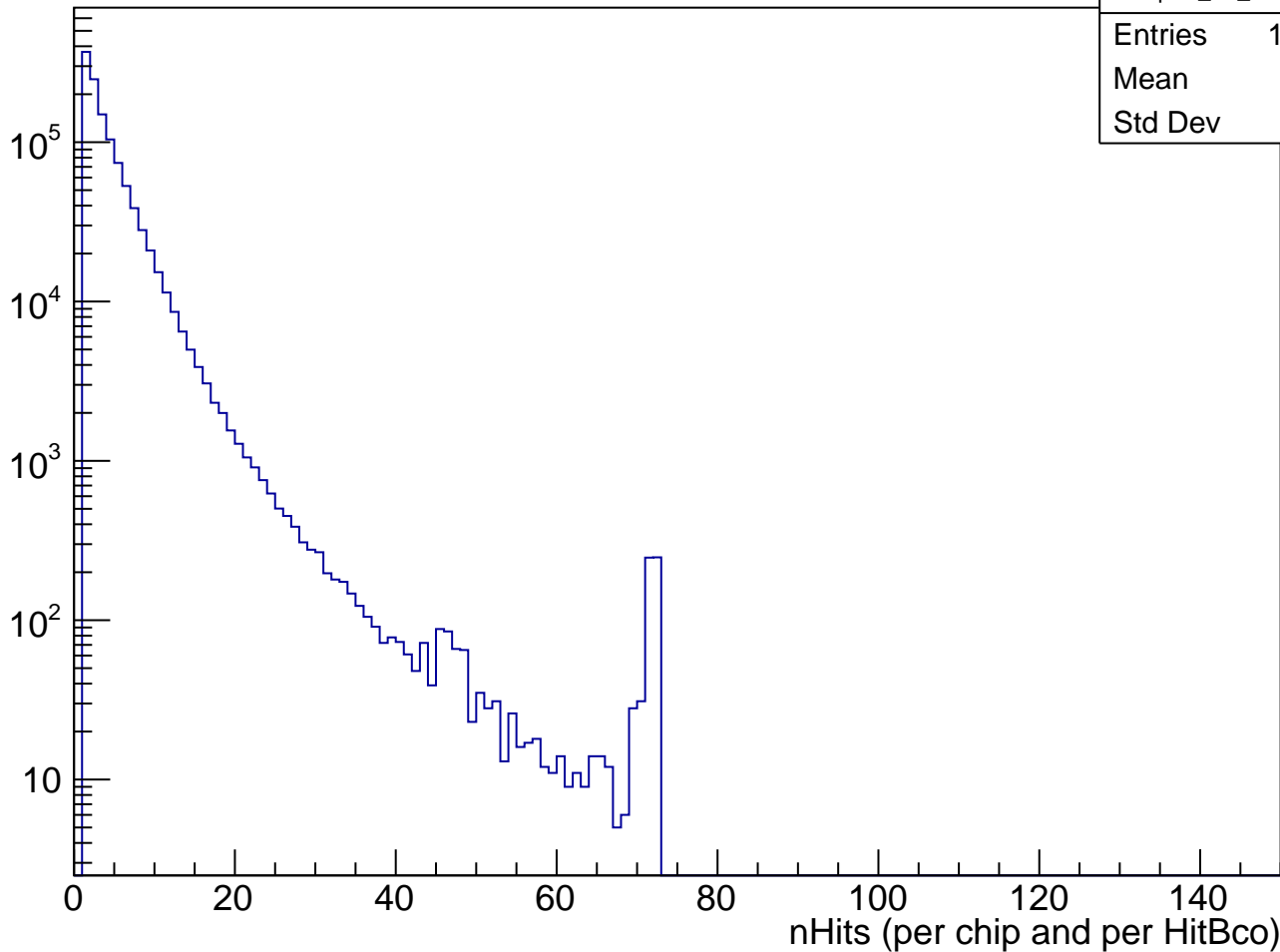
Entries



nChipHit_F2_Fch0_Chip12	
Entries	1080399
Mean	3.119
Std Dev	3.532

nChipHit_F2_Fch0_Chip13

Entries

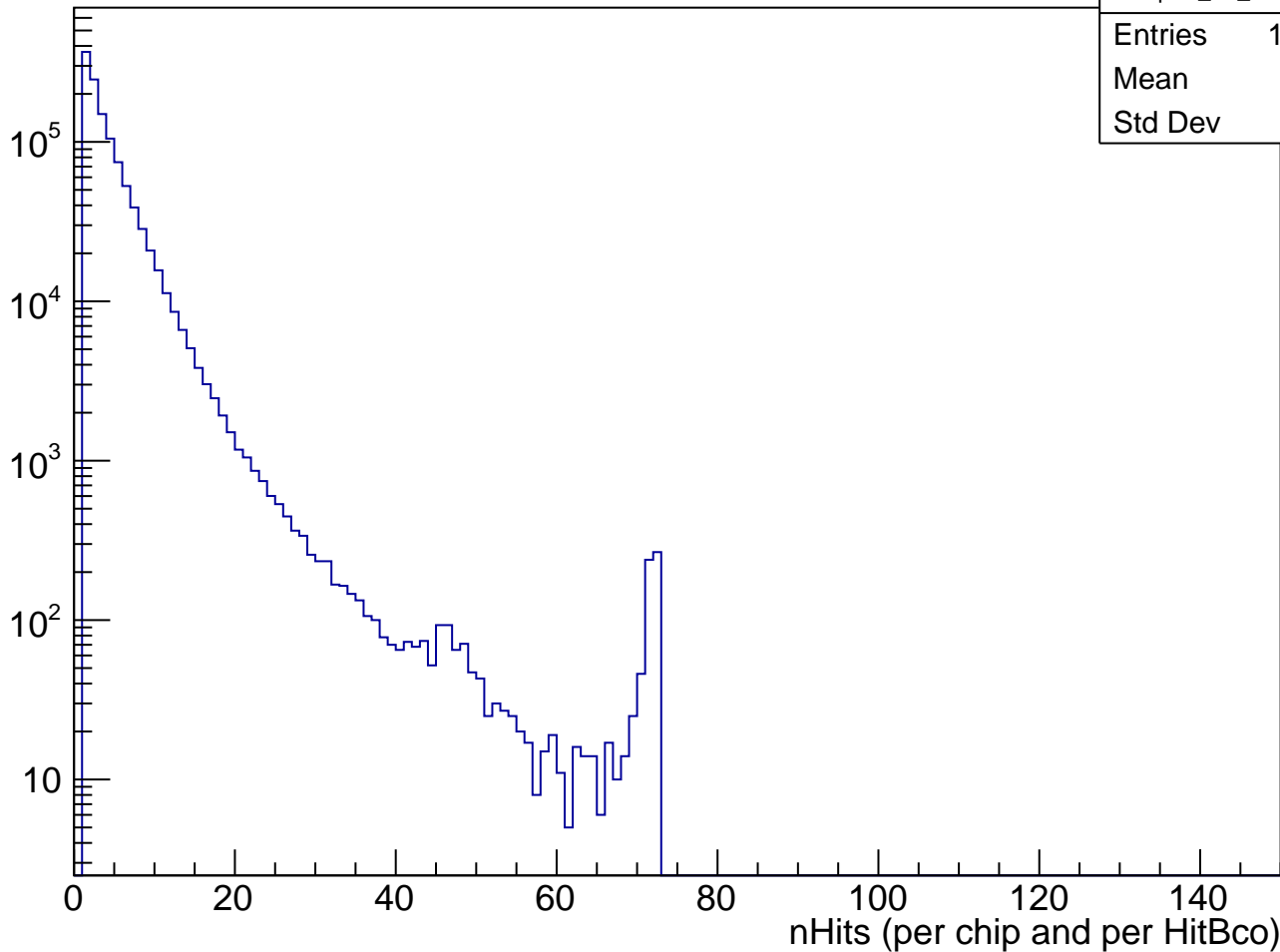


nChipHit_F2_Fch0_Chip13	
Entries	1154489
Mean	3.58
Std Dev	4.019

nHits (per chip and per HitBco)

nChipHit_F2_Fch0_Chip14

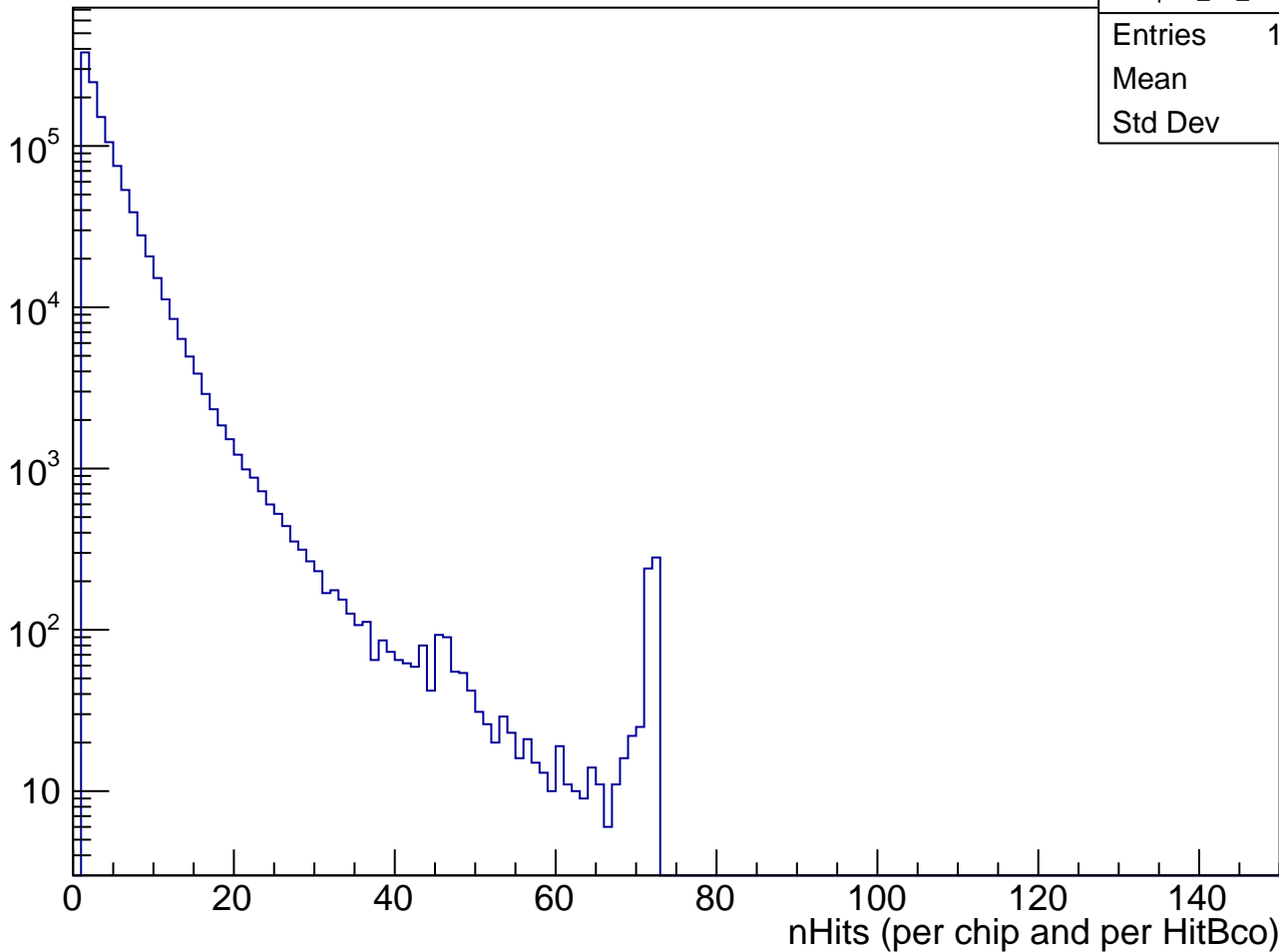
Entries



nChipHit_F2_Fch0_Chip14	
Entries	1152676
Mean	3.594
Std Dev	4.055

nChipHit_F2_Fch0_Chip15

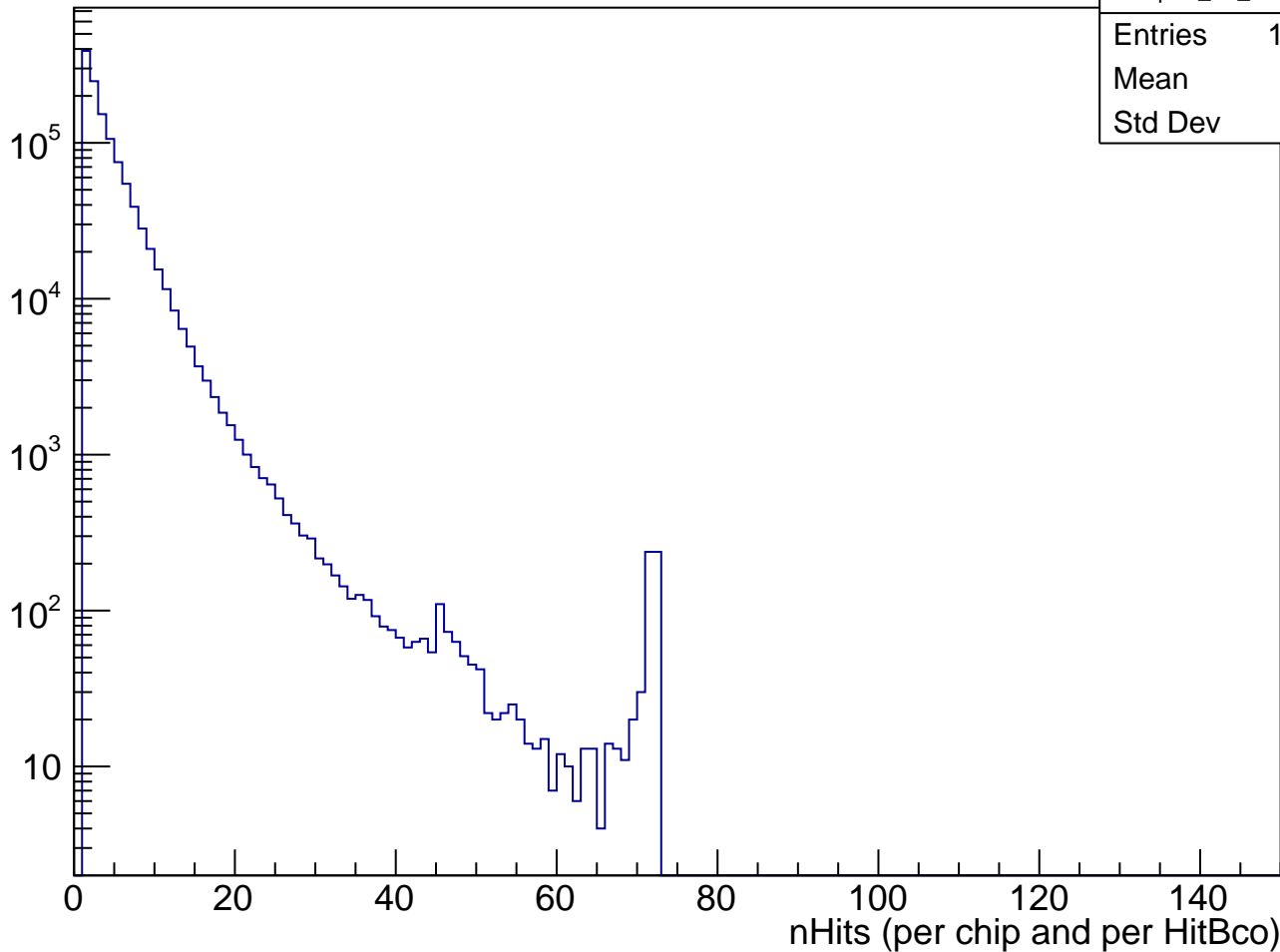
Entries



nChipHit_F2_Fch0_Chip15	
Entries	1169498
Mean	3.542
Std Dev	3.985

nChipHit_F2_Fch0_Chip16

Entries

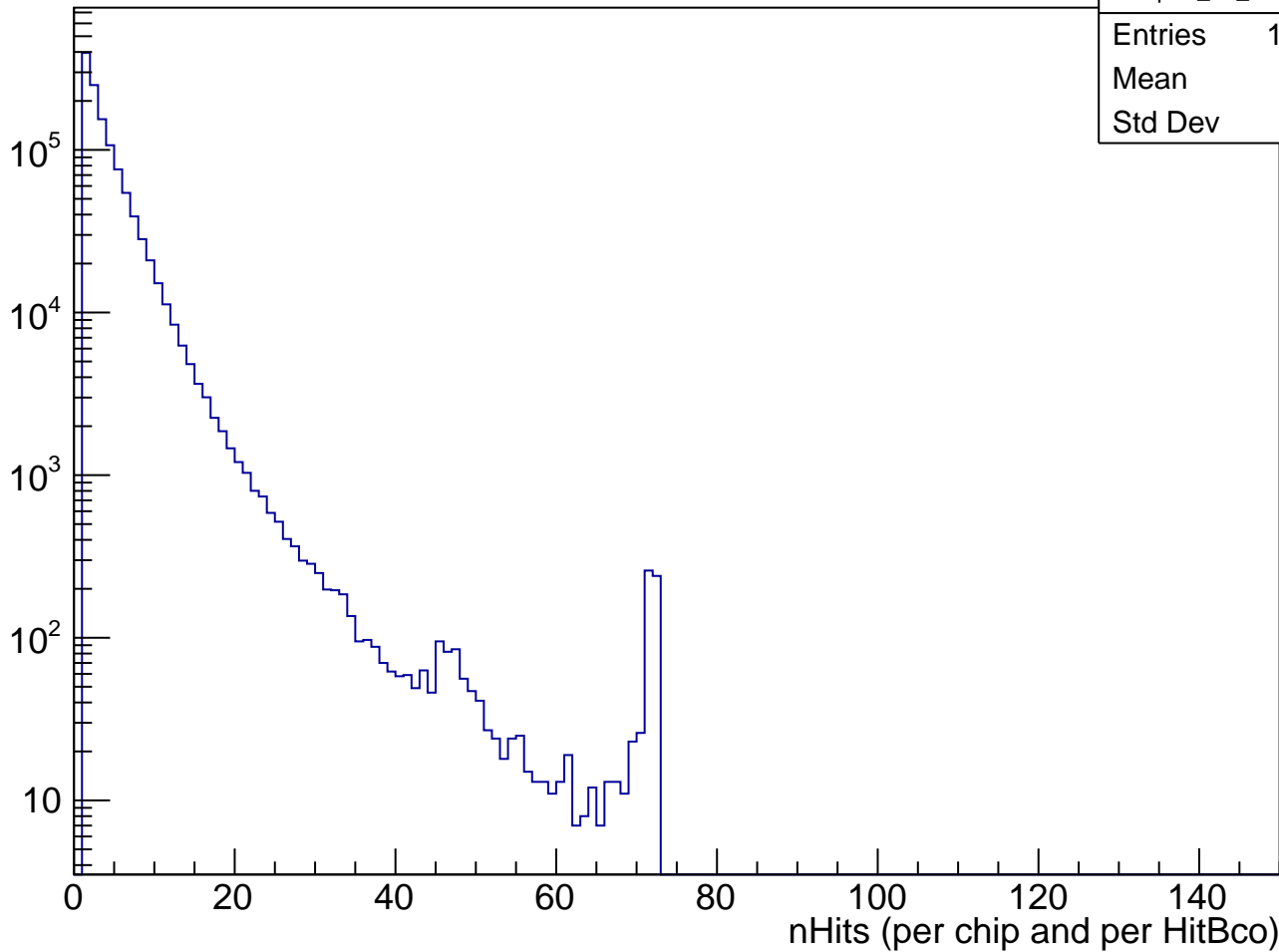


nChipHit_F2_Fch0_Chip16	
Entries	1182382
Mean	3.529
Std Dev	3.954

nHits (per chip and per HitBco)

nChipHit_F2_Fch0_Chip17

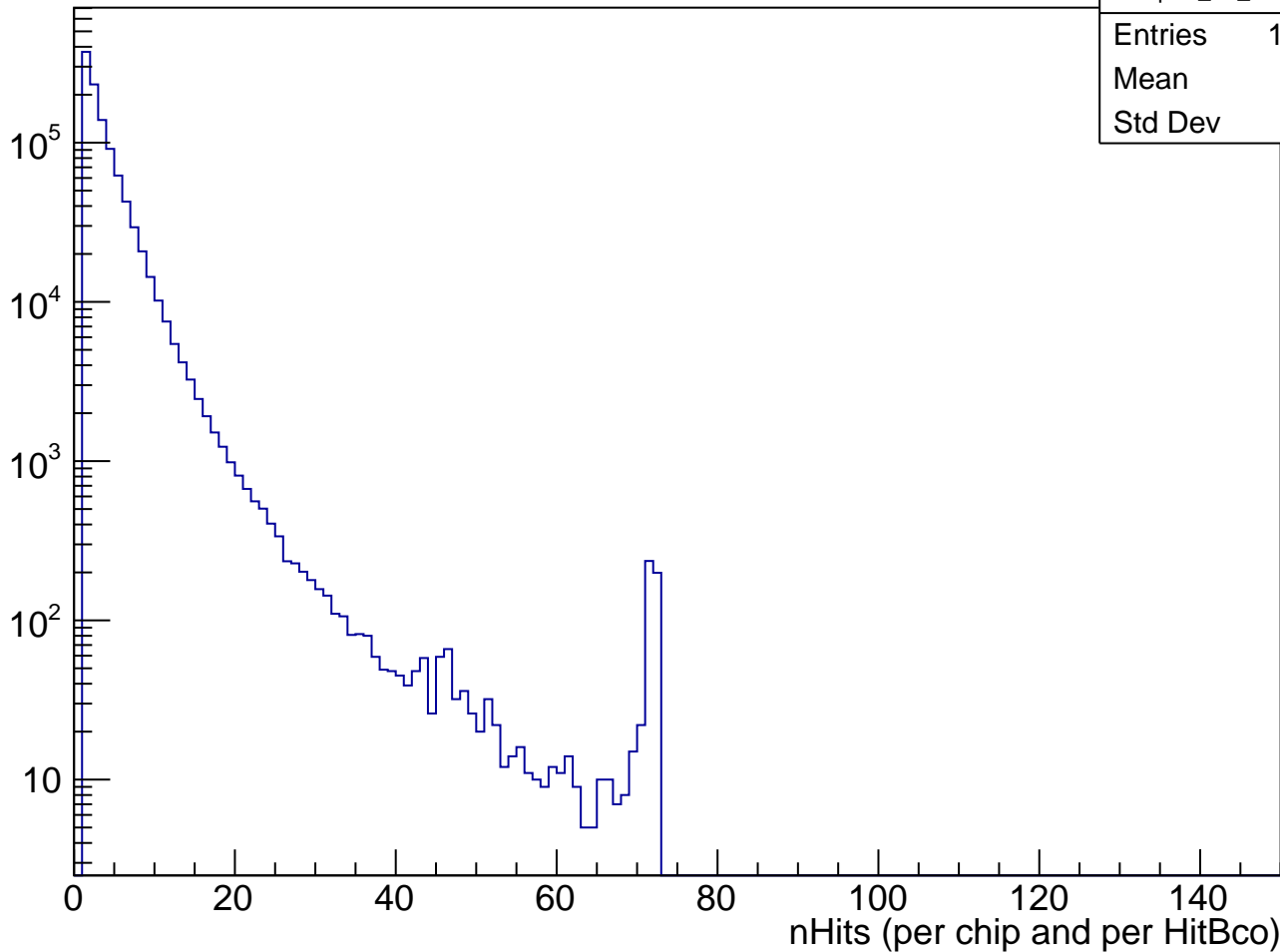
Entries



nChipHit_F2_Fch0_Chip17	
Entries	1192944
Mean	3.507
Std Dev	3.946

nChipHit_F2_Fch0_Chip18

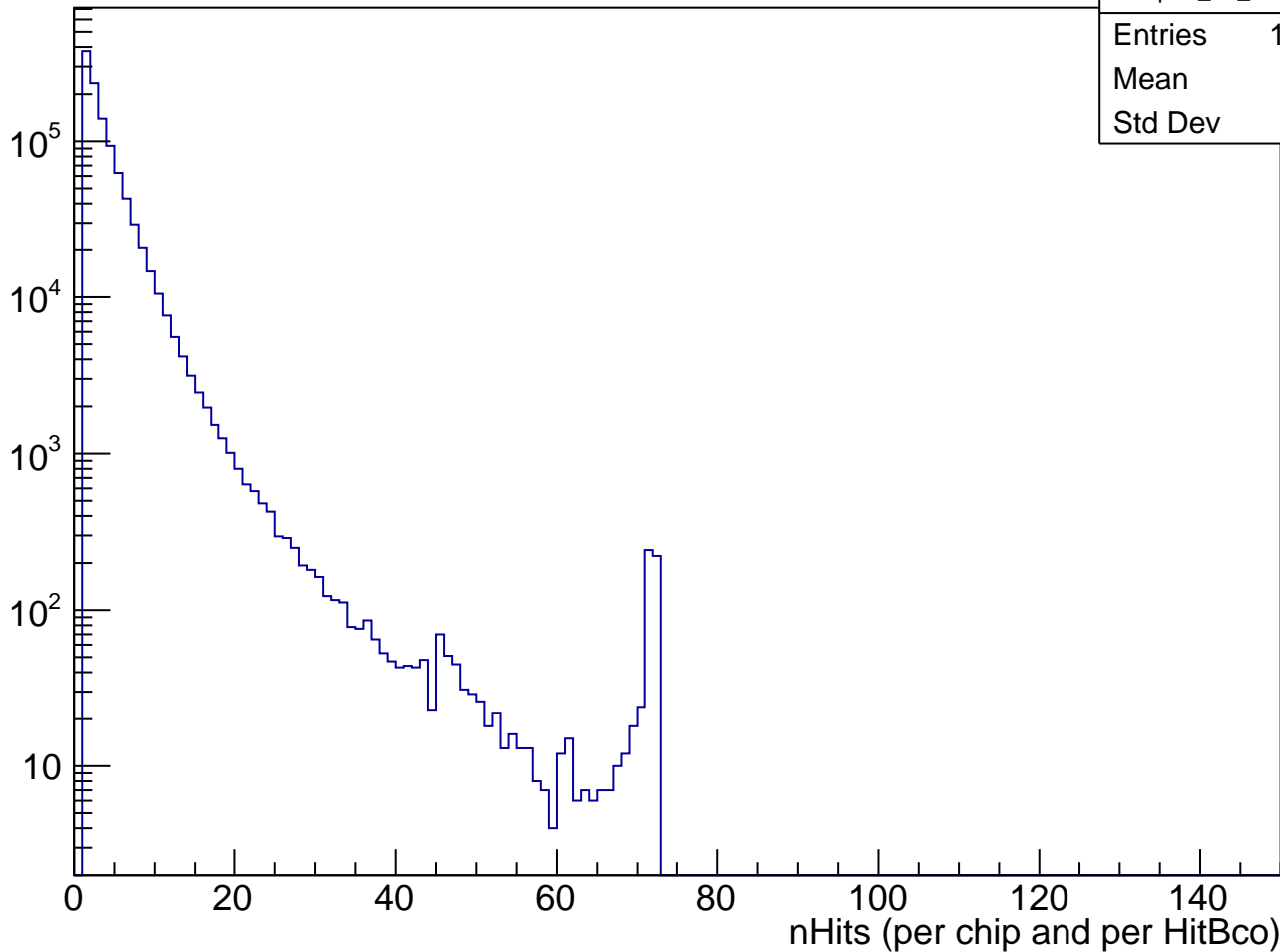
Entries



nChipHit_F2_Fch0_Chip18	
Entries	1048338
Mean	3.207
Std Dev	3.633

nChipHit_F2_Fch0_Chip19

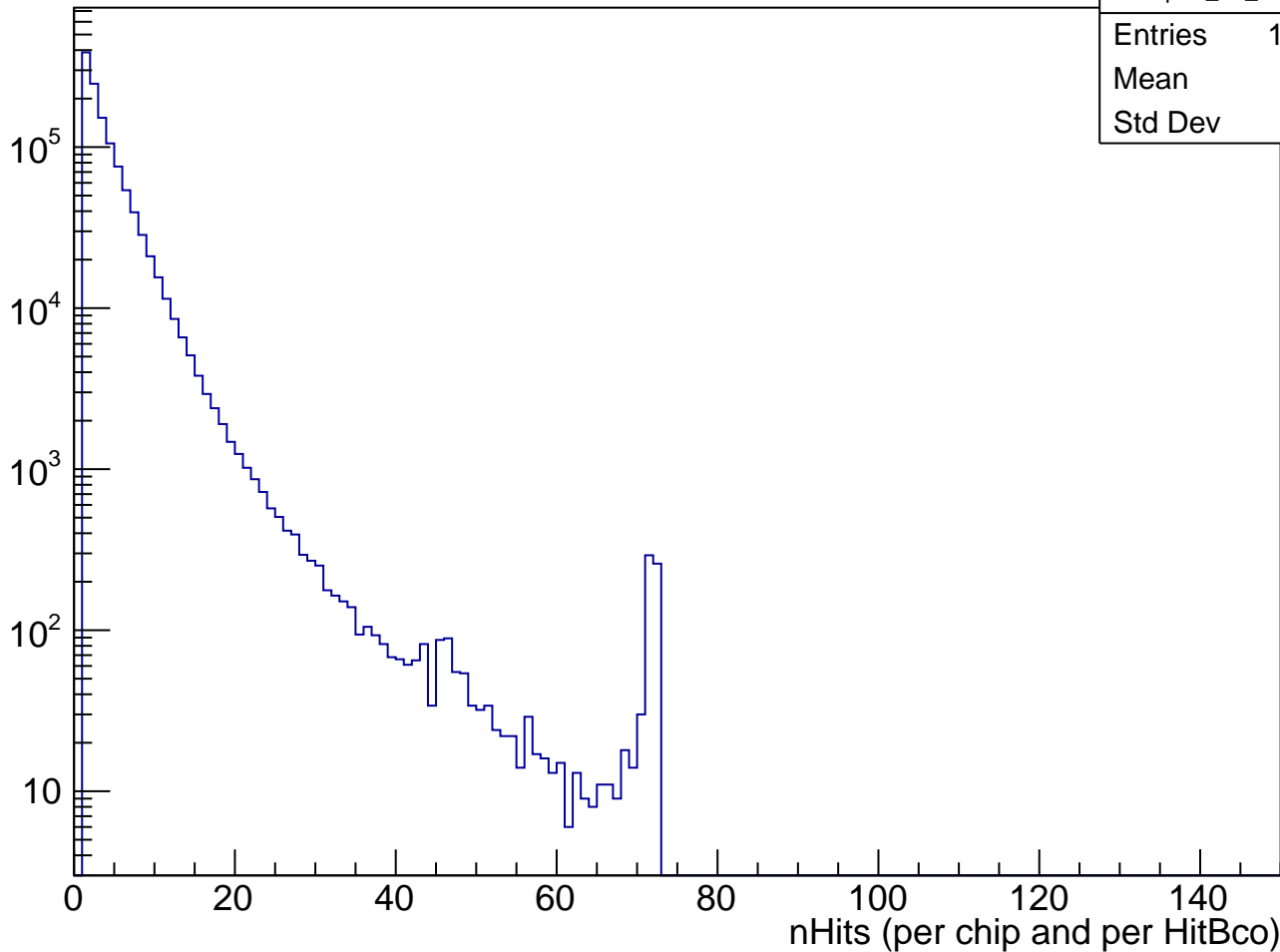
Entries



nChipHit_F2_Fch0_Chip19	
Entries	1060960
Mean	3.203
Std Dev	3.638

nChipHit_F2_Fch0_Chip2

Entries



nChipHit_F2_Fch0_Chip2

Entries 1179928

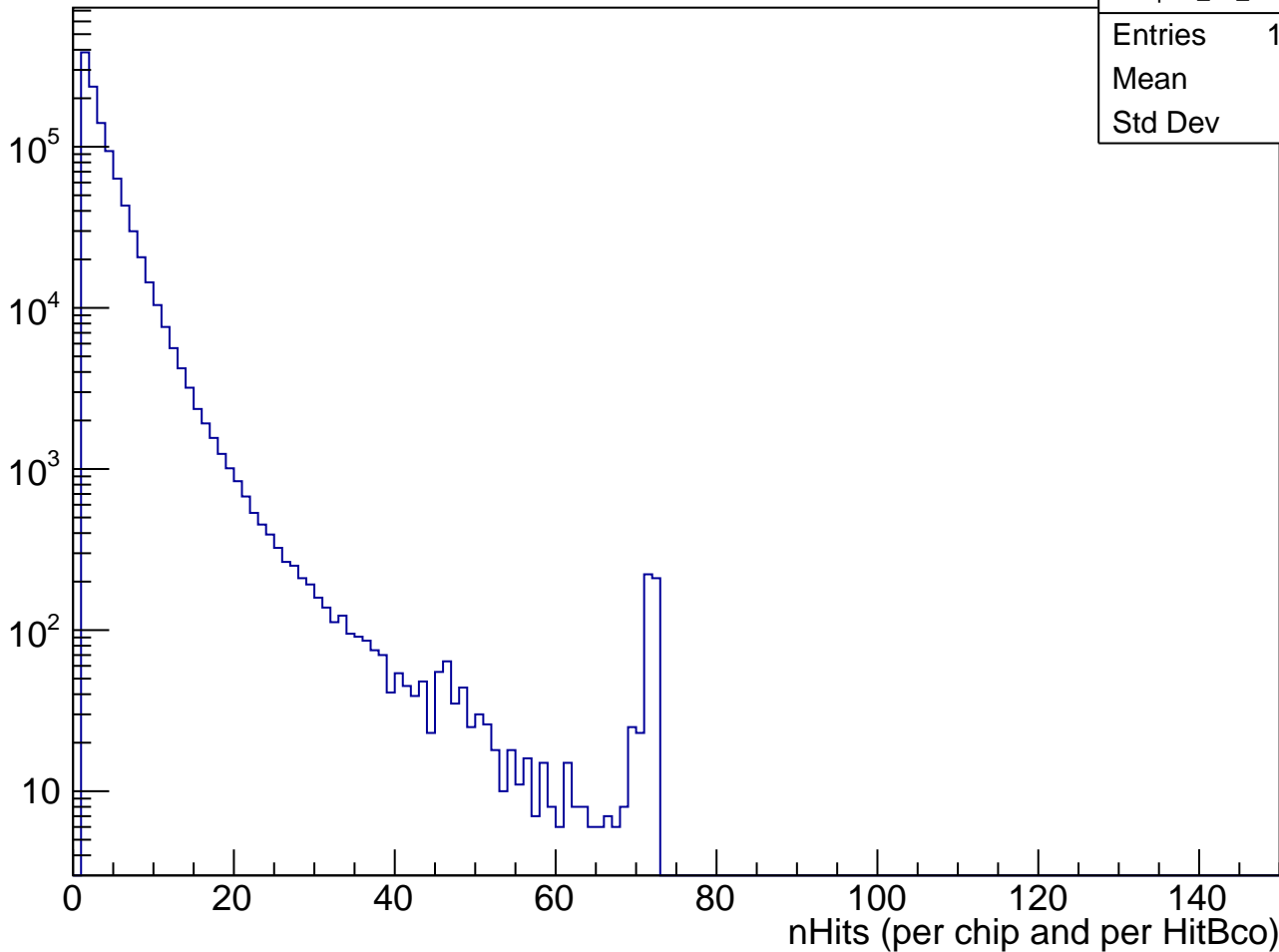
Mean 3.544

Std Dev 3.998

nHits (per chip and per HitBco)

nChipHit_F2_Fch0_Chip20

Entries

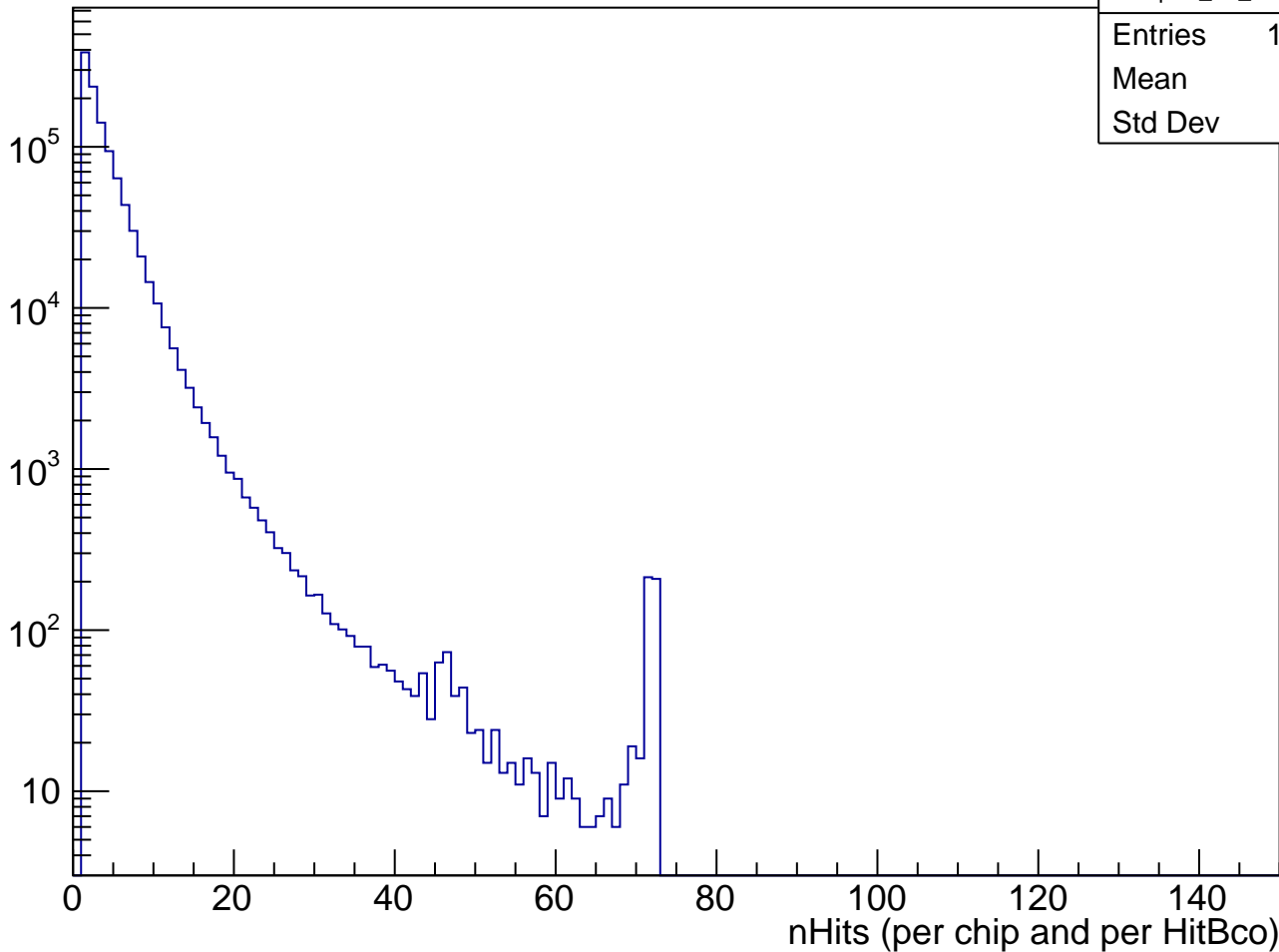


nChipHit_F2_Fch0_Chip20	
Entries	1073188
Mean	3.187
Std Dev	3.618

nHits (per chip and per HitBco)

nChipHit_F2_Fch0_Chip21

Entries

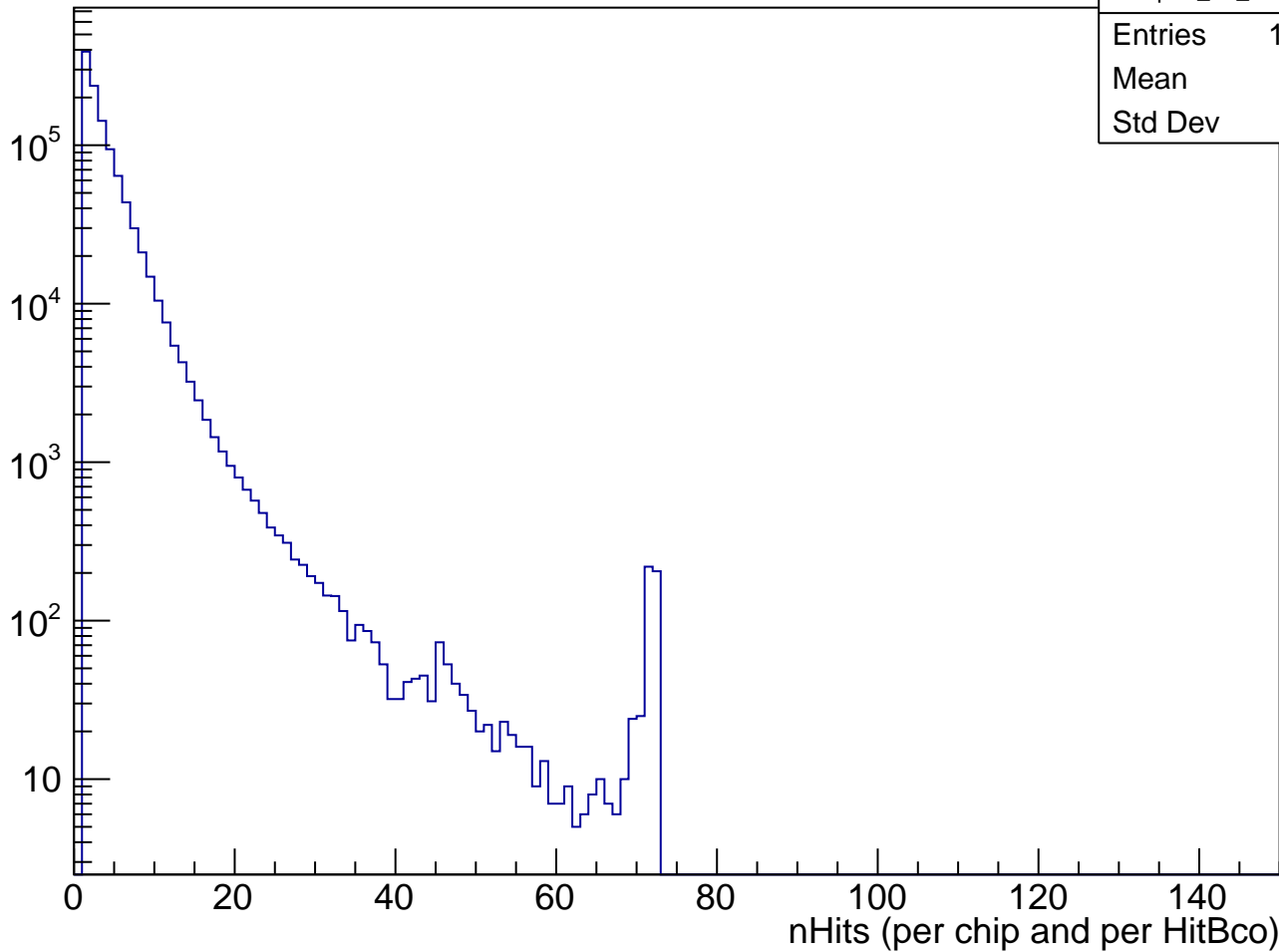


nChipHit_F2_Fch0_Chip21	
Entries	1076018
Mean	3.189
Std Dev	3.603

nHits (per chip and per HitBco)

nChipHit_F2_Fch0_Chip22

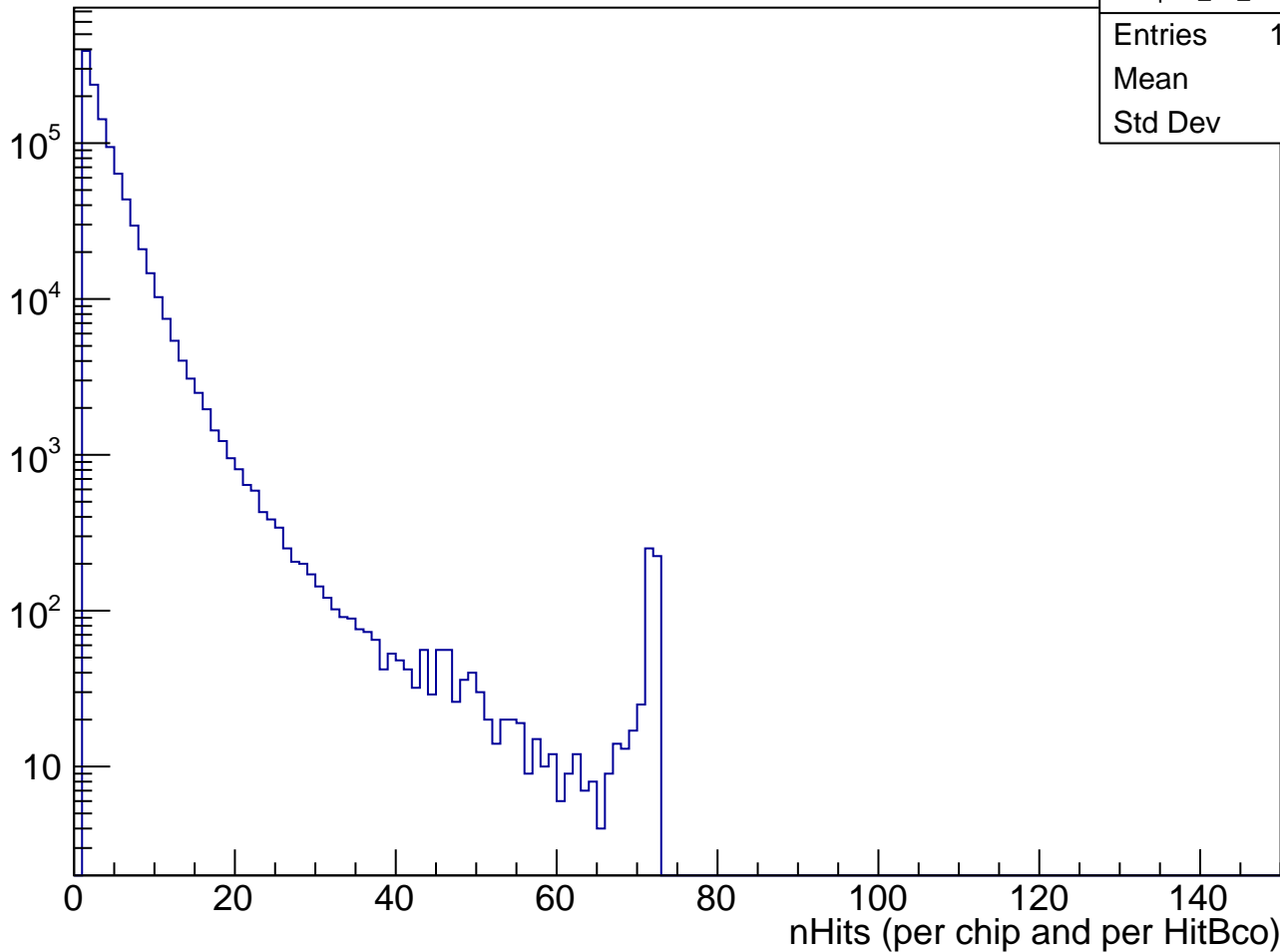
Entries



nChipHit_F2_Fch0_Chip22	
Entries	1082043
Mean	3.181
Std Dev	3.6

nChipHit_F2_Fch0_Chip23

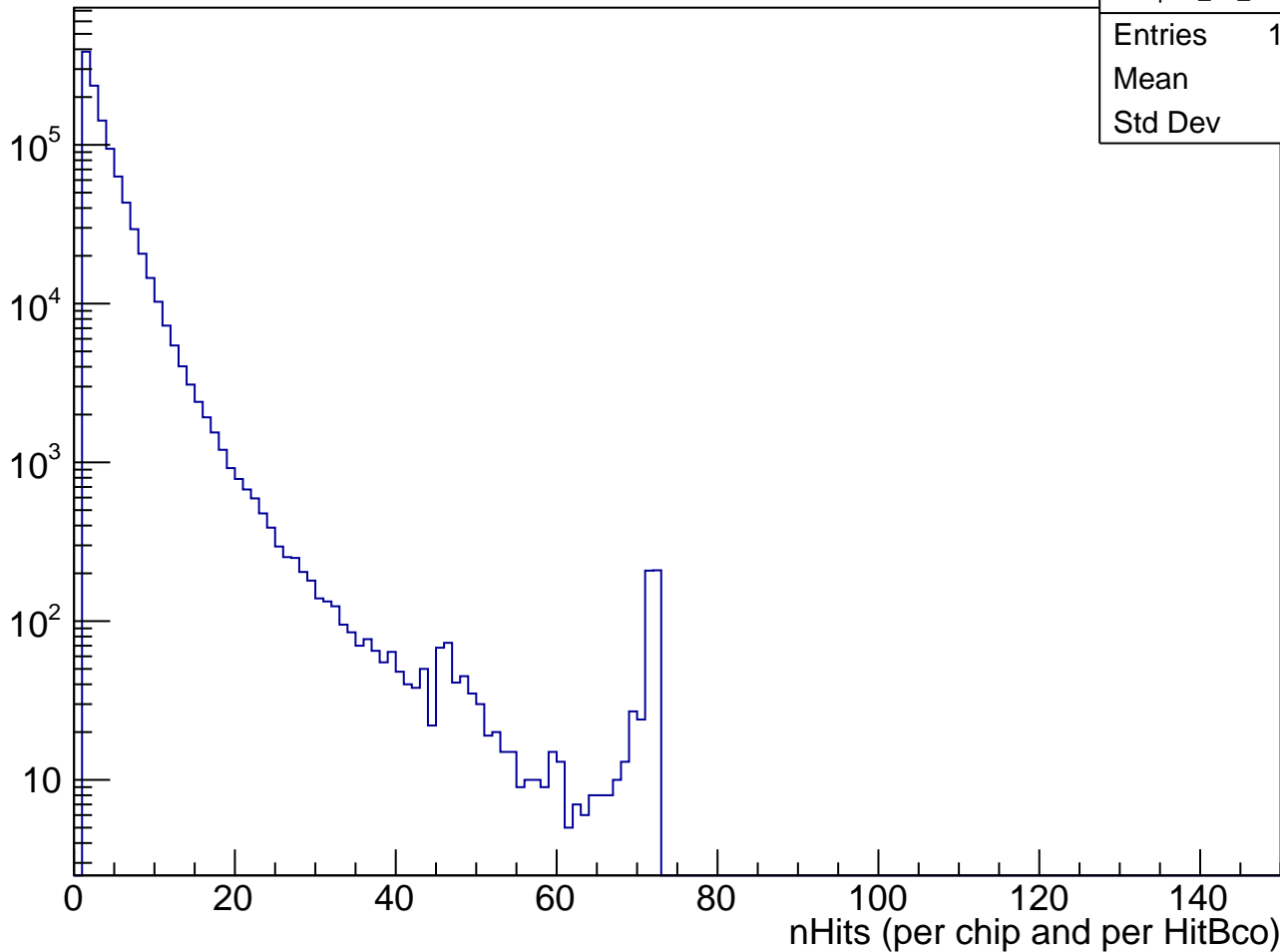
Entries



nChipHit_F2_Fch0_Chip23	
Entries	1081270
Mean	3.167
Std Dev	3.606

nChipHit_F2_Fch0_Chip24

Entries



nChipHit_F2_Fch0_Chip24

Entries 1074102

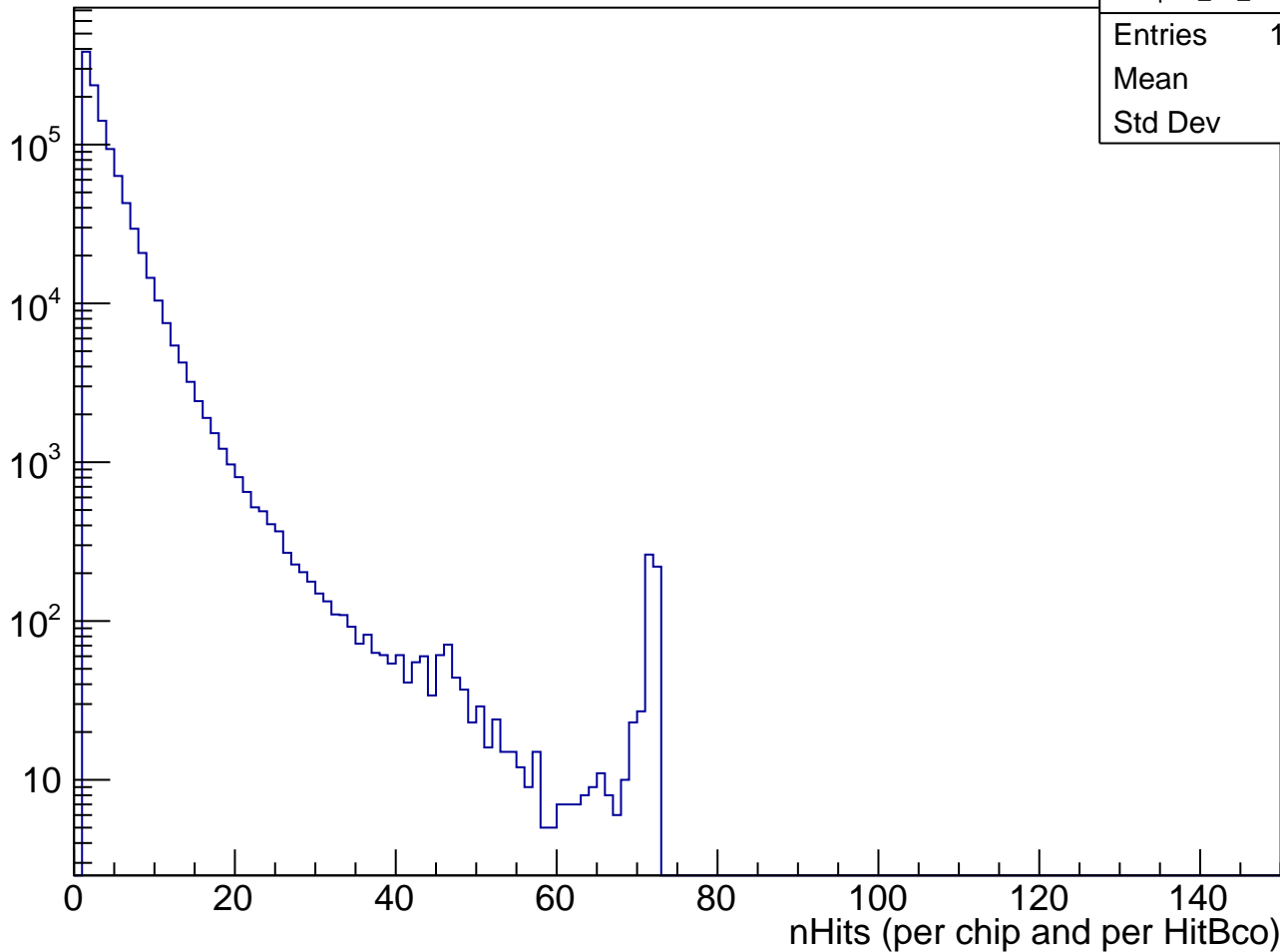
Mean 3.173

Std Dev 3.594

nHits (per chip and per HitBco)

nChipHit_F2_Fch0_Chip25

Entries

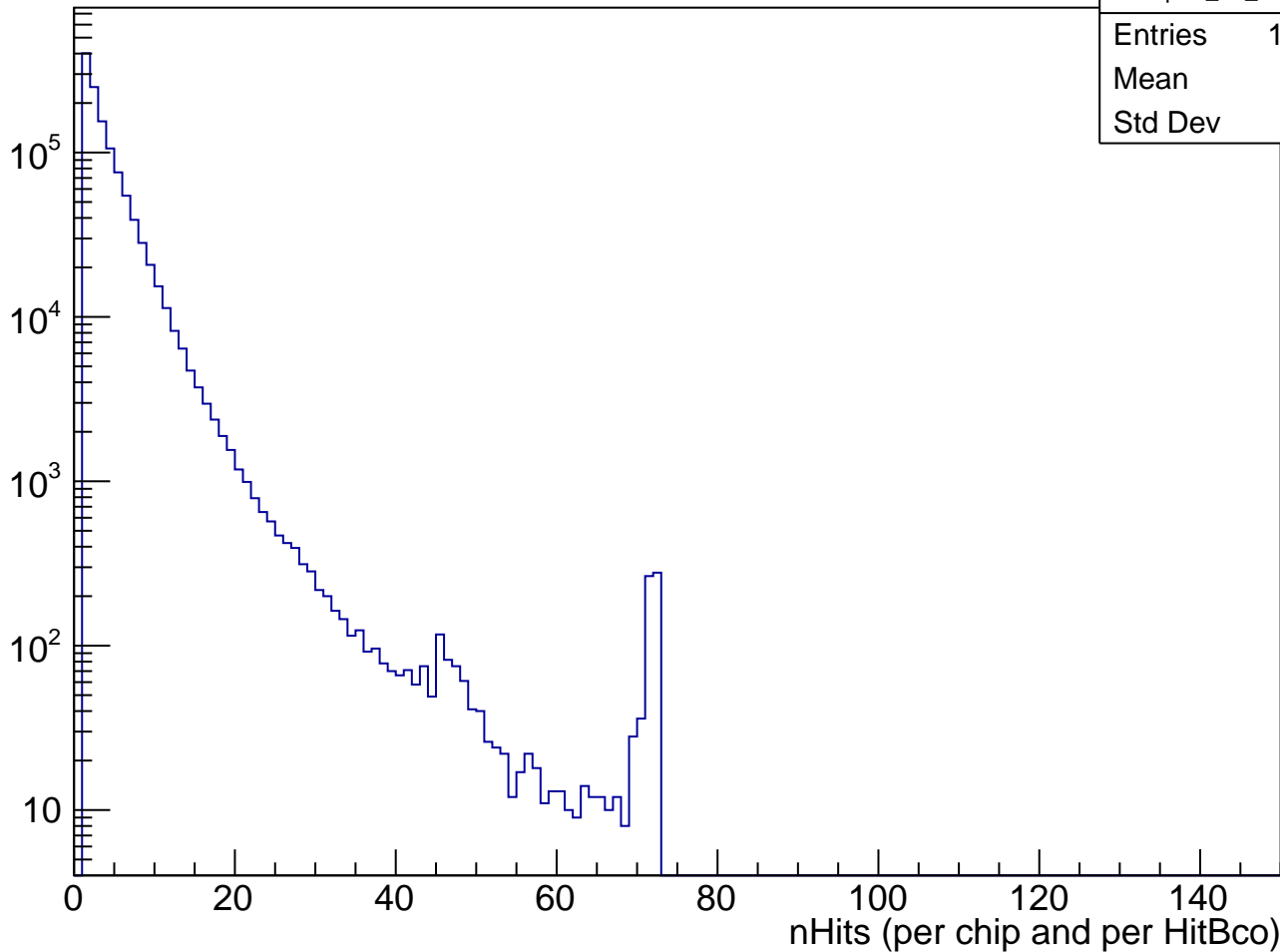


nChipHit_F2_Fch0_Chip25	
Entries	1071710
Mean	3.187
Std Dev	3.645

nHits (per chip and per HitBco)

nChipHit_F2_Fch0_Chip3

Entries



nChipHit_F2_Fch0_Chip3

Entries 1198244

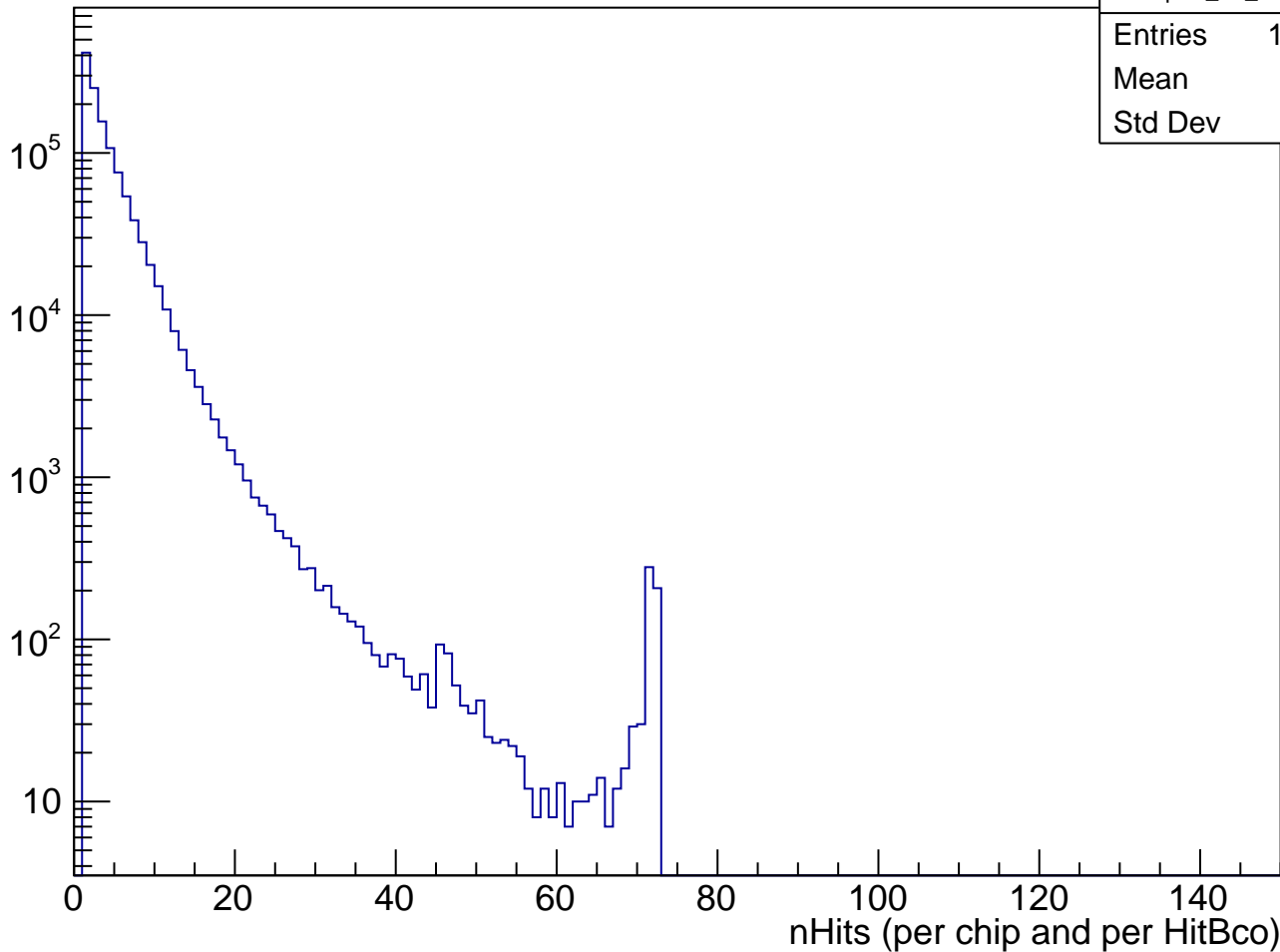
Mean 3.497

Std Dev 3.969

nHits (per chip and per HitBco)

nChipHit_F2_Fch0_Chip4

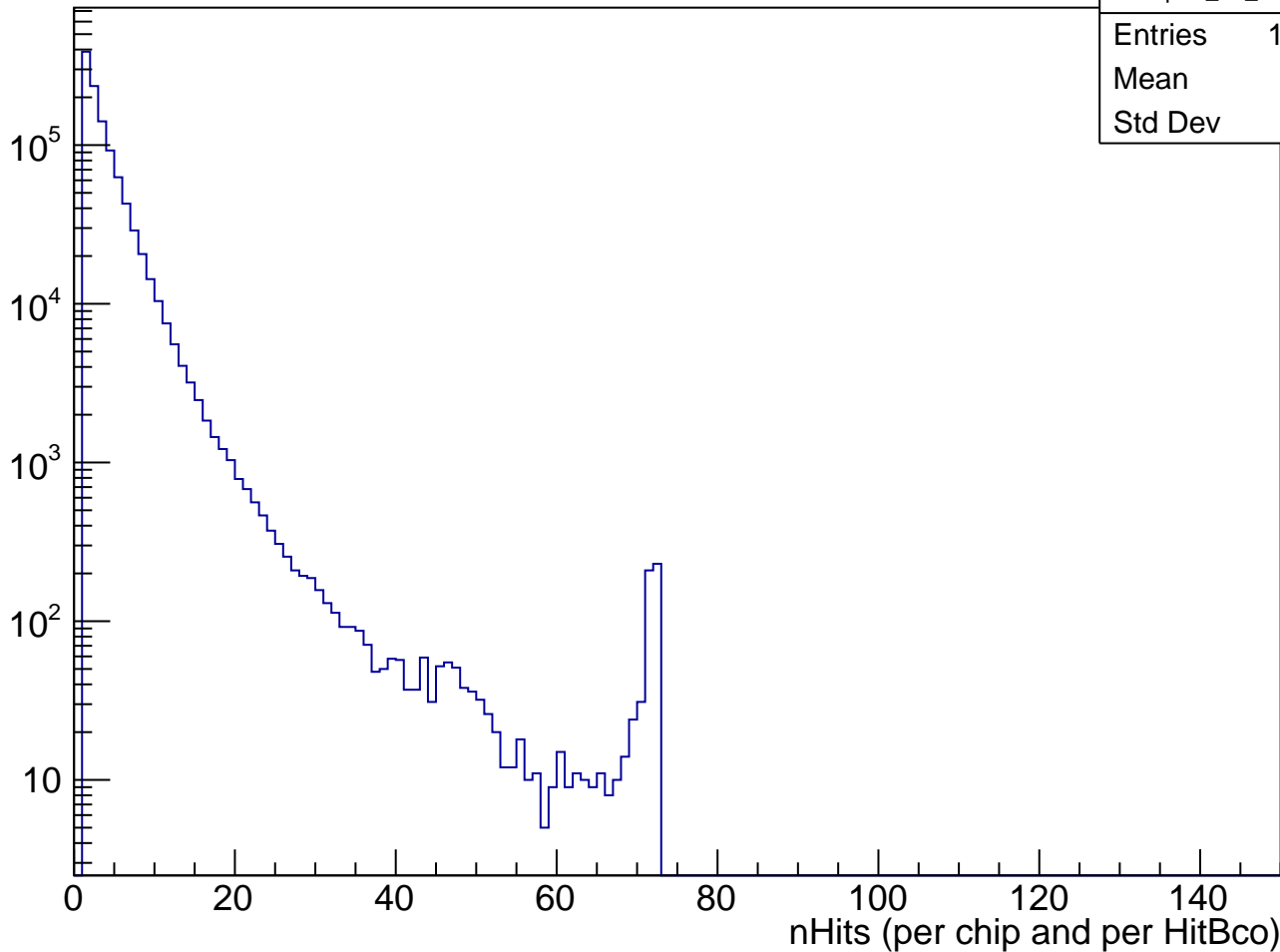
Entries



nChipHit_F2_Fch0_Chip4	
Entries	1212899
Mean	3.436
Std Dev	3.871

nChipHit_F2_Fch0_Chip5

Entries



nChipHit_F2_Fch0_Chip5

Entries 1071134

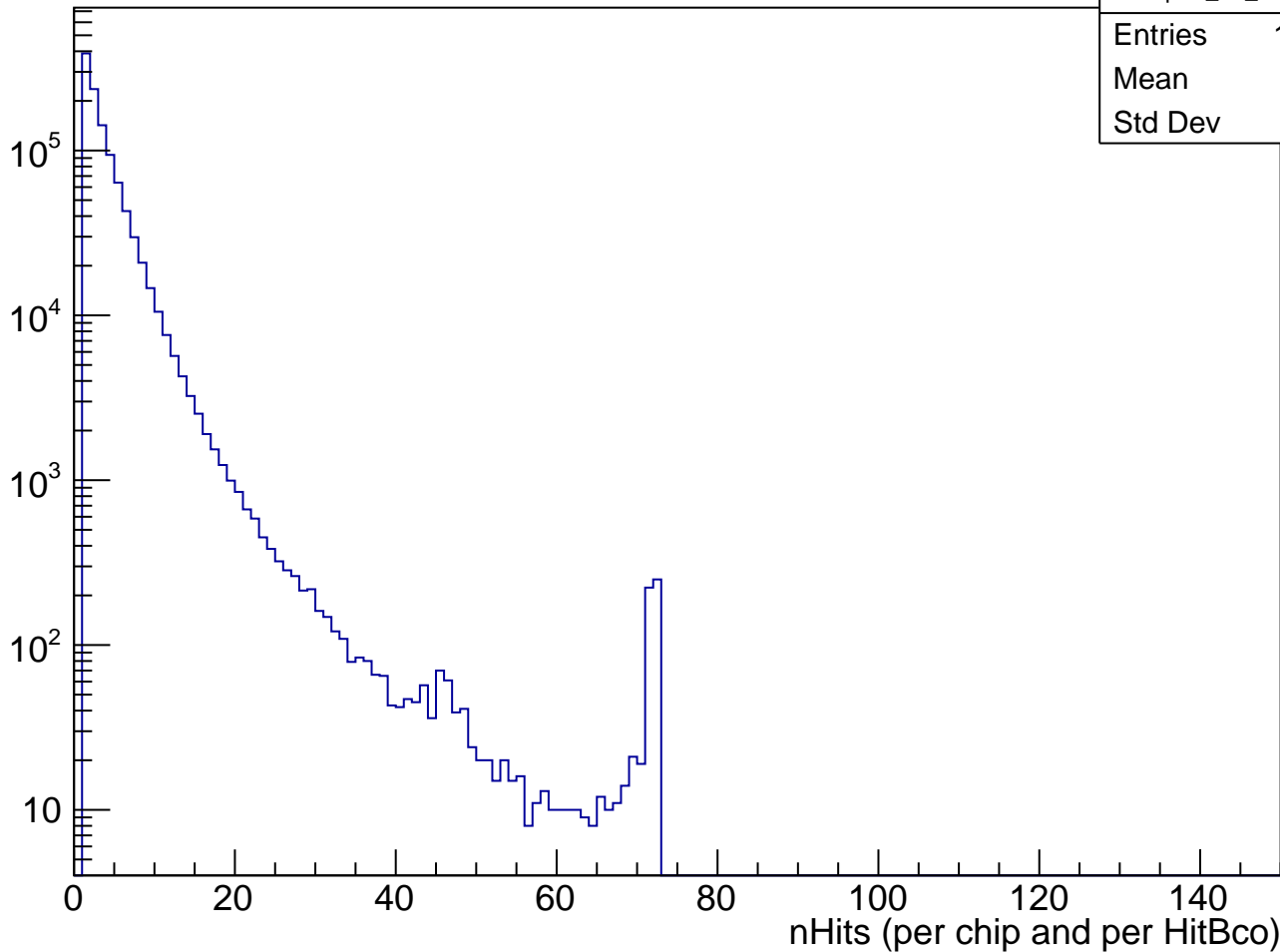
Mean 3.169

Std Dev 3.619

nHits (per chip and per HitBco)

nChipHit_F2_Fch0_Chip6

Entries

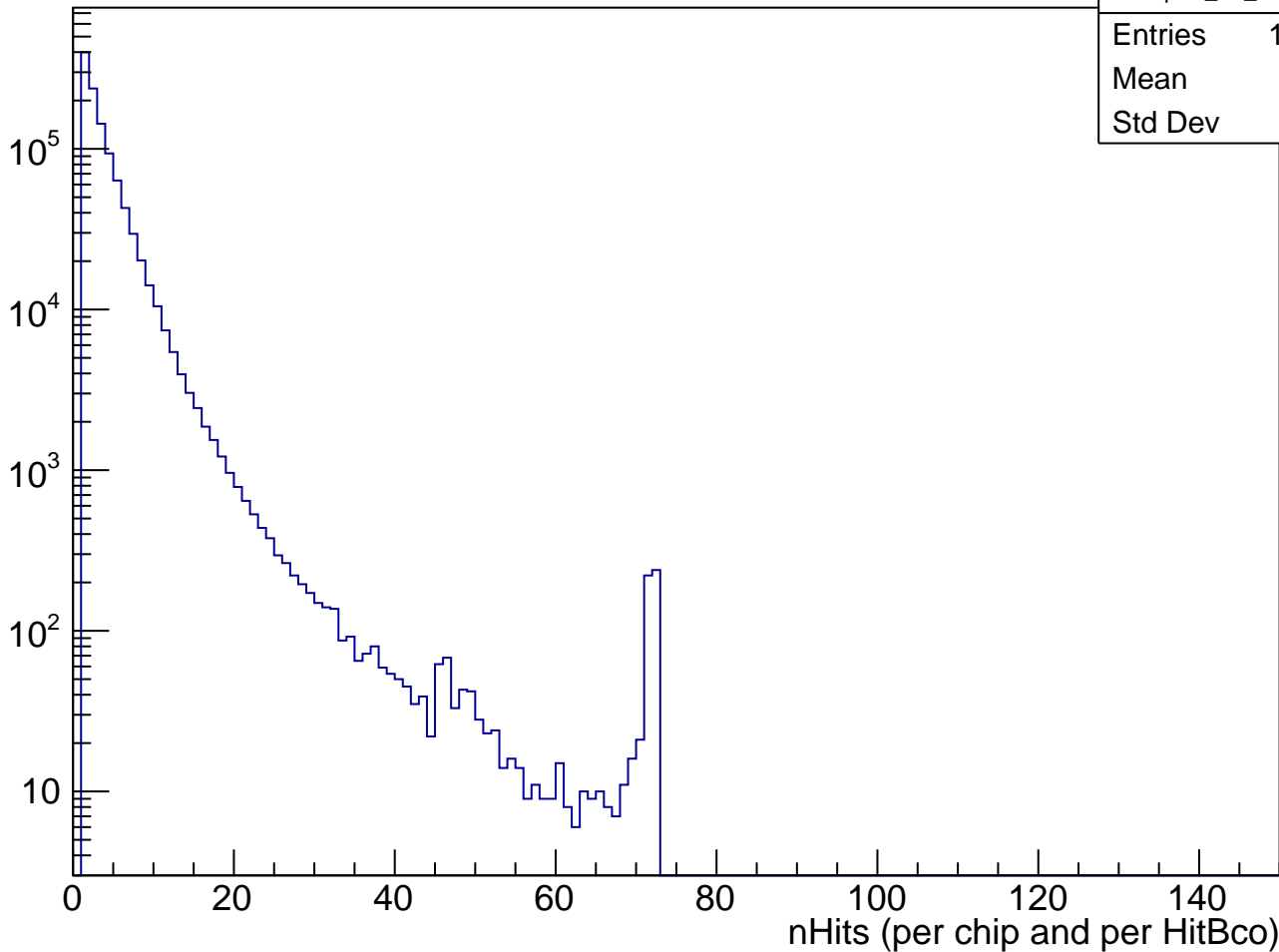


nChipHit_F2_Fch0_Chip6	
Entries	1078611
Mean	3.191
Std Dev	3.653

nHits (per chip and per HitBco)

nChipHit_F2_Fch0_Chip7

Entries

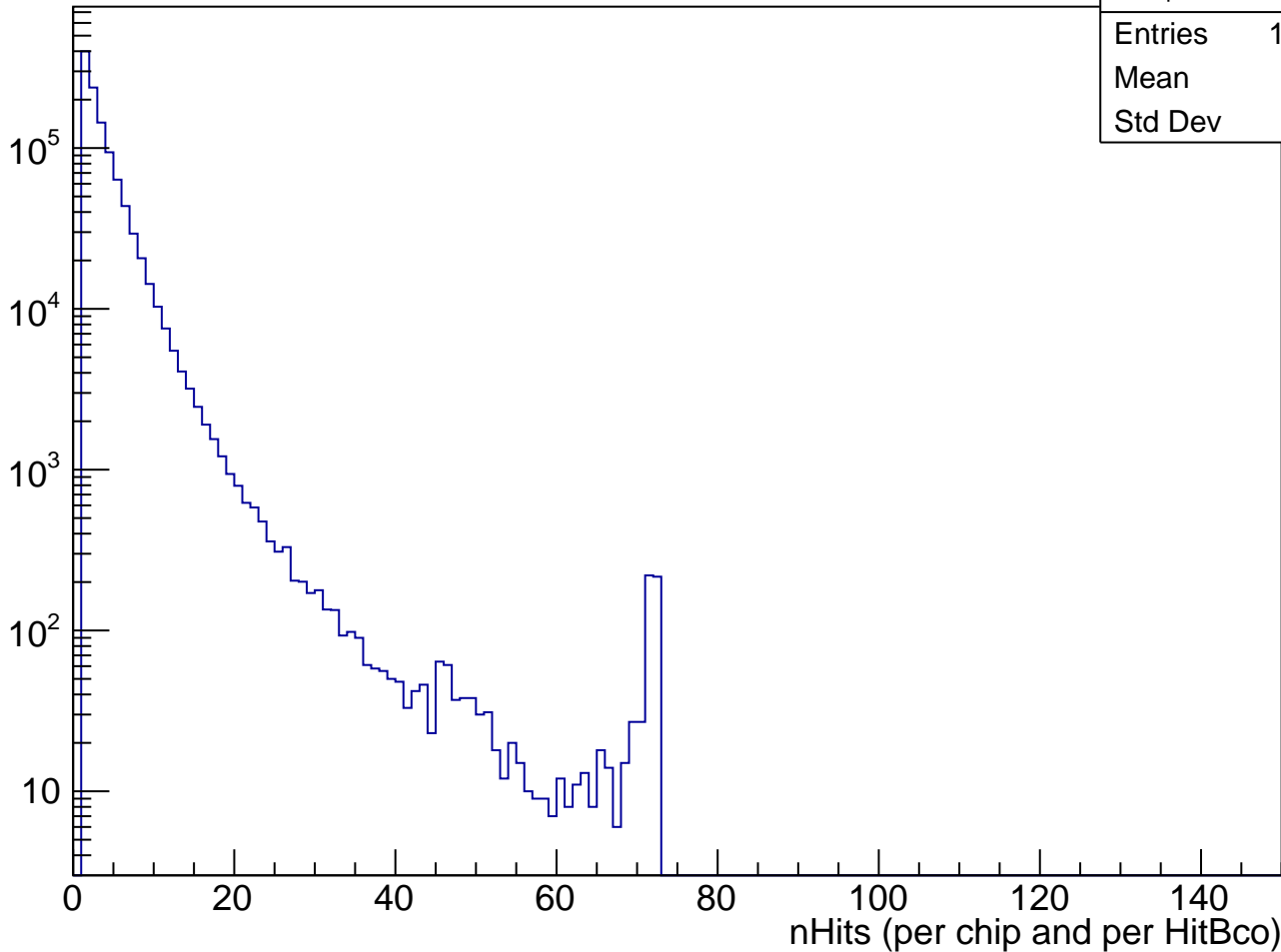


nChipHit_F2_Fch0_Chip7	
Entries	1088933
Mean	3.14
Std Dev	3.586

nHits (per chip and per HitBco)

nChipHit_F2_Fch0_Chip8

Entries



nChipHit_F2_Fch0_Chip8

Entries 1092047

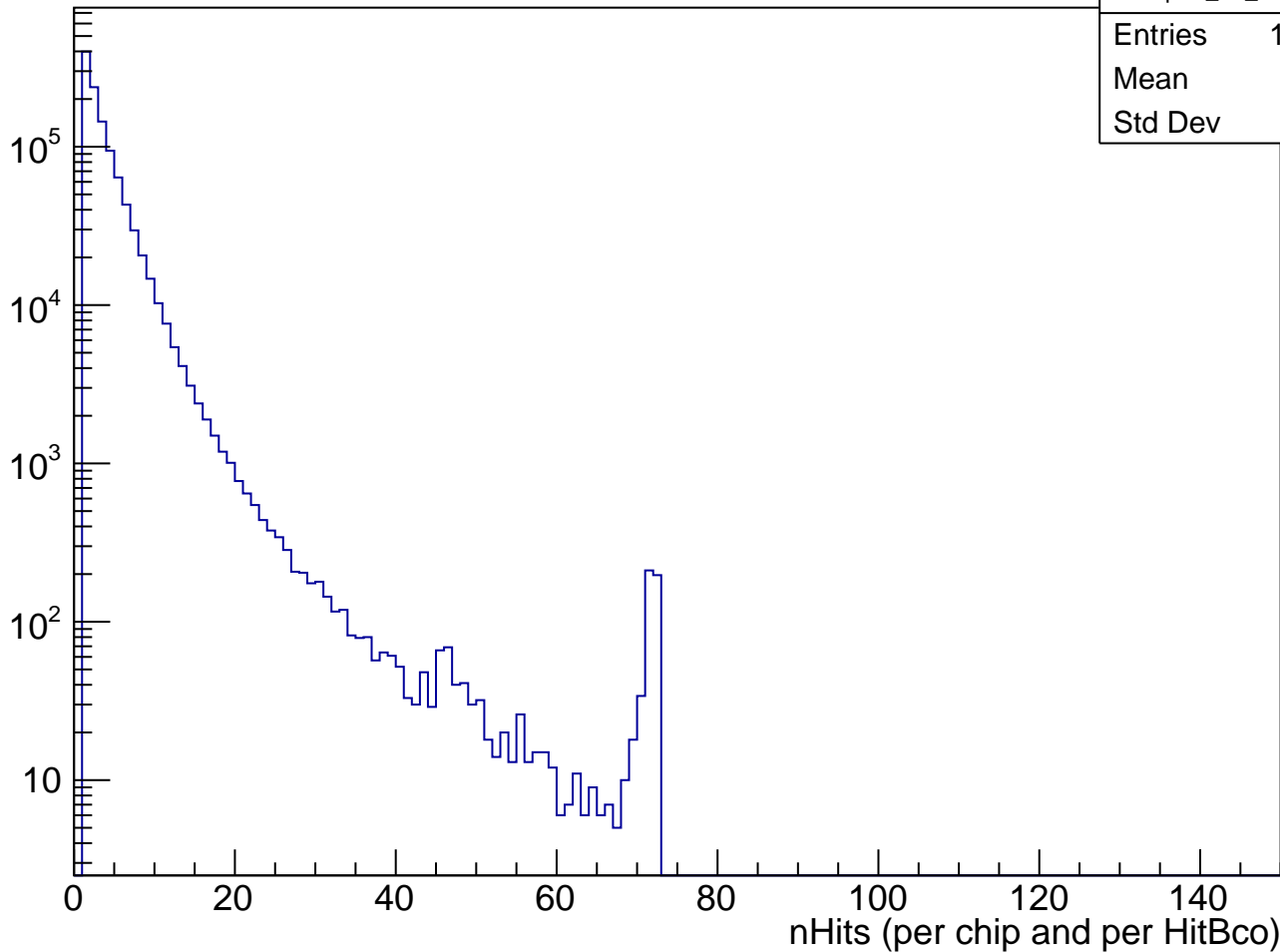
Mean 3.151

Std Dev 3.601

nHits (per chip and per HitBco)

nChipHit_F2_Fch0_Chip9

Entries



nChipHit_F2_Fch0_Chip9

Entries 1091613

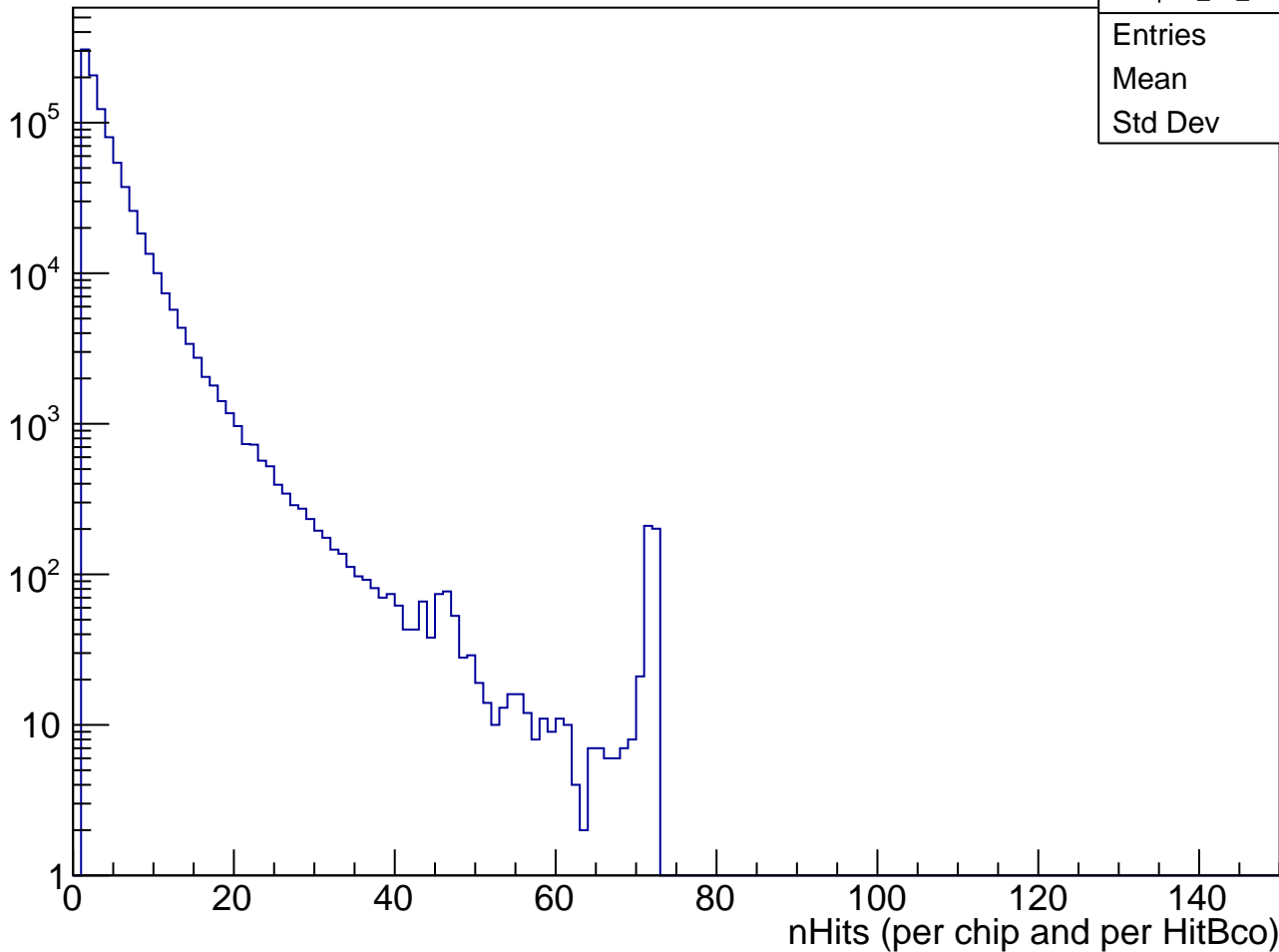
Mean 3.15

Std Dev 3.575

nHits (per chip and per HitBco)

nChipHit_F2_Fch10_Chip0

Entries

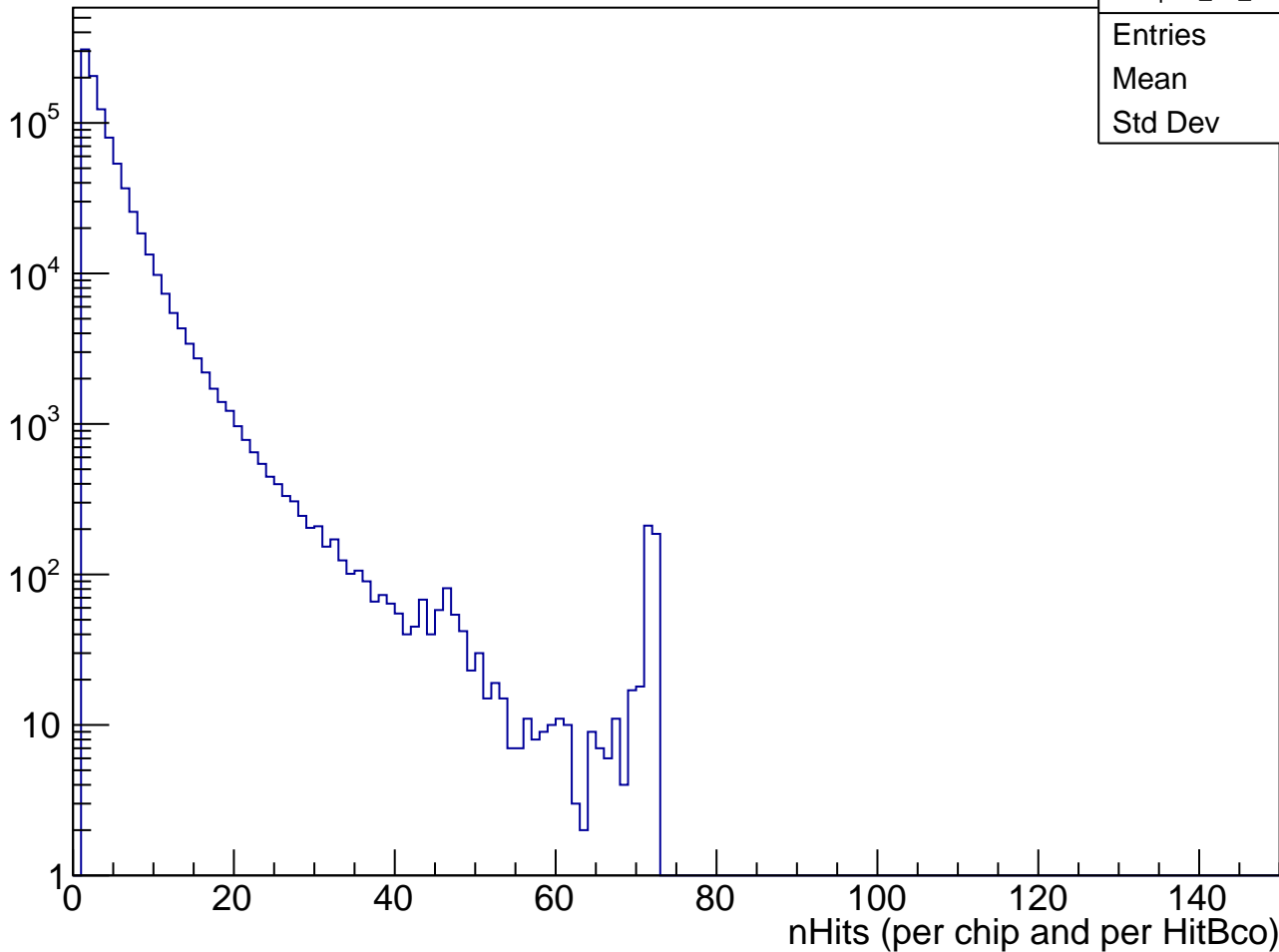


nChipHit_F2_Fch10_Chip0	
Entries	912685
Mean	3.376
Std Dev	3.913

nHits (per chip and per HitBco)

nChipHit_F2_Fch10_Chip1

Entries

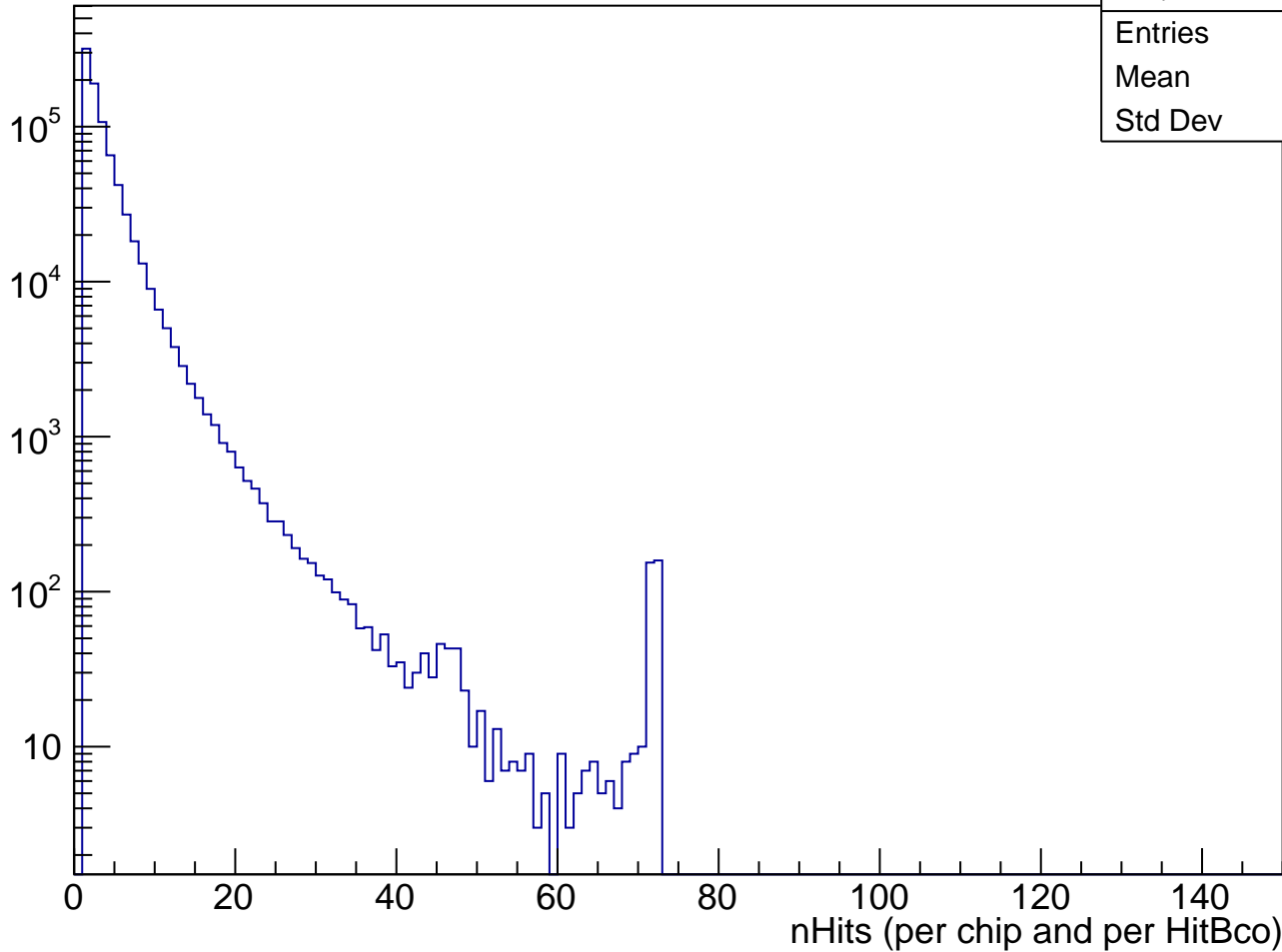


nChipHit_F2_Fch10_Chip1	
Entries	910726
Mean	3.361
Std Dev	3.894

nHits (per chip and per HitBco)

nChipHit_F2_Fch10_Chip10

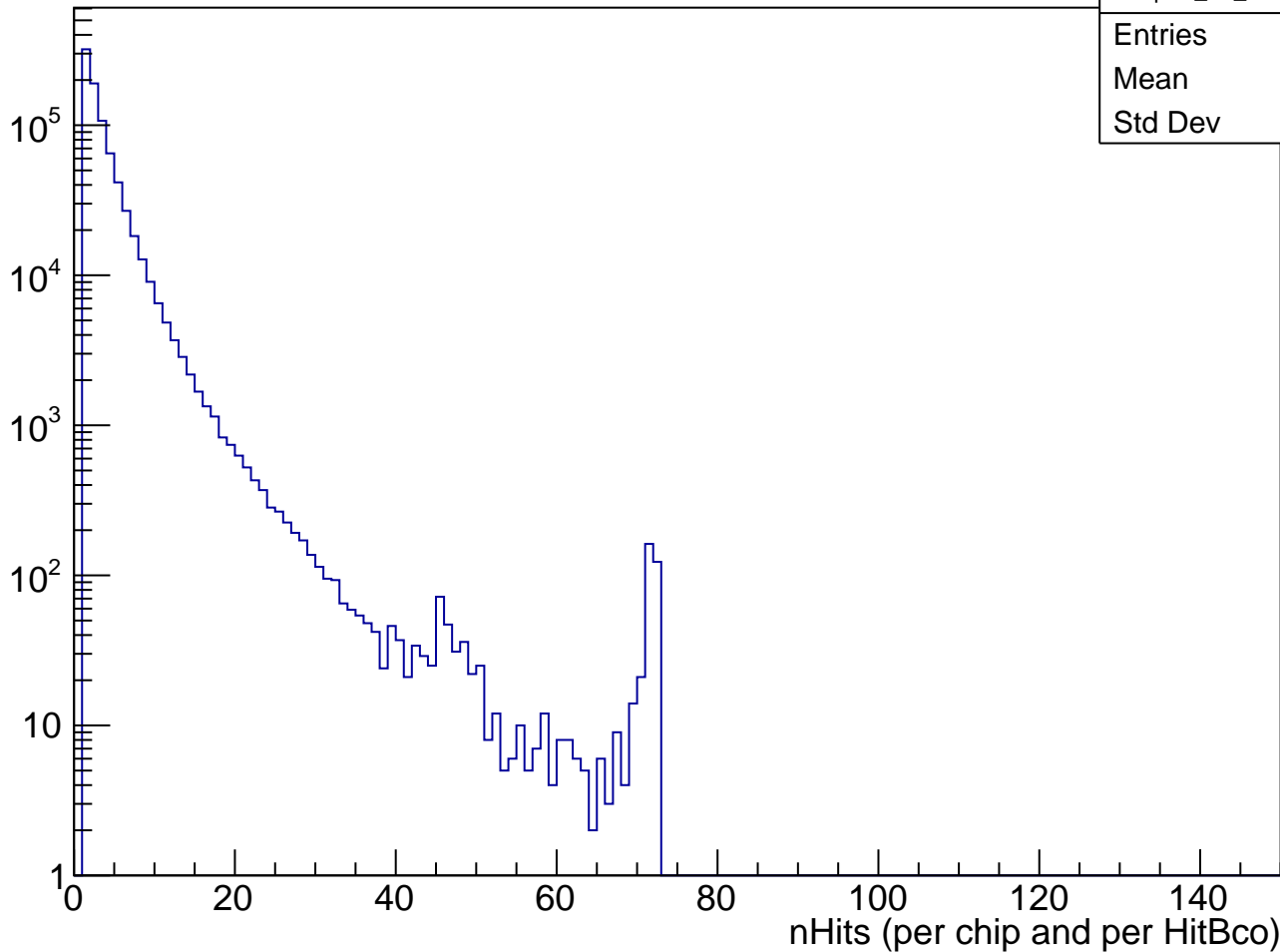
Entries



nHits (per chip and per HitBco)

nChipHit_F2_Fch10_Chip11

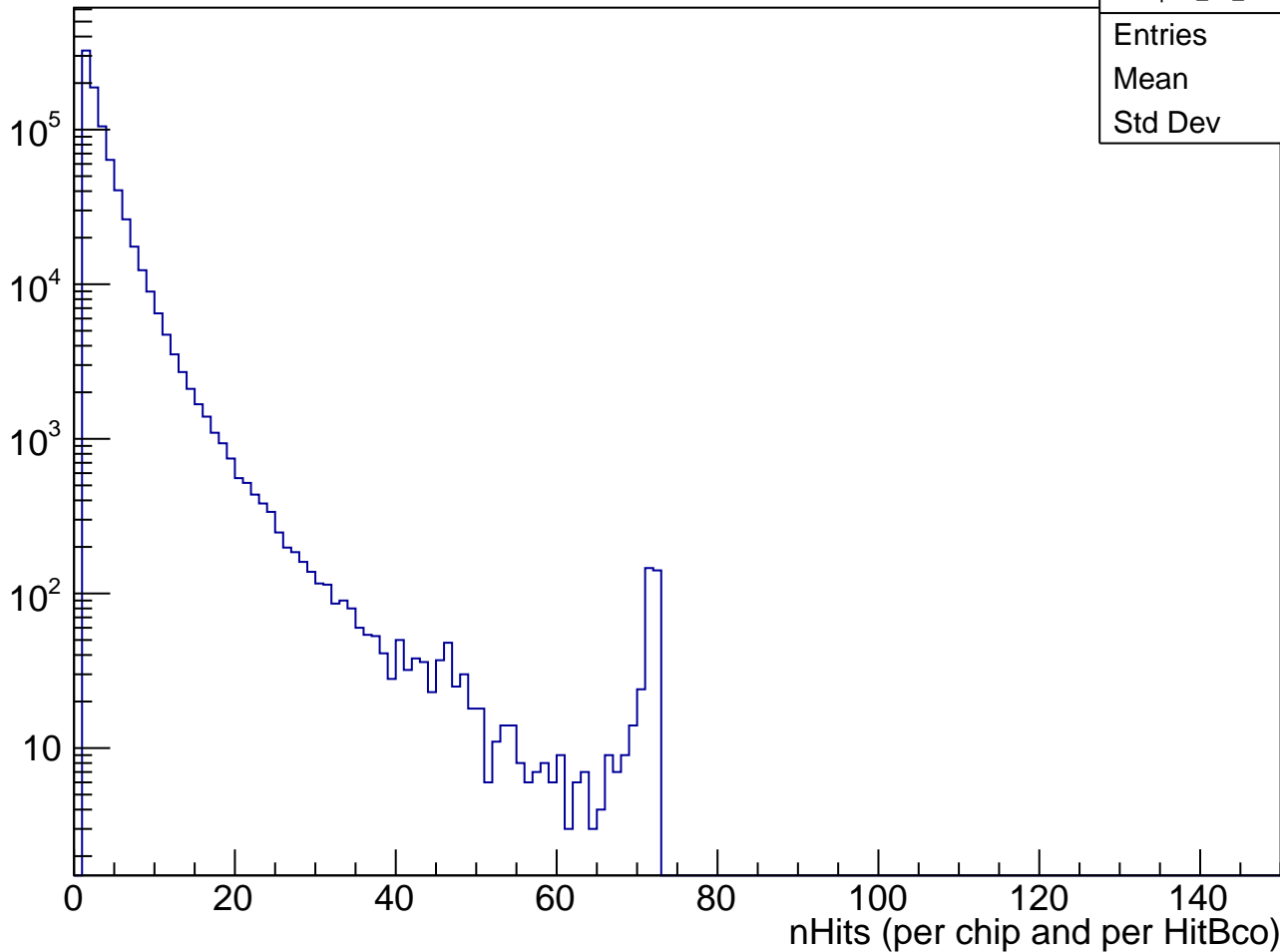
Entries



nChipHit_F2_Fch10_Chip11	
Entries	821453
Mean	2.958
Std Dev	3.47

nChipHit_F2_Fch10_Chip12

Entries



nChipHit_F2_Fch10_Chip12

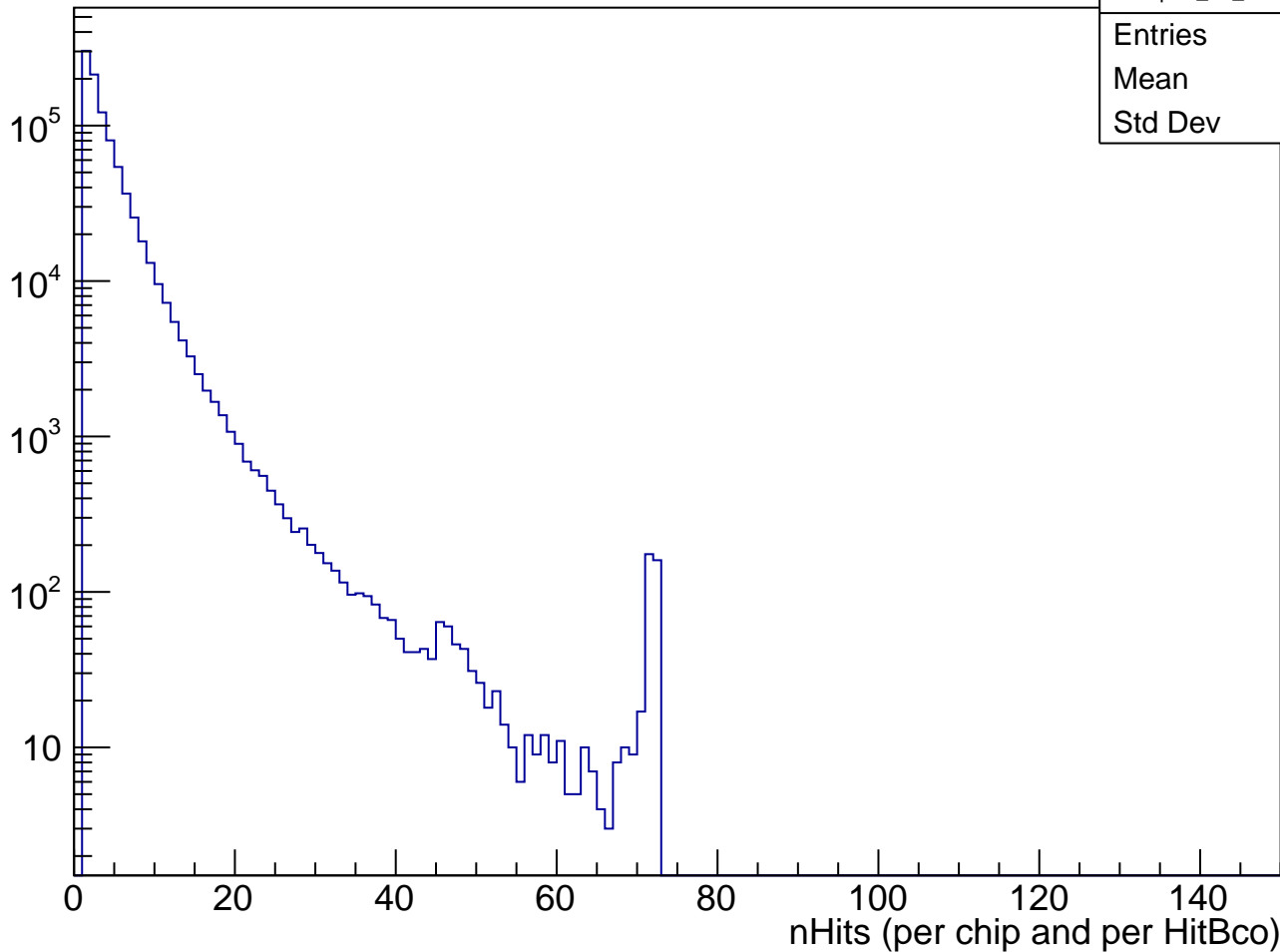
Entries 816389

Mean 2.935

Std Dev 3.485

nChipHit_F2_Fch10_Chip13

Entries



nChipHit_F2_Fch10_Chip13

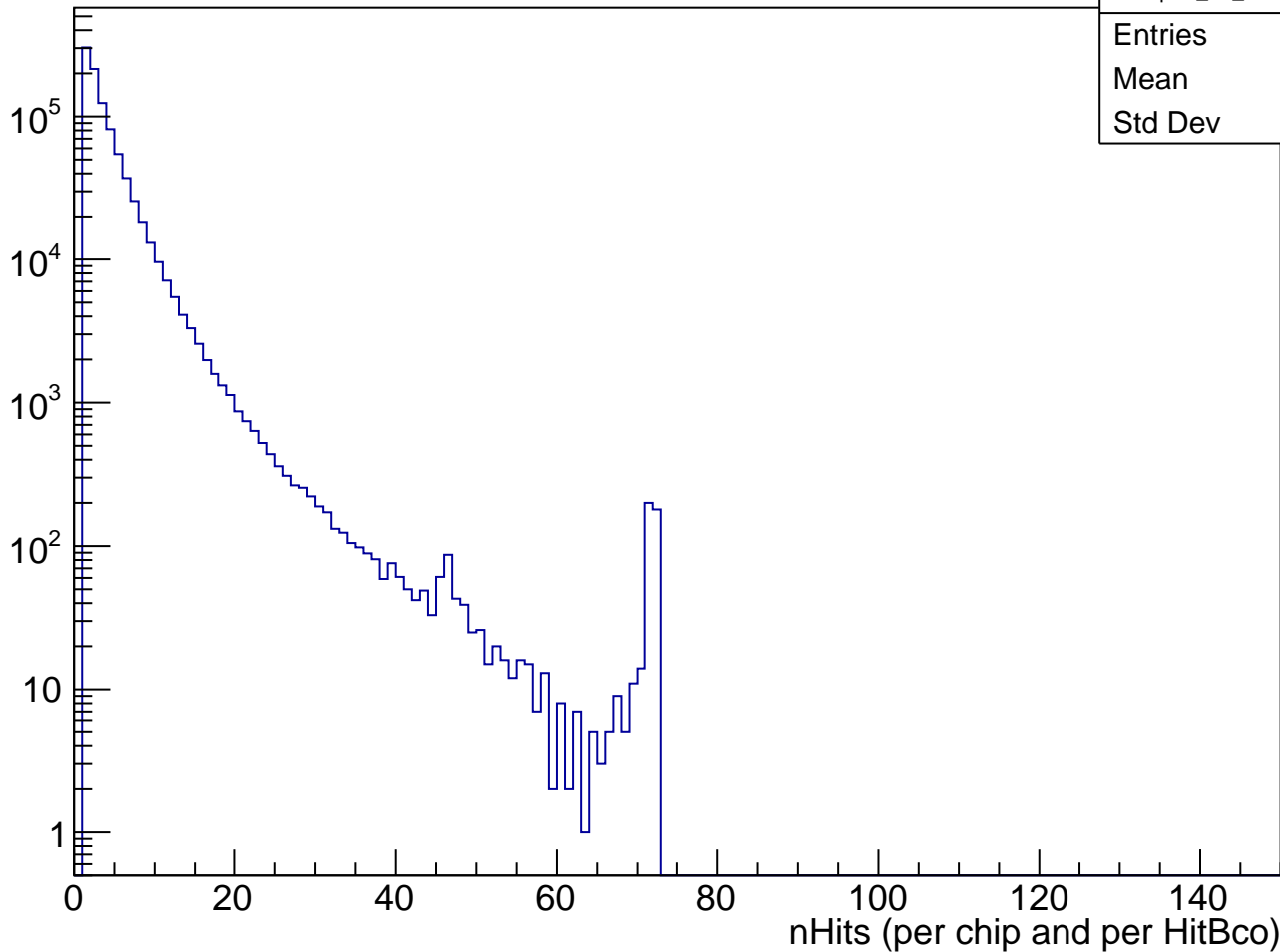
Entries 910350

Mean 3.328

Std Dev 3.787

nChipHit_F2_Fch10_Chip14

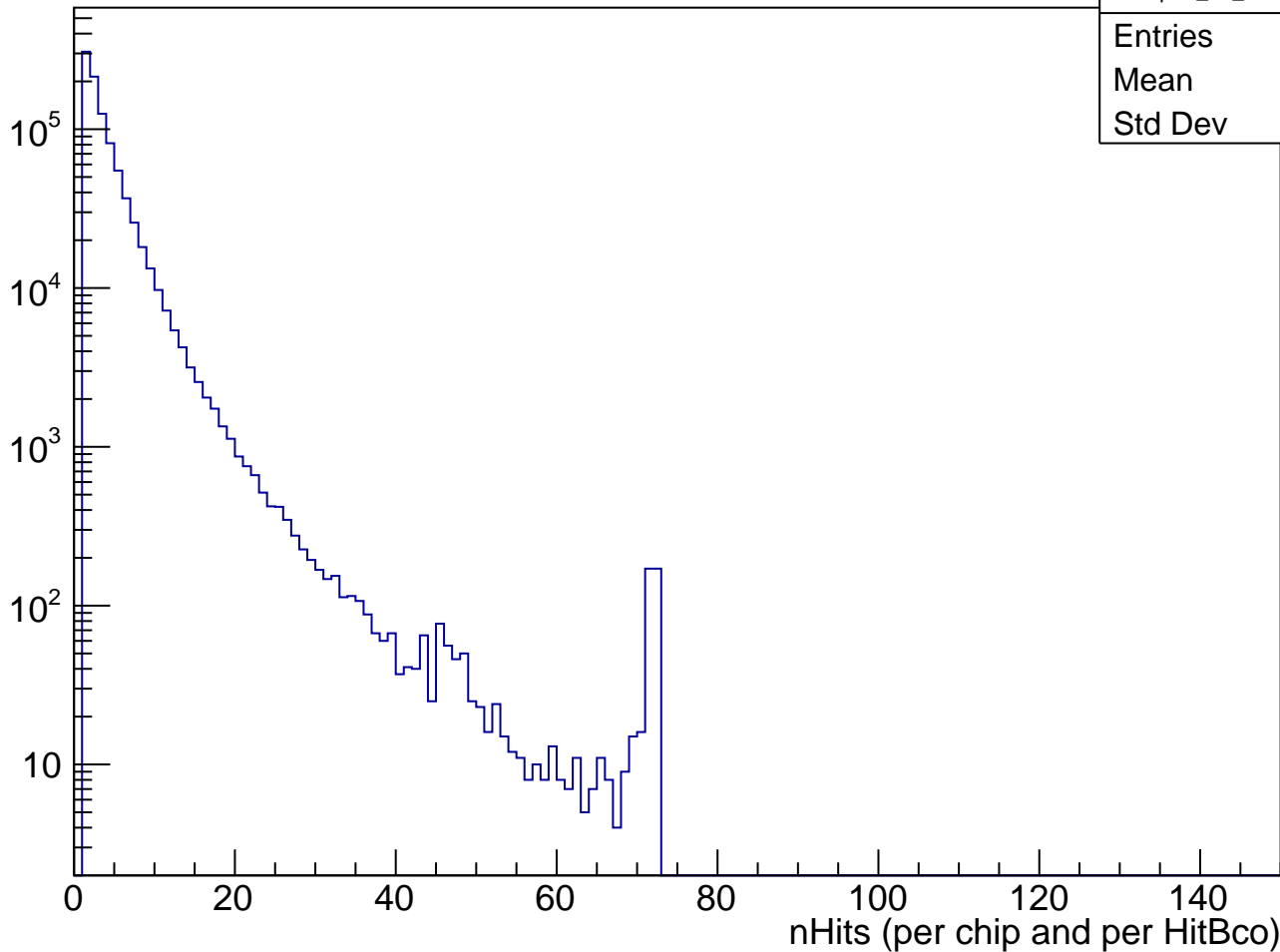
Entries



nChipHit_F2_Fch10_Chip14	
Entries	917435
Mean	3.334
Std Dev	3.81

nChipHit_F2_Fch10_Chip15

Entries



nChipHit_F2_Fch10_Chip15

Entries 922741

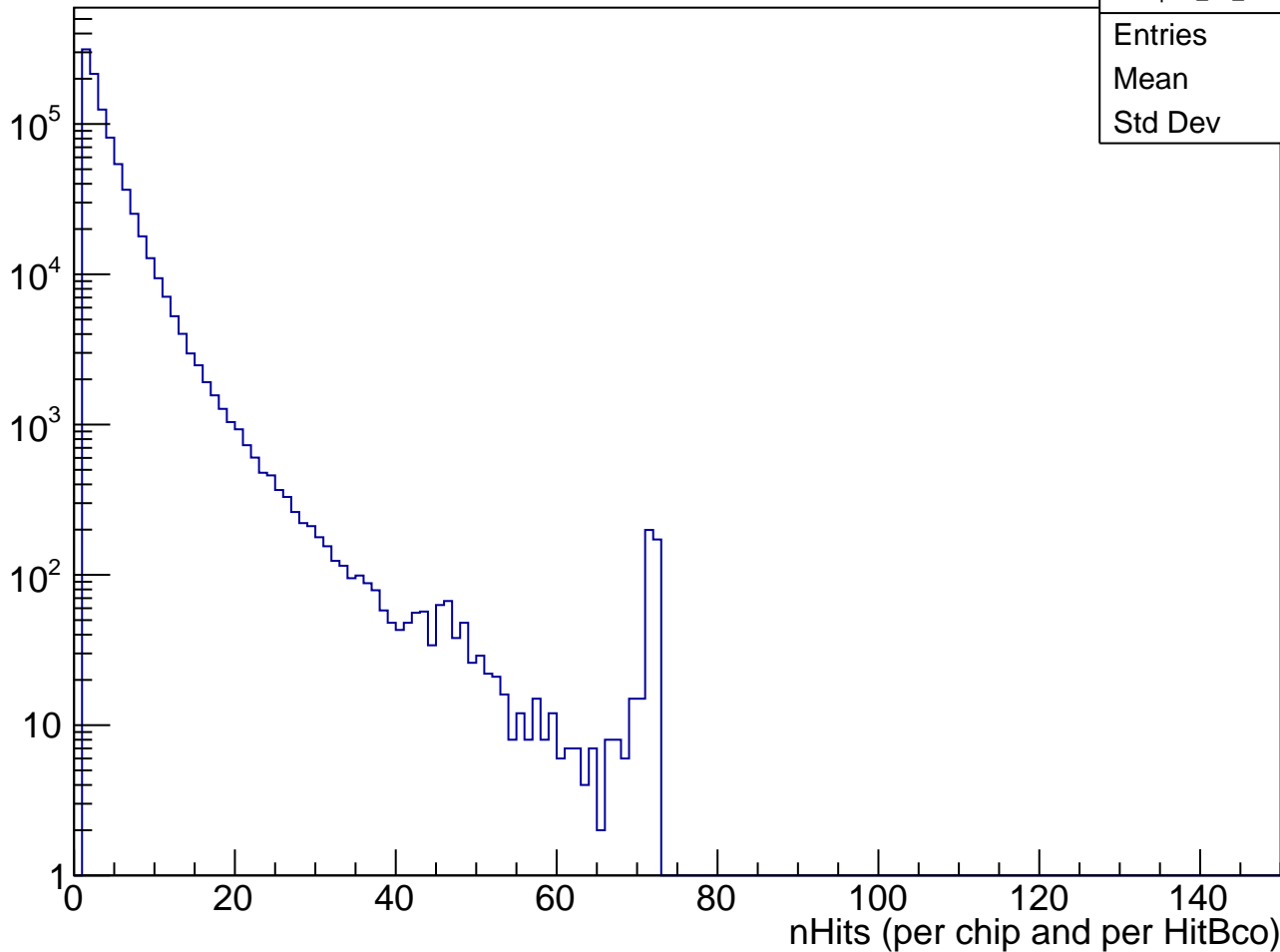
Mean 3.326

Std Dev 3.793

nHits (per chip and per HitBco)

nChipHit_F2_Fch10_Chip16

Entries



nChipHit_F2_Fch10_Chip16

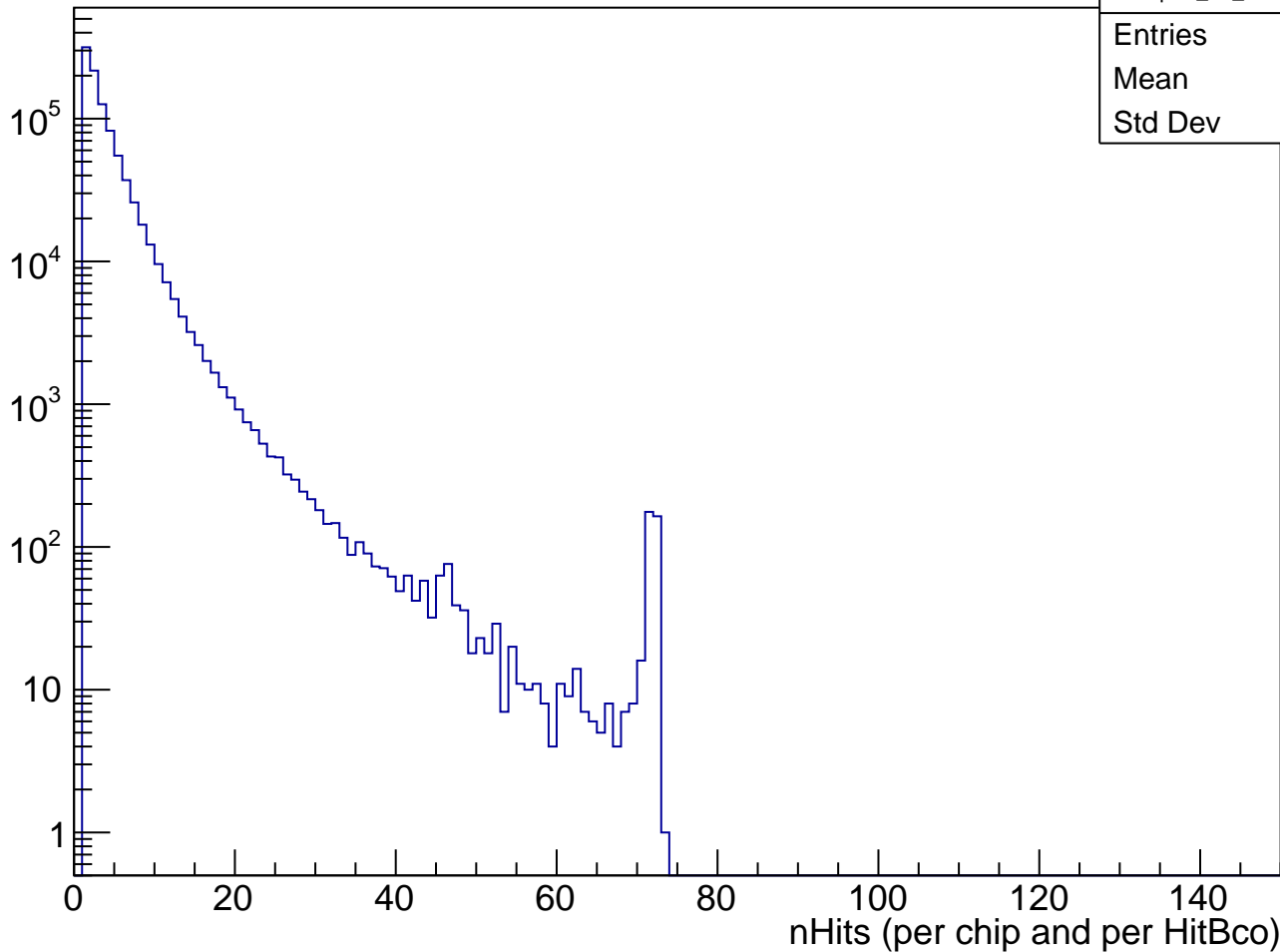
Entries 925585

Mean 3.281

Std Dev 3.767

nChipHit_F2_Fch10_Chip17

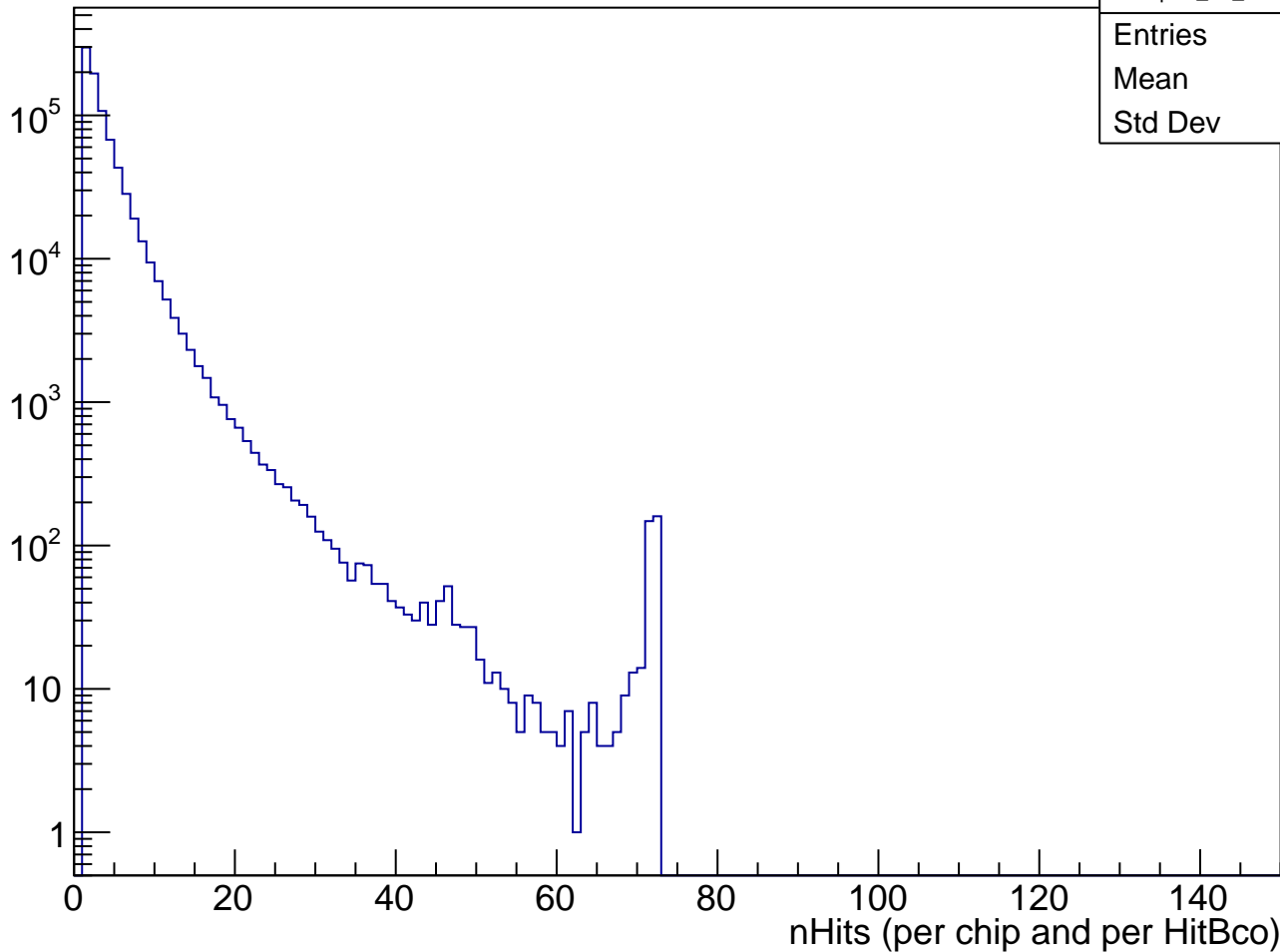
Entries



nChipHit_F2_Fch10_Chip17	
Entries	936059
Mean	3.299
Std Dev	3.769

nChipHit_F2_Fch10_Chip18

Entries



nChipHit_F2_Fch10_Chip18

Entries 814160

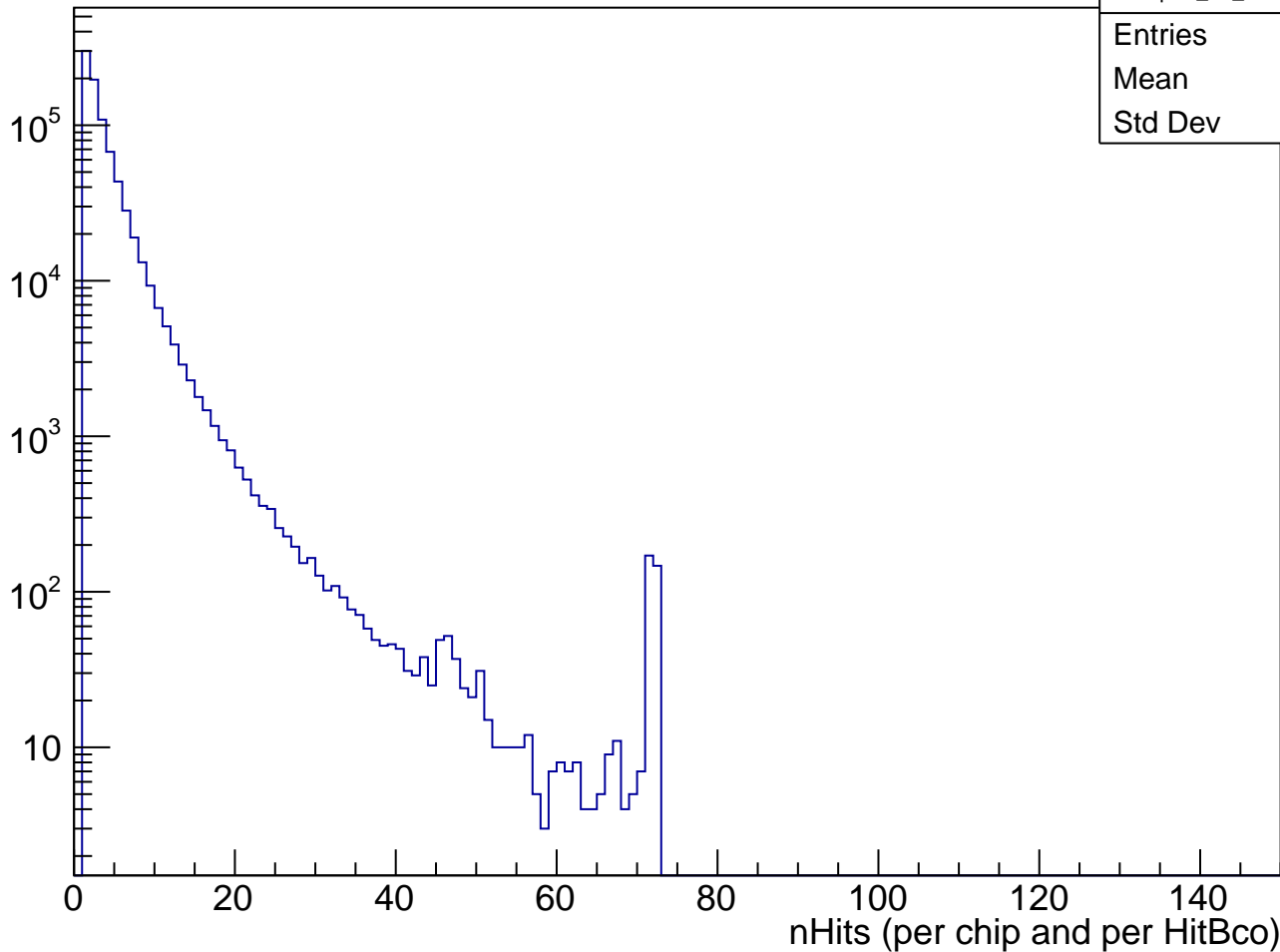
Mean 3.057

Std Dev 3.55

nHits (per chip and per HitBco)

nChipHit_F2_Fch10_Chip19

Entries



nChipHit_F2_Fch10_Chip19

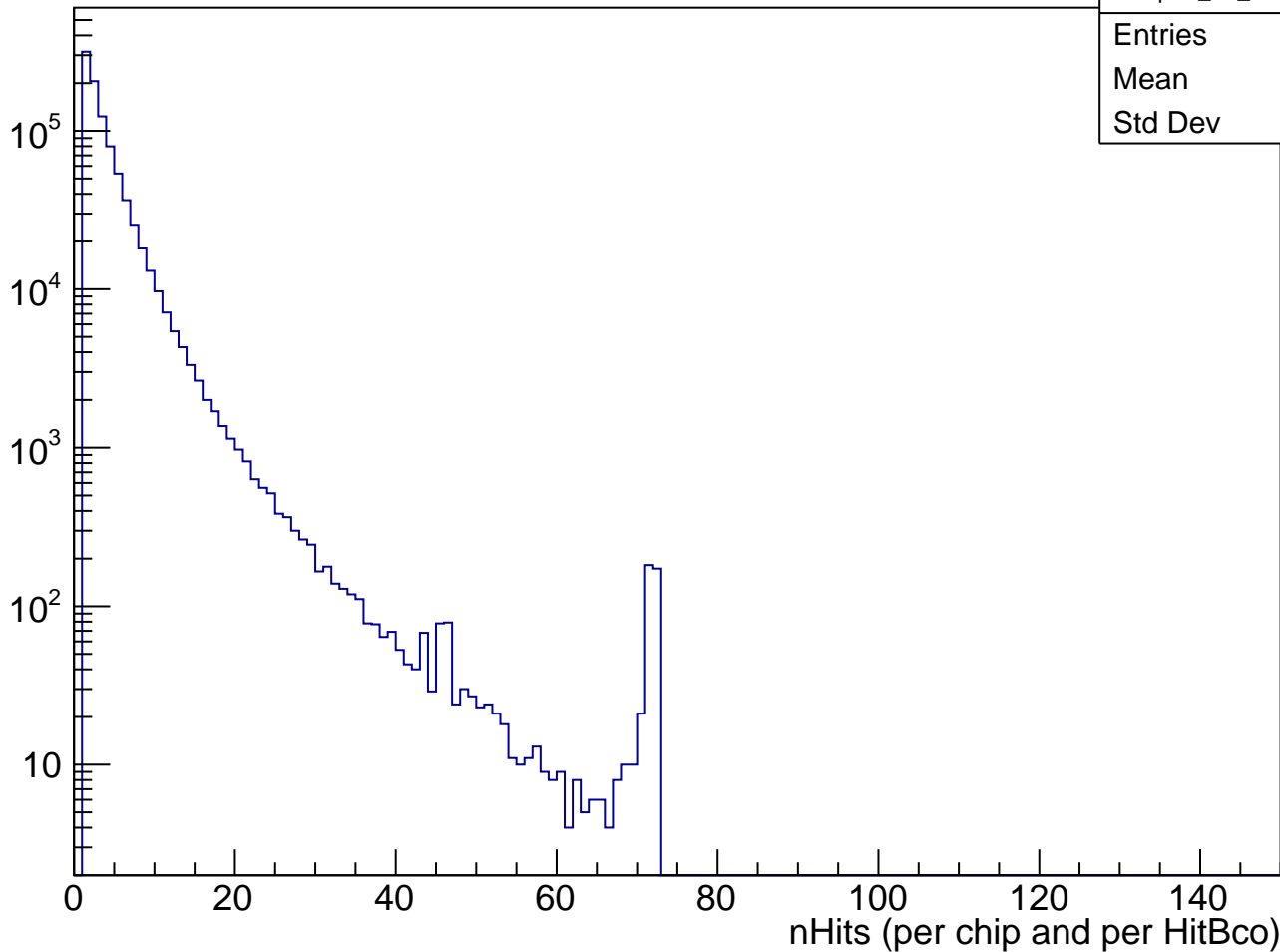
Entries 818613

Mean 3.042

Std Dev 3.545

nChipHit_F2_Fch10_Chip2

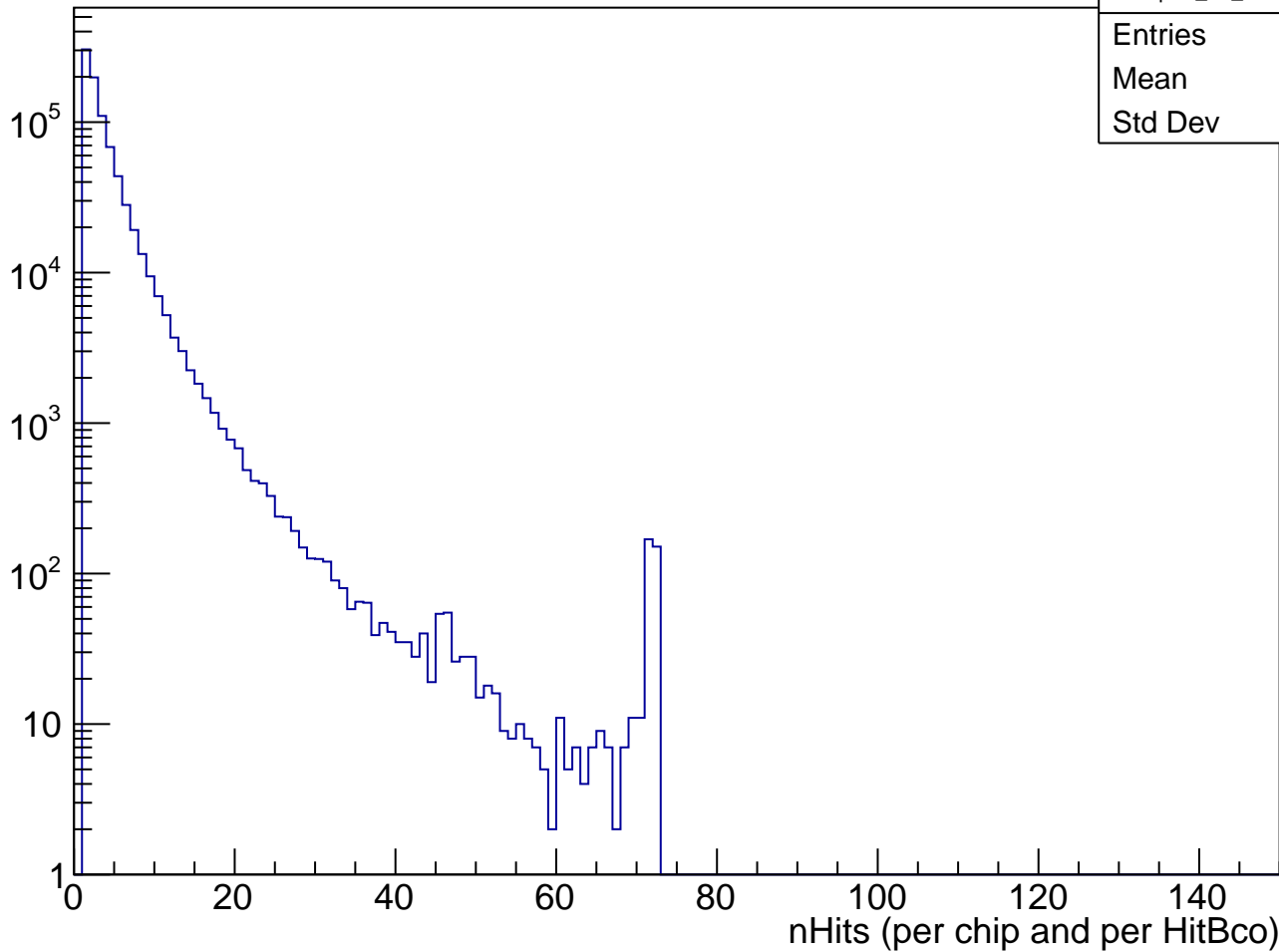
Entries



nChipHit_F2_Fch10_Chip2	
Entries	917605
Mean	3.324
Std Dev	3.844

nChipHit_F2_Fch10_Chip20

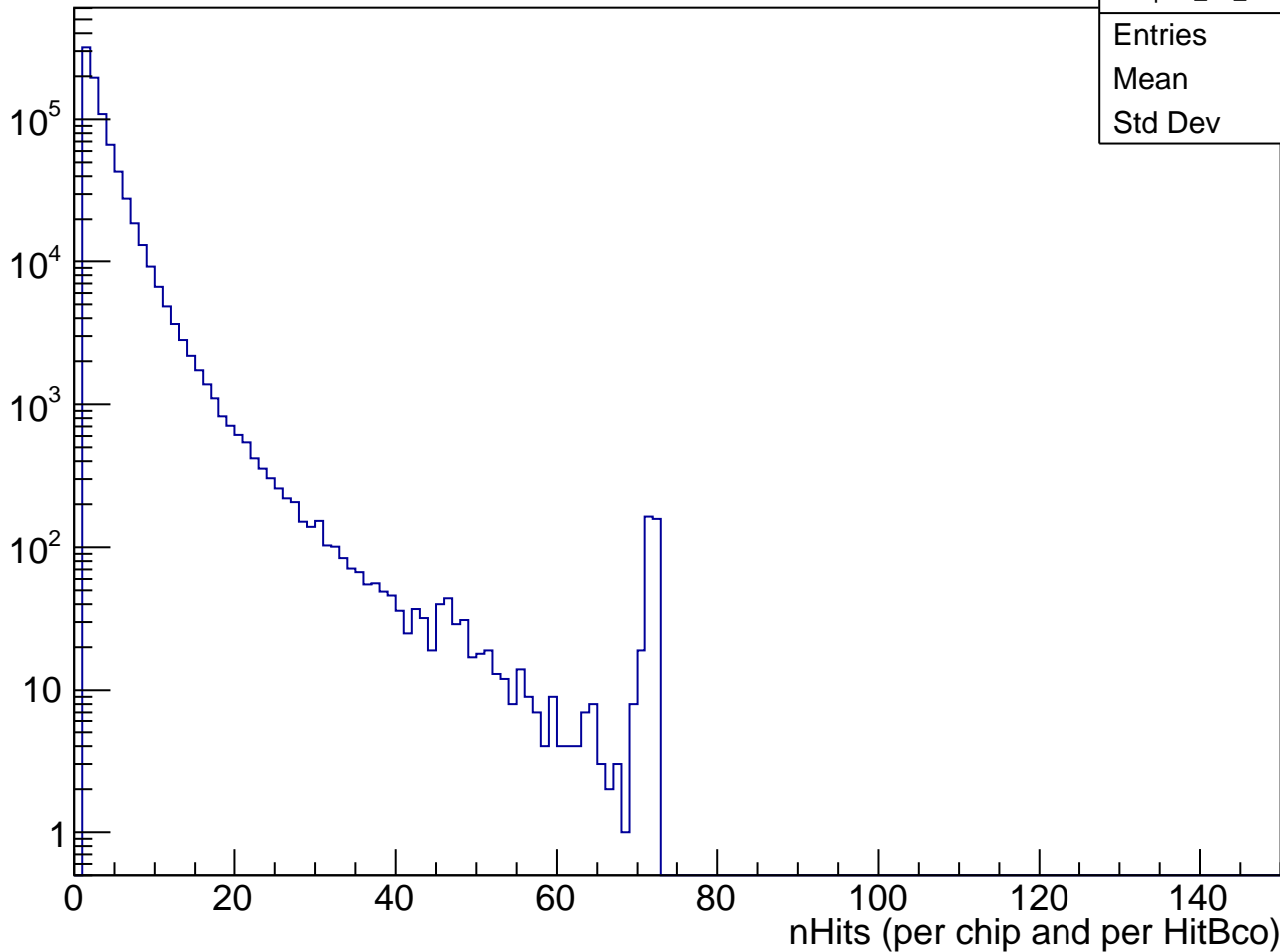
Entries



nChipHit_F2_Fch10_Chip20	
Entries	826953
Mean	3.035
Std Dev	3.523

nChipHit_F2_Fch10_Chip21

Entries

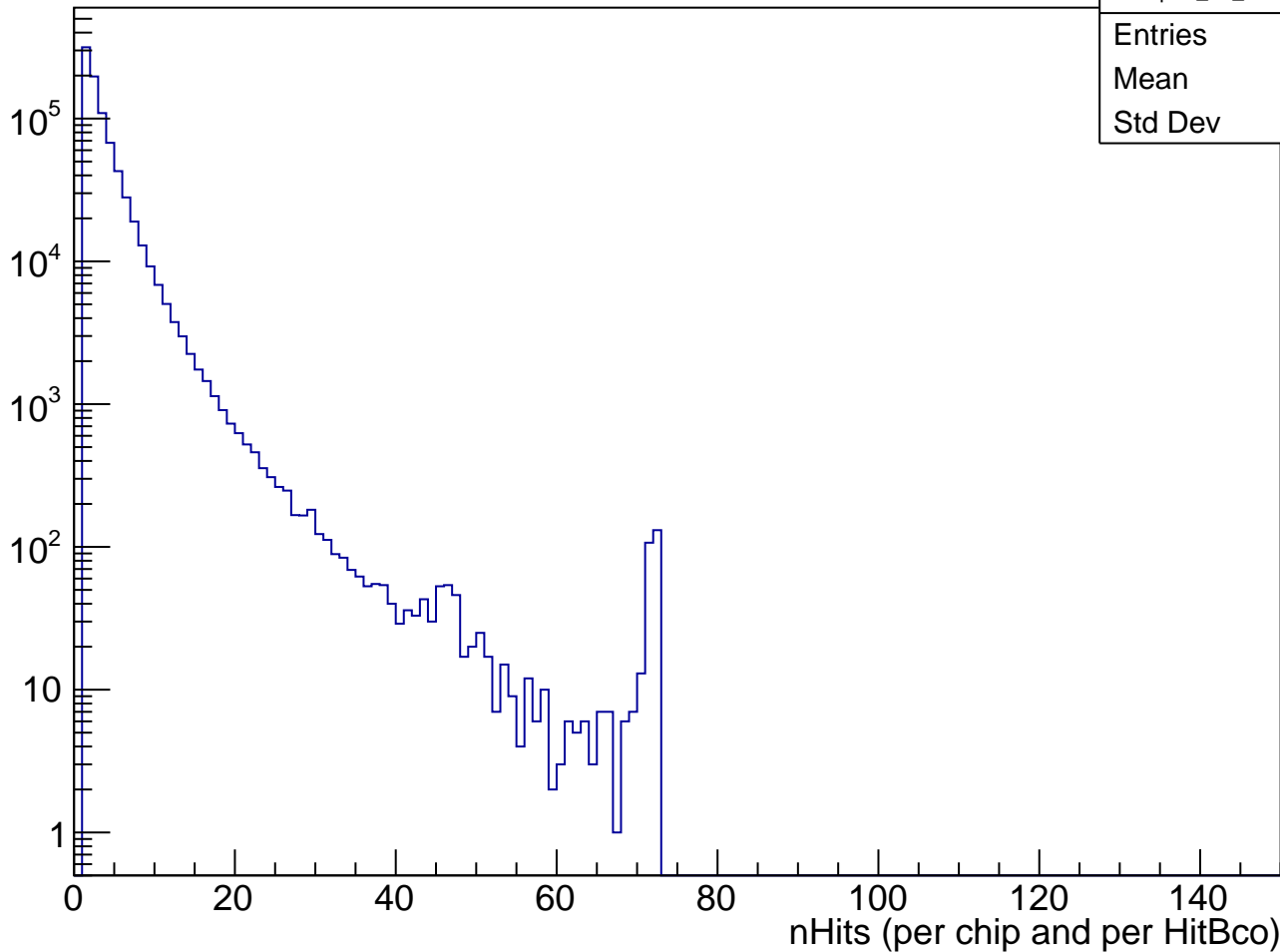


nChipHit_F2_Fch10_Chip21	
Entries	832337
Mean	2.975
Std Dev	3.484

nHits (per chip and per HitBco)

nChipHit_F2_Fch10_Chip22

Entries



nChipHit_F2_Fch10_Chip22

Entries 833357

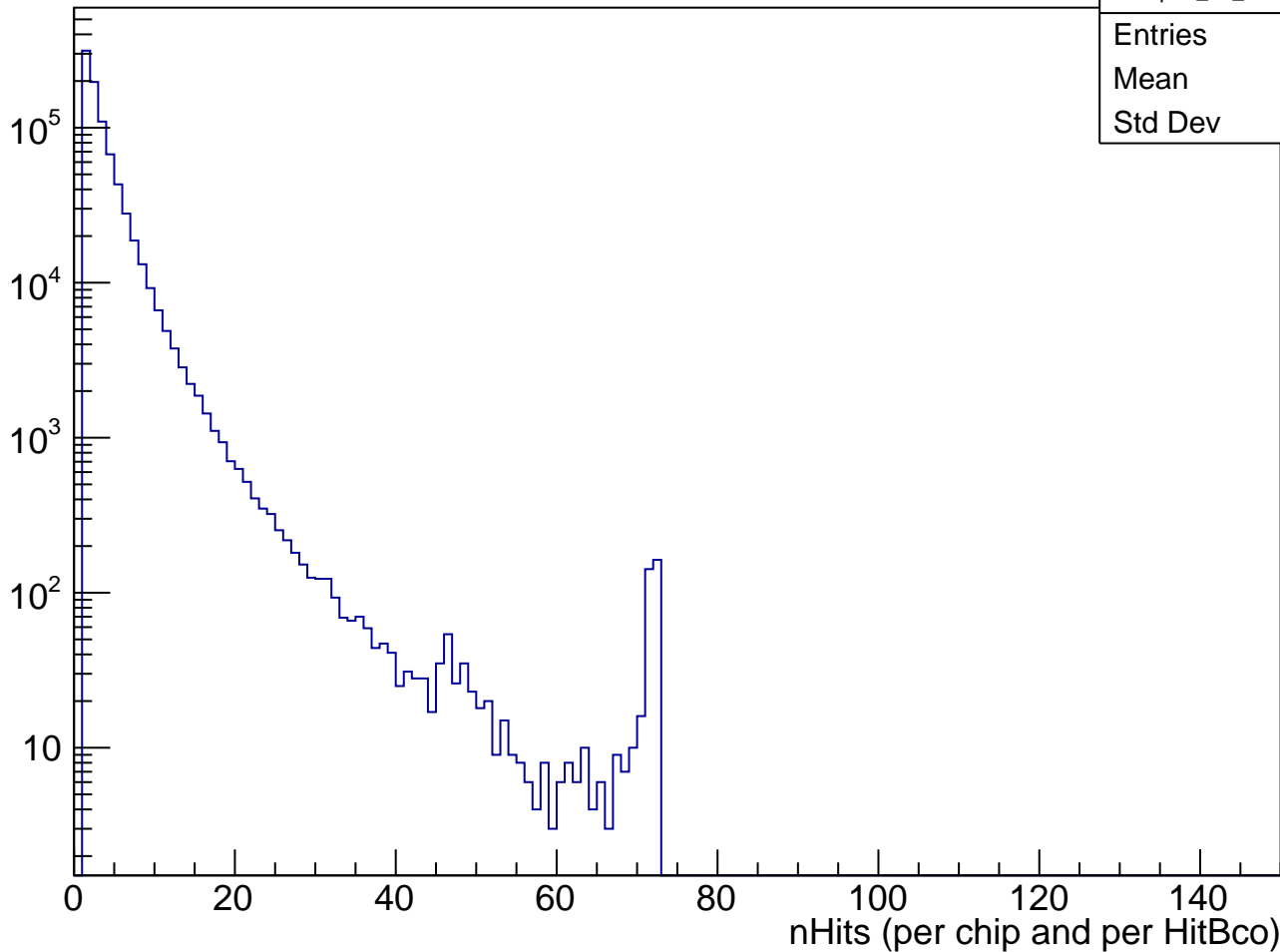
Mean 2.991

Std Dev 3.444

nHits (per chip and per HitBco)

nChipHit_F2_Fch10_Chip23

Entries



nChipHit_F2_Fch10_Chip23

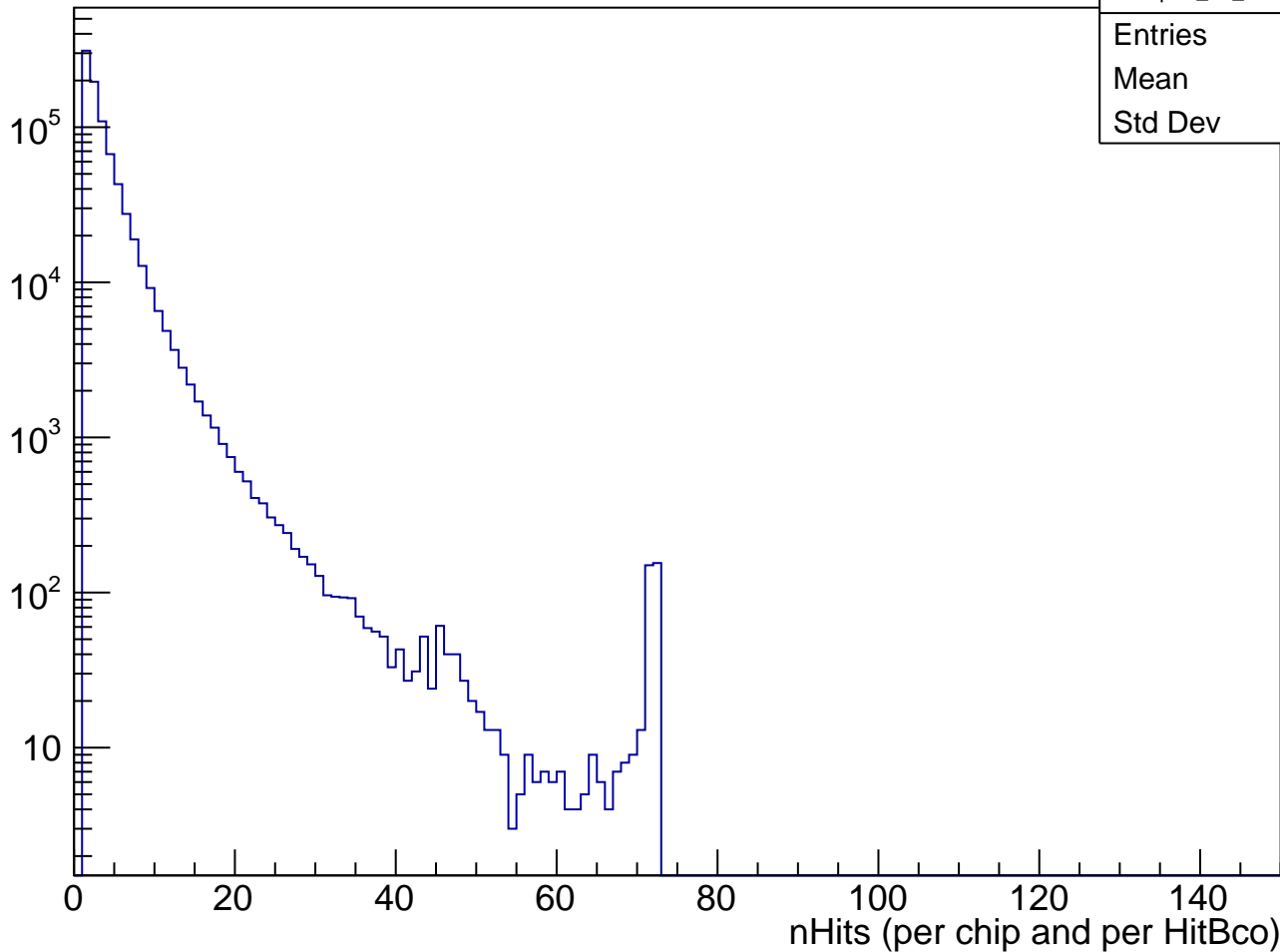
Entries 831230

Mean 2.99

Std Dev 3.478

nChipHit_F2_Fch10_Chip24

Entries

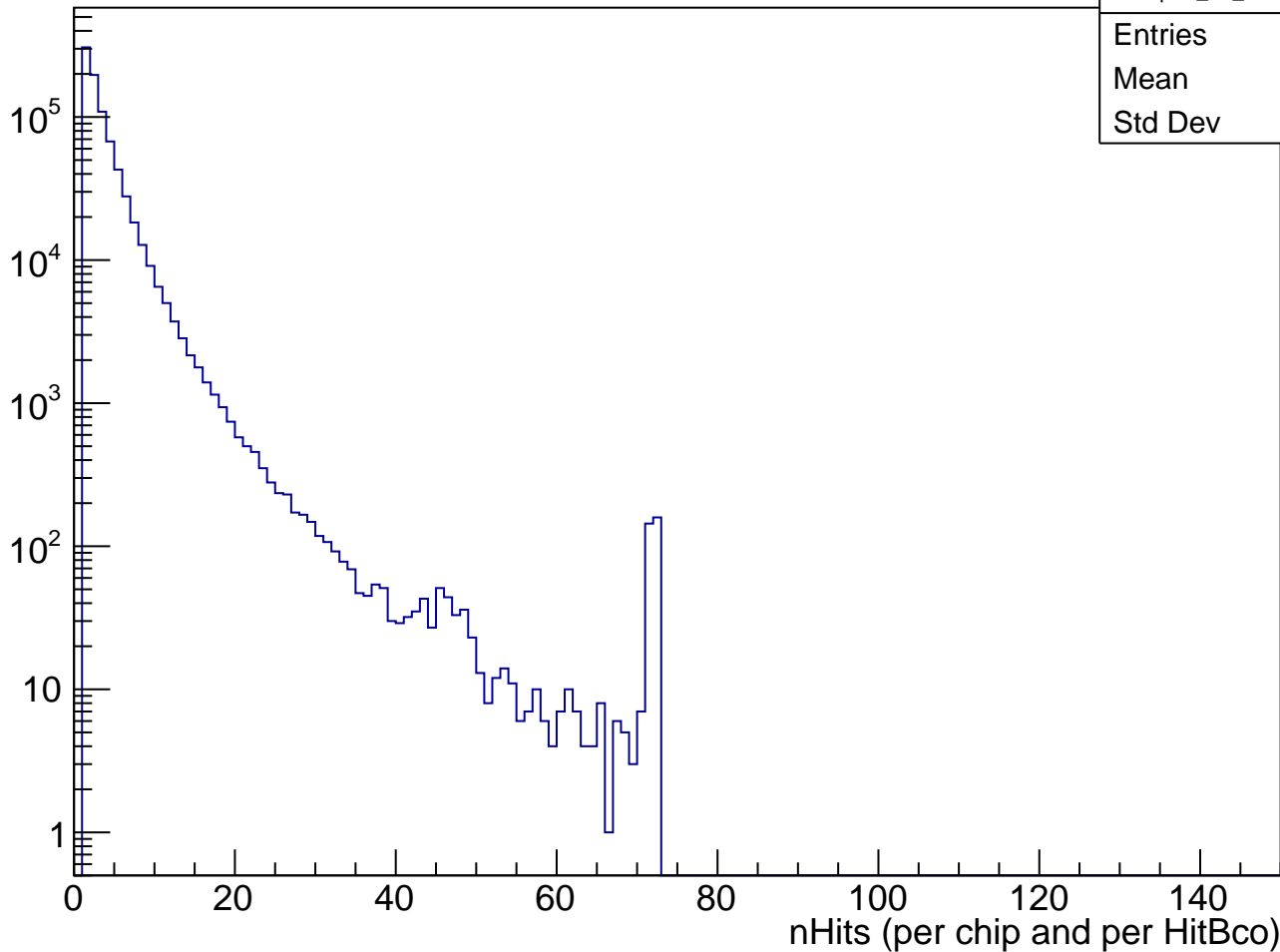


nChipHit_F2_Fch10_Chip24	
Entries	826216
Mean	2.995
Std Dev	3.5

nHits (per chip and per HitBco)

nChipHit_F2_Fch10_Chip25

Entries

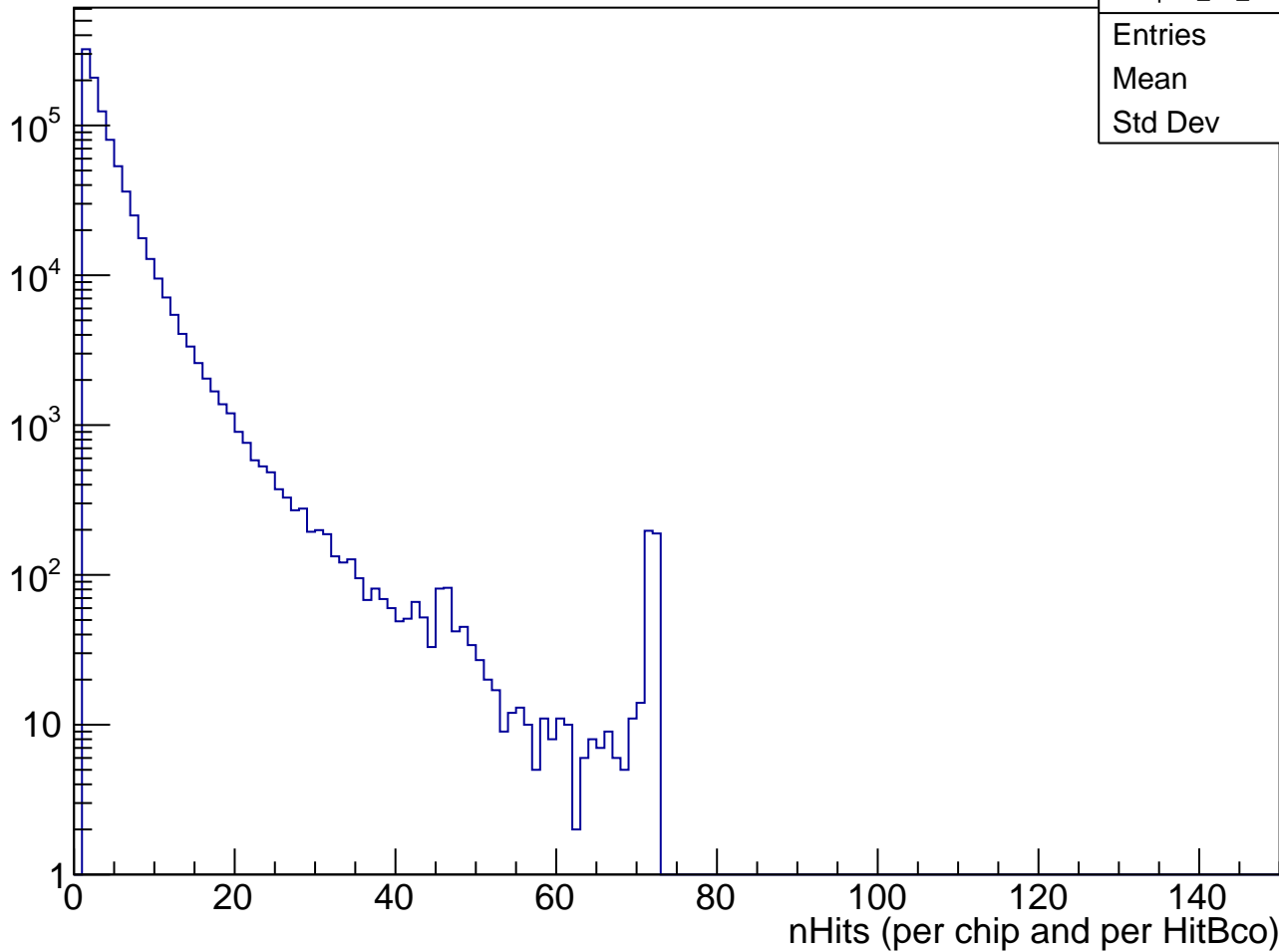


nChipHit_F2_Fch10_Chip25	
Entries	821836
Mean	2.998
Std Dev	3.471

nHits (per chip and per HitBco)

nChipHit_F2_Fch10_Chip3

Entries

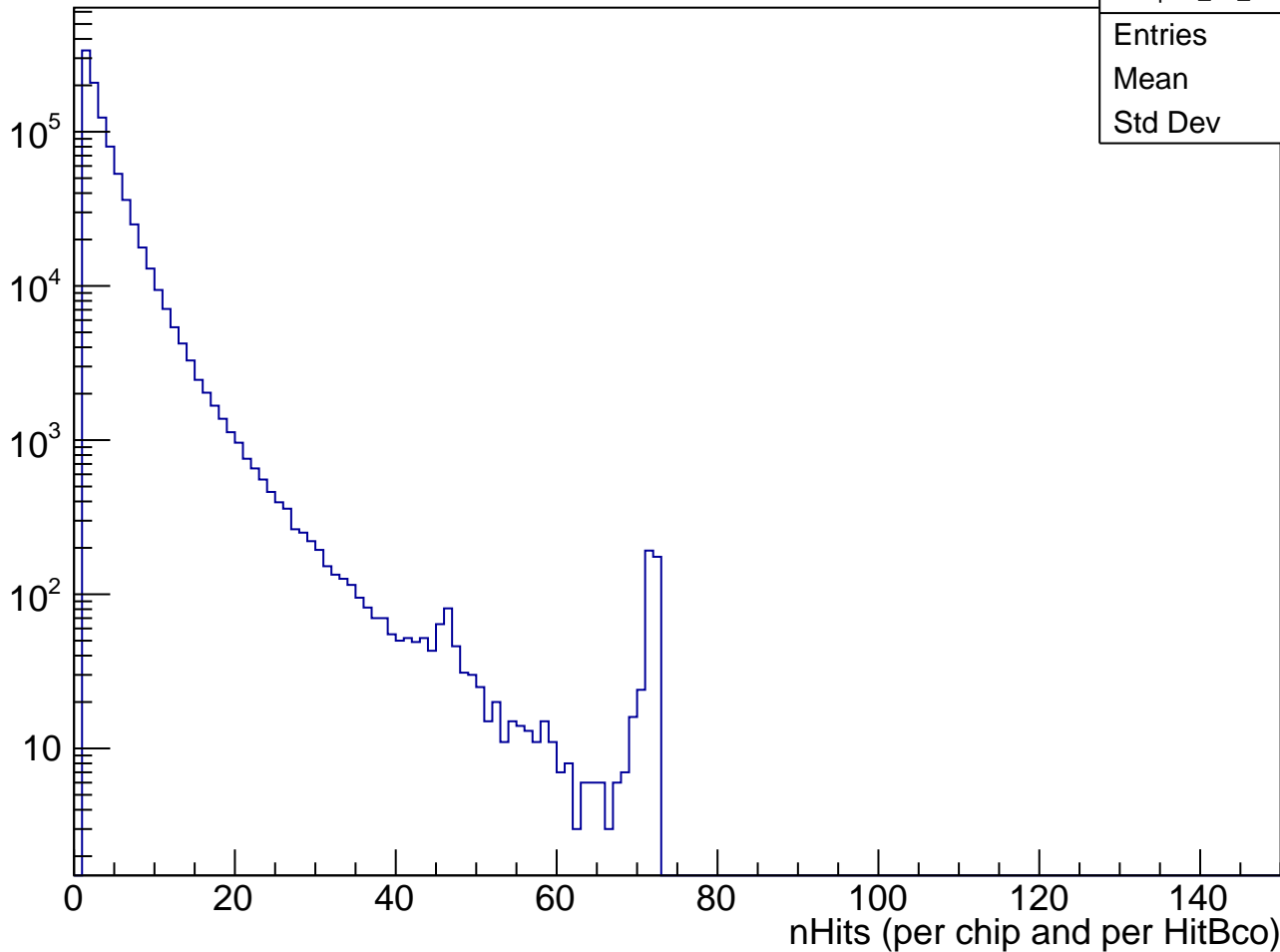


nChipHit_F2_Fch10_Chip3	
Entries	925770
Mean	3.29
Std Dev	3.834

nHits (per chip and per HitBco)

nChipHit_F2_Fch10_Chip4

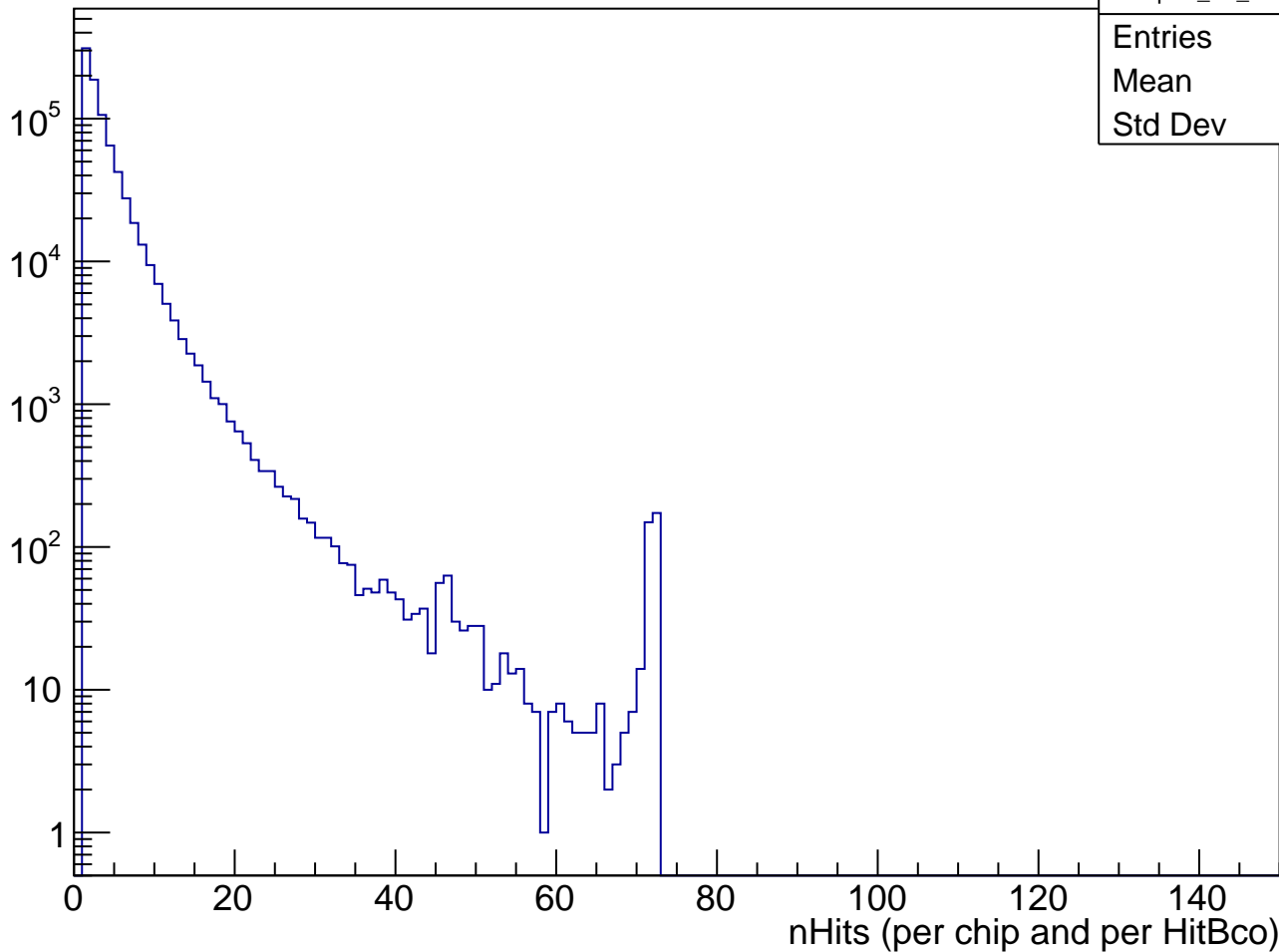
Entries



nChipHit_F2_Fch10_Chip4	
Entries	938976
Mean	3.254
Std Dev	3.806

nChipHit_F2_Fch10_Chip5

Entries

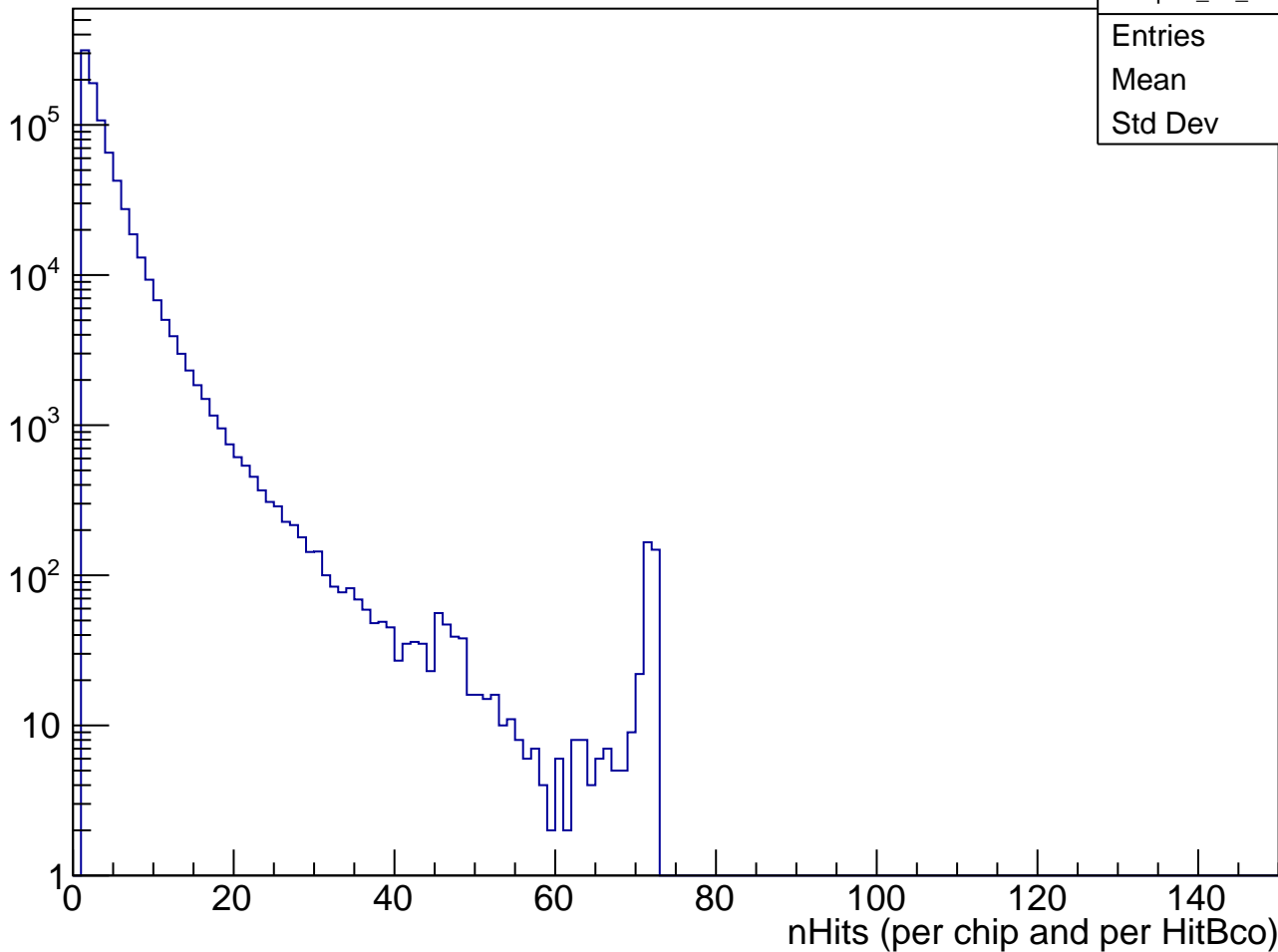


nChipHit_F2_Fch10_Chip5	
Entries	812661
Mean	3.02
Std Dev	3.564

nHits (per chip and per HitBco)

nChipHit_F2_Fch10_Chip6

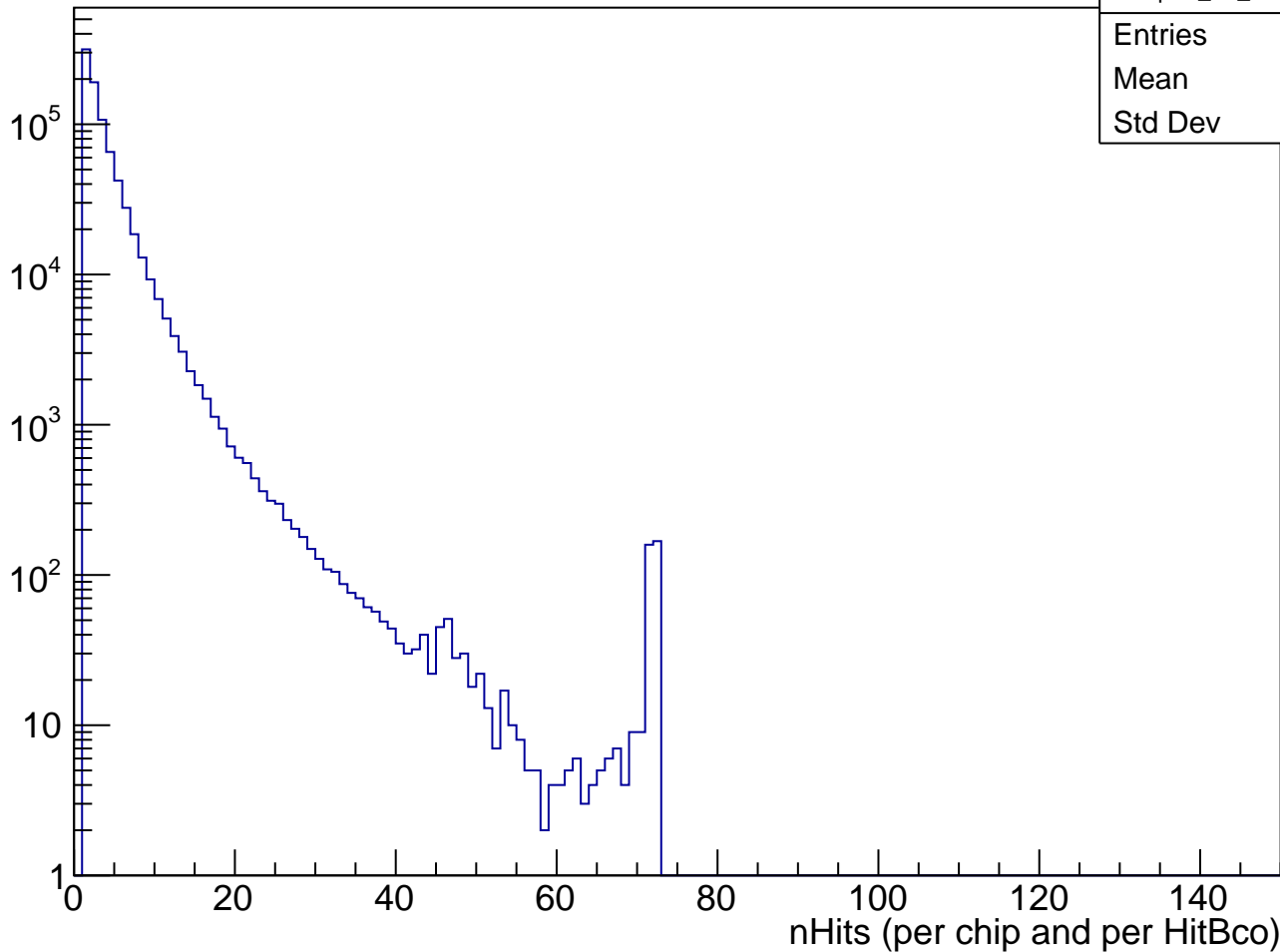
Entries



nChipHit_F2_Fch10_Chip6	
Entries	819493
Mean	3.012
Std Dev	3.552

nChipHit_F2_Fch10_Chip7

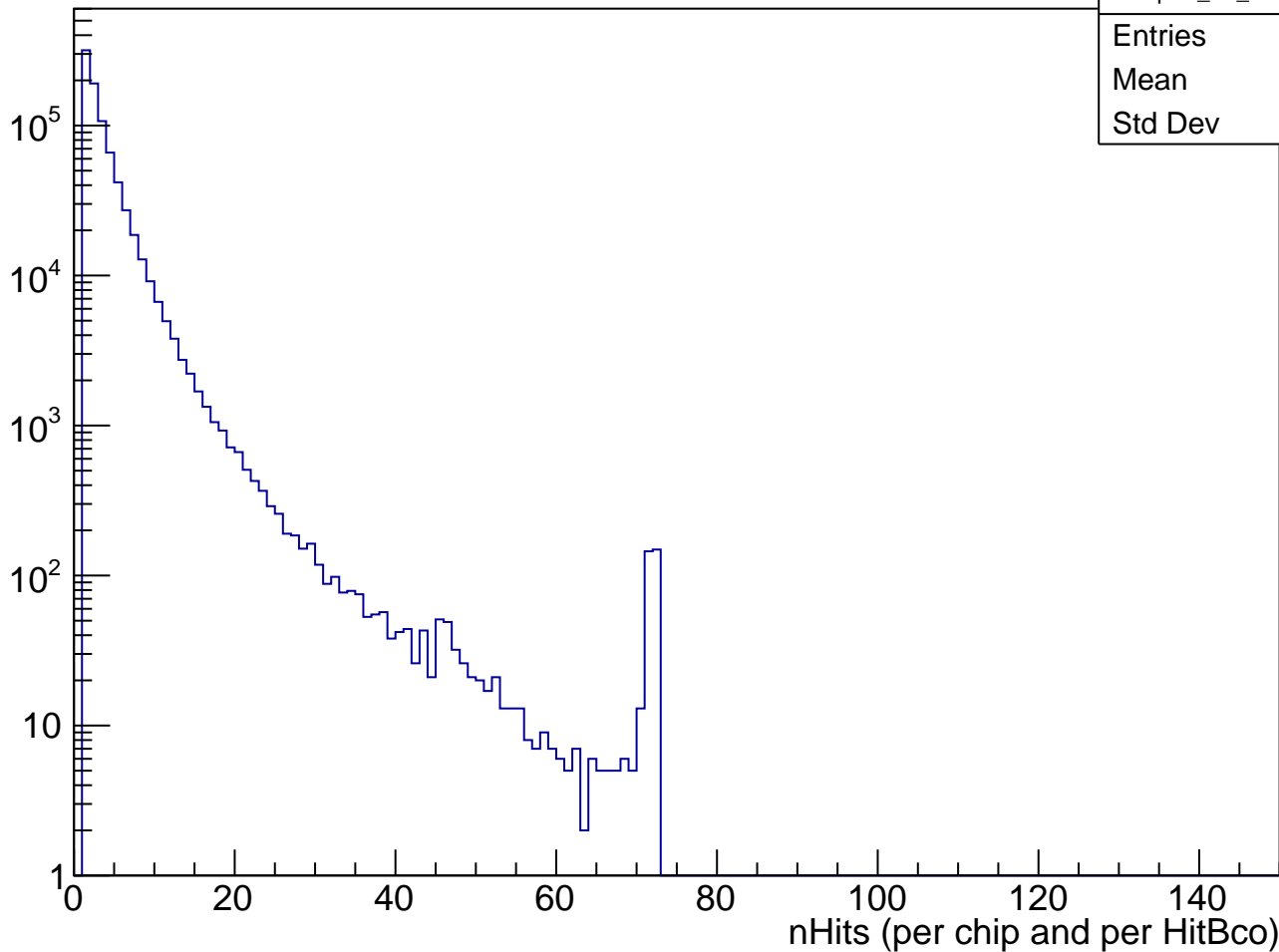
Entries



nChipHit_F2_Fch10_Chip7	
Entries	821209
Mean	3.006
Std Dev	3.54

nChipHit_F2_Fch10_Chip8

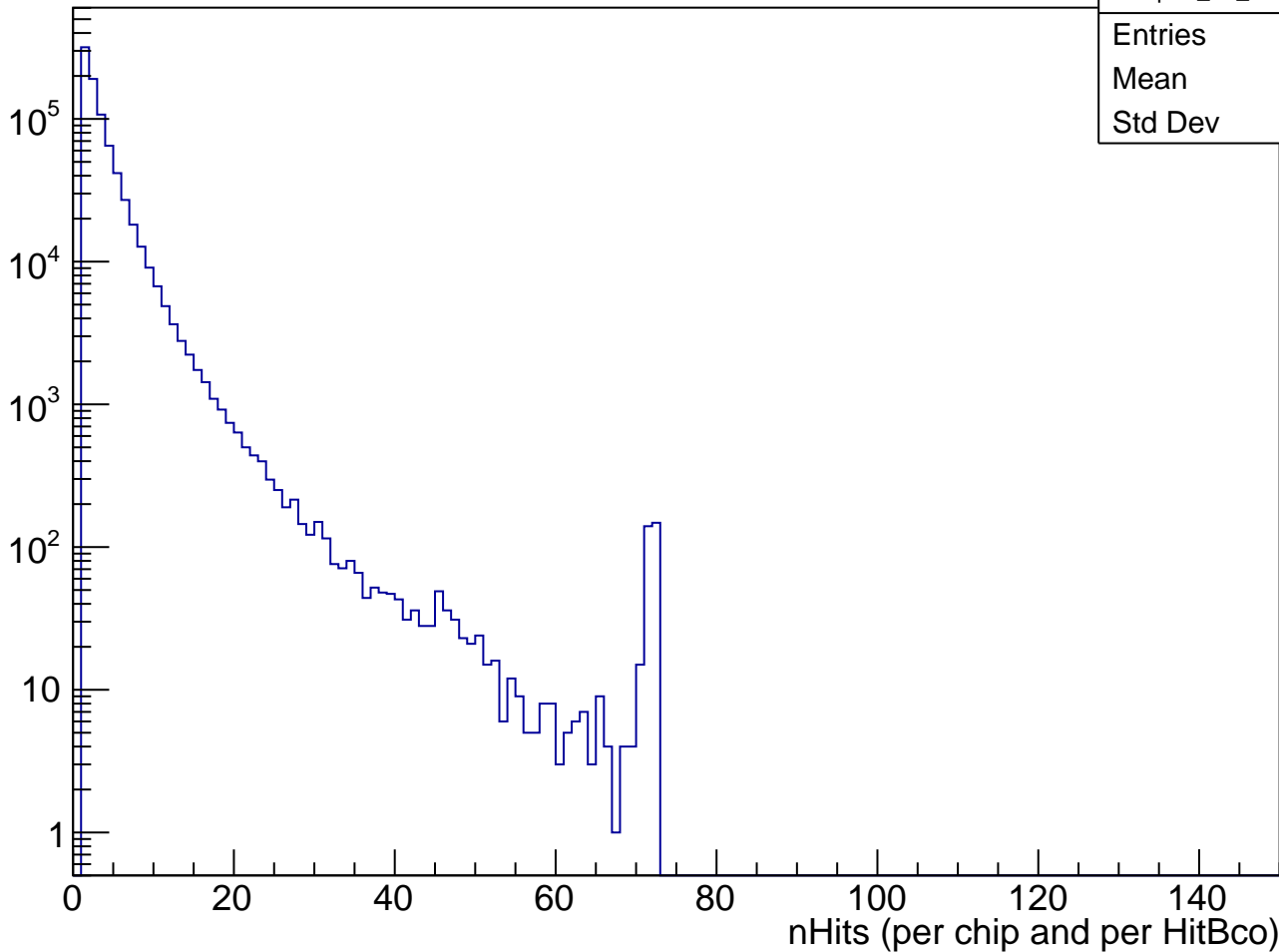
Entries



nChipHit_F2_Fch10_Chip8	
Entries	821678
Mean	2.978
Std Dev	3.495

nChipHit_F2_Fch10_Chip9

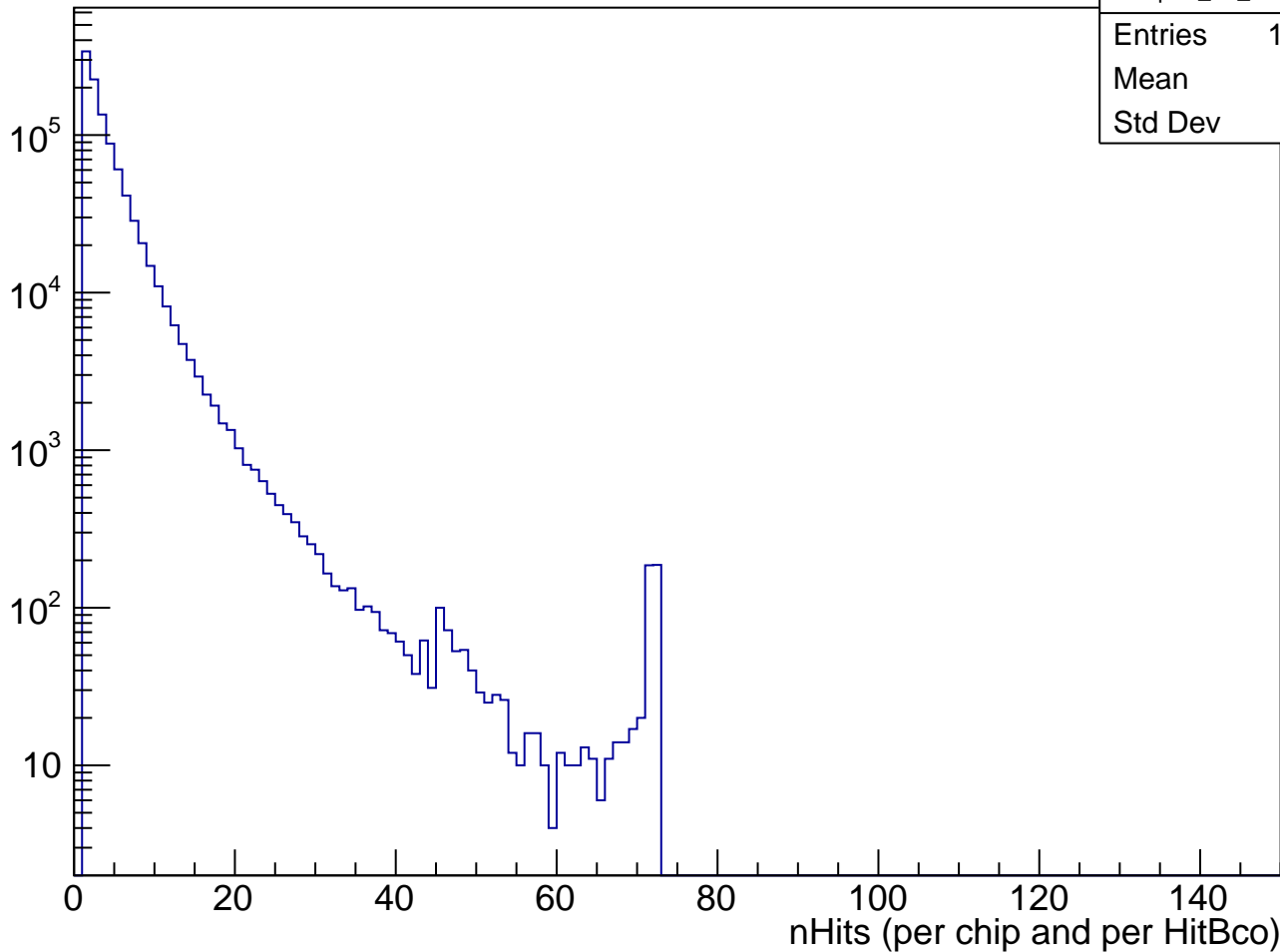
Entries



nChipHit_F2_Fch10_Chip9	
Entries	820229
Mean	2.97
Std Dev	3.473

nChipHit_F2_Fch11_Chip0

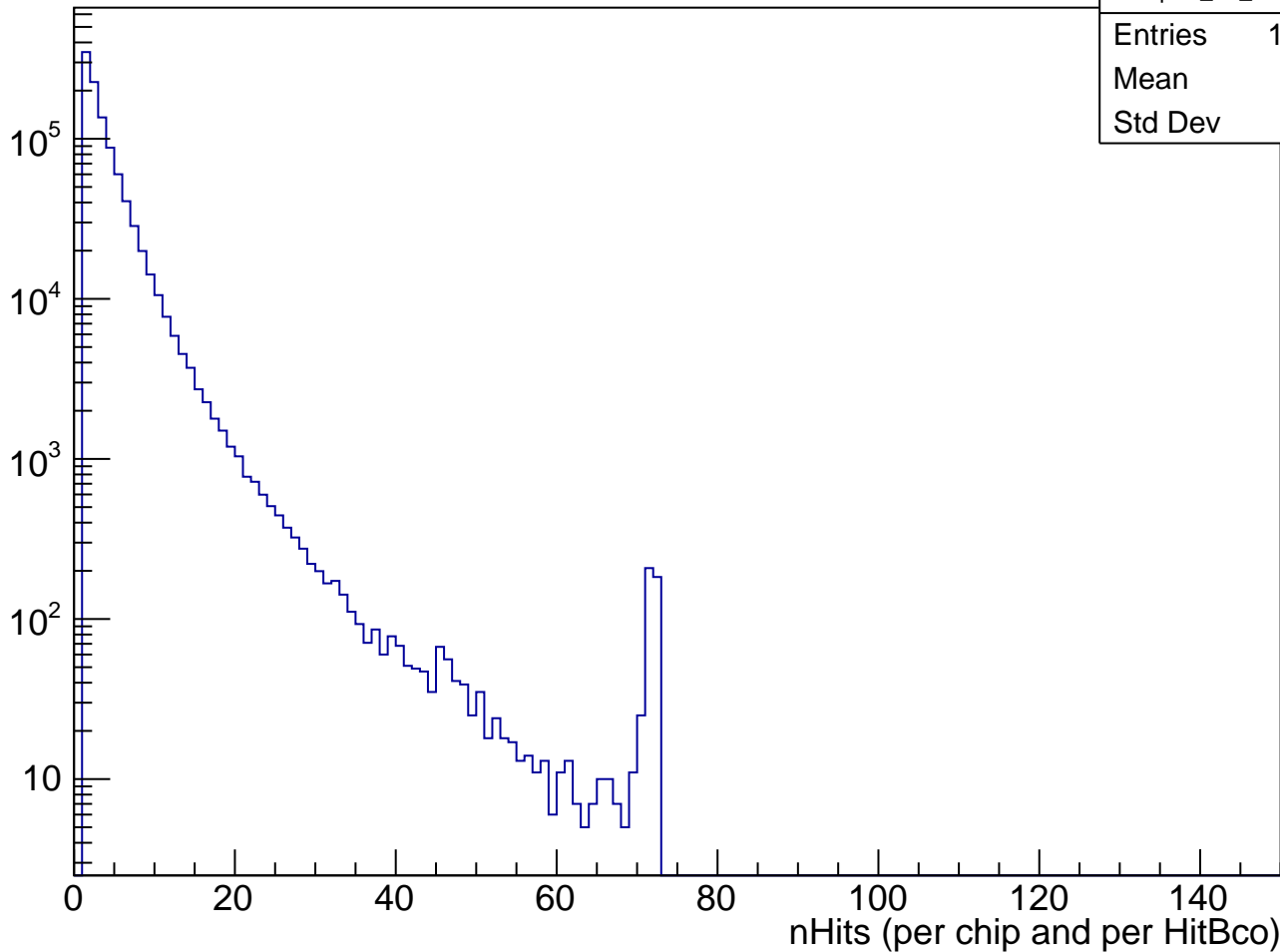
Entries



nChipHit_F2_Fch11_Chip0	
Entries	1005949
Mean	3.368
Std Dev	3.88

nChipHit_F2_Fch11_Chip1

Entries

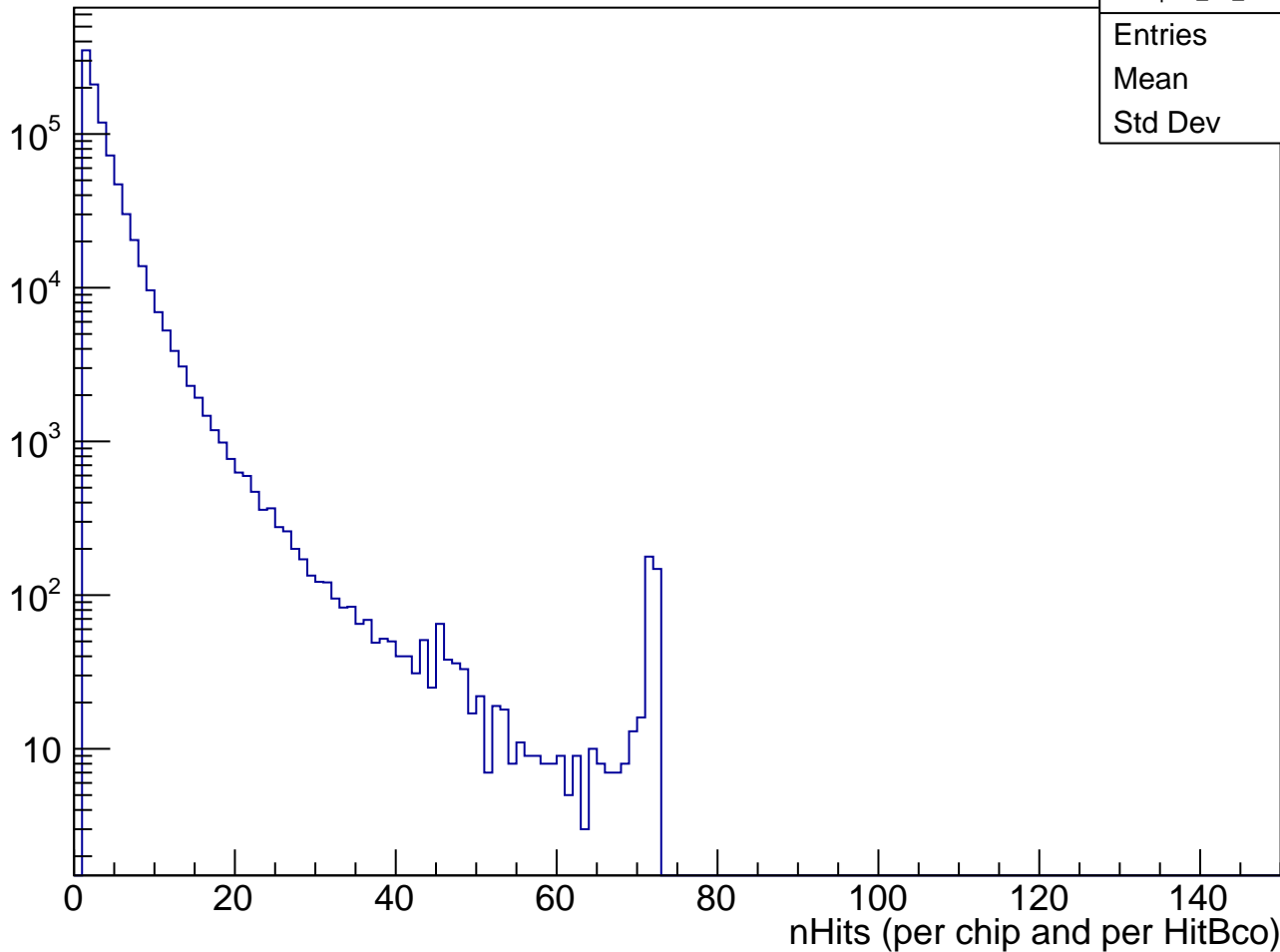


nChipHit_F2_Fch11_Chip1	
Entries	1011322
Mean	3.311
Std Dev	3.805

nHits (per chip and per HitBco)

nChipHit_F2_Fch11_Chip10

Entries



nChipHit_F2_Fch11_Chip10

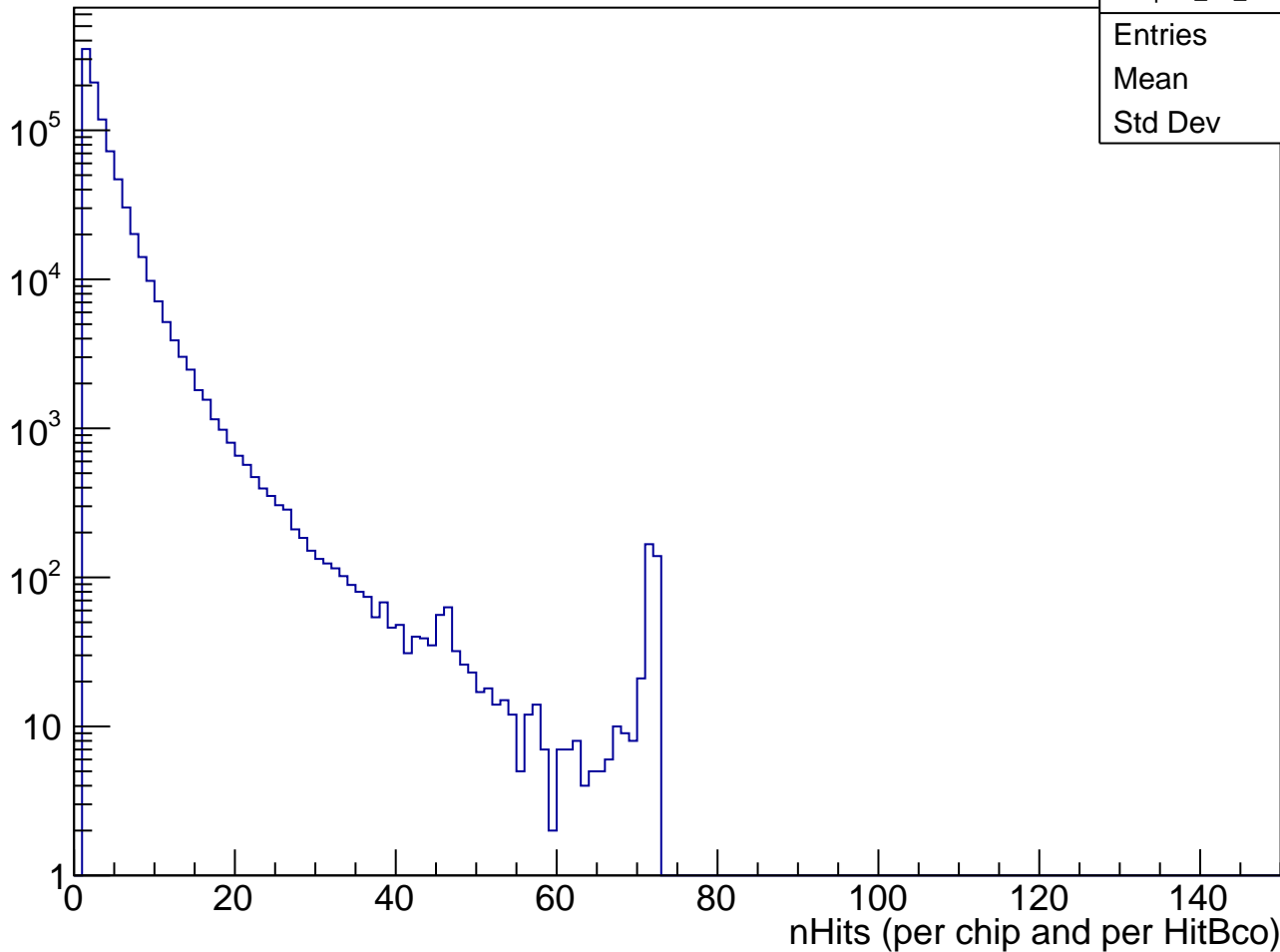
Entries 905896

Mean 2.962

Std Dev 3.476

nChipHit_F2_Fch11_Chip11

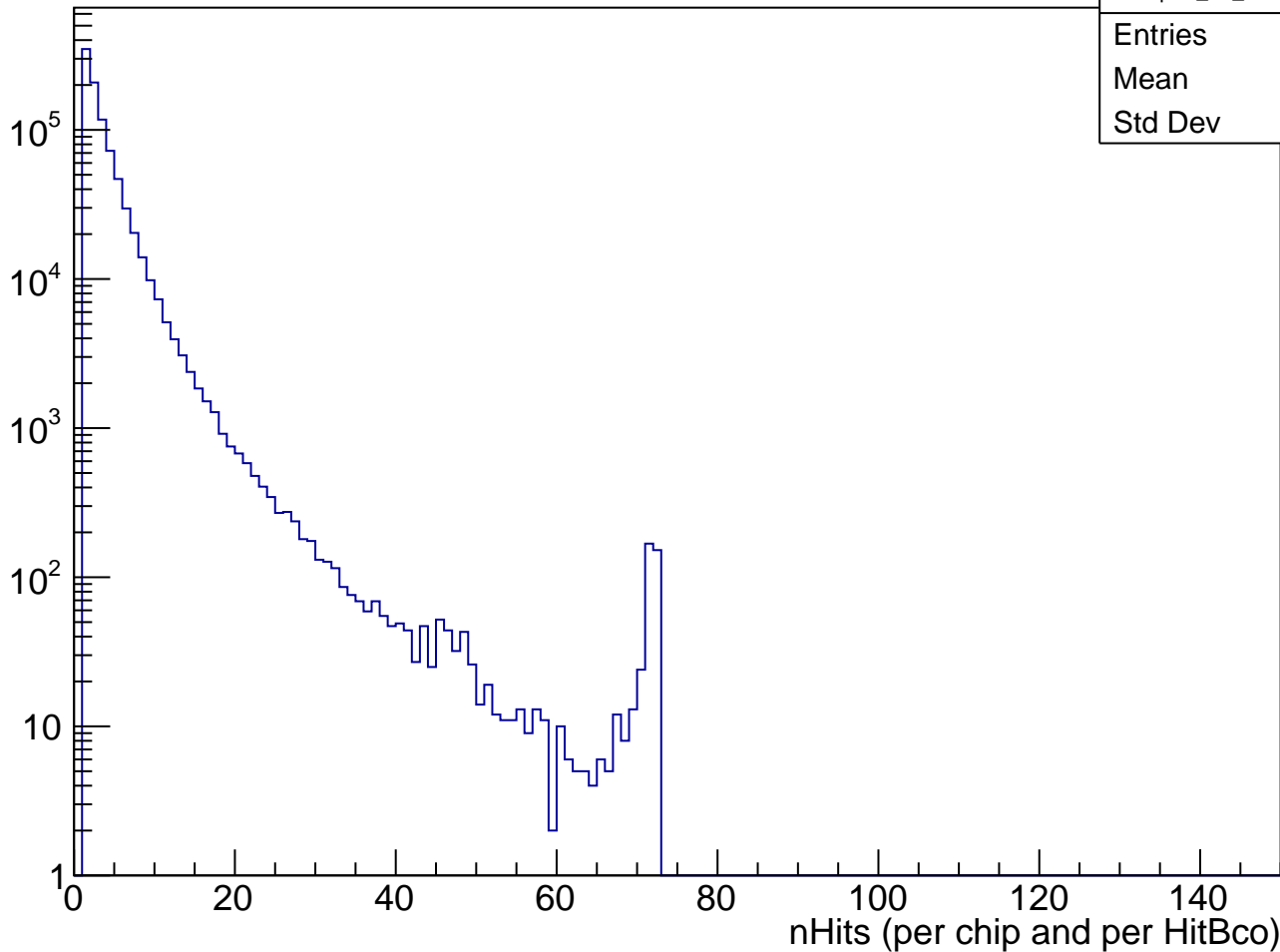
Entries



nChipHit_F2_Fch11_Chip11	
Entries	905472
Mean	2.97
Std Dev	3.49

nChipHit_F2_Fch11_Chip12

Entries



nChipHit_F2_Fch11_Chip12

Entries 900356

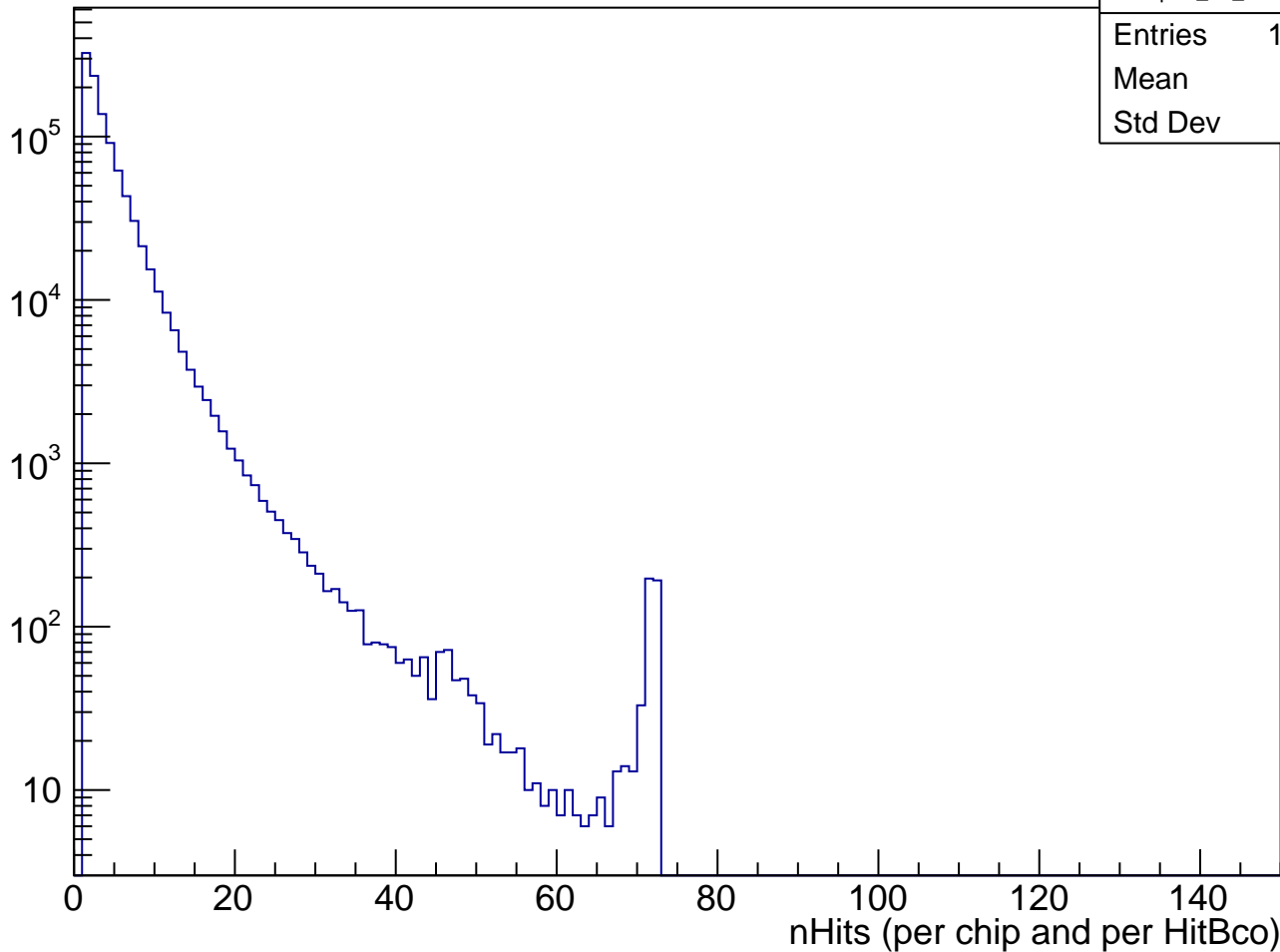
Mean 2.977

Std Dev 3.509

nHits (per chip and per HitBco)

nChipHit_F2_Fch11_Chip13

Entries



nChipHit_F2_Fch11_Chip13

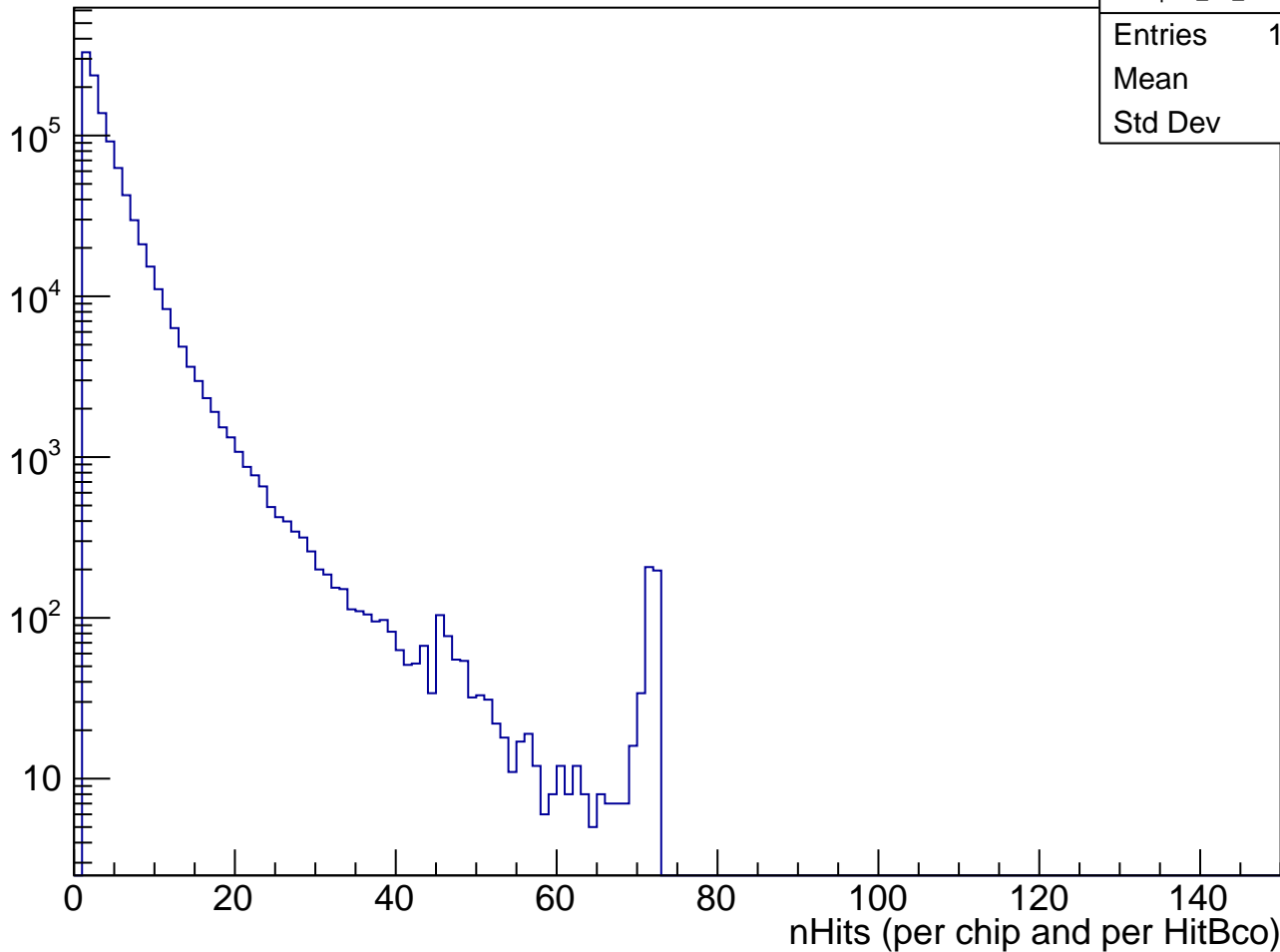
Entries 1014022

Mean 3.419

Std Dev 3.872

nChipHit_F2_Fch11_Chip14

Entries



nChipHit_F2_Fch11_Chip14

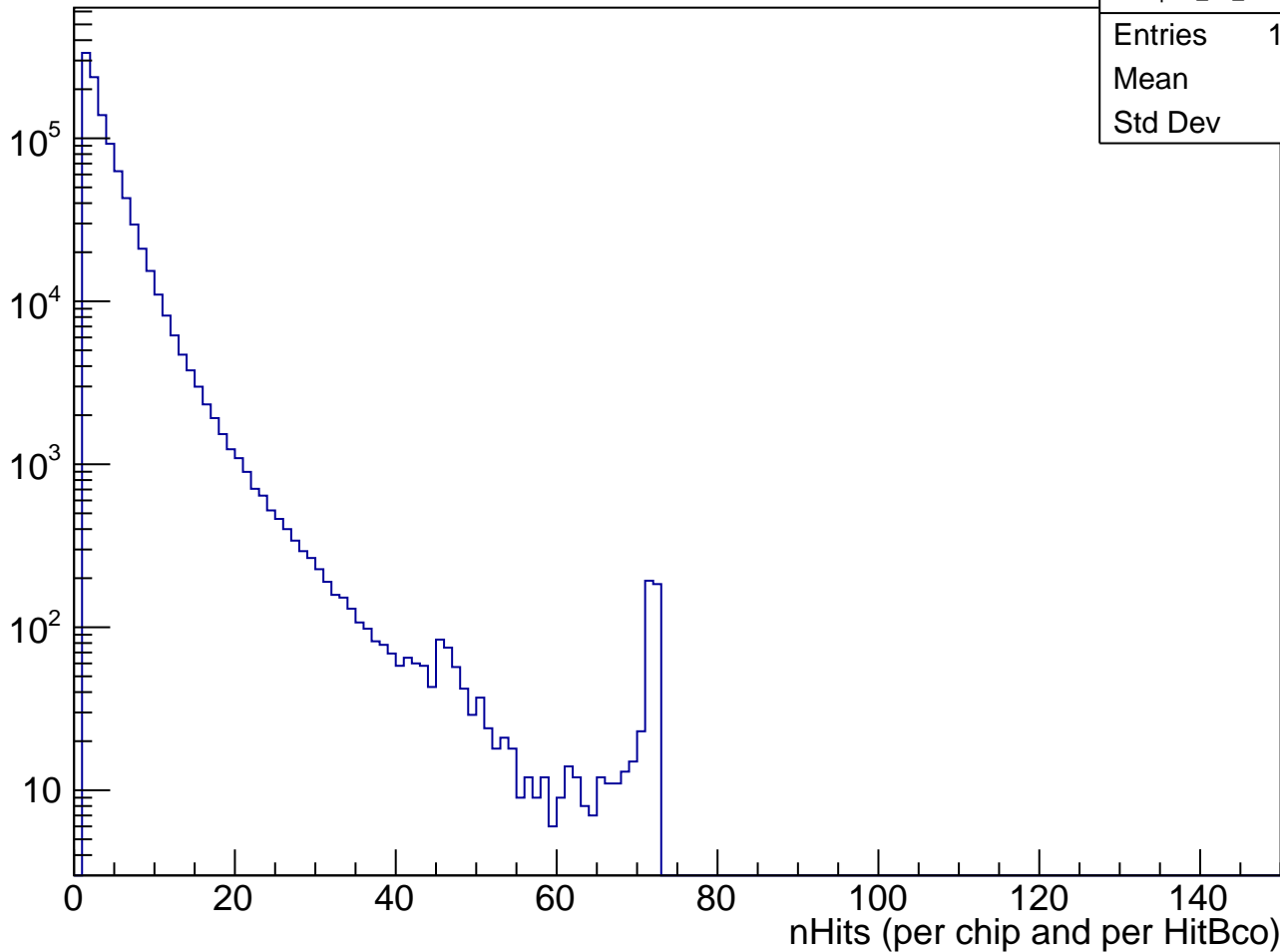
Entries 1019532

Mean 3.406

Std Dev 3.896

nChipHit_F2_Fch11_Chip15

Entries



nChipHit_F2_Fch11_Chip15

Entries 1026478

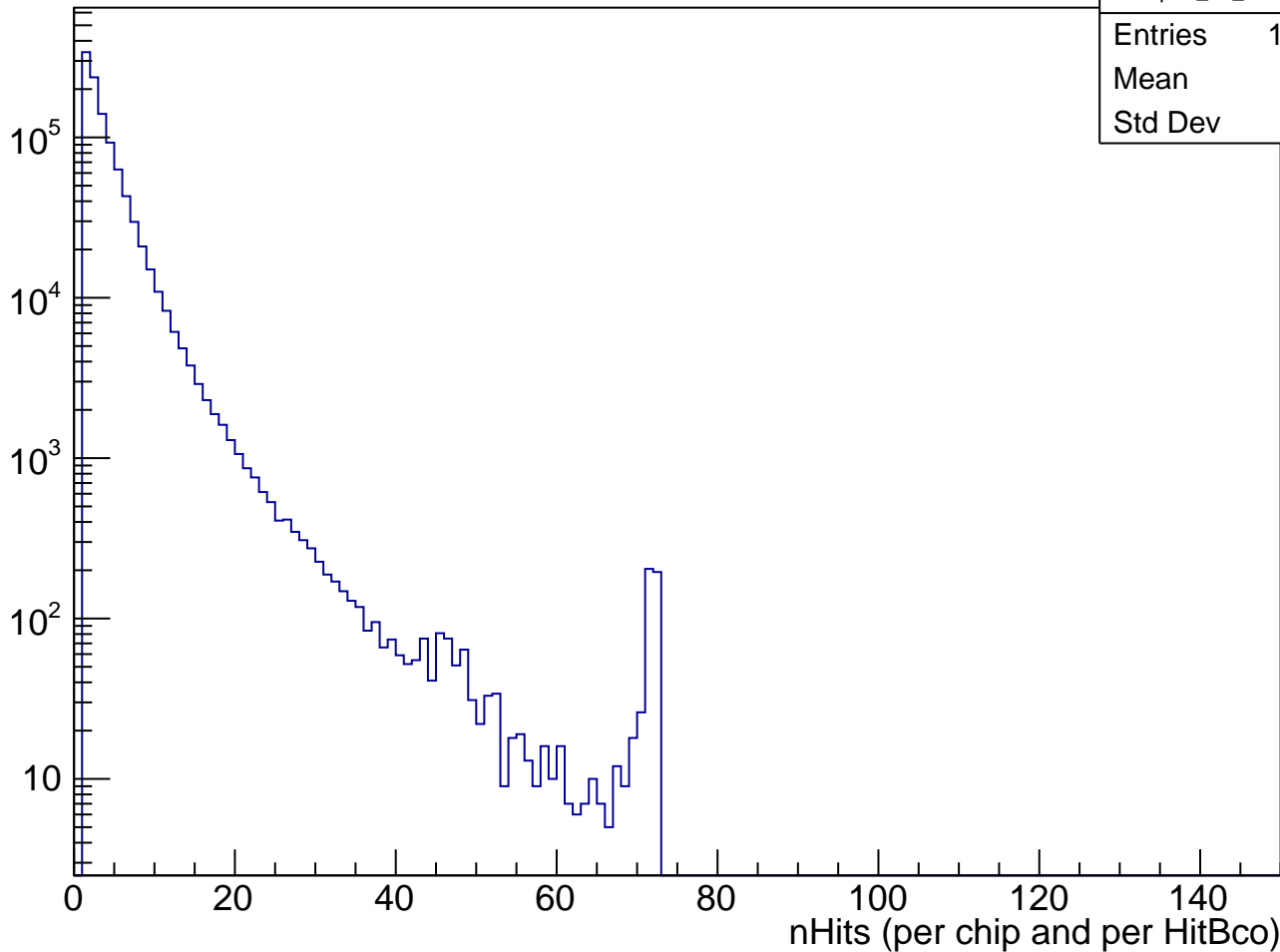
Mean 3.388

Std Dev 3.859

nHits (per chip and per HitBco)

nChipHit_F2_Fch11_Chip16

Entries



nChipHit_F2_Fch11_Chip16

Entries 1033712

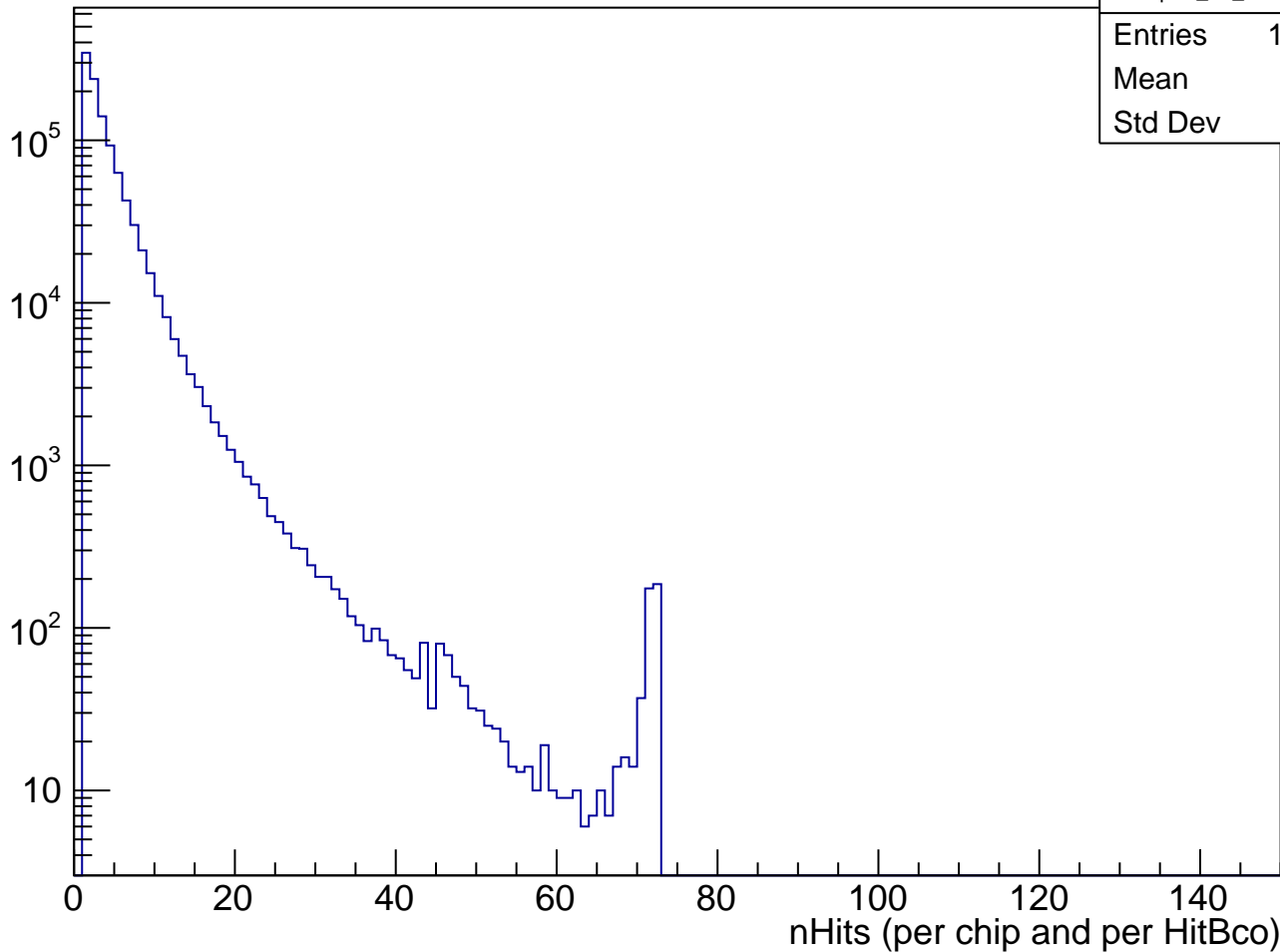
Mean 3.377

Std Dev 3.869

nHits (per chip and per HitBco)

nChipHit_F2_Fch11_Chip17

Entries



nChipHit_F2_Fch11_Chip17

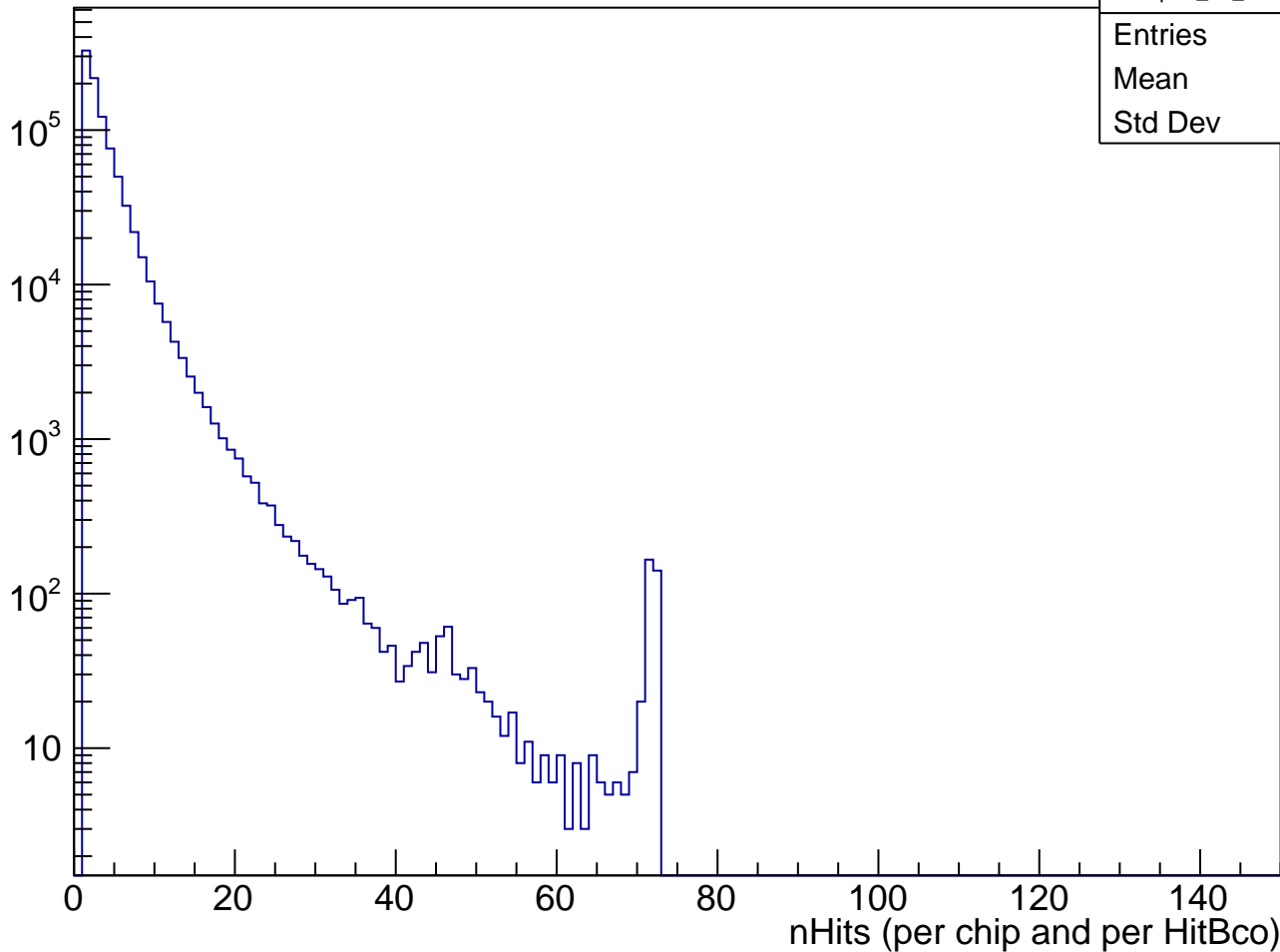
Entries 1040784

Mean 3.354

Std Dev 3.826

nChipHit_F2_Fch11_Chip18

Entries



nChipHit_F2_Fch11_Chip18

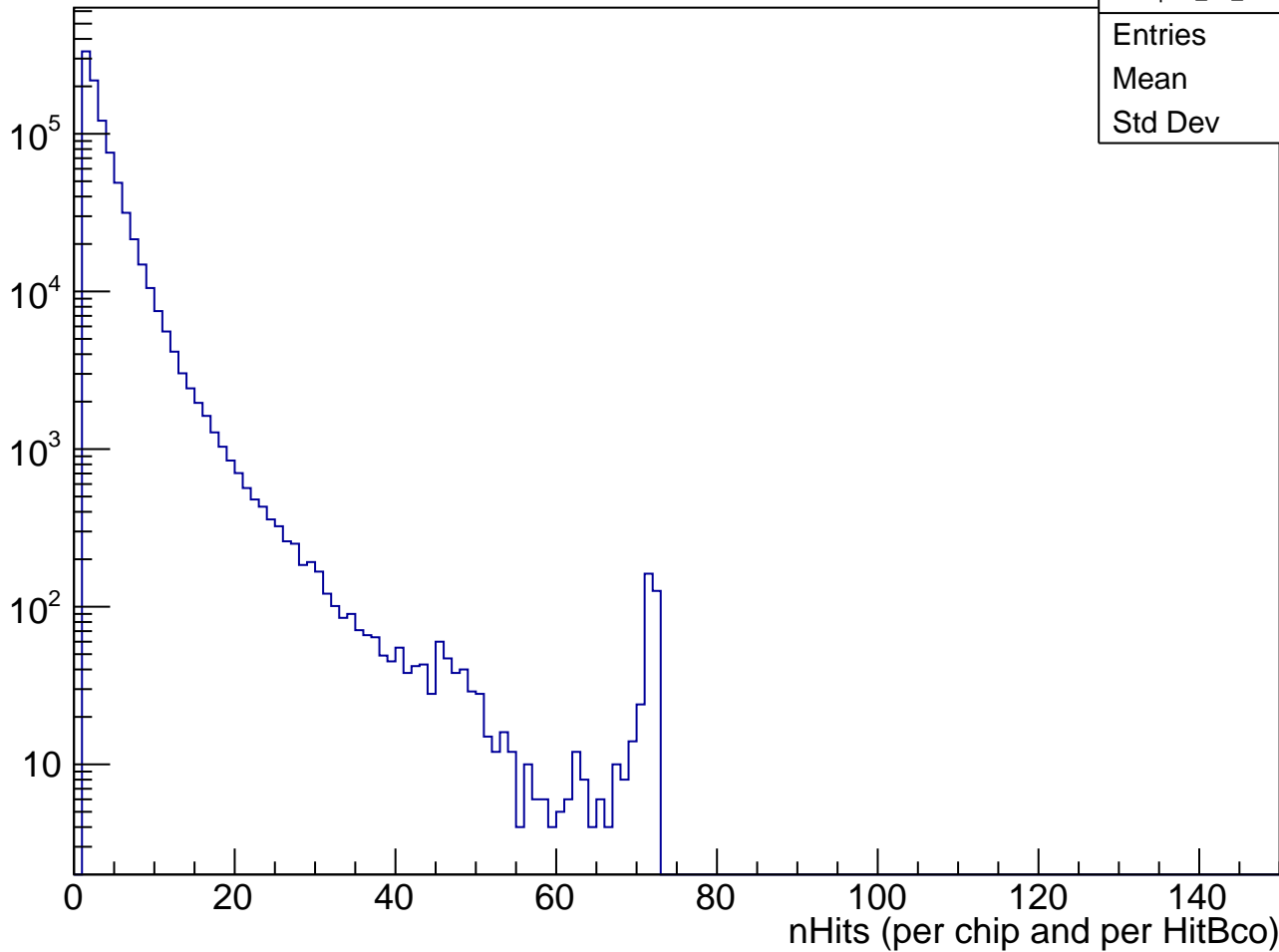
Entries 907434

Mean 3.072

Std Dev 3.519

nChipHit_F2_Fch11_Chip19

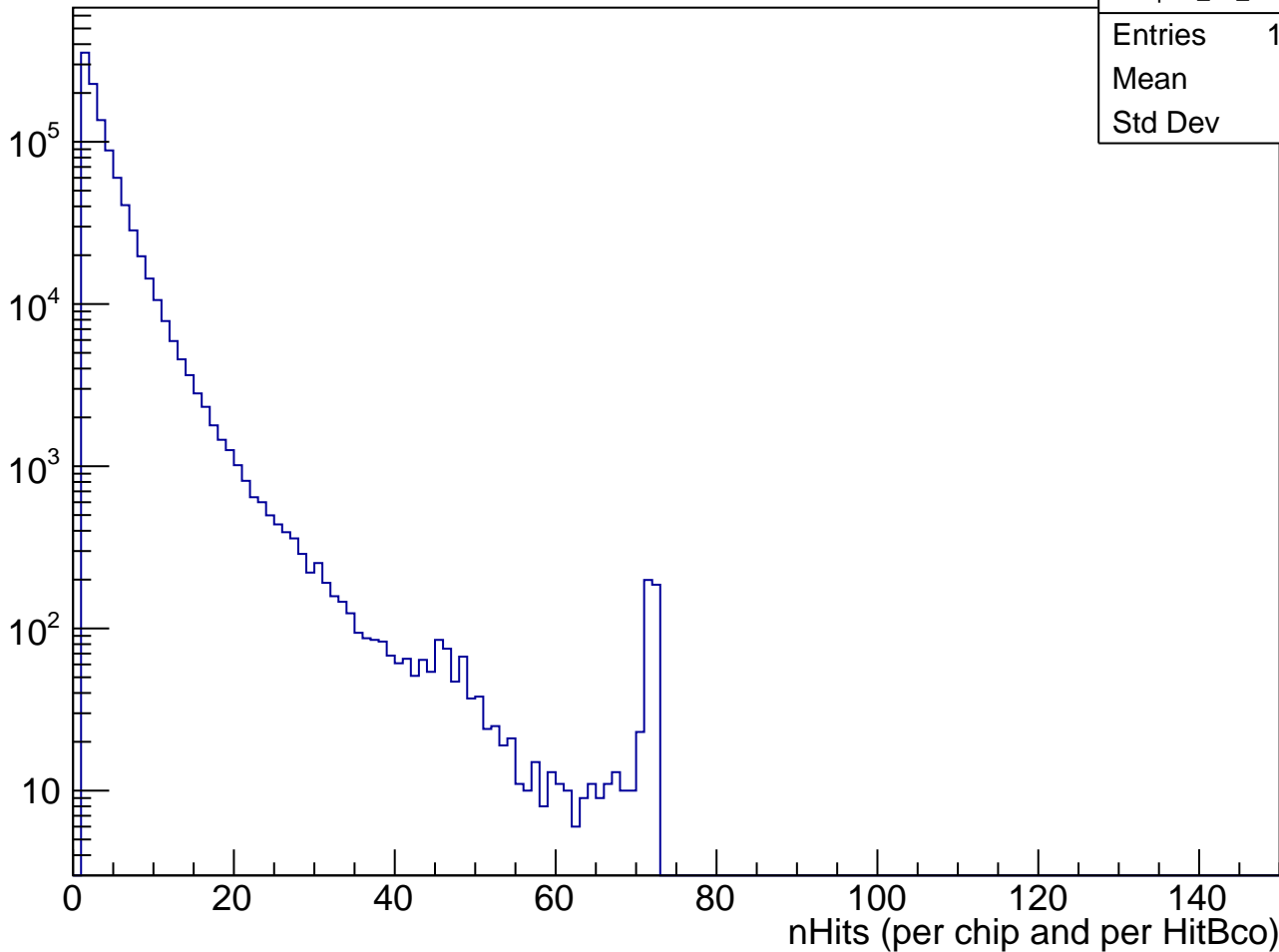
Entries



nChipHit_F2_Fch11_Chip19	
Entries	910655
Mean	3.045
Std Dev	3.513

nChipHit_F2_Fch11_Chip2

Entries

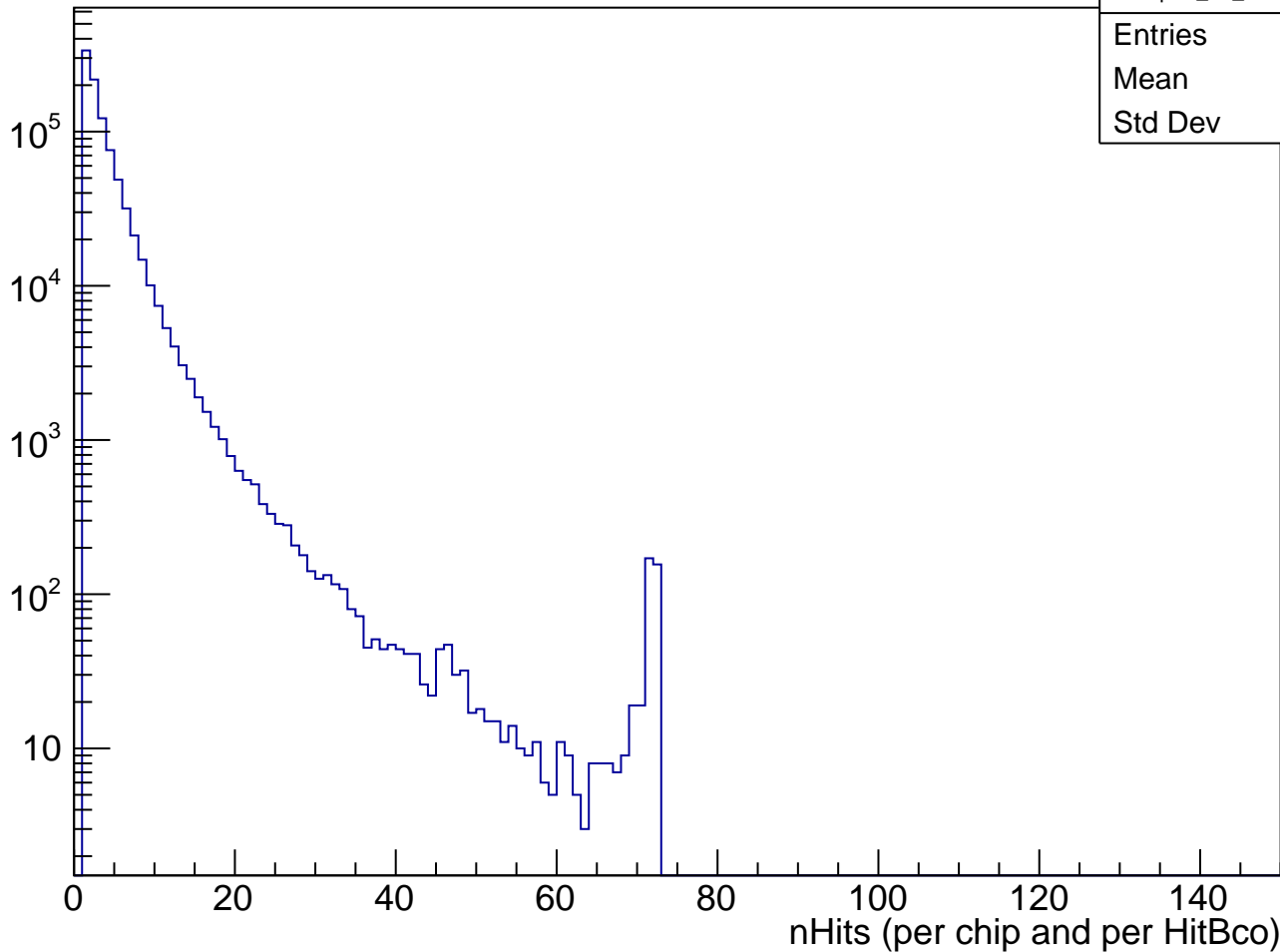


nChipHit_F2_Fch11_Chip2	
Entries	1019964
Mean	3.307
Std Dev	3.848

nHits (per chip and per HitBco)

nChipHit_F2_Fch11_Chip20

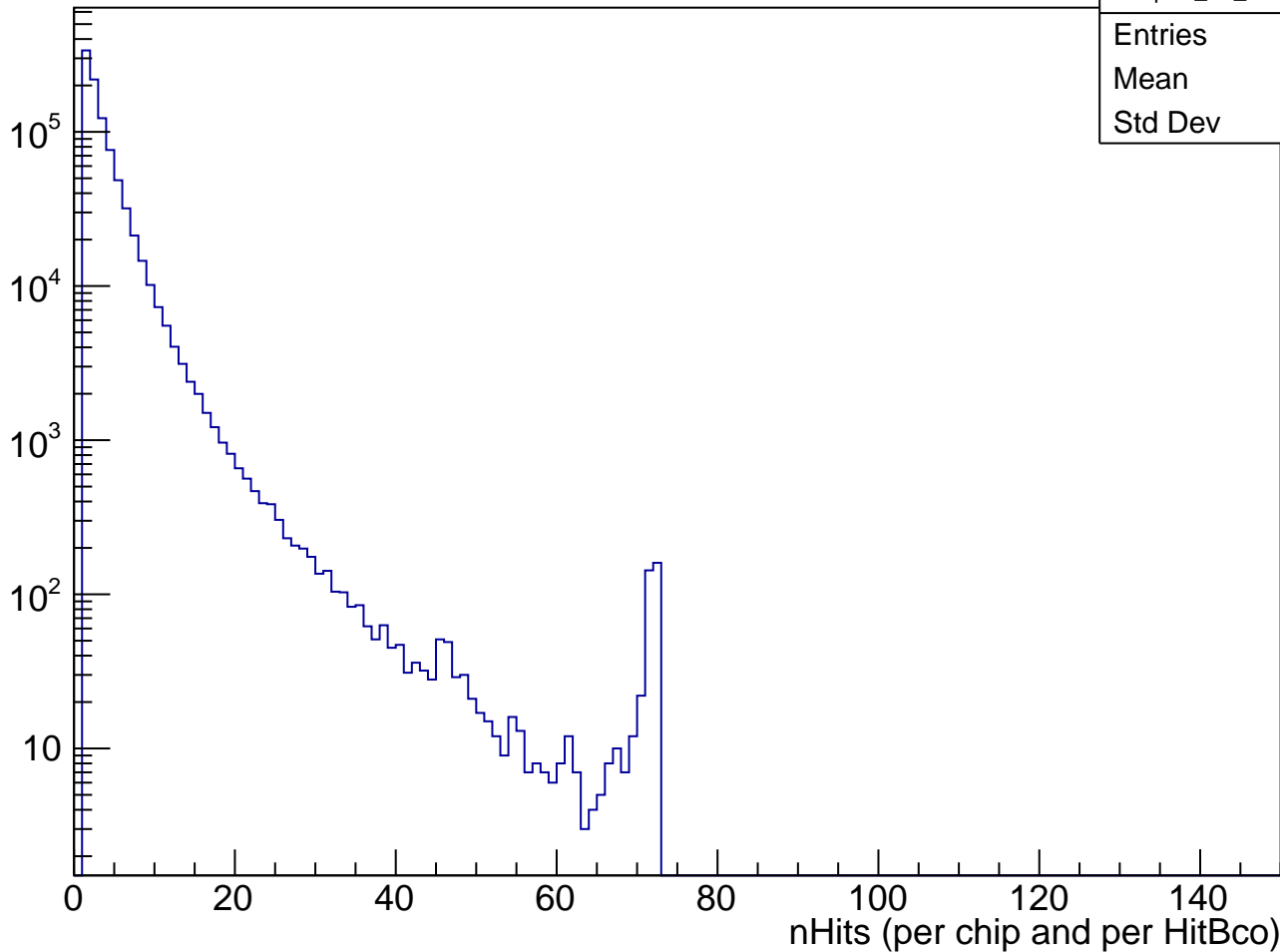
Entries



nChipHit_F2_Fch11_Chip20	
Entries	912024
Mean	3.021
Std Dev	3.485

nChipHit_F2_Fch11_Chip21

Entries

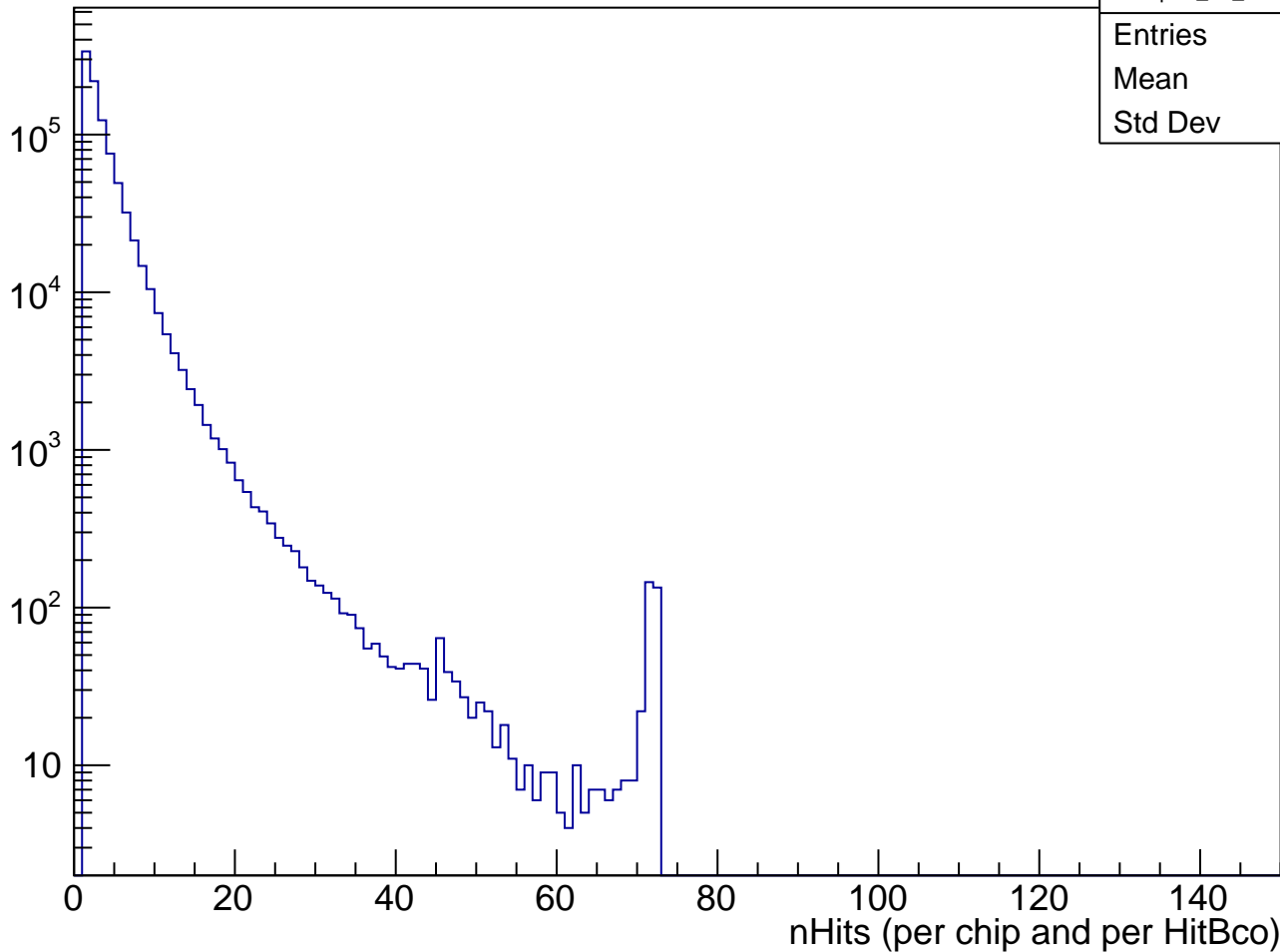


nChipHit_F2_Fch11_Chip21	
Entries	915729
Mean	3.019
Std Dev	3.475

nHits (per chip and per HitBco)

nChipHit_F2_Fch11_Chip22

Entries



nChipHit_F2_Fch11_Chip22

Entries 916002

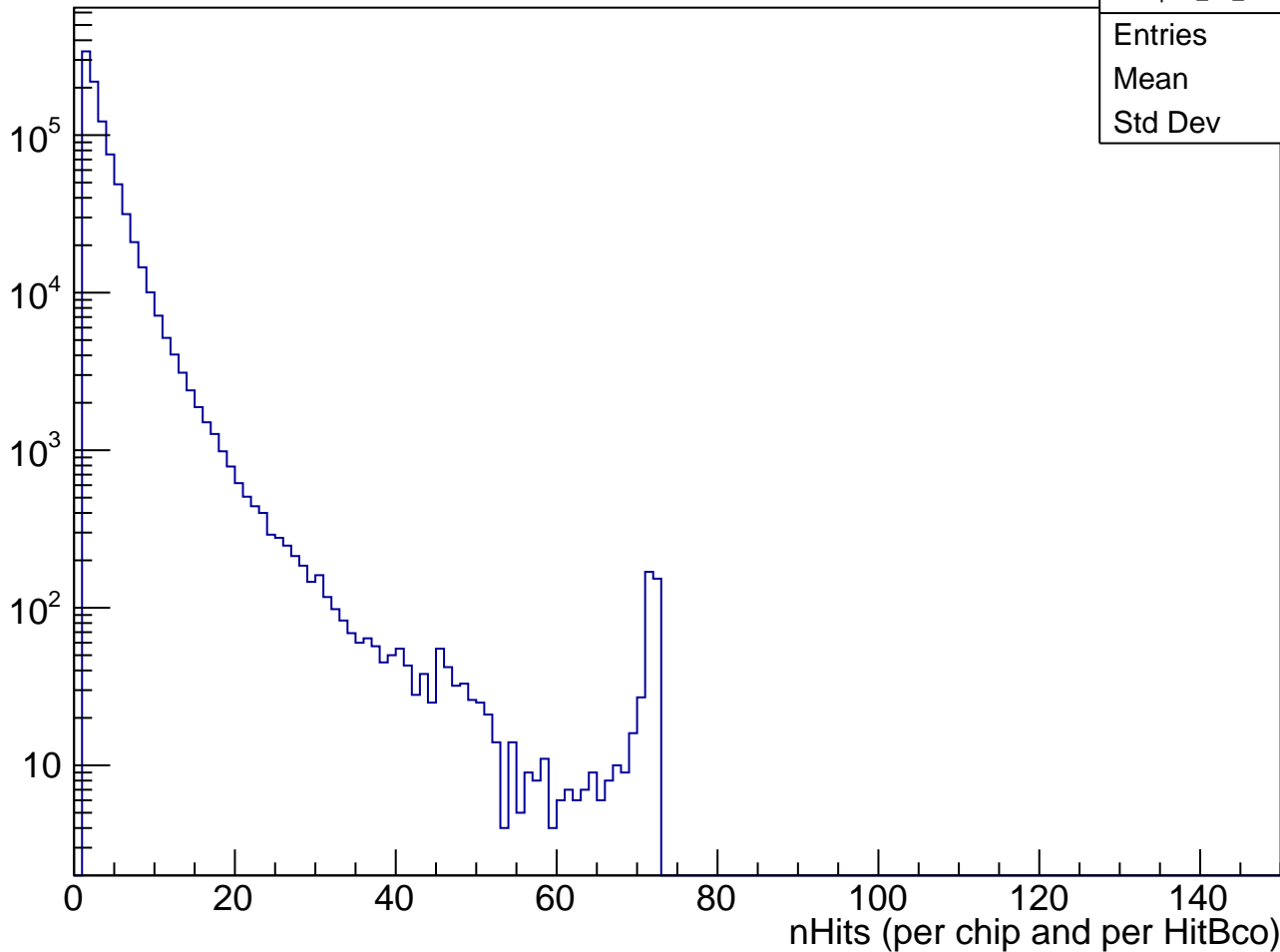
Mean 3.021

Std Dev 3.453

nHits (per chip and per HitBco)

nChipHit_F2_Fch11_Chip23

Entries



nChipHit_F2_Fch11_Chip23

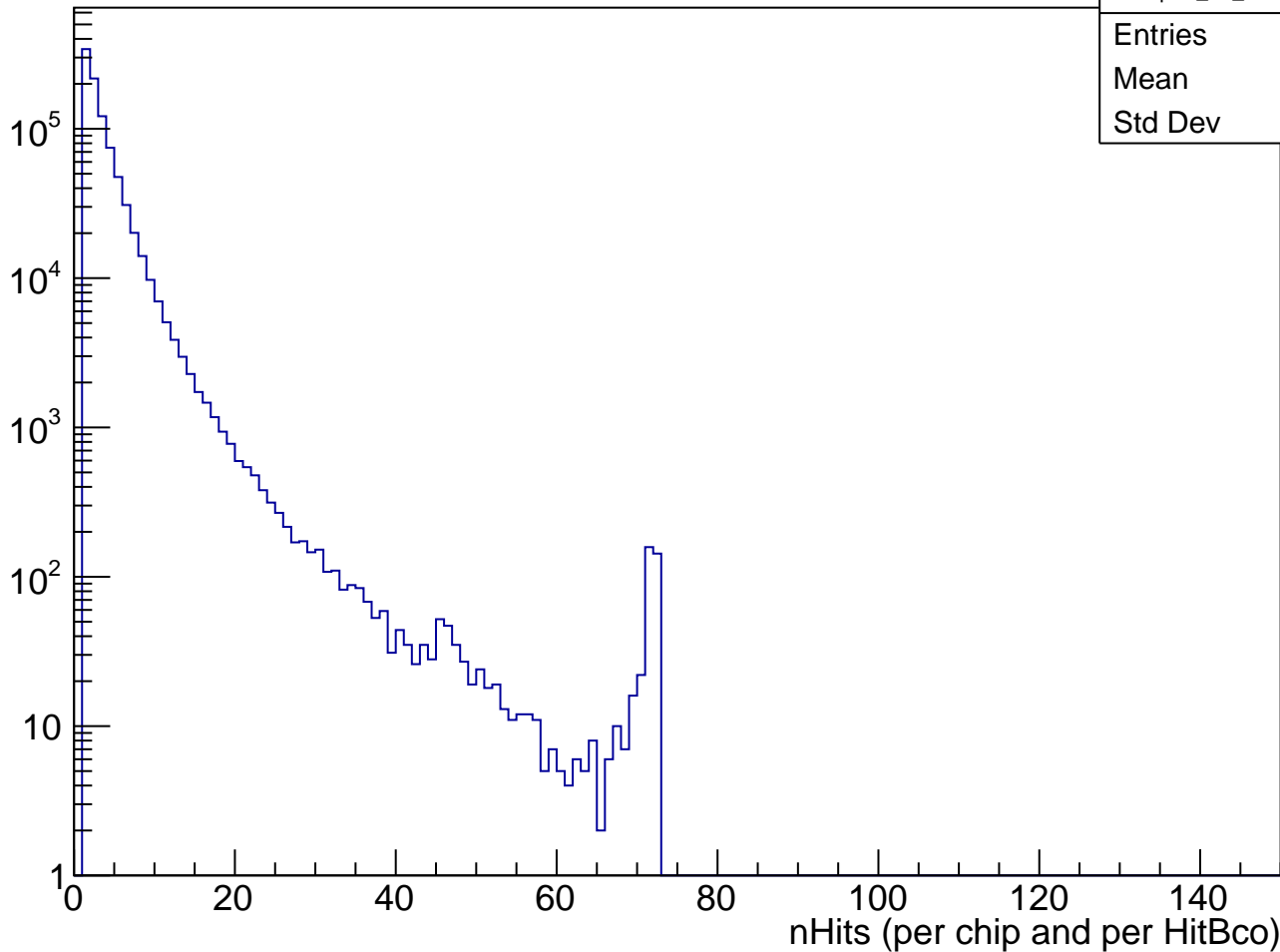
Entries 915009

Mean 3

Std Dev 3.472

nChipHit_F2_Fch11_Chip24

Entries



nChipHit_F2_Fch11_Chip24

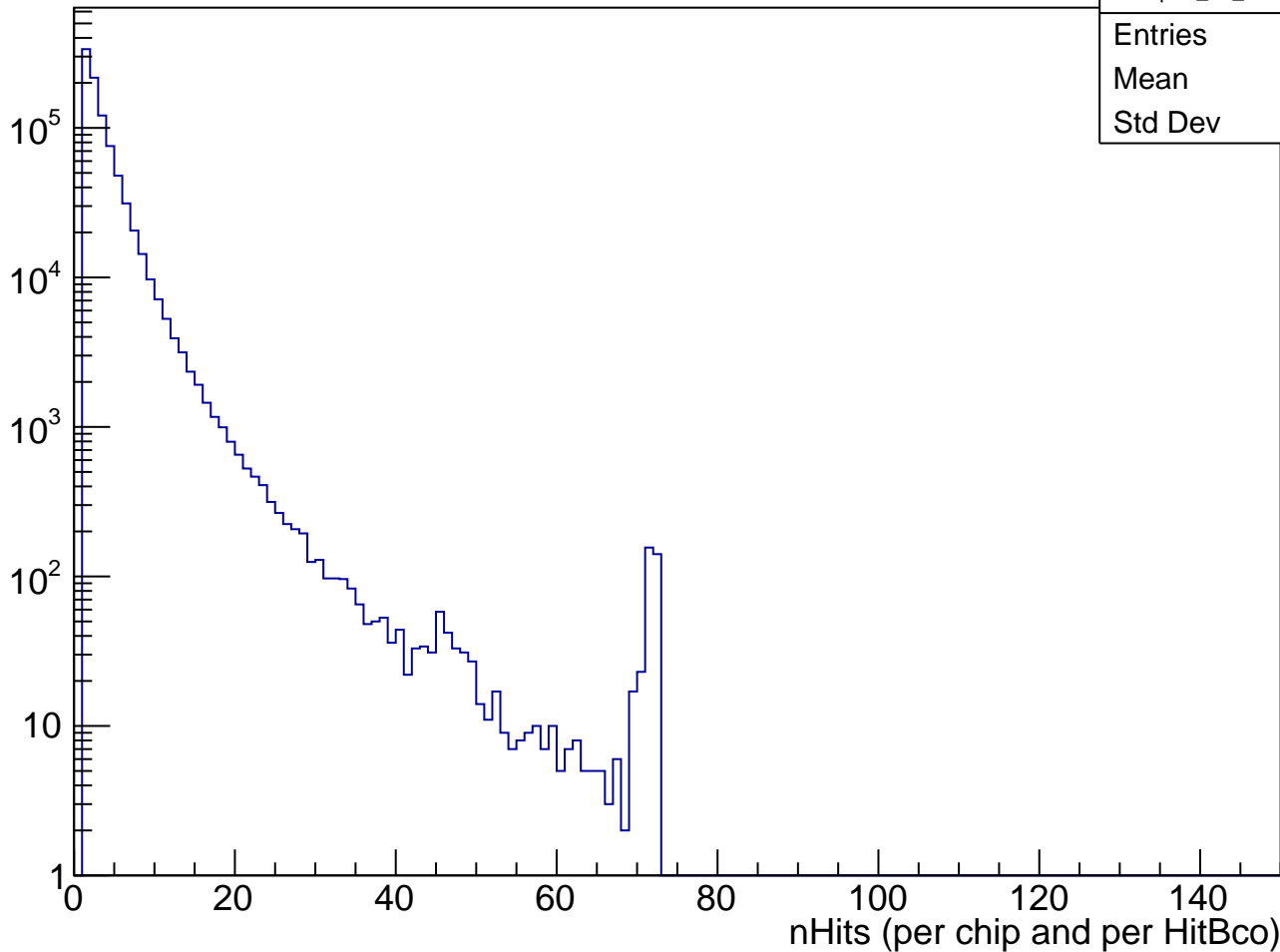
Entries 909615

Mean 2.969

Std Dev 3.428

nChipHit_F2_Fch11_Chip25

Entries



nChipHit_F2_Fch11_Chip25

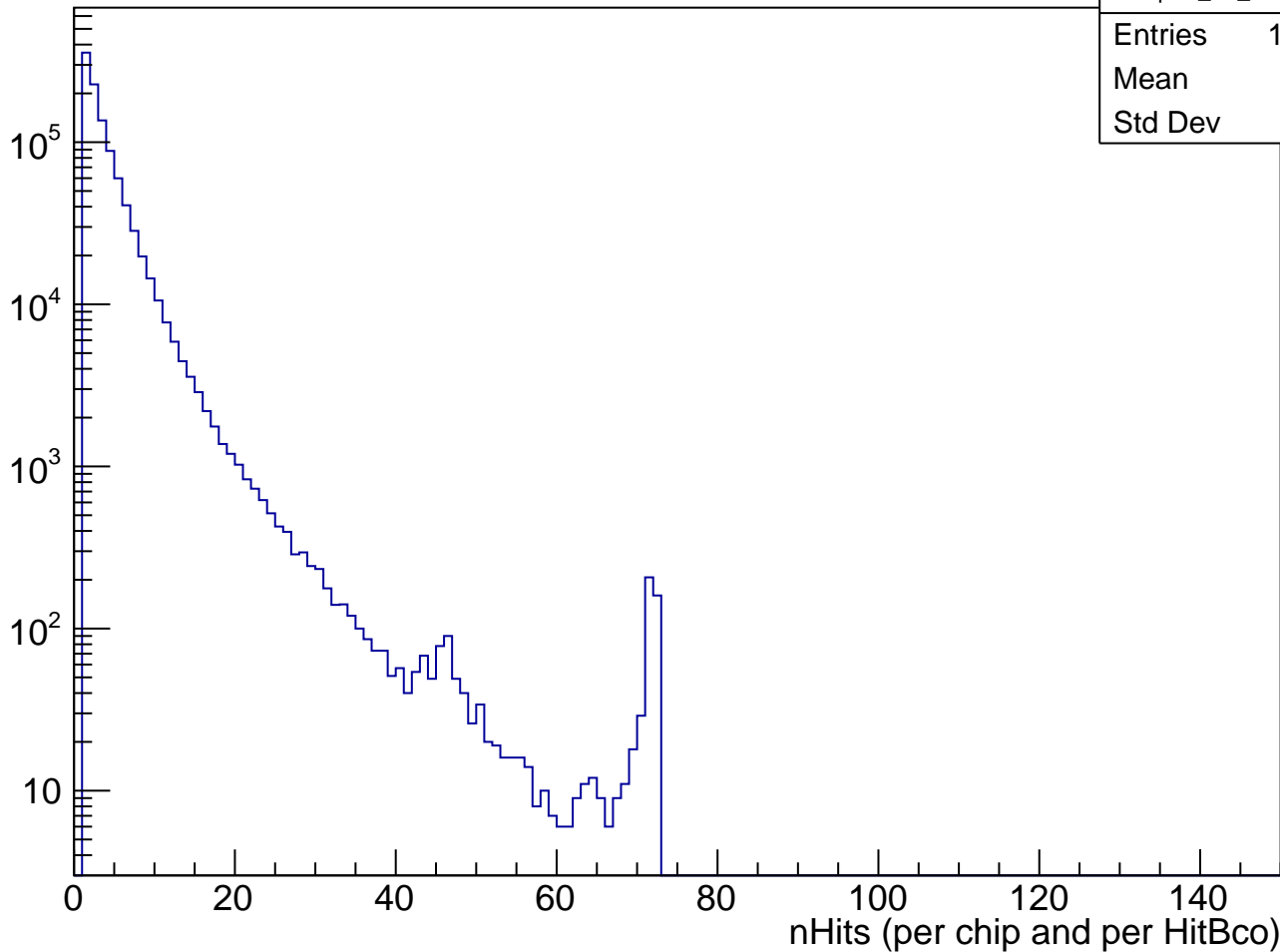
Entries 906656

Mean 2.995

Std Dev 3.428

nChipHit_F2_Fch11_Chip3

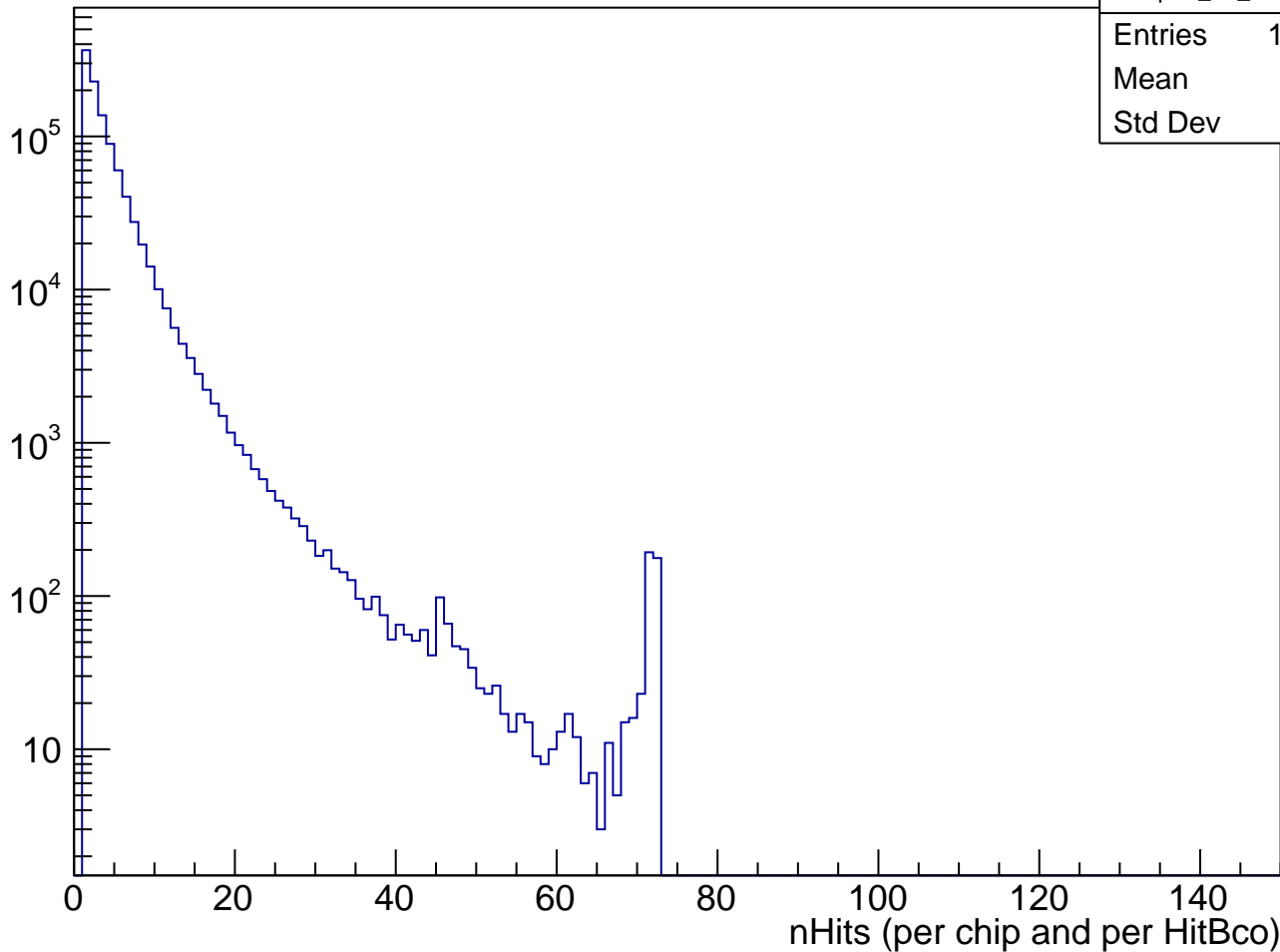
Entries



nChipHit_F2_Fch11_Chip3	
Entries	1021312
Mean	3.291
Std Dev	3.801

nChipHit_F2_Fch11_Chip4

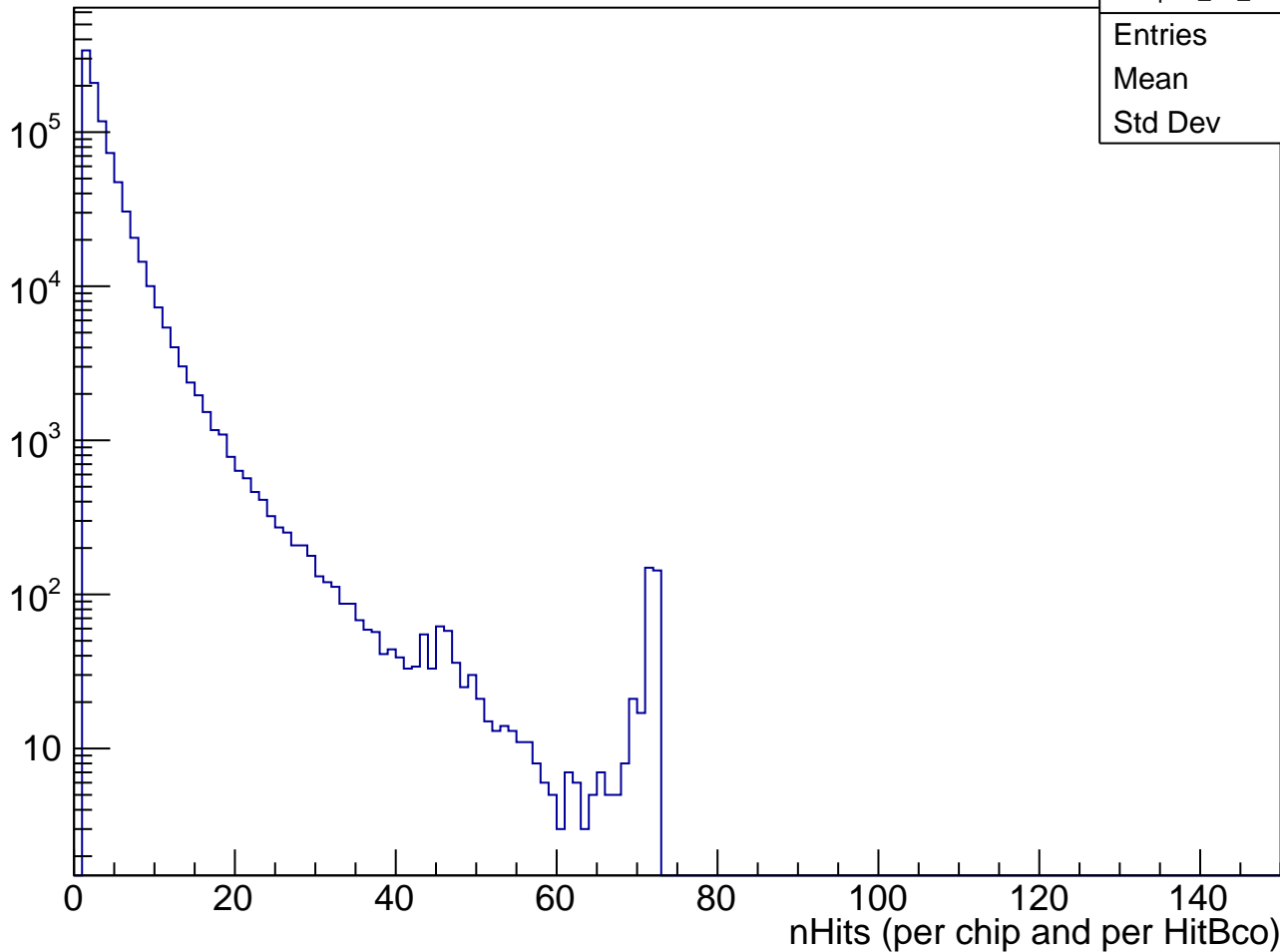
Entries



nChipHit_F2_Fch11_Chip4	
Entries	1030829
Mean	3.257
Std Dev	3.781

nChipHit_F2_Fch11_Chip5

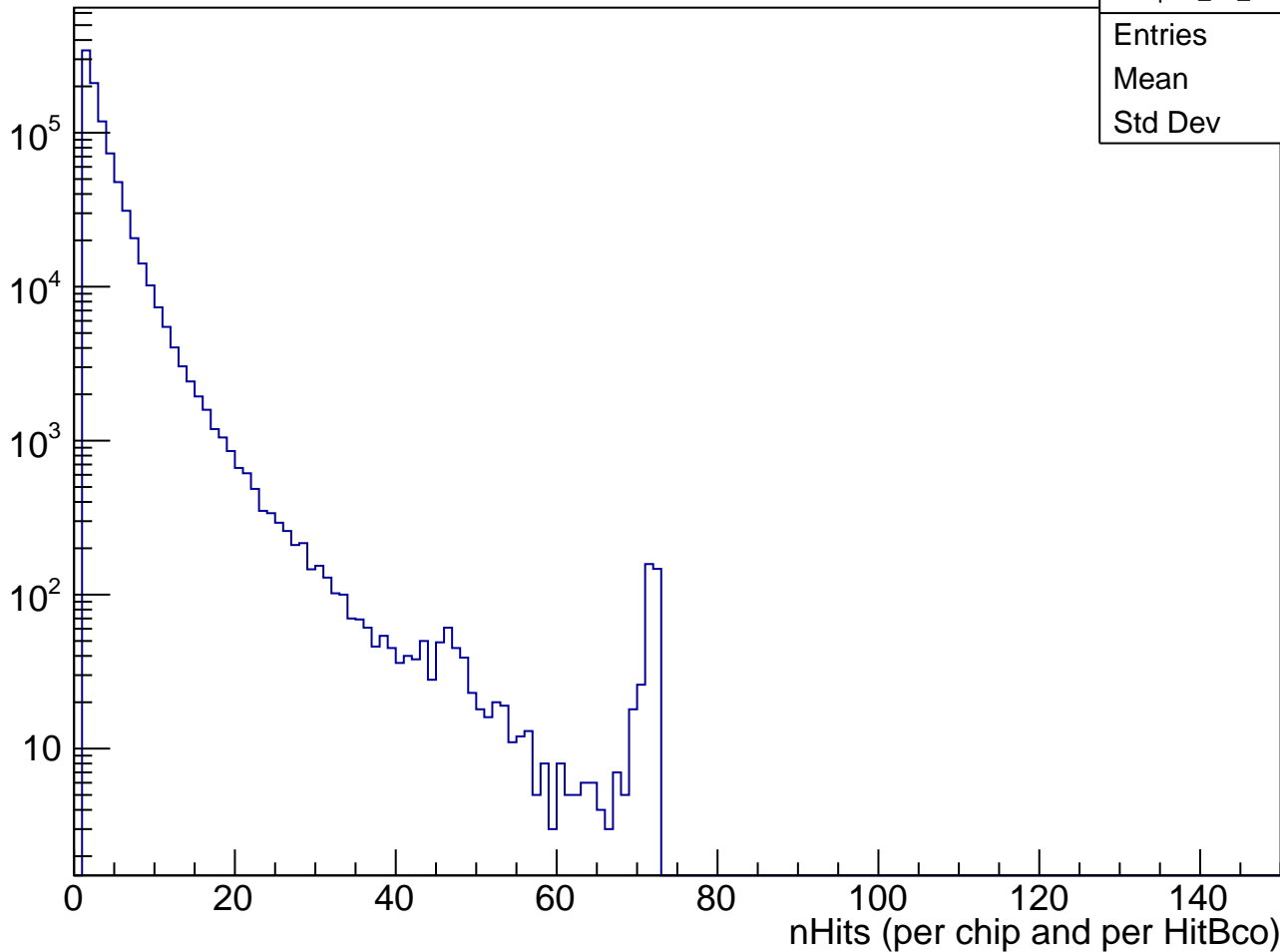
Entries



nChipHit_F2_Fch11_Chip5	
Entries	895299
Mean	3.007
Std Dev	3.494

nChipHit_F2_Fch11_Chip6

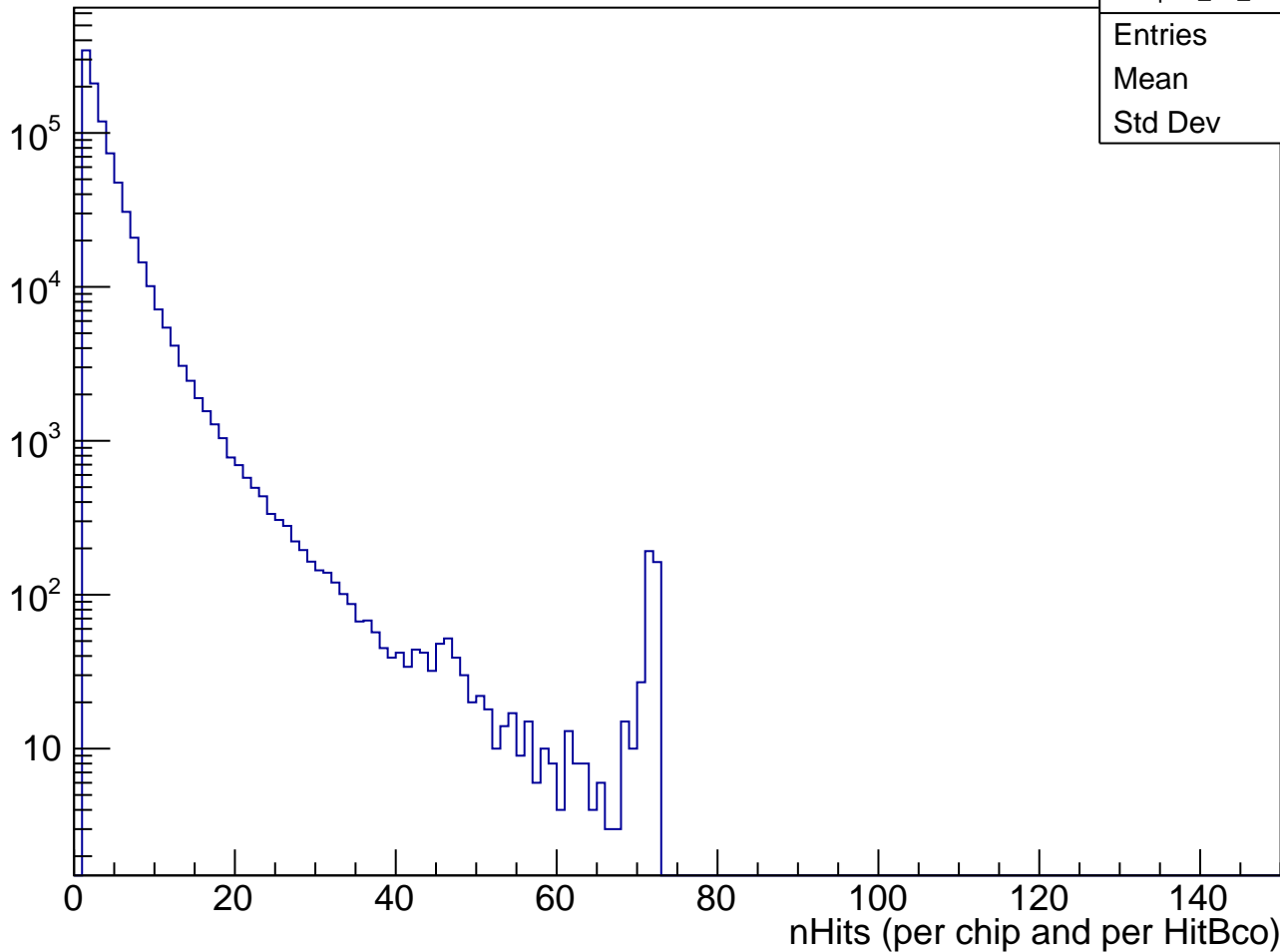
Entries



nChipHit_F2_Fch11_Chip6	
Entries	902885
Mean	3.008
Std Dev	3.512

nChipHit_F2_Fch11_Chip7

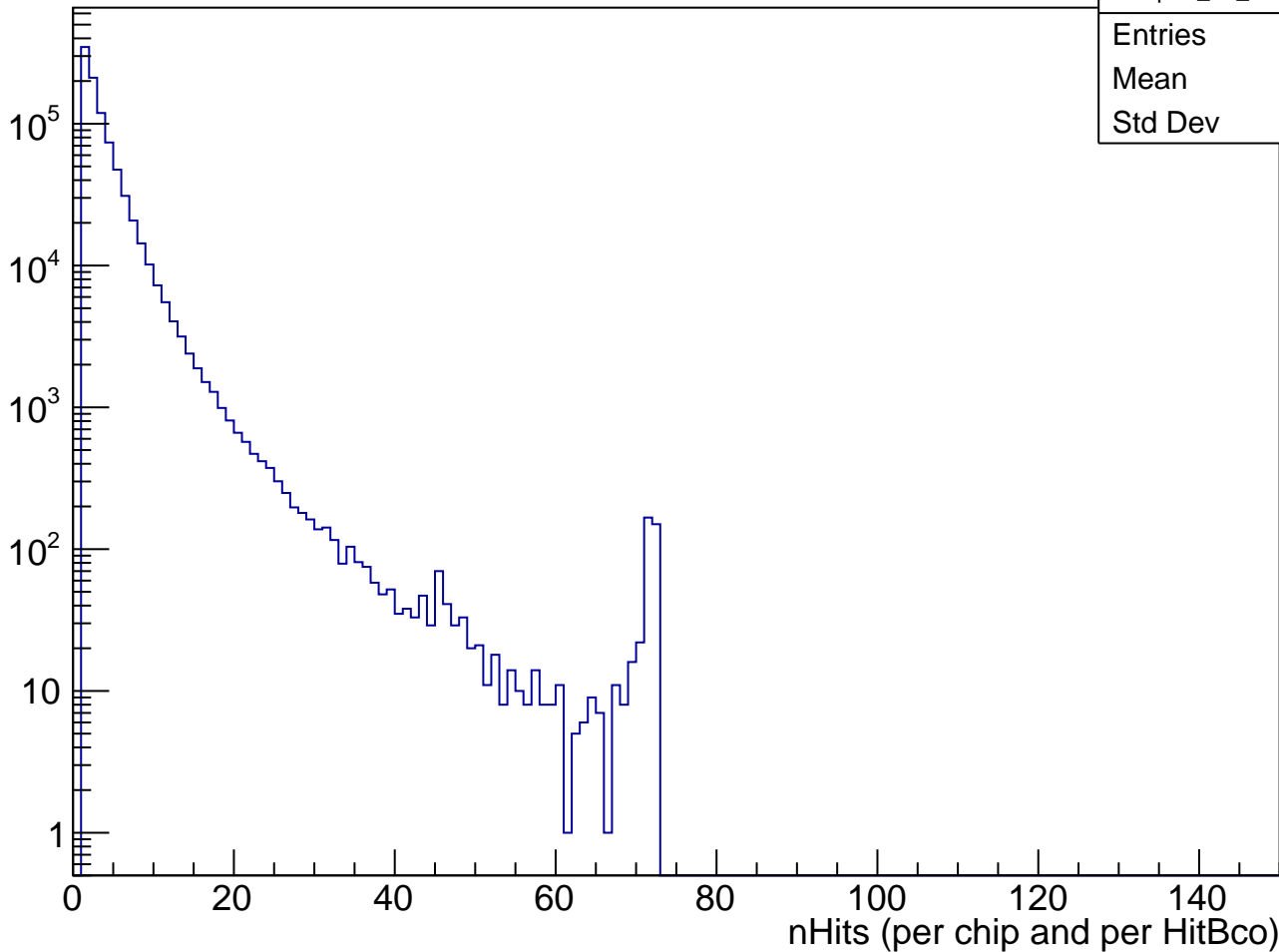
Entries



nChipHit_F2_Fch11_Chip7	
Entries	904476
Mean	3.012
Std Dev	3.557

nChipHit_F2_Fch11_Chip8

Entries

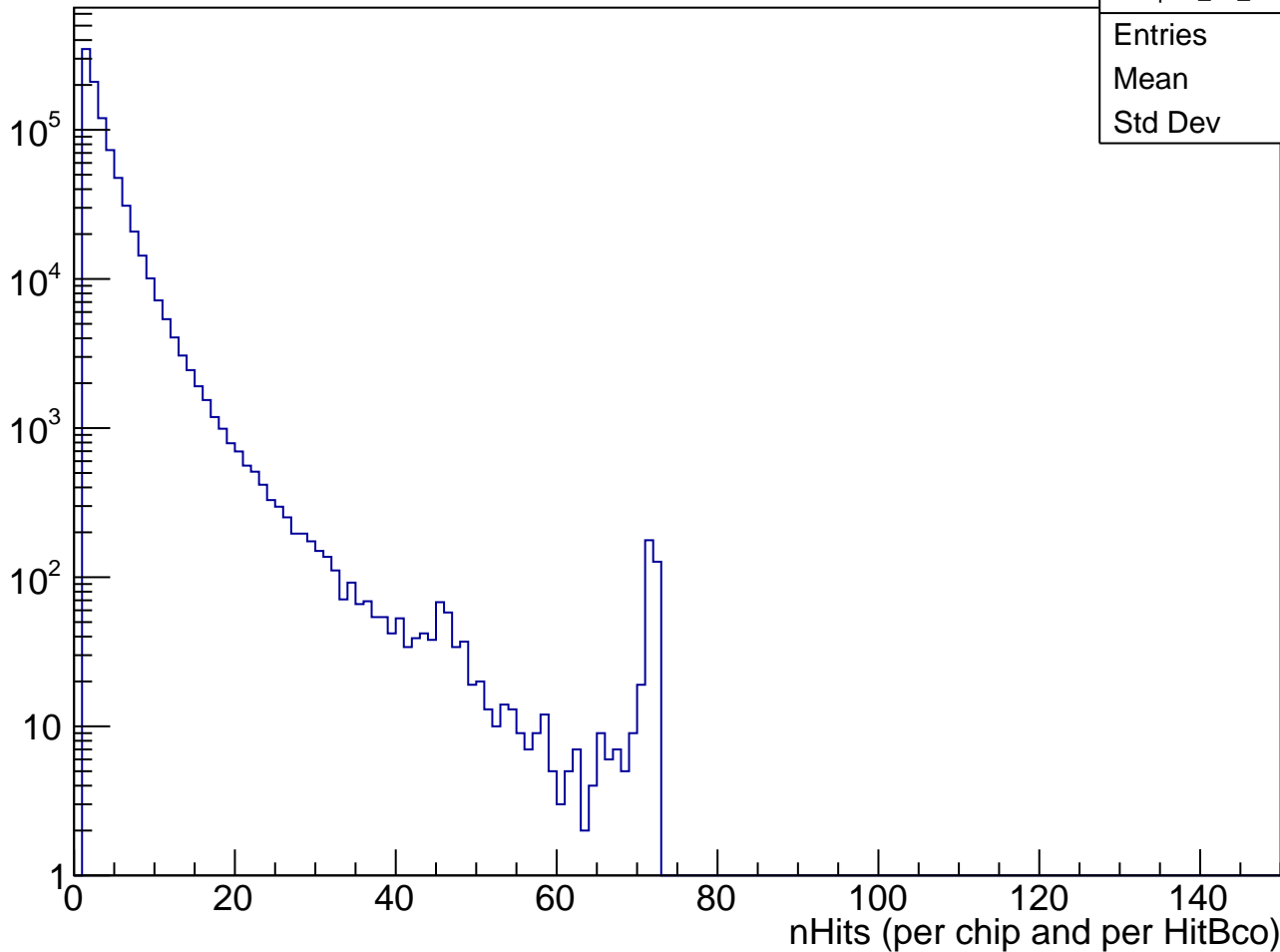


nChipHit_F2_Fch11_Chip8	
Entries	909437
Mean	2.997
Std Dev	3.51

nHits (per chip and per HitBco)

nChipHit_F2_Fch11_Chip9

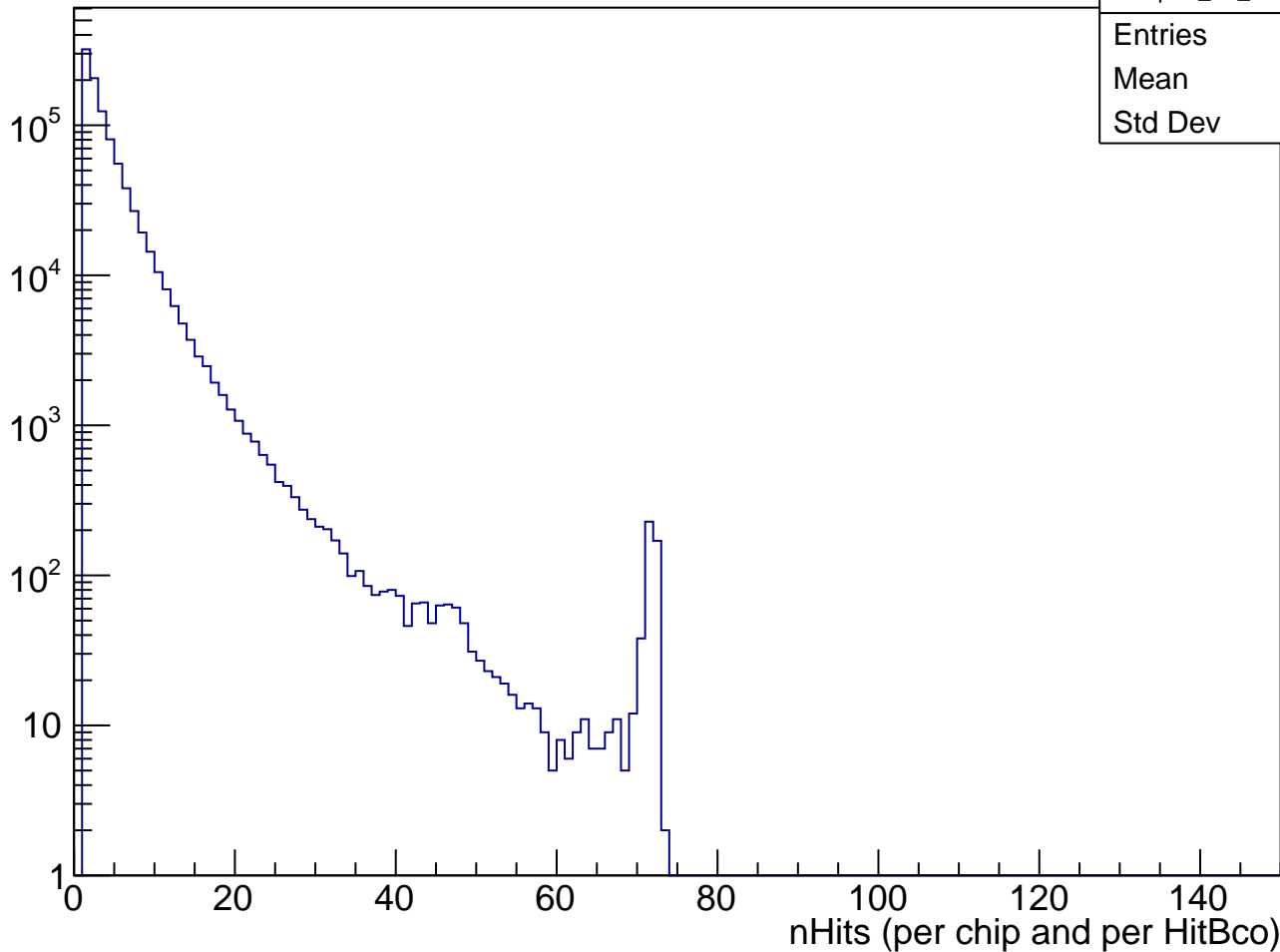
Entries



nChipHit_F2_Fch11_Chip9	
Entries	908939
Mean	2.992
Std Dev	3.487

nChipHit_F2_Fch12_Chip0

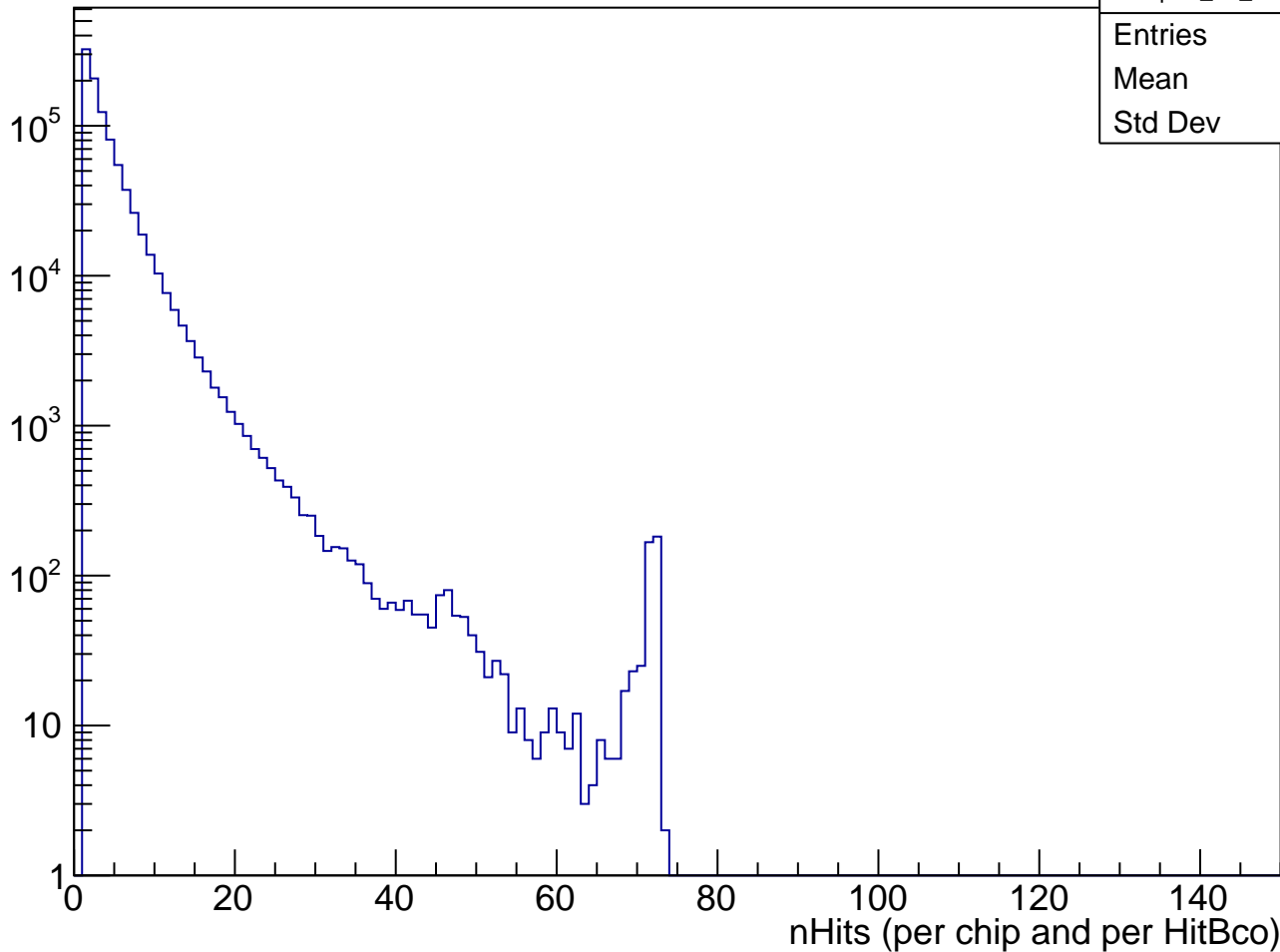
Entries



nChipHit_F2_Fch12_Chip0	
Entries	936688
Mean	3.411
Std Dev	3.998

nChipHit_F2_Fch12_Chip1

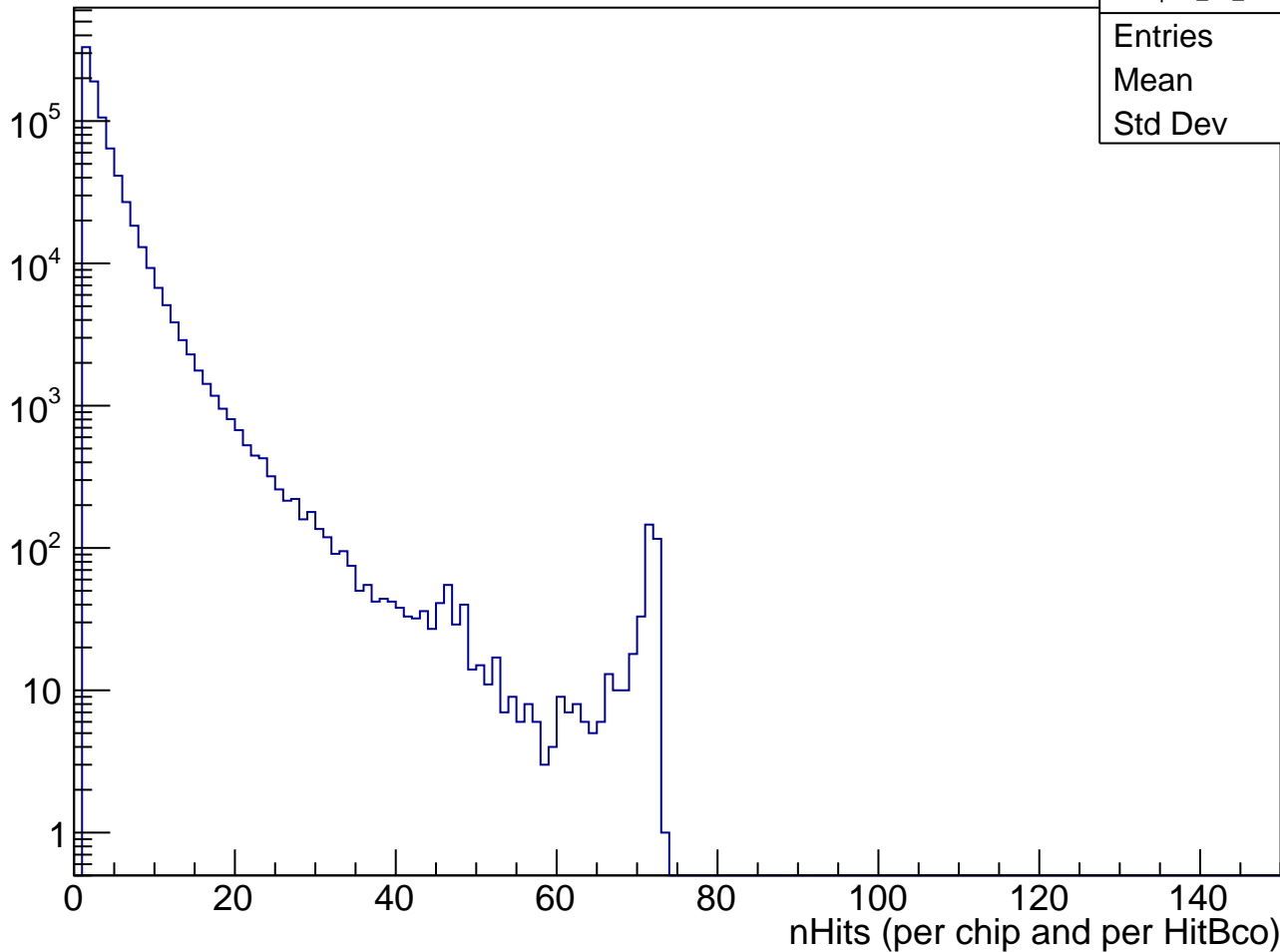
Entries



nChipHit_F2_Fch12_Chip1	
Entries	936092
Mean	3.367
Std Dev	3.931

nChipHit_F2_Fch12_Chip10

Entries



nChipHit_F2_Fch12_Chip10

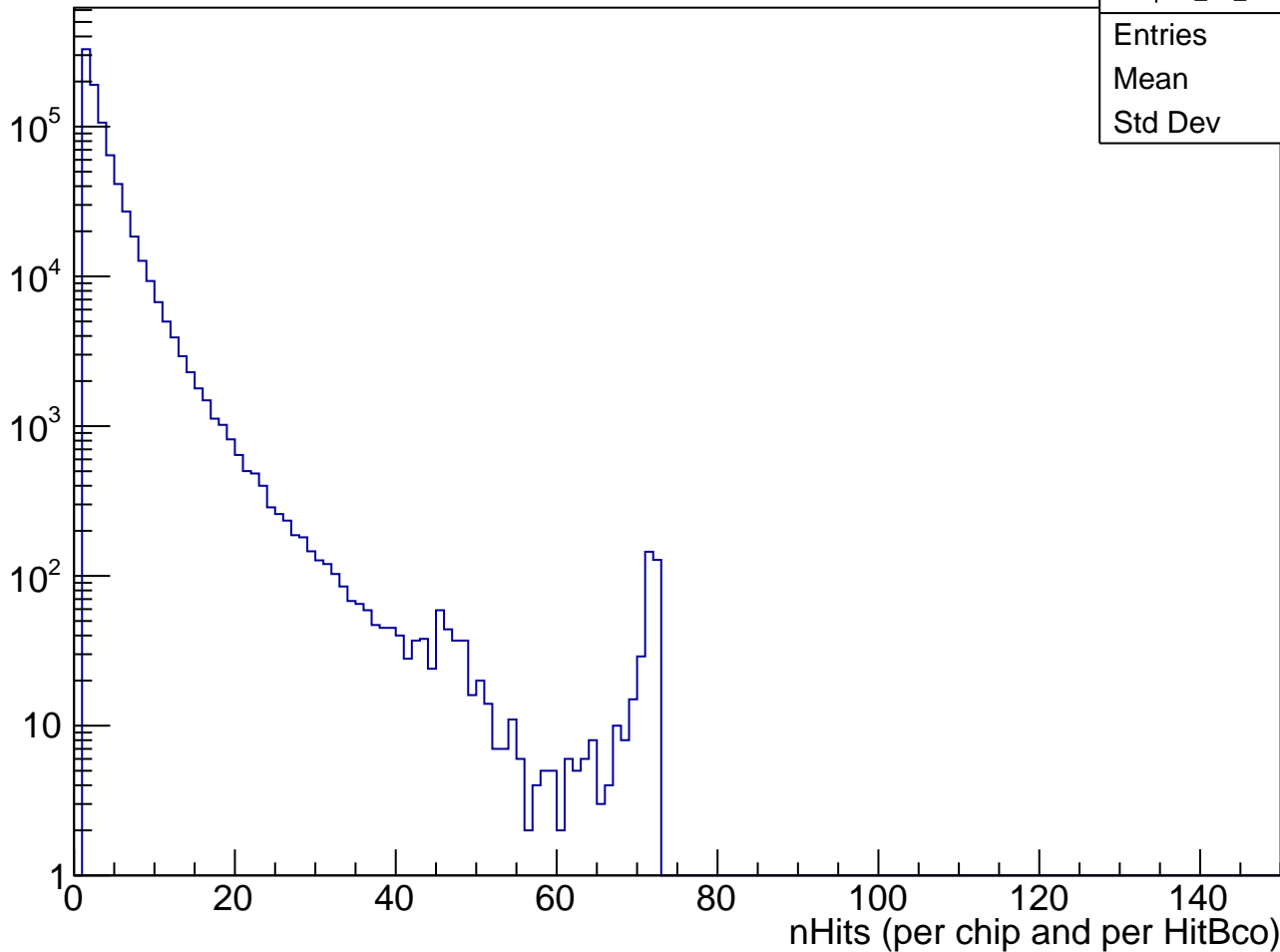
Entries 831461

Mean 2.96

Std Dev 3.516

nChipHit_F2_Fch12_Chip11

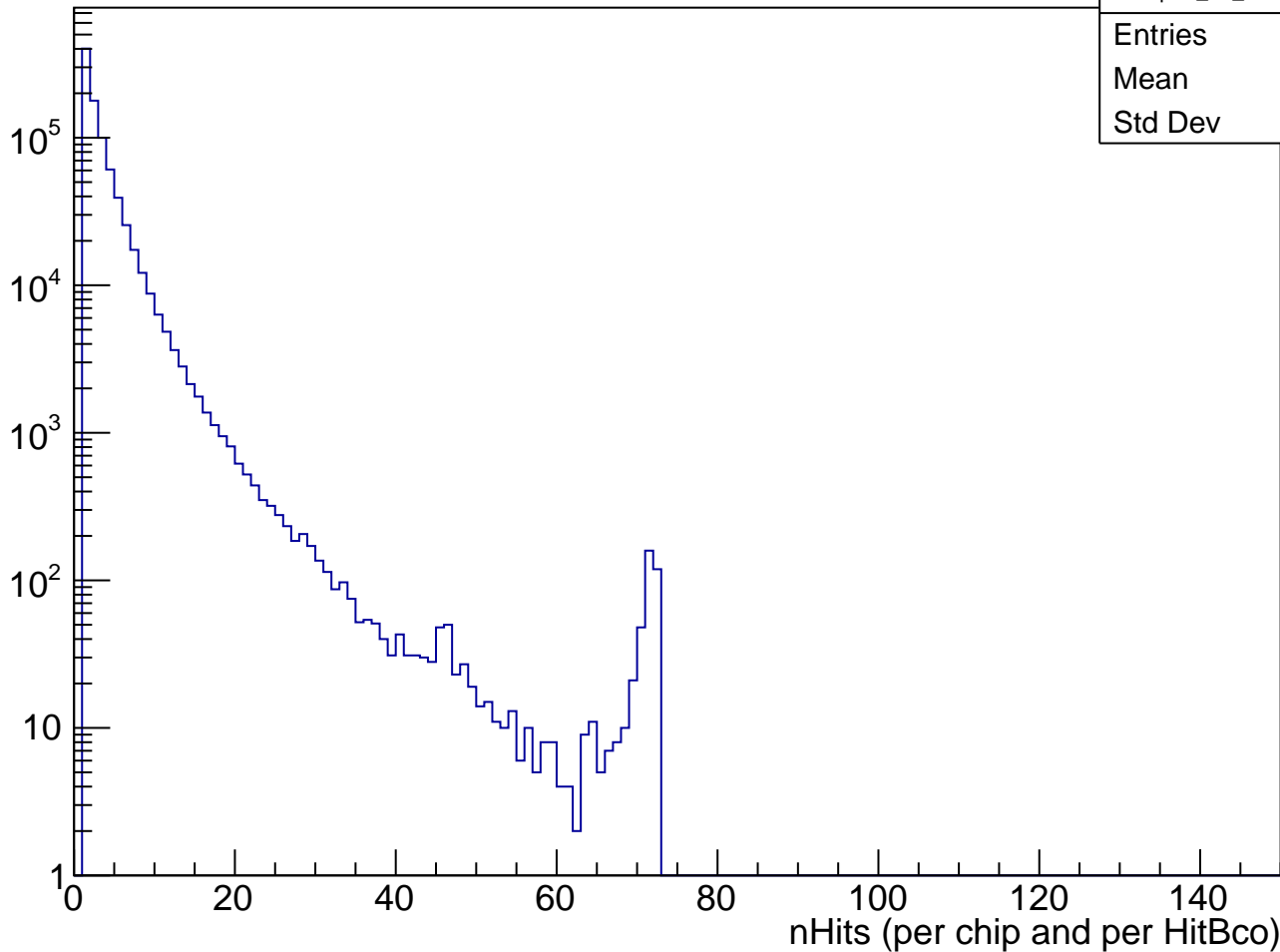
Entries



nChipHit_F2_Fch12_Chip11	
Entries	830505
Mean	2.964
Std Dev	3.507

nChipHit_F2_Fch12_Chip12

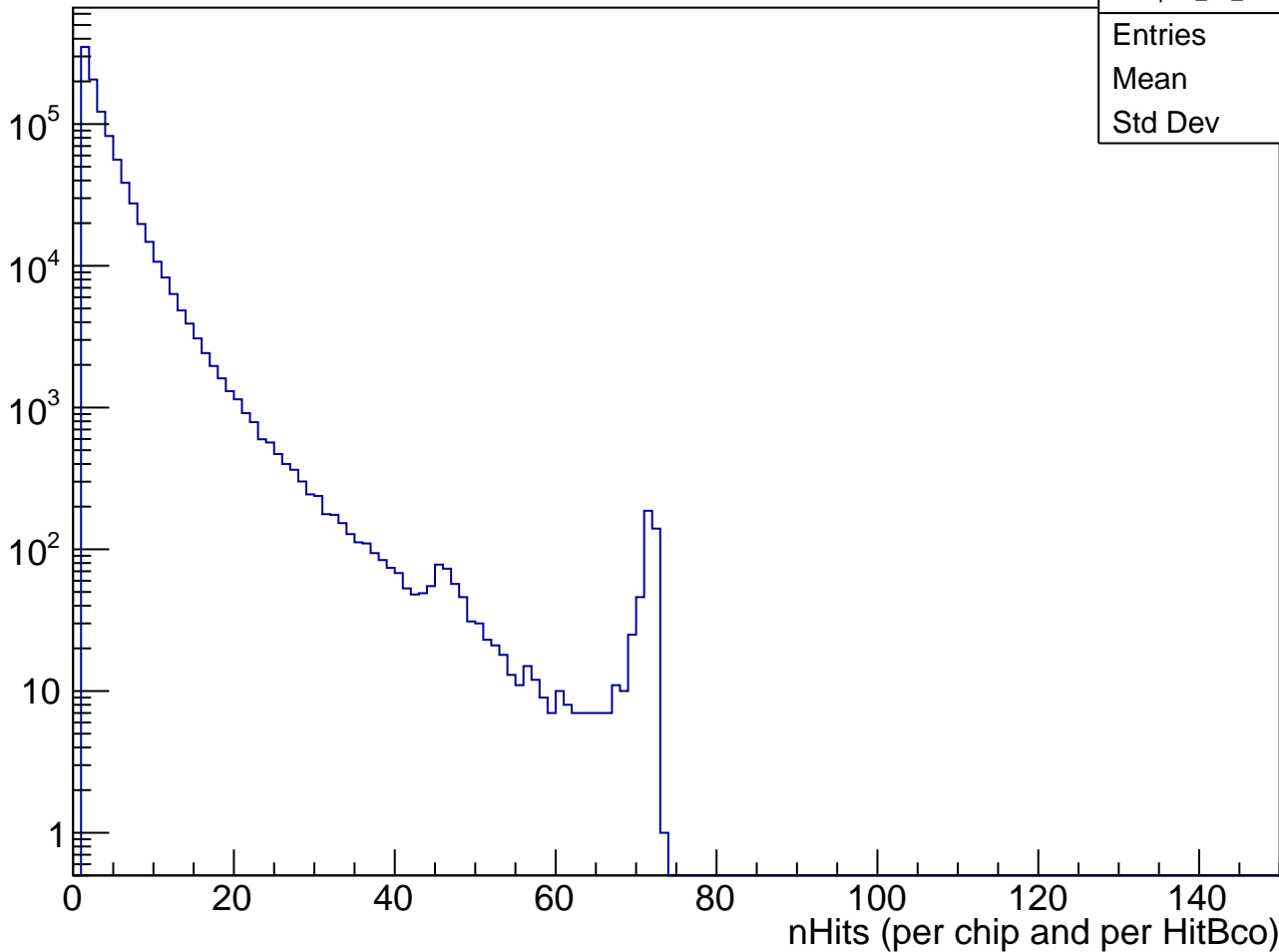
Entries



nChipHit_F2_Fch12_Chip12	
Entries	874226
Mean	2.774
Std Dev	3.449

nChipHit_F2_Fch12_Chip13

Entries



nChipHit_F2_Fch12_Chip13

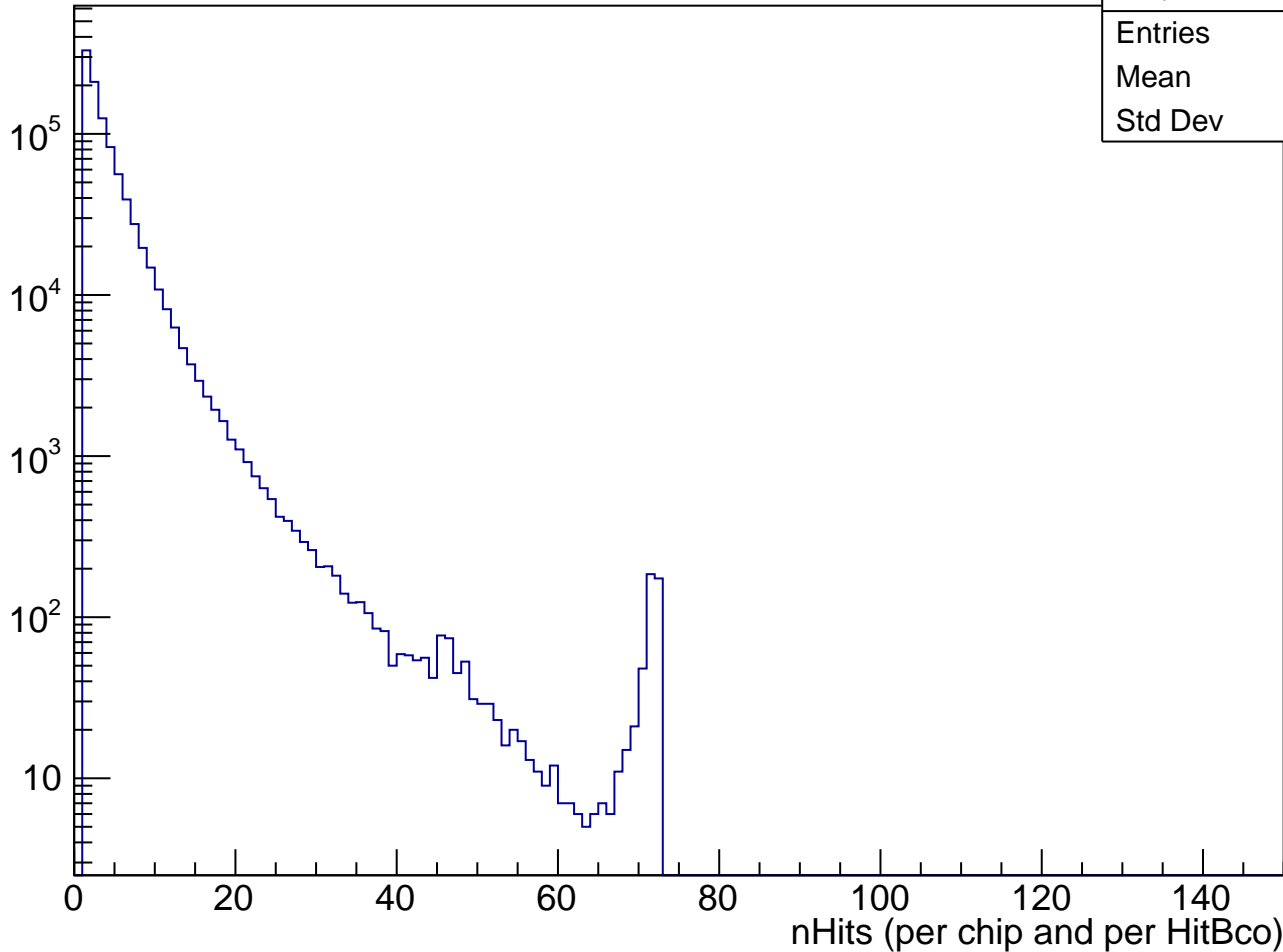
Entries 970445

Mean 3.363

Std Dev 3.958

nChipHit_F2_Fch12_Chip14

Entries



nChipHit_F2_Fch12_Chip14

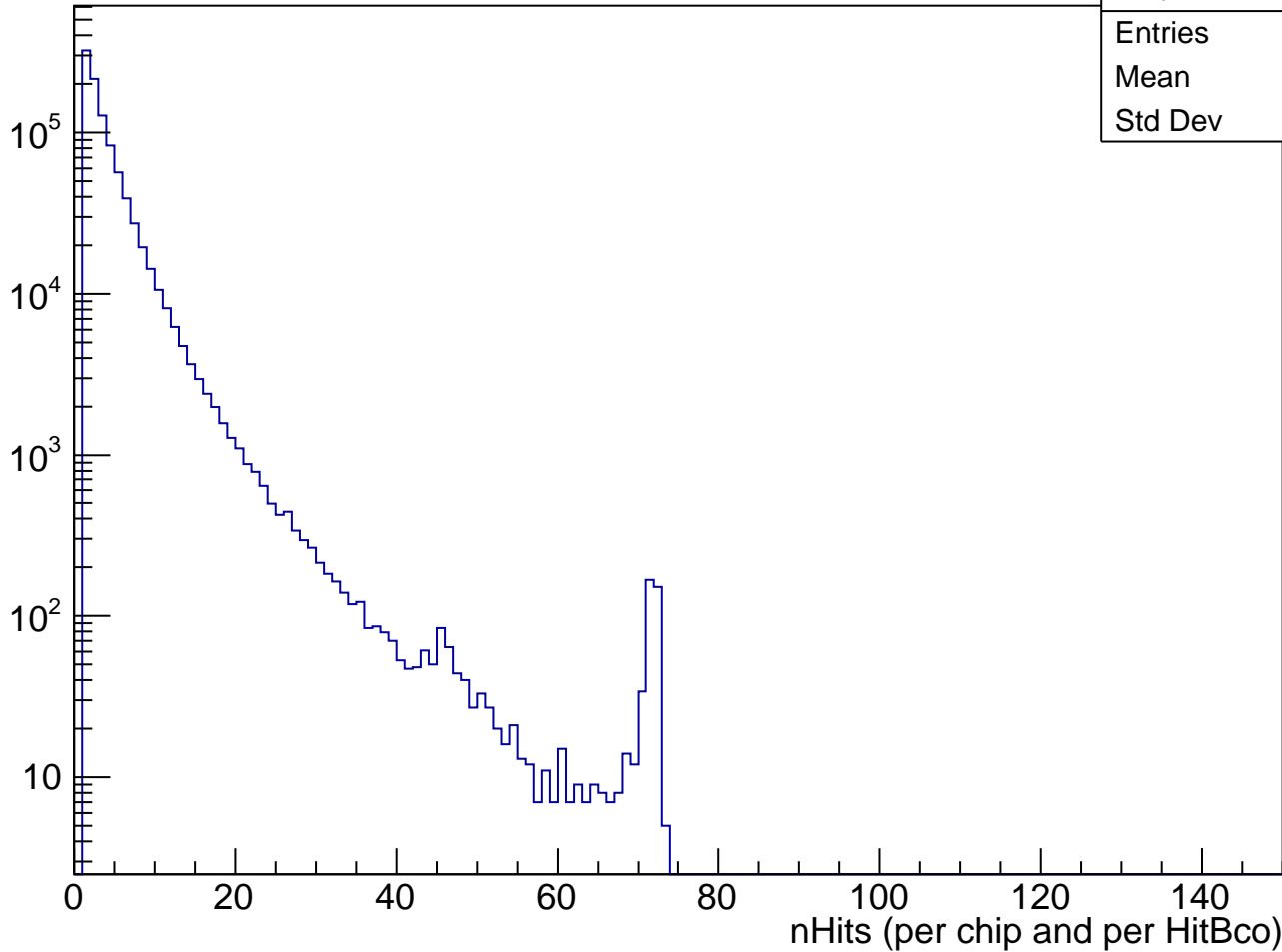
Entries 957186

Mean 3.399

Std Dev 3.97

nChipHit_F2_Fch12_Chip15

Entries



nChipHit_F2_Fch12_Chip15

Entries 956320

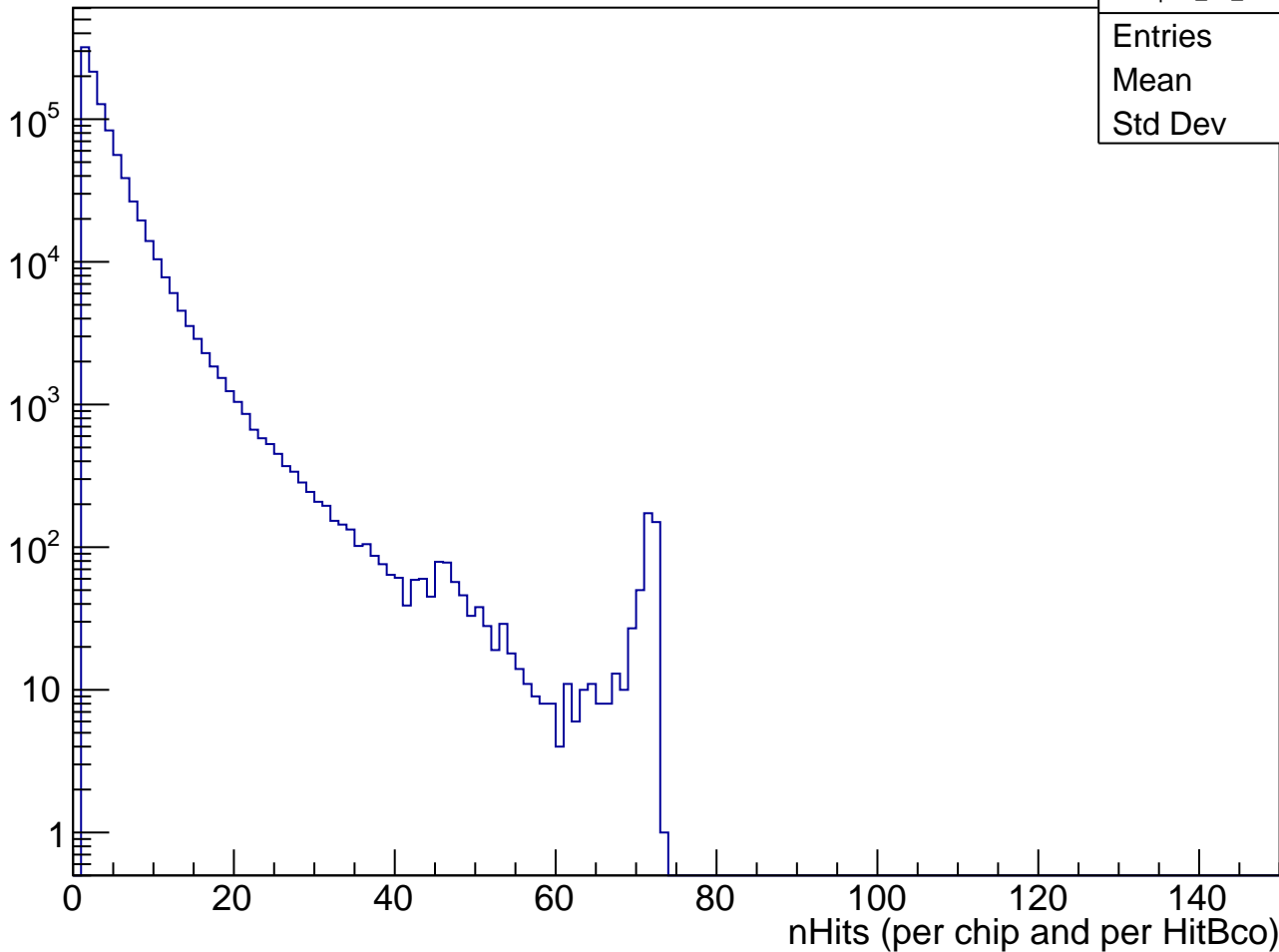
Mean 3.4

Std Dev 3.921

nHits (per chip and per HitBco)

nChipHit_F2_Fch12_Chip16

Entries



nChipHit_F2_Fch12_Chip16

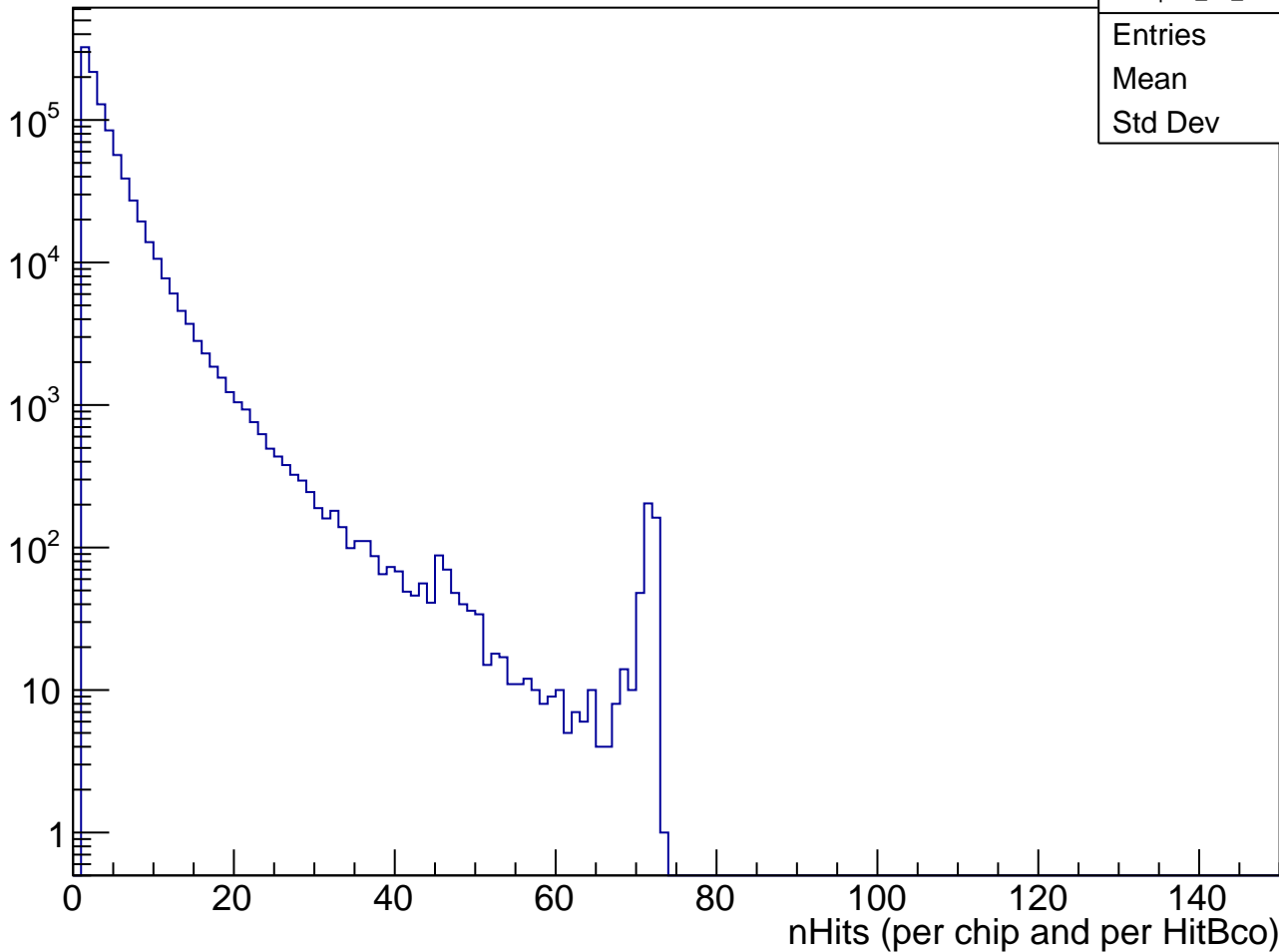
Entries 950331

Mean 3.383

Std Dev 3.925

nChipHit_F2_Fch12_Chip17

Entries



nChipHit_F2_Fch12_Chip17

Entries 961310

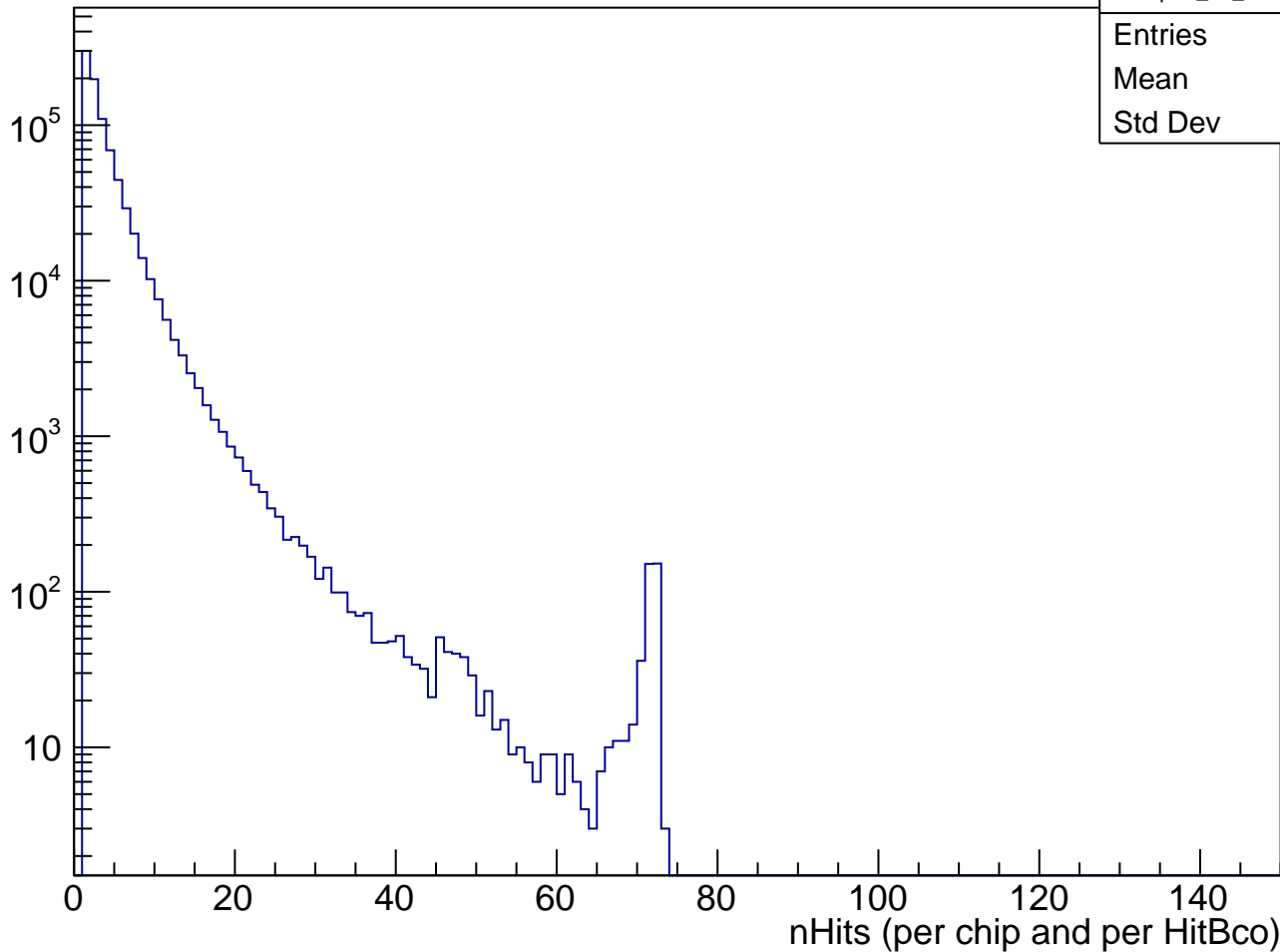
Mean 3.374

Std Dev 3.906

nHits (per chip and per HitBco)

nChipHit_F2_Fch12_Chip18

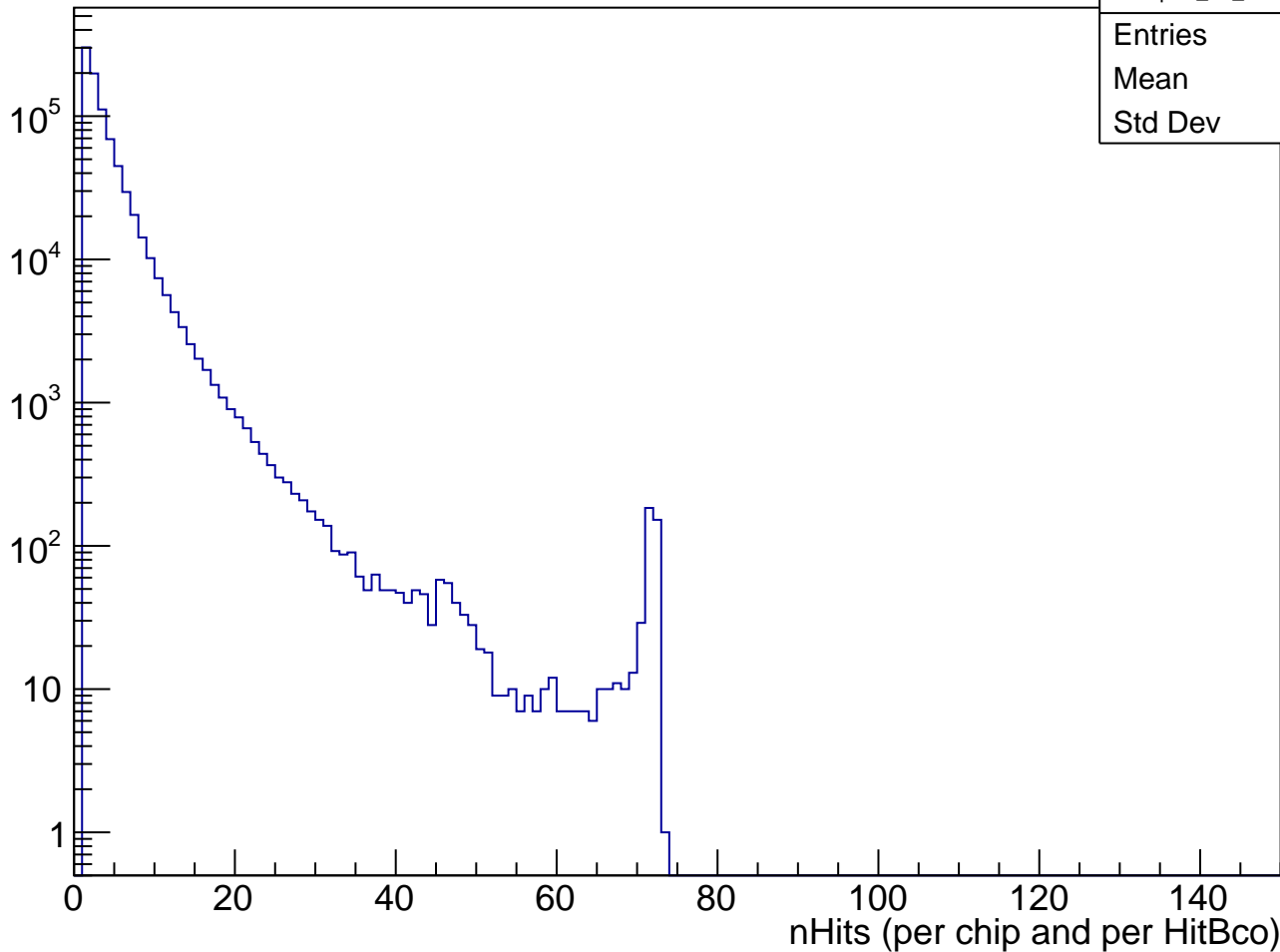
Entries



nChipHit_F2_Fch12_Chip18	
Entries	830337
Mean	3.117
Std Dev	3.653

nChipHit_F2_Fch12_Chip19

Entries



nChipHit_F2_Fch12_Chip19

Entries 836370

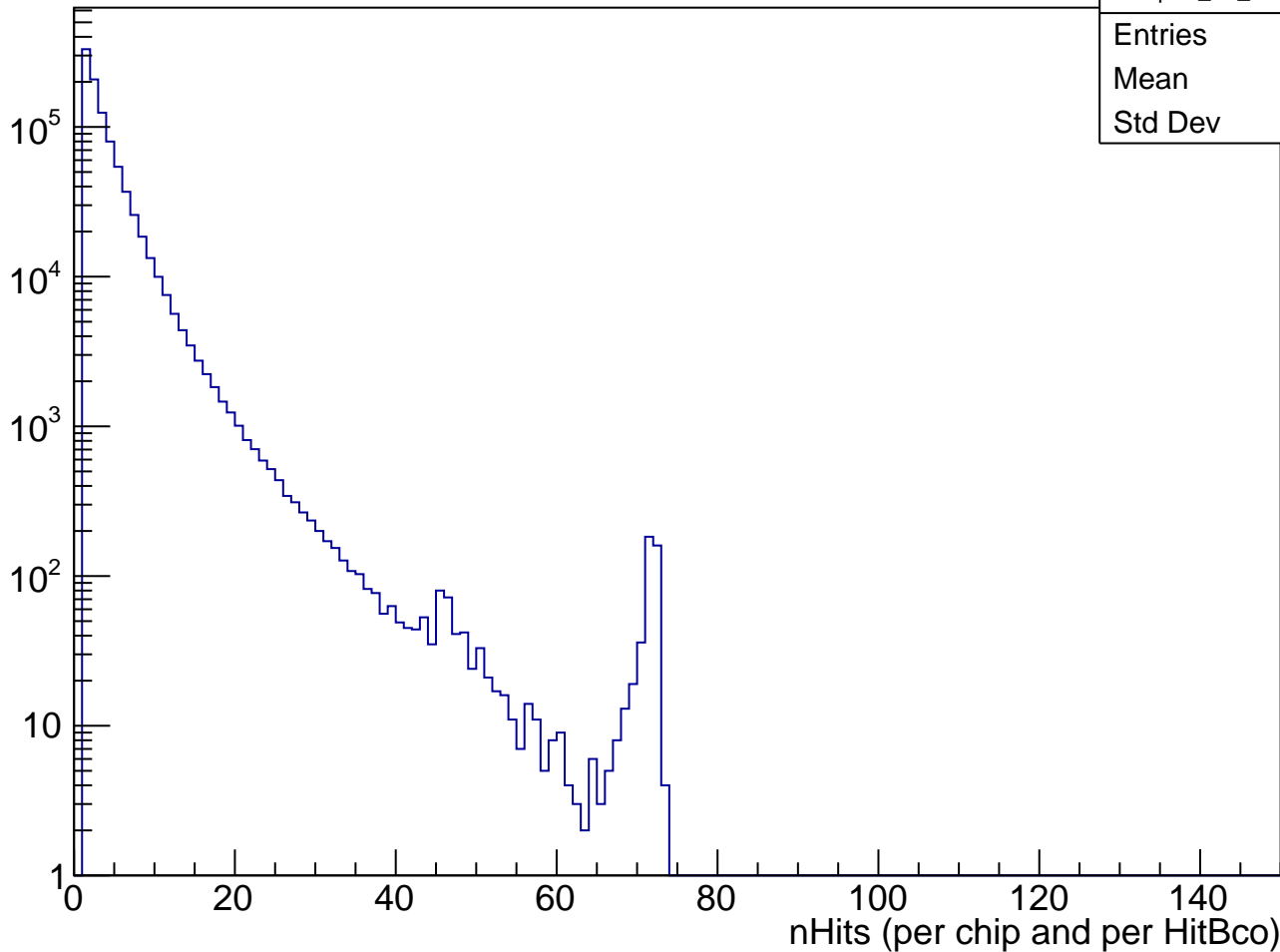
Mean 3.132

Std Dev 3.702

nHits (per chip and per HitBco)

nChipHit_F2_Fch12_Chip2

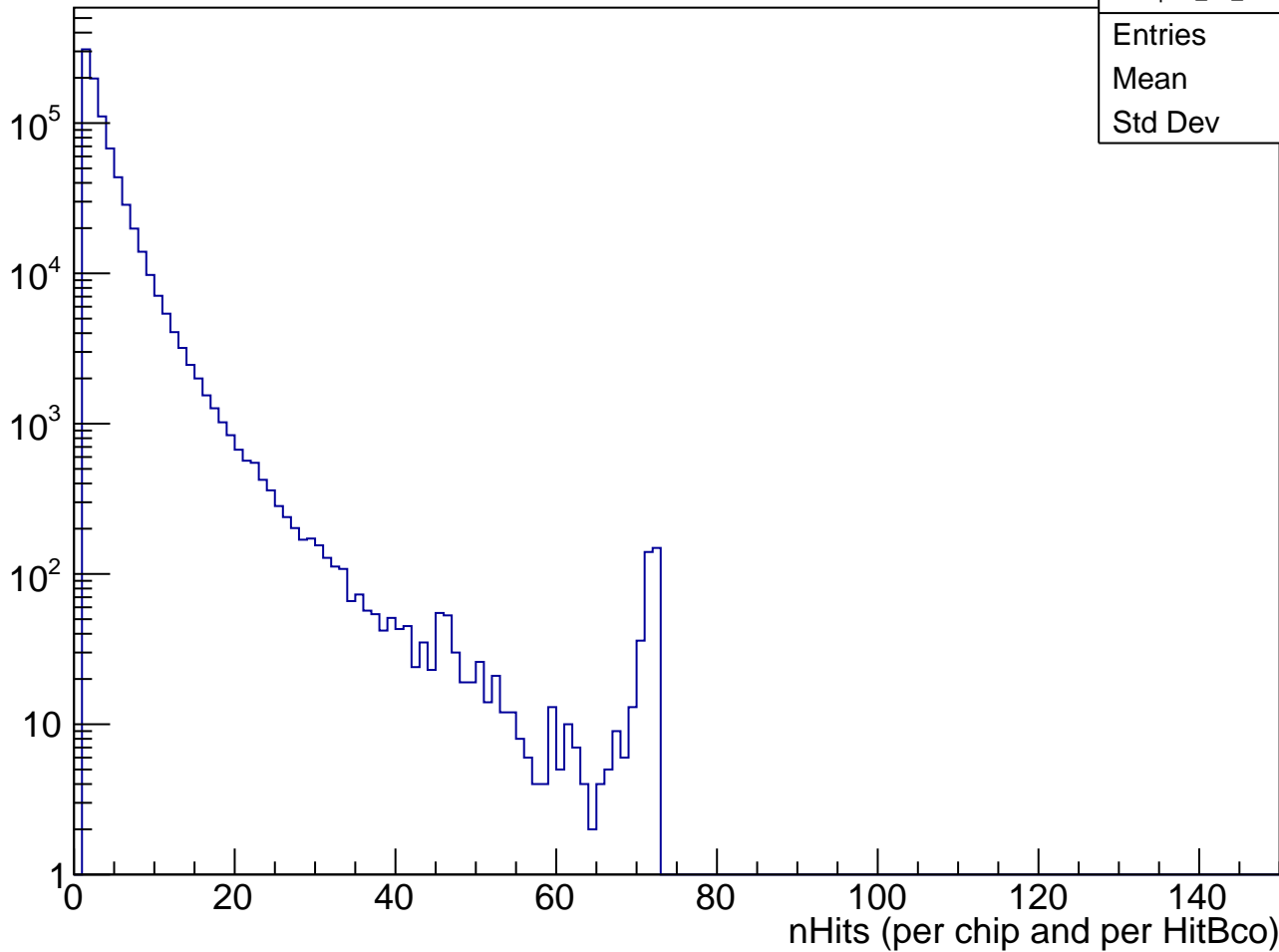
Entries



nChipHit_F2_Fch12_Chip2	
Entries	939576
Mean	3.315
Std Dev	3.858

nChipHit_F2_Fch12_Chip20

Entries



nChipHit_F2_Fch12_Chip20

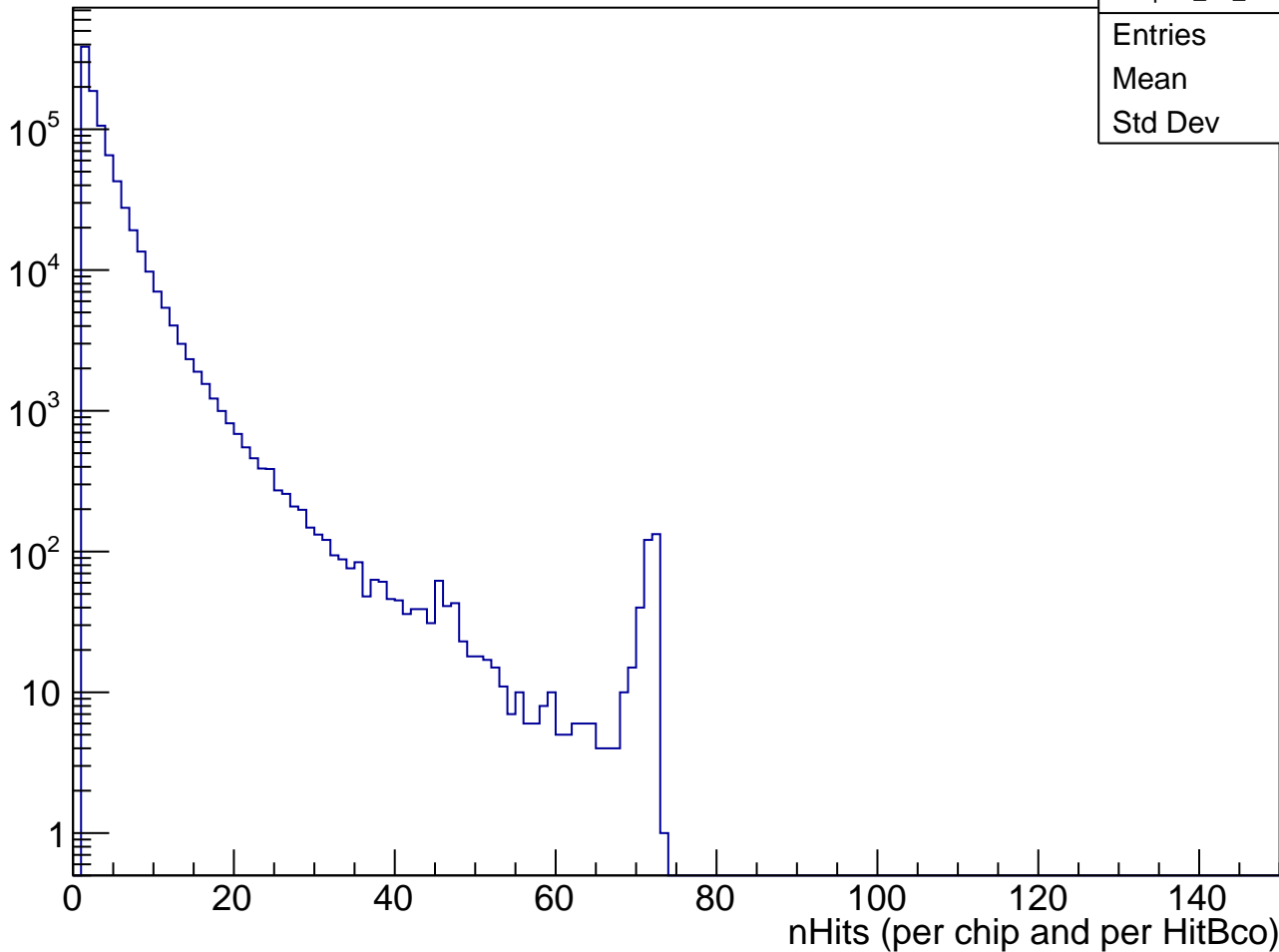
Entries 836025

Mean 3.067

Std Dev 3.591

nChipHit_F2_Fch12_Chip21

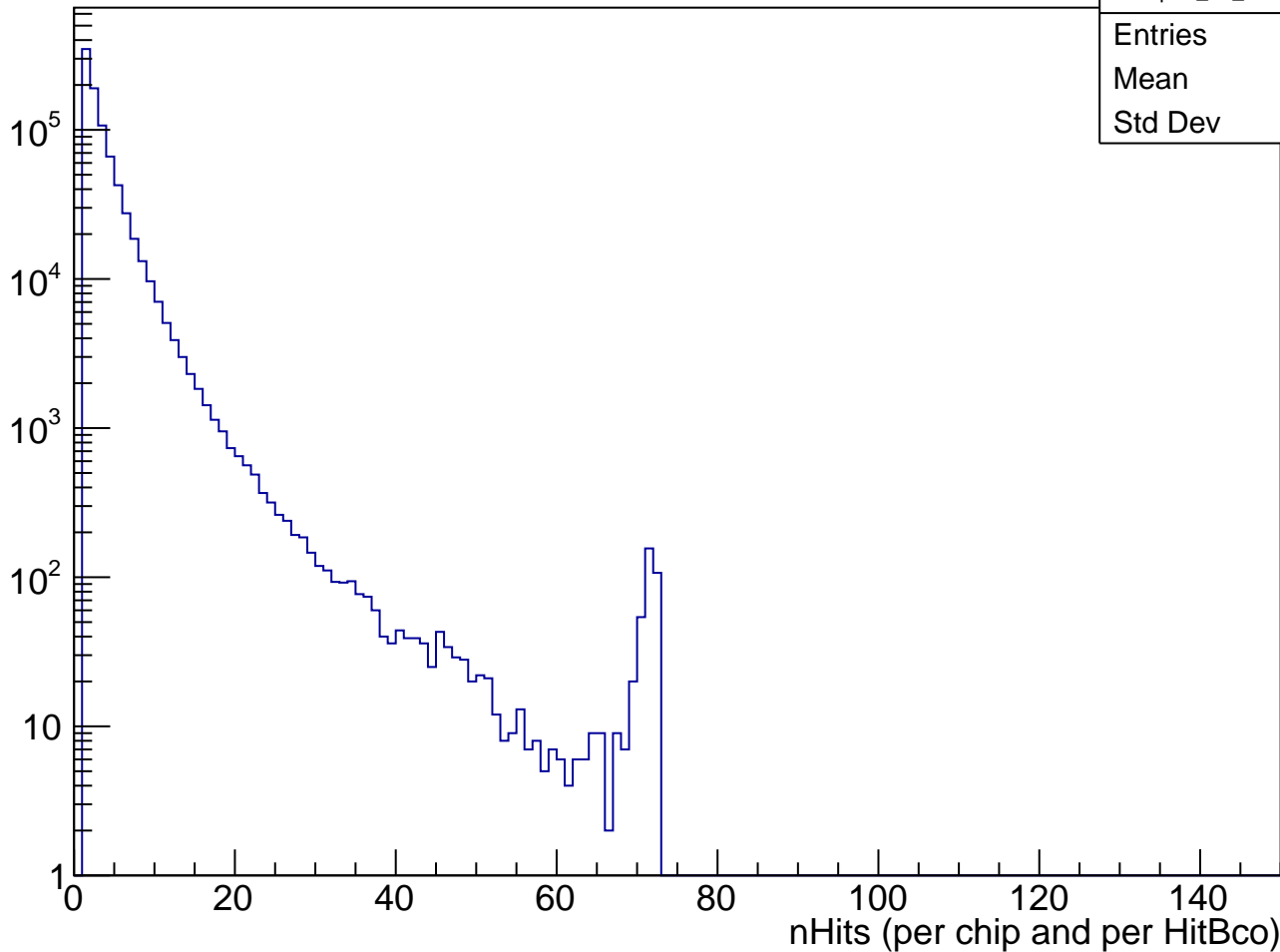
Entries



nChipHit_F2_Fch12_Chip21	
Entries	890967
Mean	2.878
Std Dev	3.482

nChipHit_F2_Fch12_Chip22

Entries



nChipHit_F2_Fch12_Chip22

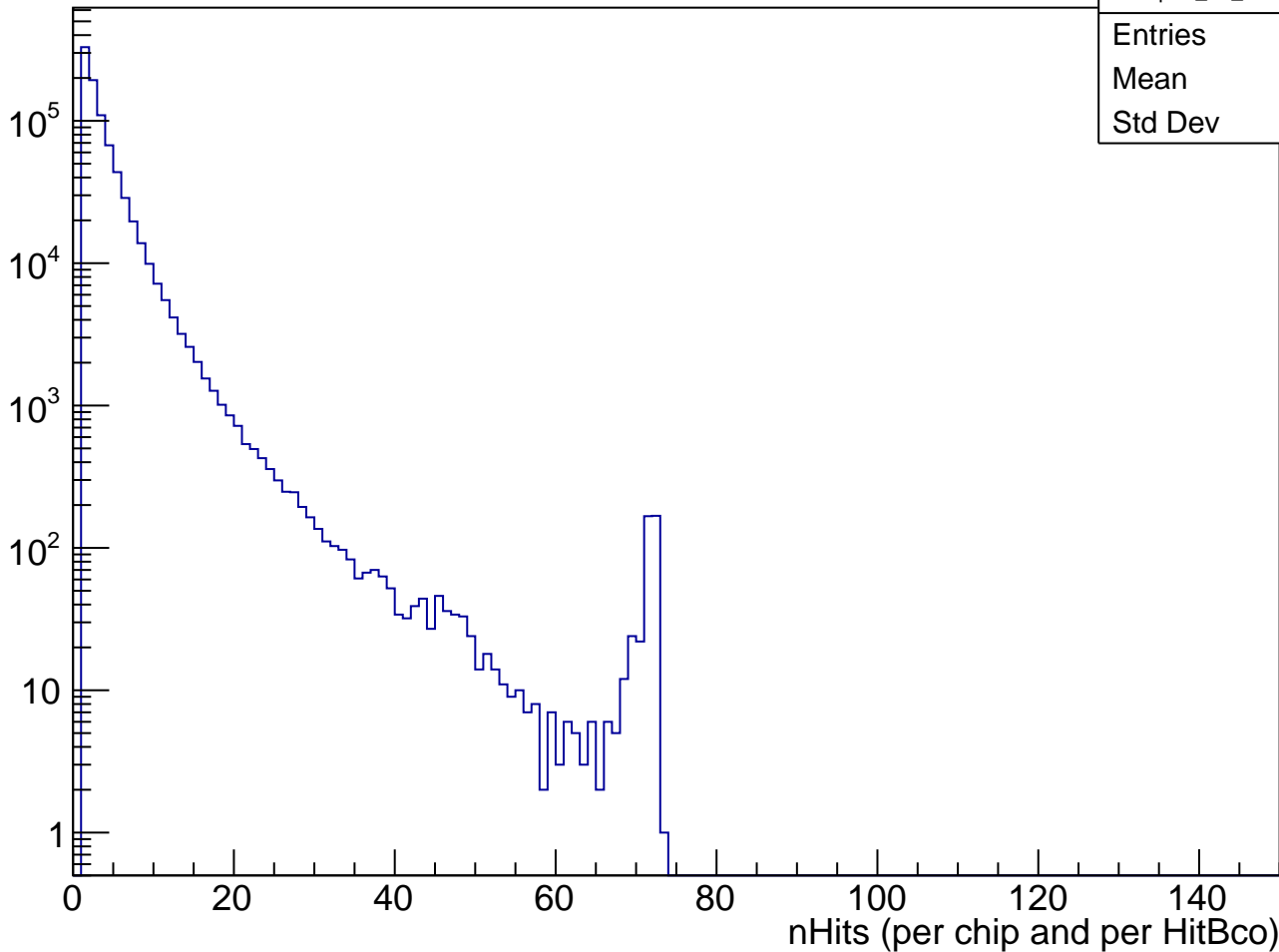
Entries 855547

Mean 2.938

Std Dev 3.507

nChipHit_F2_Fch12_Chip23

Entries



nChipHit_F2_Fch12_Chip23

Entries 849800

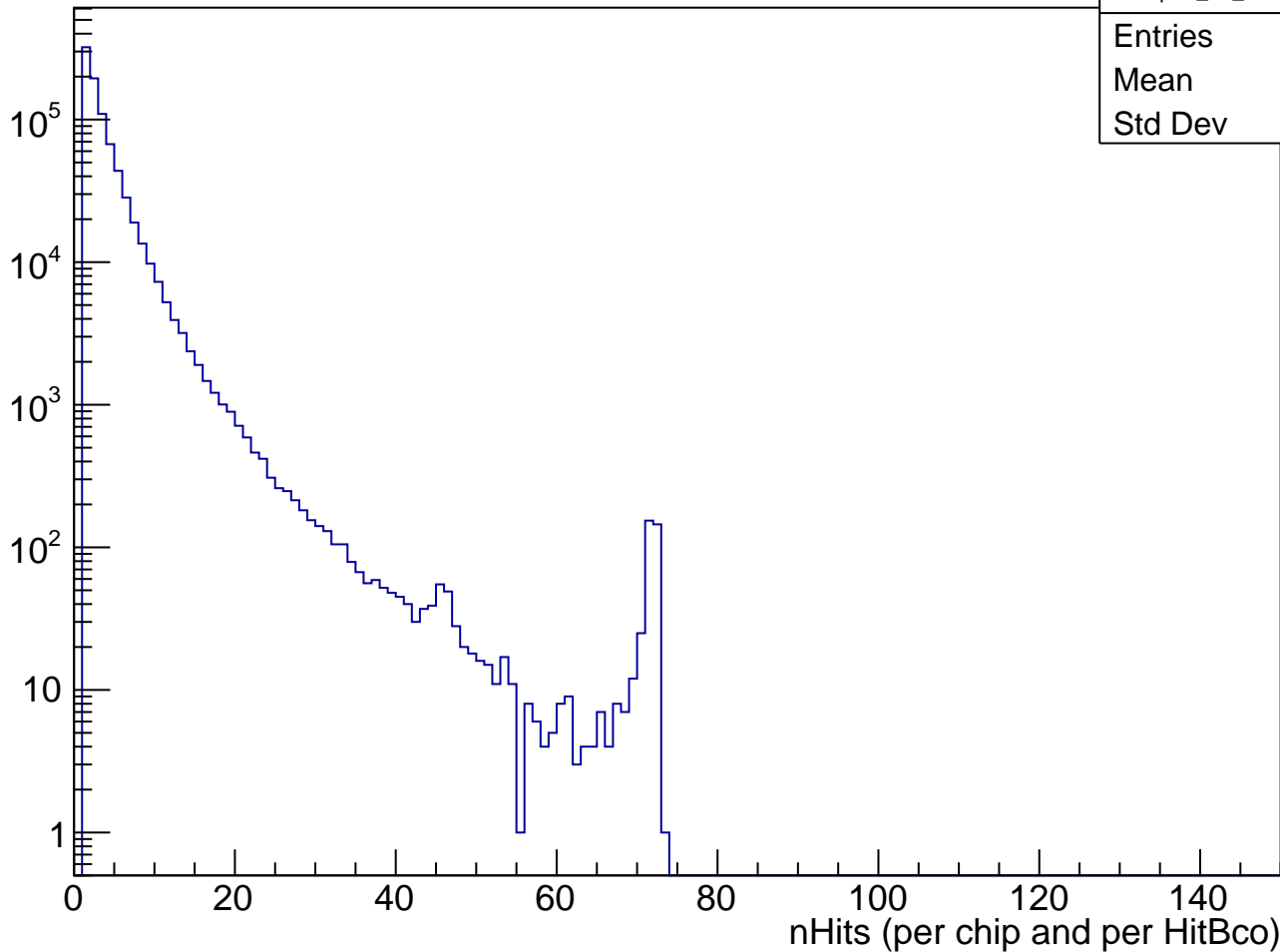
Mean 3.033

Std Dev 3.615

nHits (per chip and per HitBco)

nChipHit_F2_Fch12_Chip24

Entries



nChipHit_F2_Fch12_Chip24

Entries 840751

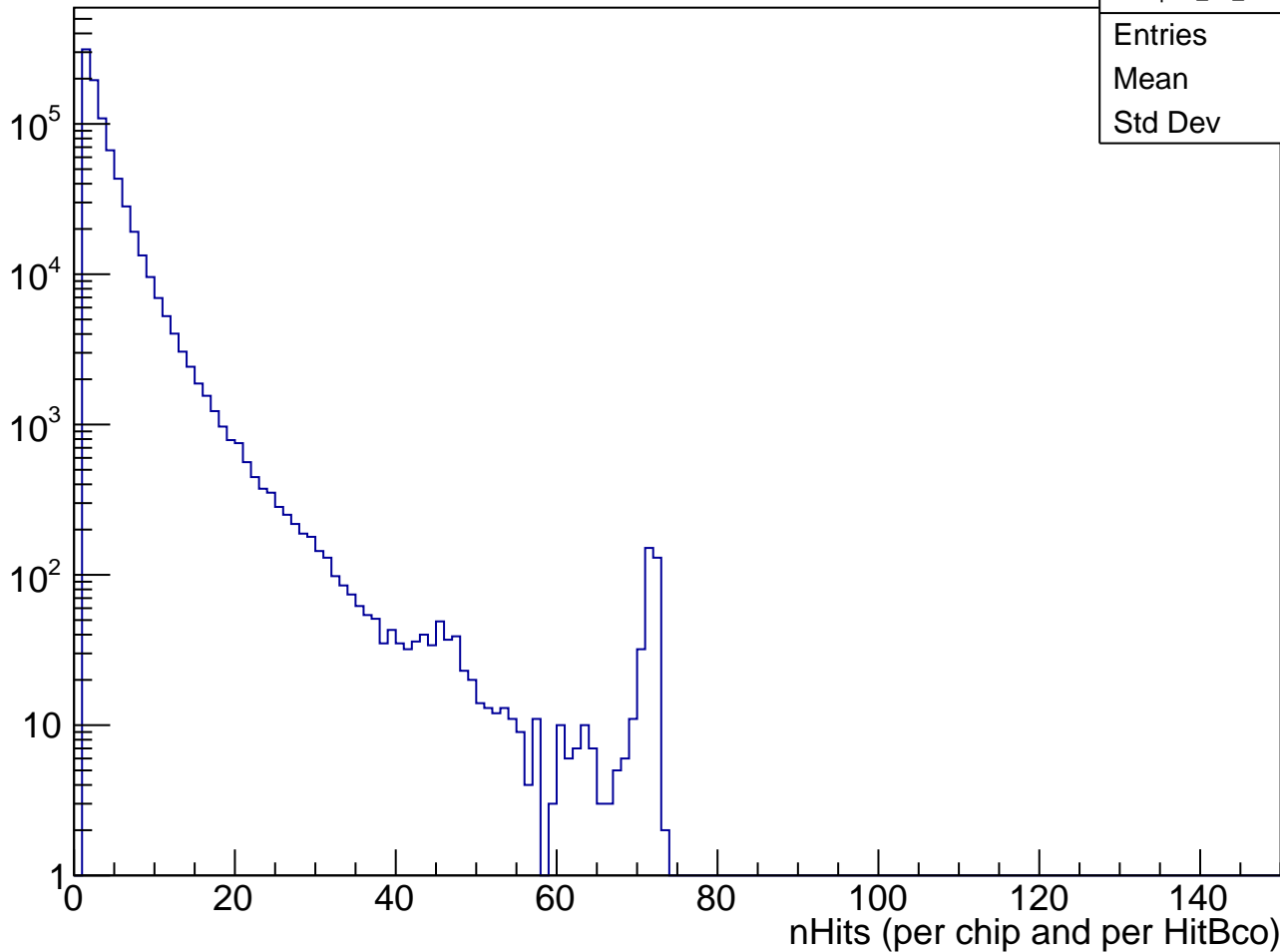
Mean 3.025

Std Dev 3.563

nHits (per chip and per HitBco)

nChipHit_F2_Fch12_Chip25

Entries



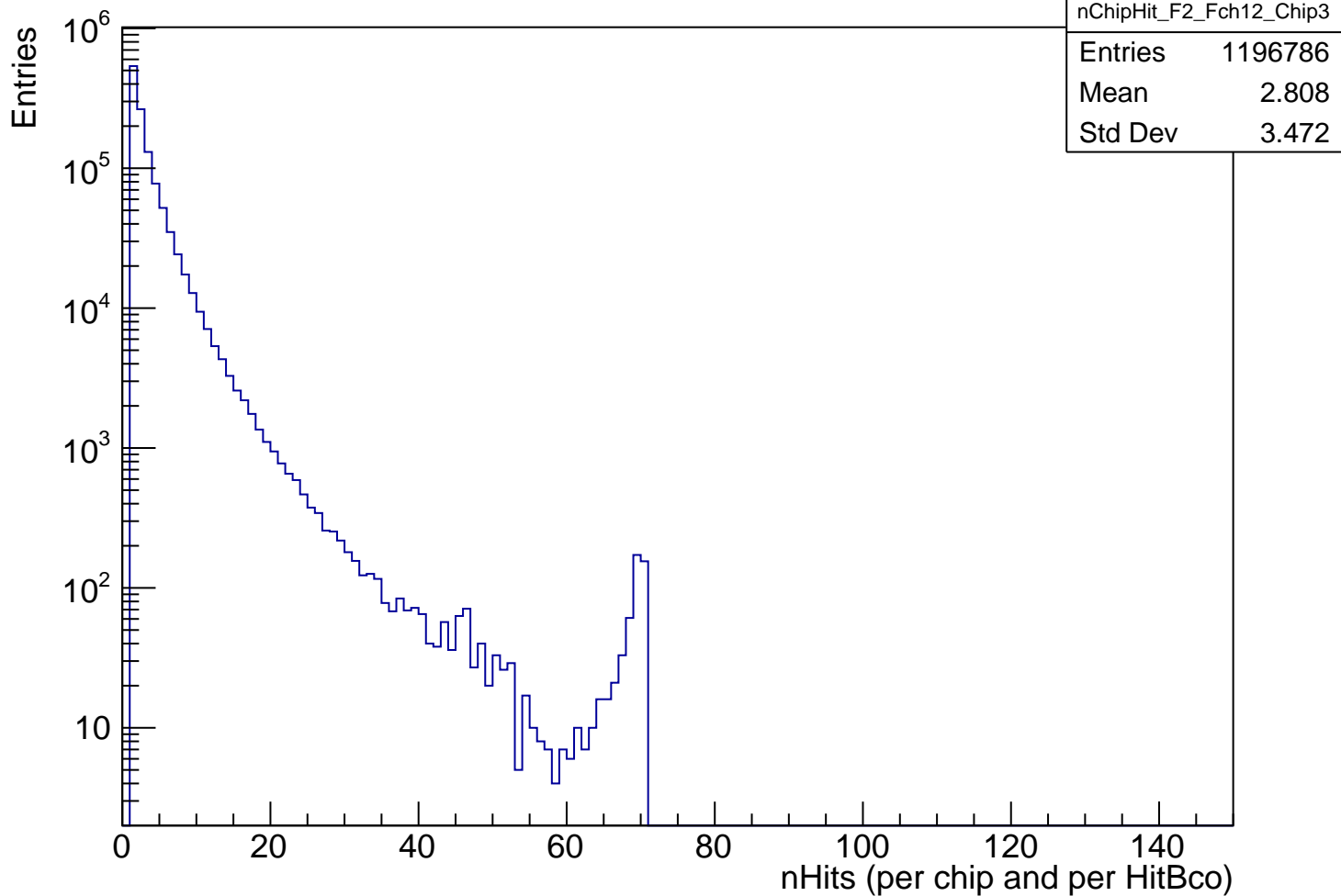
nChipHit_F2_Fch12_Chip25

Entries 831219

Mean 3.034

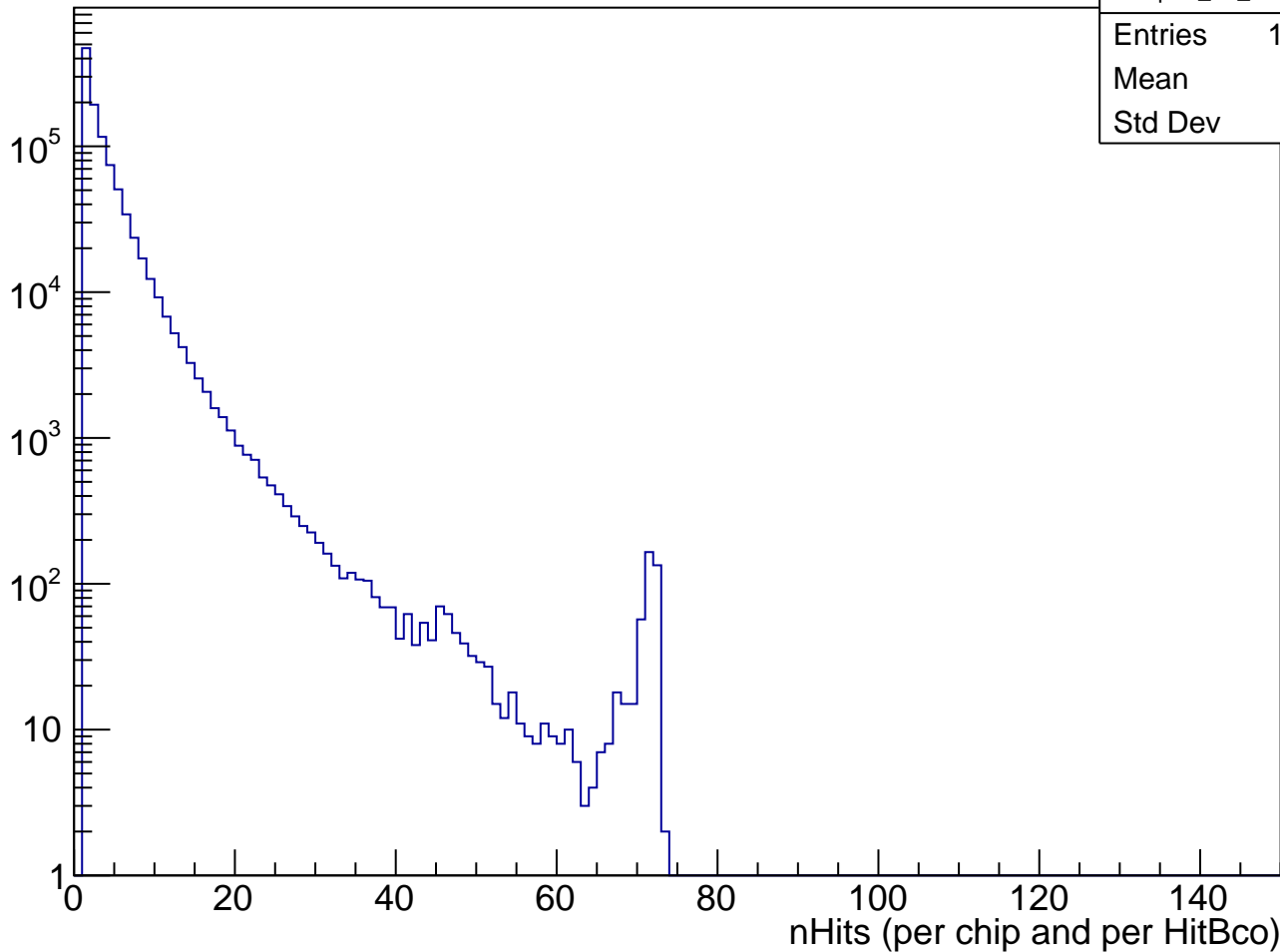
Std Dev 3.551

nChipHit_F2_Fch12_Chip3



nChipHit_F2_Fch12_Chip4

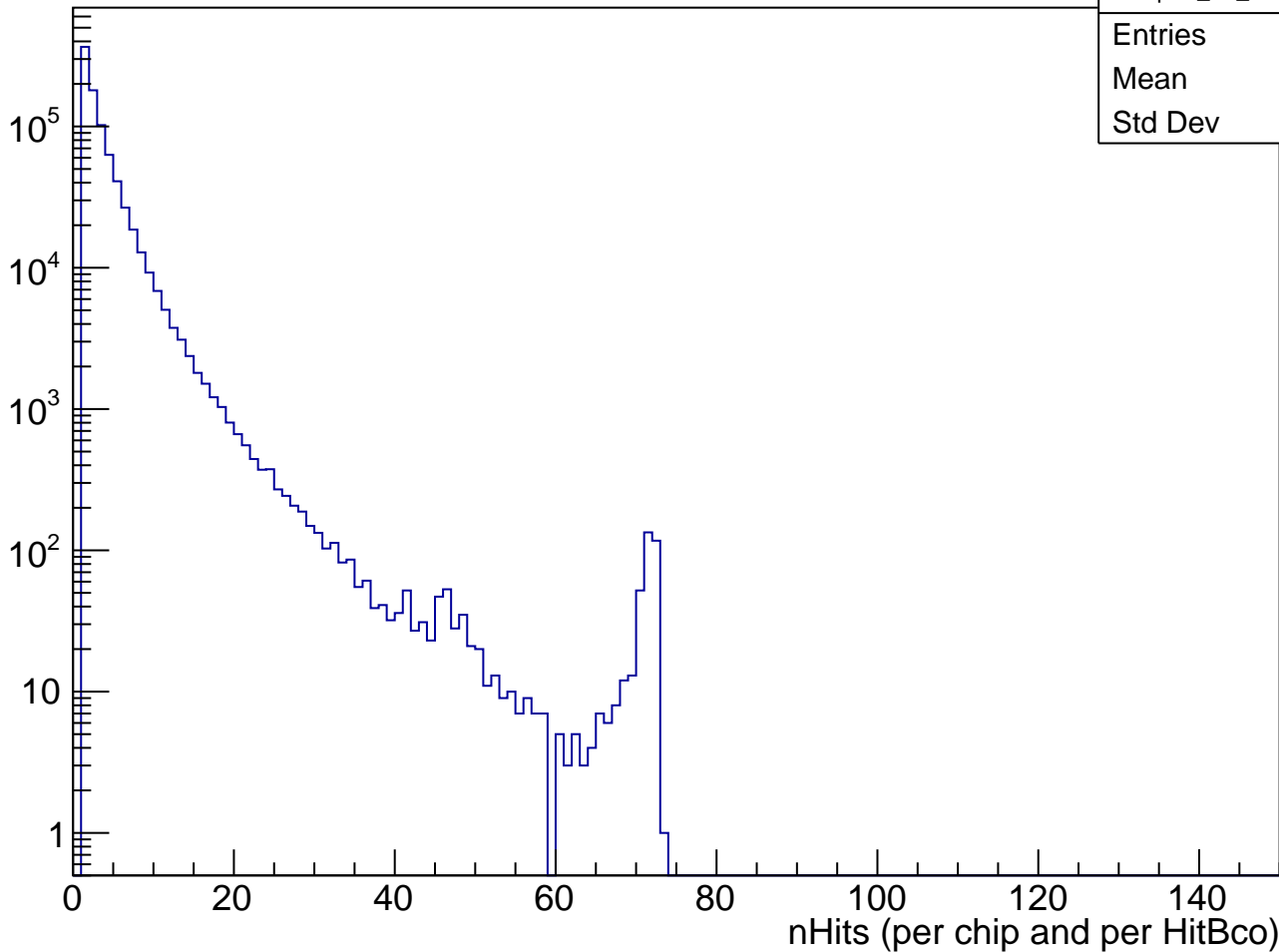
Entries



nChipHit_F2_Fch12_Chip4	
Entries	1037059
Mean	2.952
Std Dev	3.678

nChipHit_F2_Fch12_Chip5

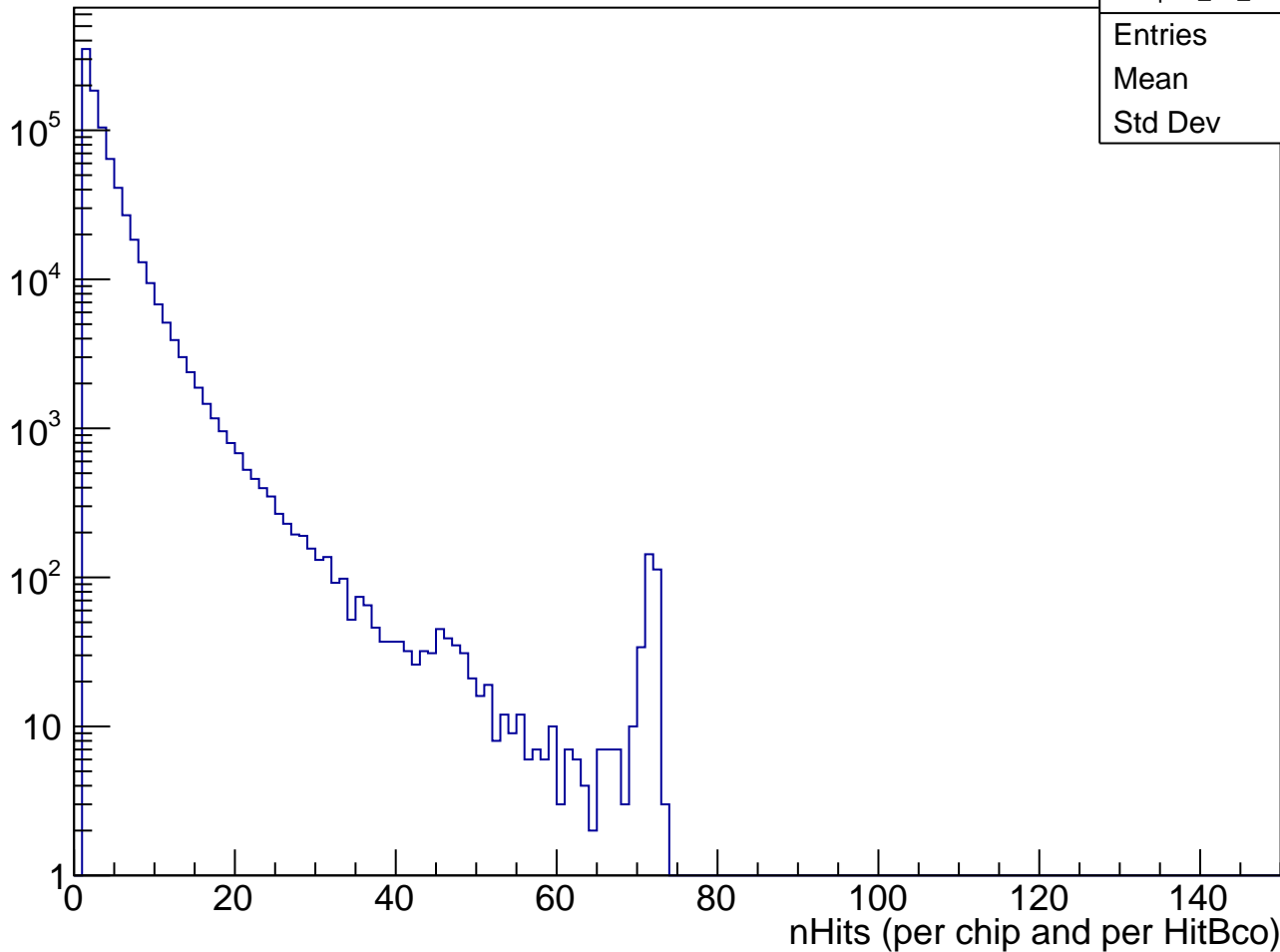
Entries



nChipHit_F2_Fch12_Chip5	
Entries	852207
Mean	2.894
Std Dev	3.501

nChipHit_F2_Fch12_Chip6

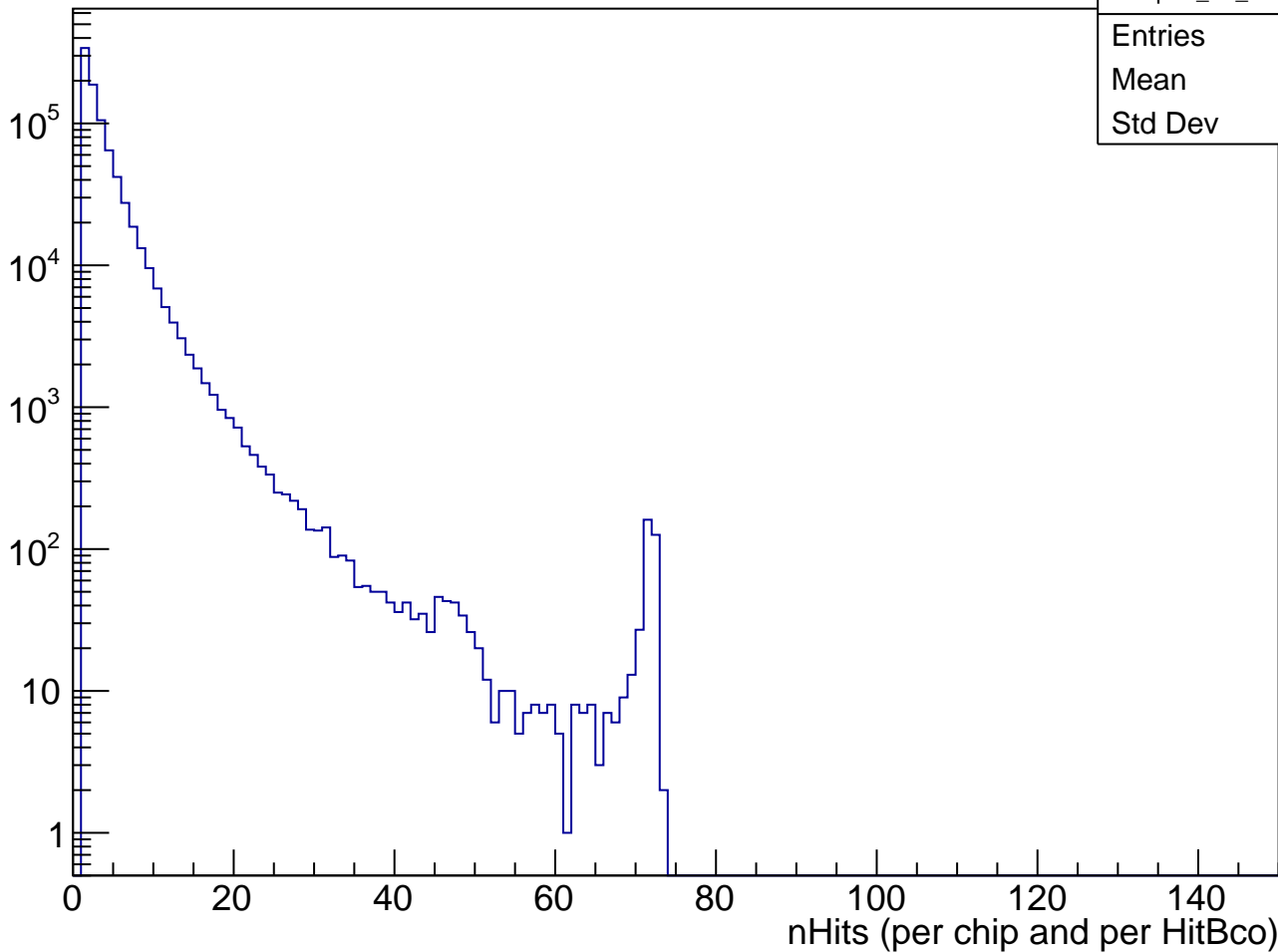
Entries



nChipHit_F2_Fch12_Chip6	
Entries	845568
Mean	2.925
Std Dev	3.491

nChipHit_F2_Fch12_Chip7

Entries

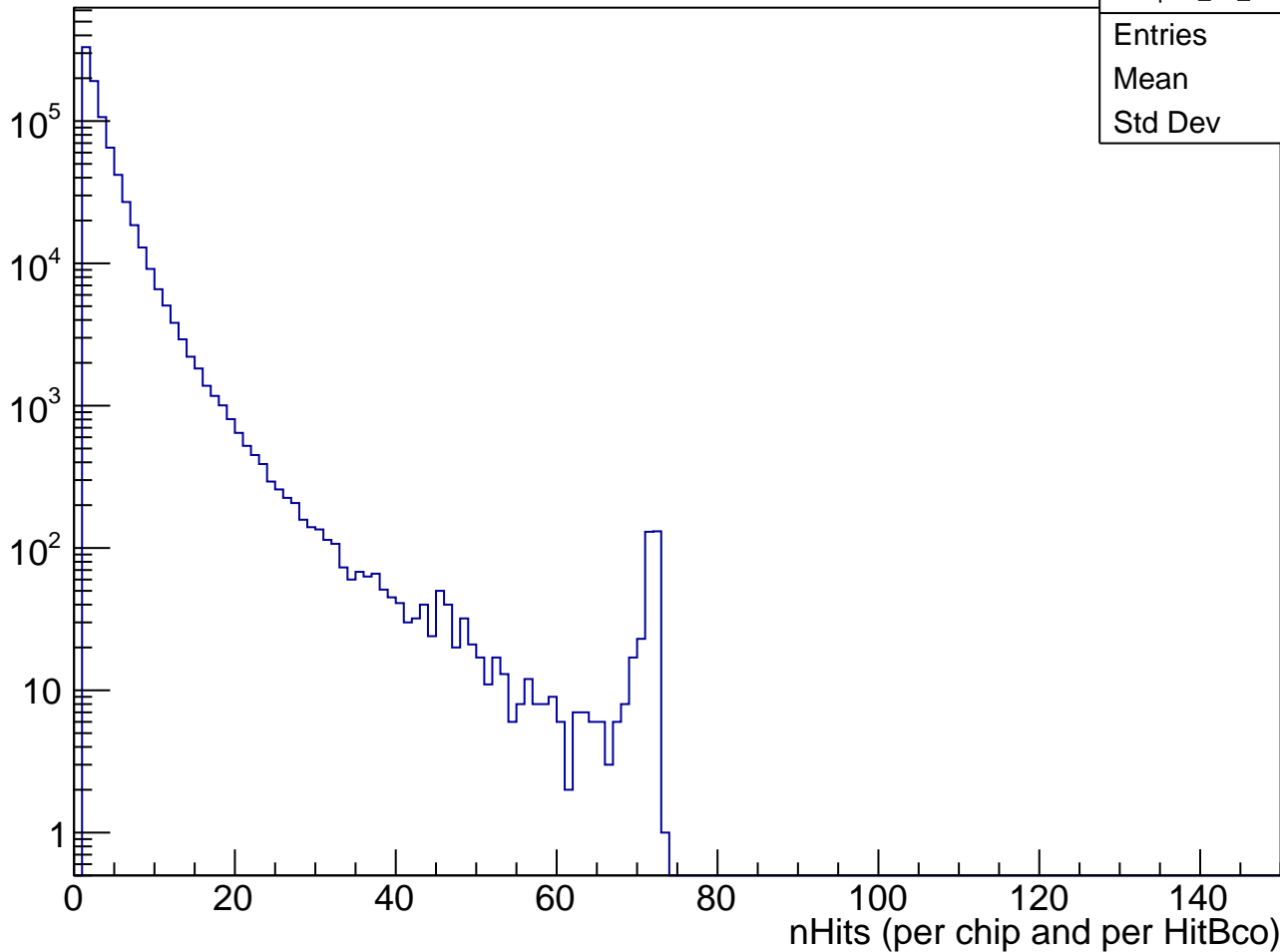


nChipHit_F2_Fch12_Chip7	
Entries	841373
Mean	2.962
Std Dev	3.537

nHits (per chip and per HitBco)

nChipHit_F2_Fch12_Chip8

Entries

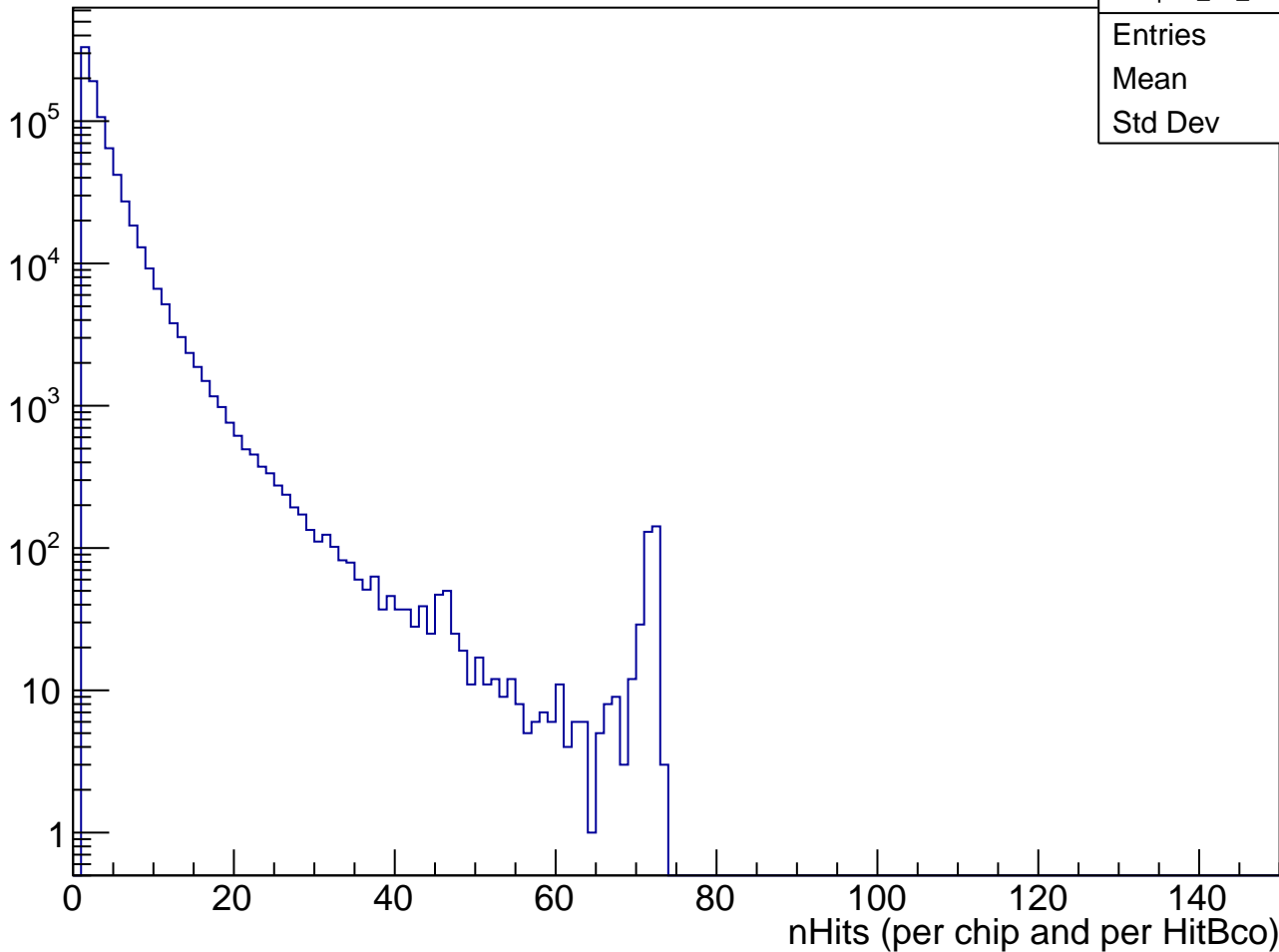


nChipHit_F2_Fch12_Chip8	
Entries	834473
Mean	2.955
Std Dev	3.487

nHits (per chip and per HitBco)

nChipHit_F2_Fch12_Chip9

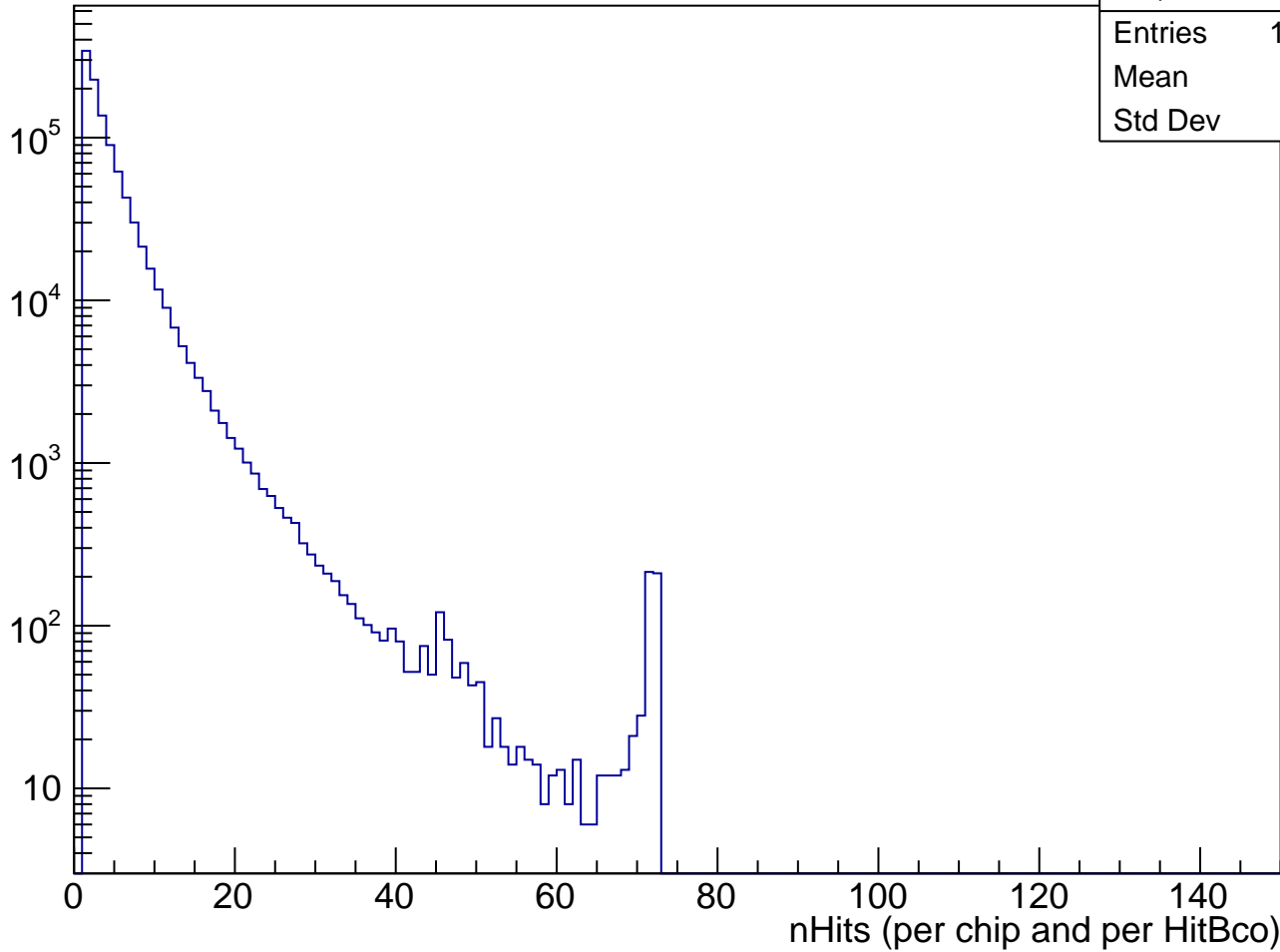
Entries



nChipHit_F2_Fch12_Chip9	
Entries	835429
Mean	2.959
Std Dev	3.491

nChipHit_F2_Fch13_Chip0

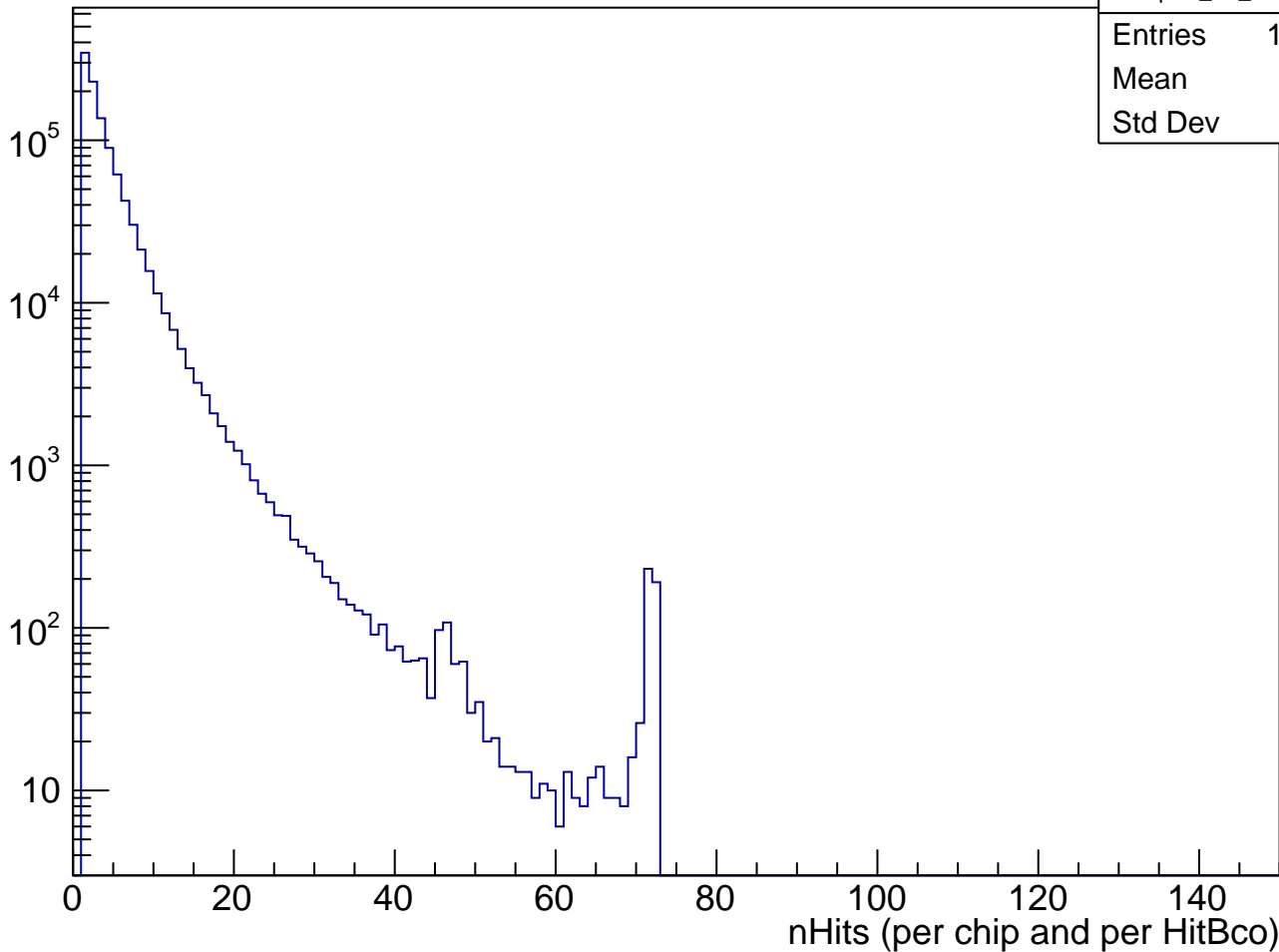
Entries



nChipHit_F2_Fch13_Chip0	
Entries	1024543
Mean	3.456
Std Dev	4.037

nChipHit_F2_Fch13_Chip1

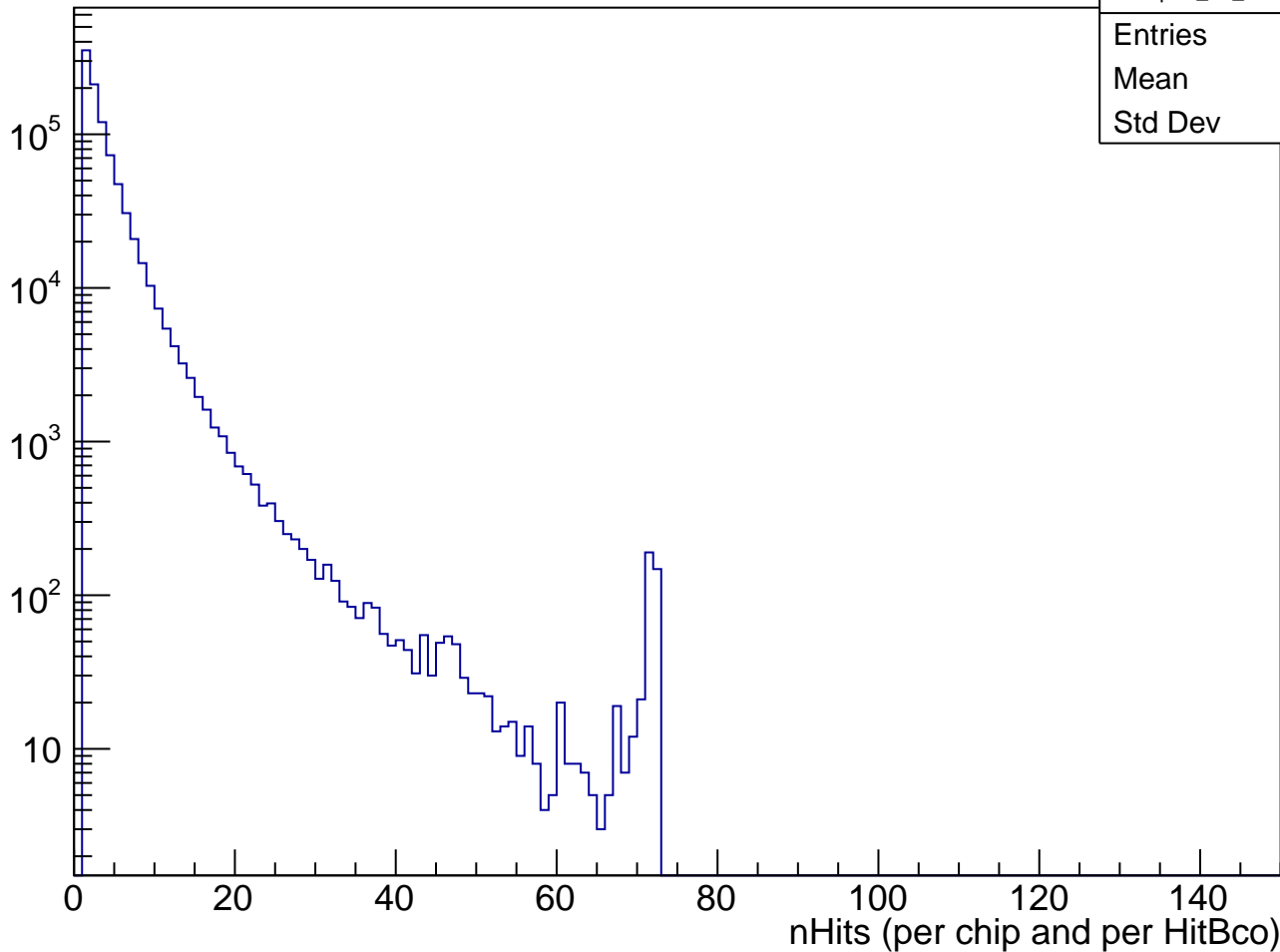
Entries



nChipHit_F2_Fch13_Chip1	
Entries	1028495
Mean	3.43
Std Dev	4

nChipHit_F2_Fch13_Chip10

Entries



nChipHit_F2_Fch13_Chip10

Entries 915540

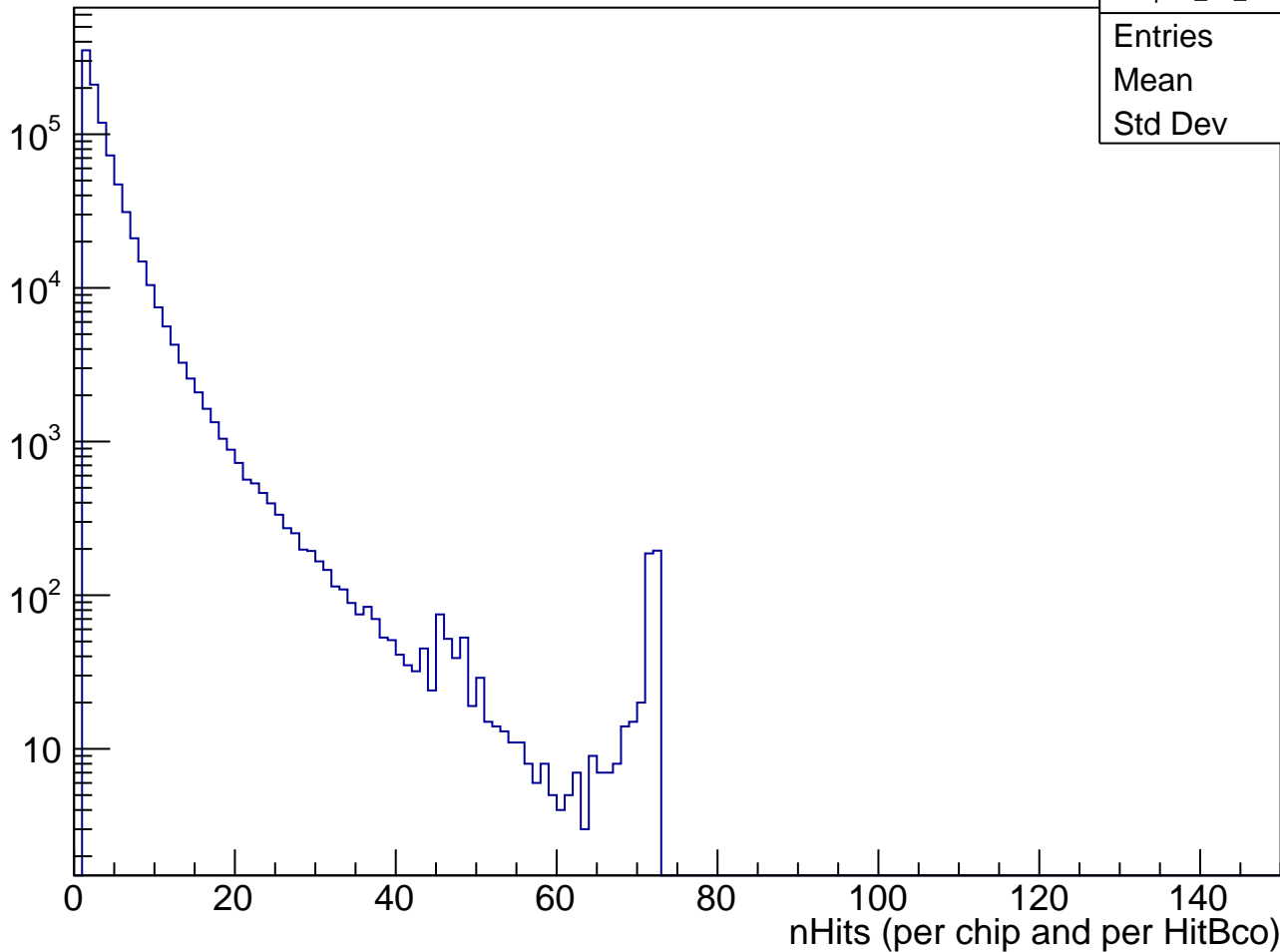
Mean 3.005

Std Dev 3.57

nHits (per chip and per HitBco)

nChipHit_F2_Fch13_Chip11

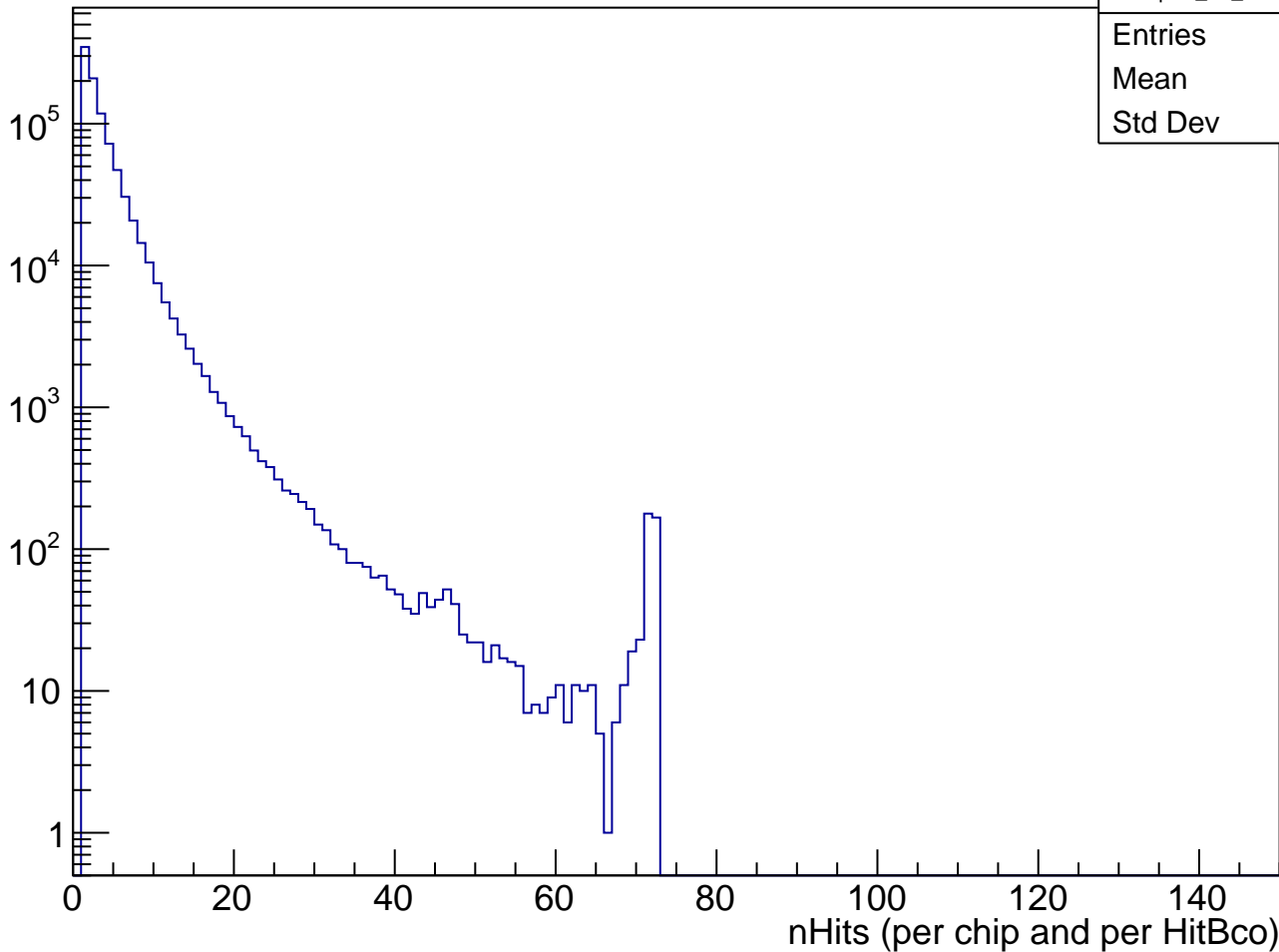
Entries



nChipHit_F2_Fch13_Chip11	
Entries	914731
Mean	3.026
Std Dev	3.624

nChipHit_F2_Fch13_Chip12

Entries

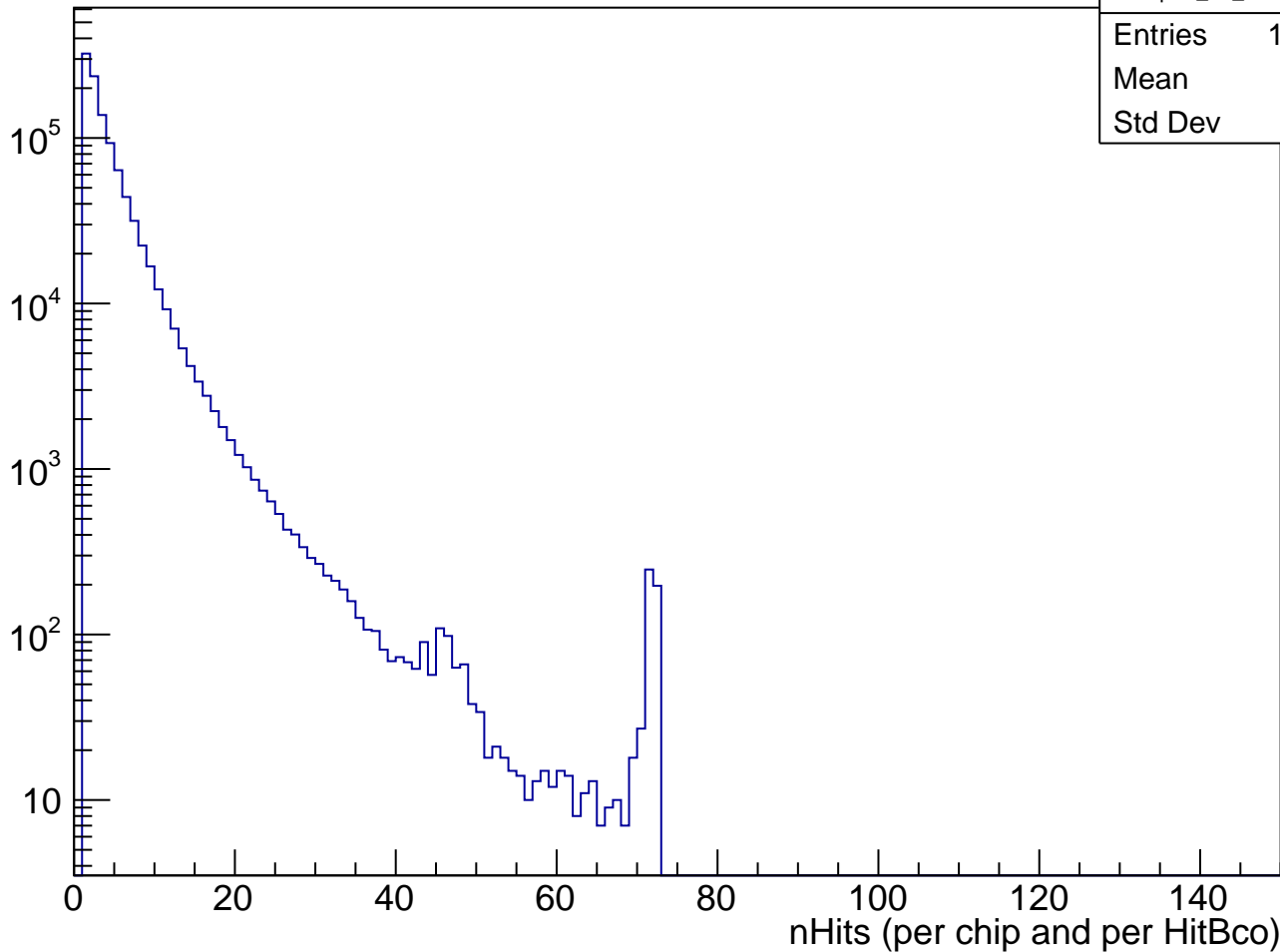


nChipHit_F2_Fch13_Chip12	
Entries	905402
Mean	3.024
Std Dev	3.604

nHits (per chip and per HitBco)

nChipHit_F2_Fch13_Chip13

Entries



nChipHit_F2_Fch13_Chip13

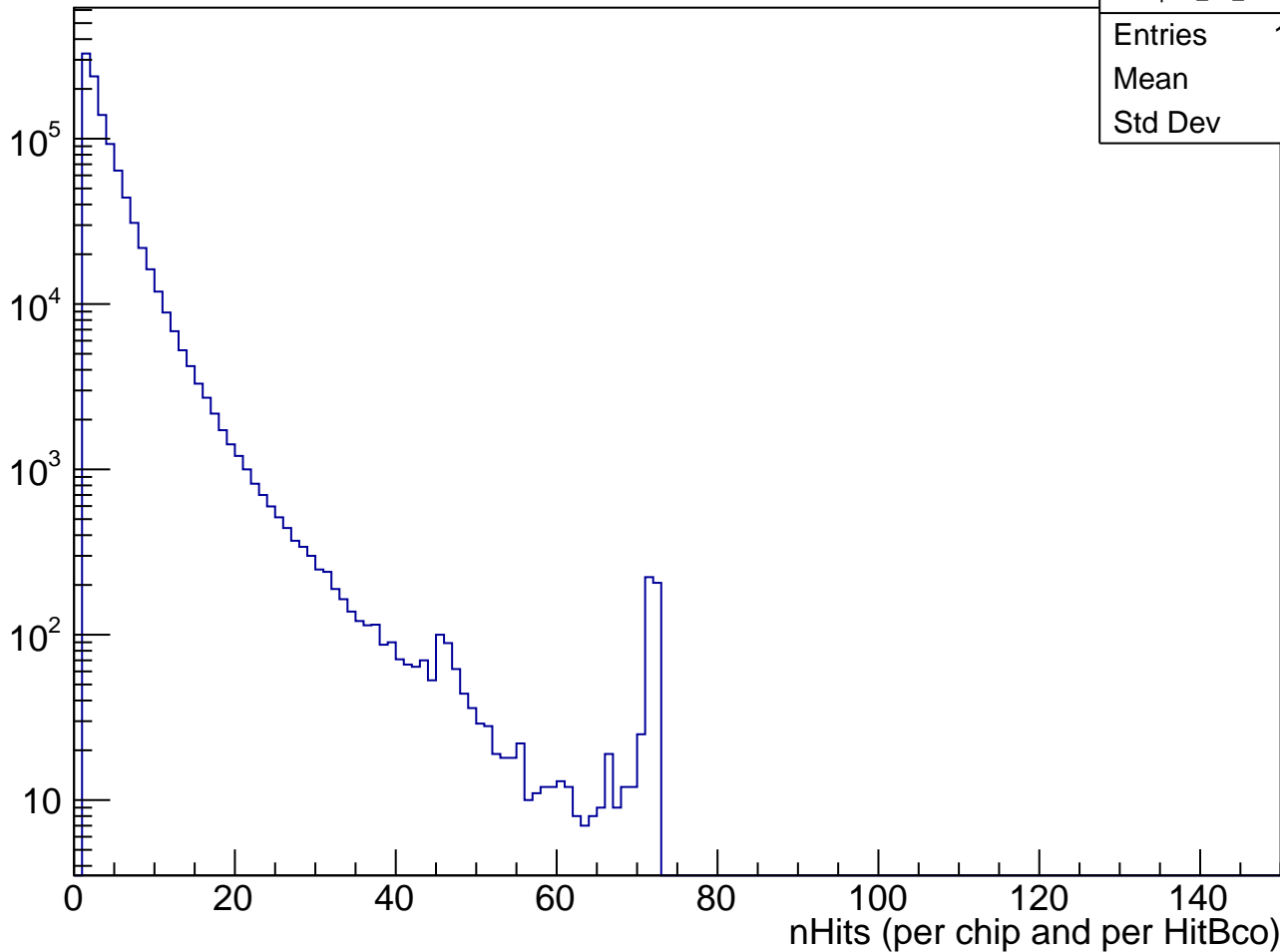
Entries 1028436

Mean 3.529

Std Dev 4.073

nChipHit_F2_Fch13_Chip14

Entries



nChipHit_F2_Fch13_Chip14

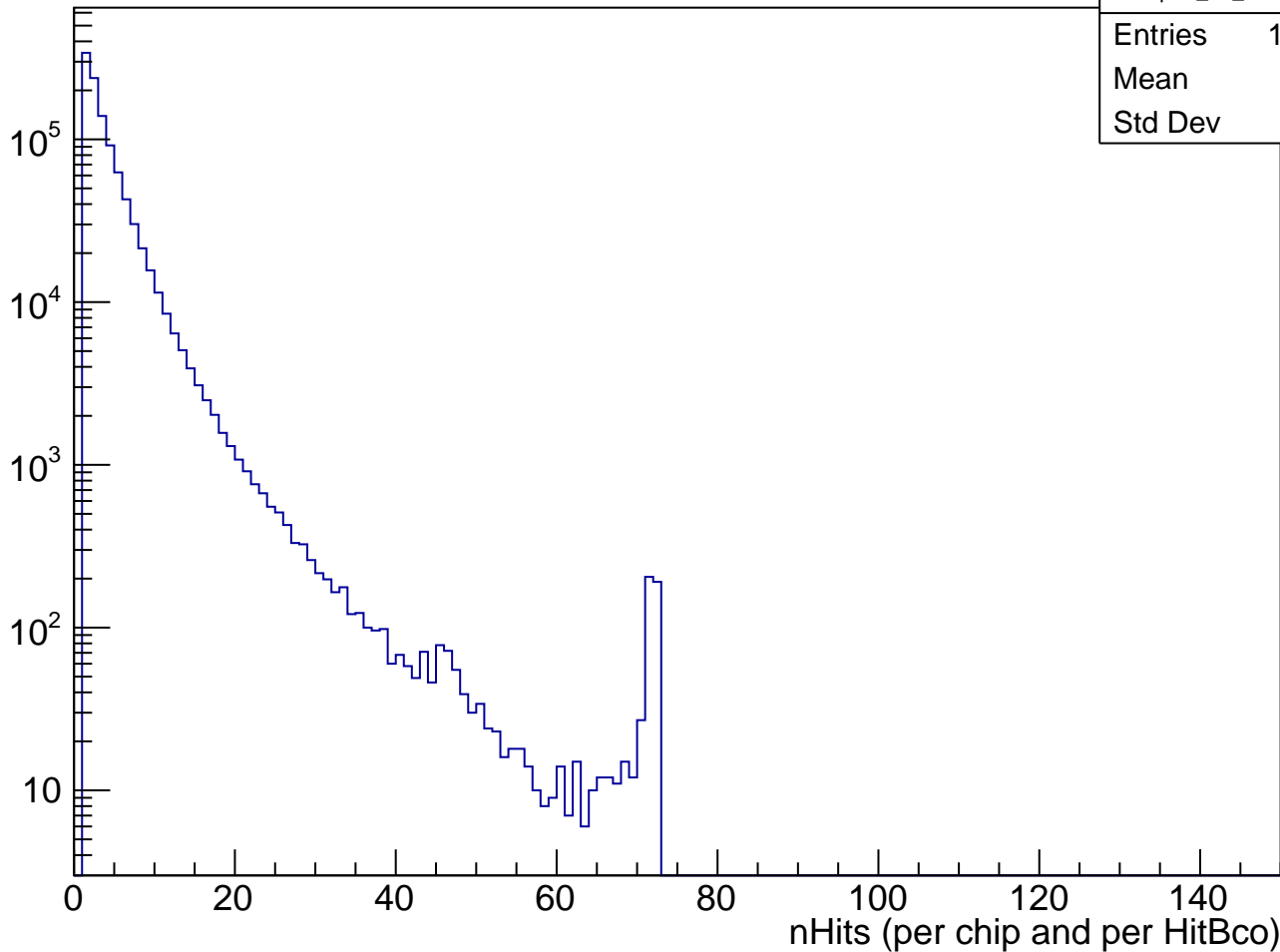
Entries 1032241

Mean 3.491

Std Dev 4.019

nChipHit_F2_Fch13_Chip15

Entries



nChipHit_F2_Fch13_Chip15

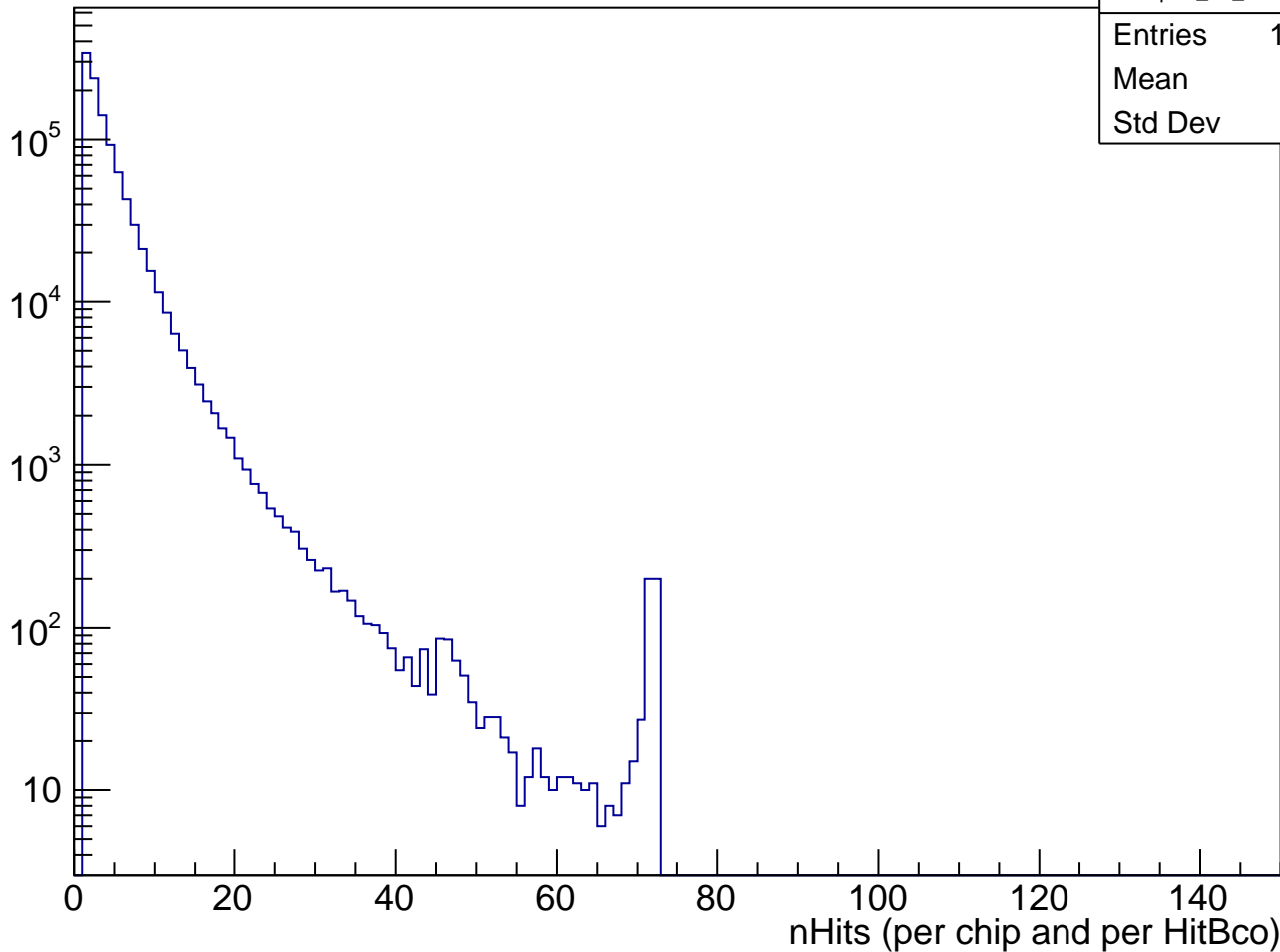
Entries 1036267

Mean 3.403

Std Dev 3.907

nChipHit_F2_Fch13_Chip16

Entries



nChipHit_F2_Fch13_Chip16

Entries 1038340

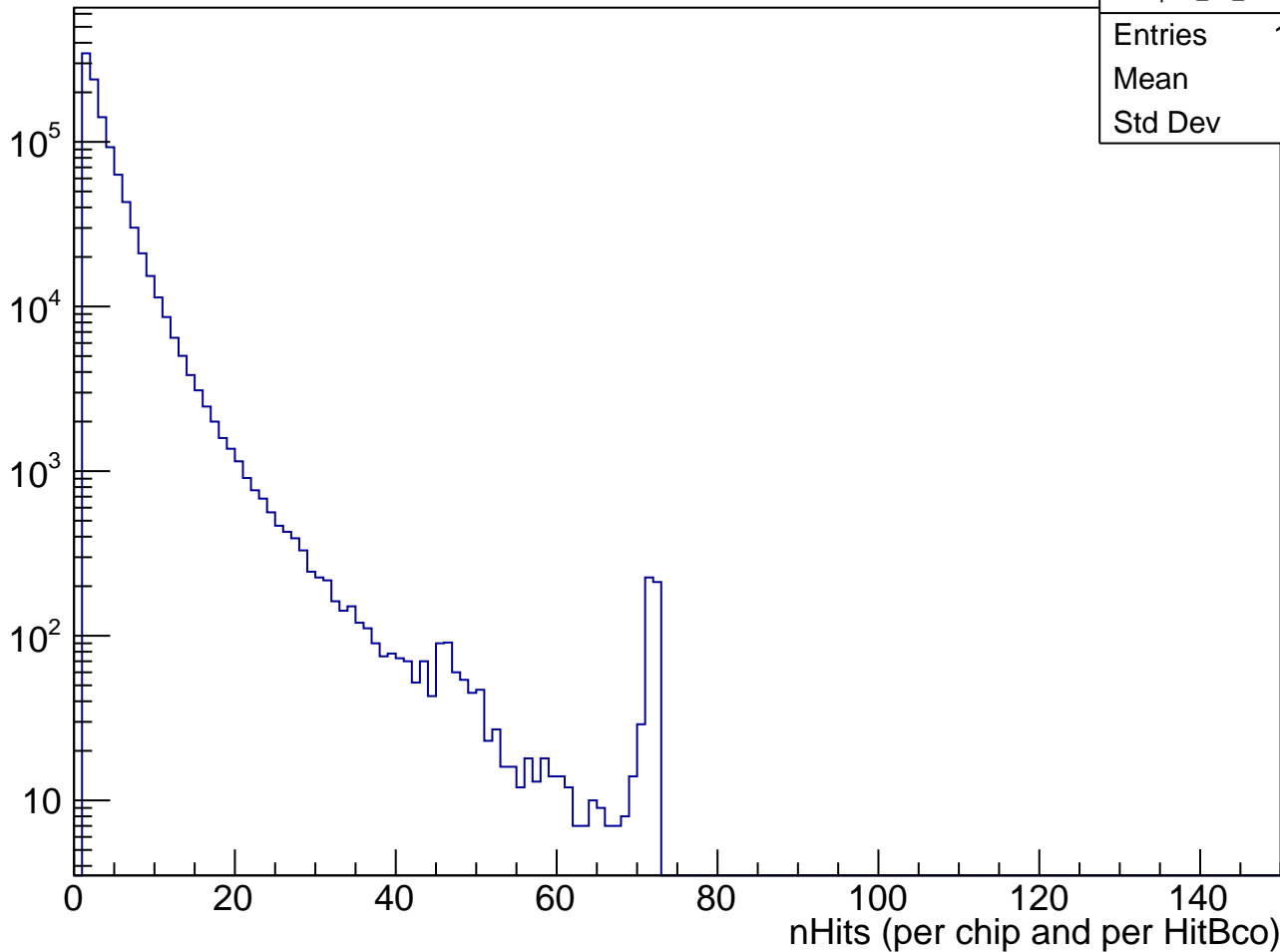
Mean 3.411

Std Dev 3.926

nHits (per chip and per HitBco)

nChipHit_F2_Fch13_Chip17

Entries



nChipHit_F2_Fch13_Chip17

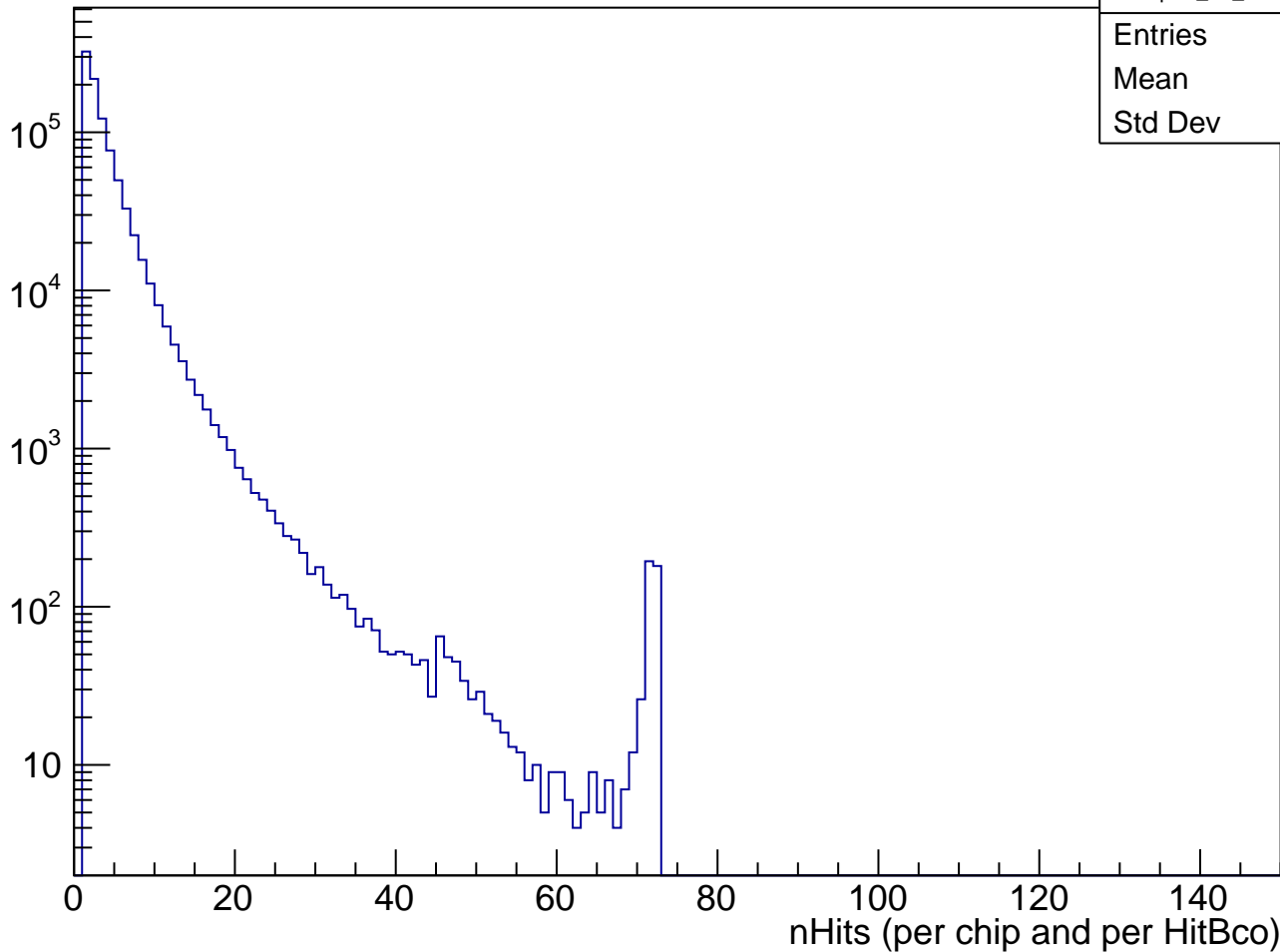
Entries 1046081

Mean 3.397

Std Dev 3.94

nChipHit_F2_Fch13_Chip18

Entries



nChipHit_F2_Fch13_Chip18

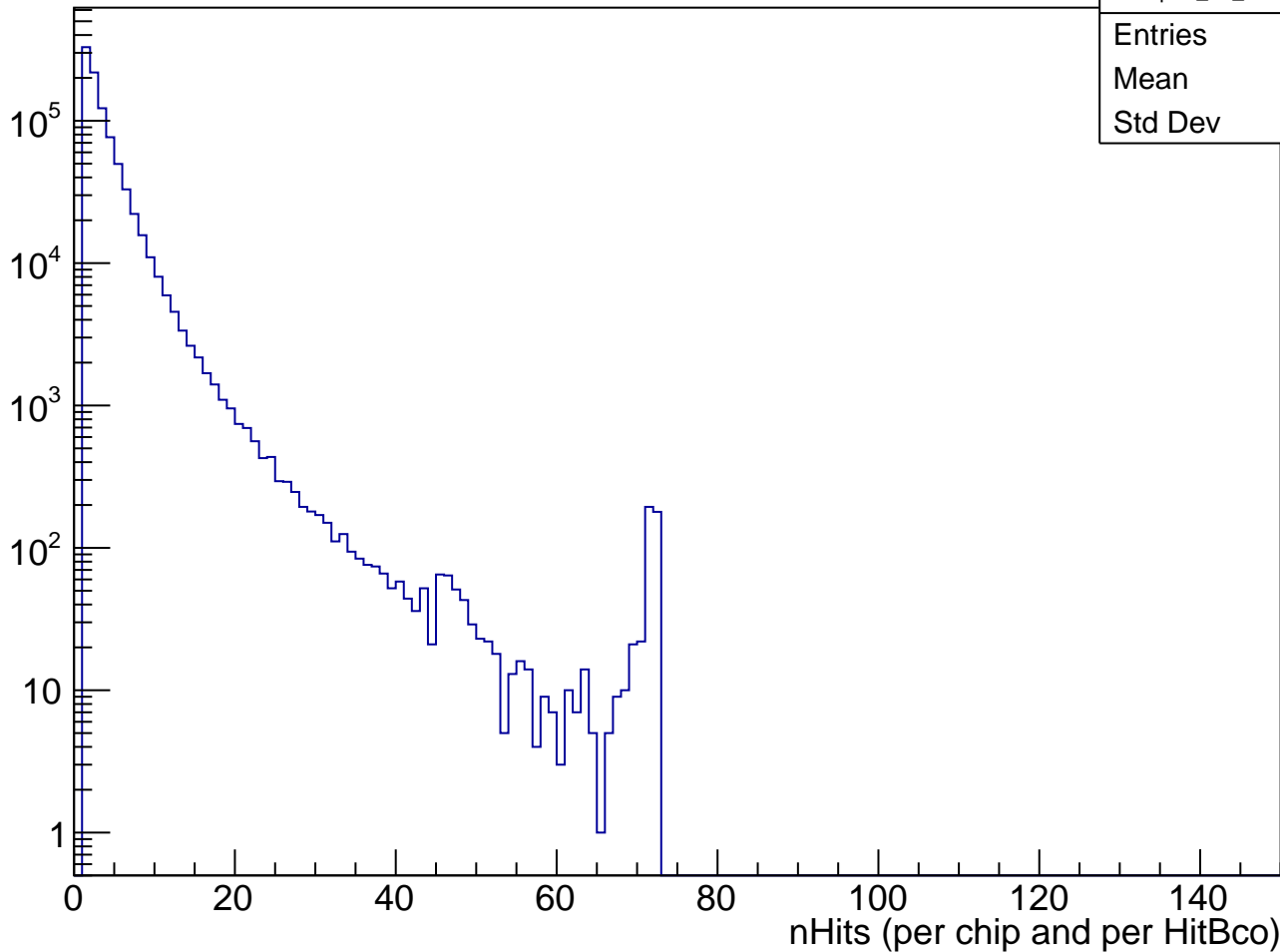
Entries 910443

Mean 3.137

Std Dev 3.675

nChipHit_F2_Fch13_Chip19

Entries



nChipHit_F2_Fch13_Chip19

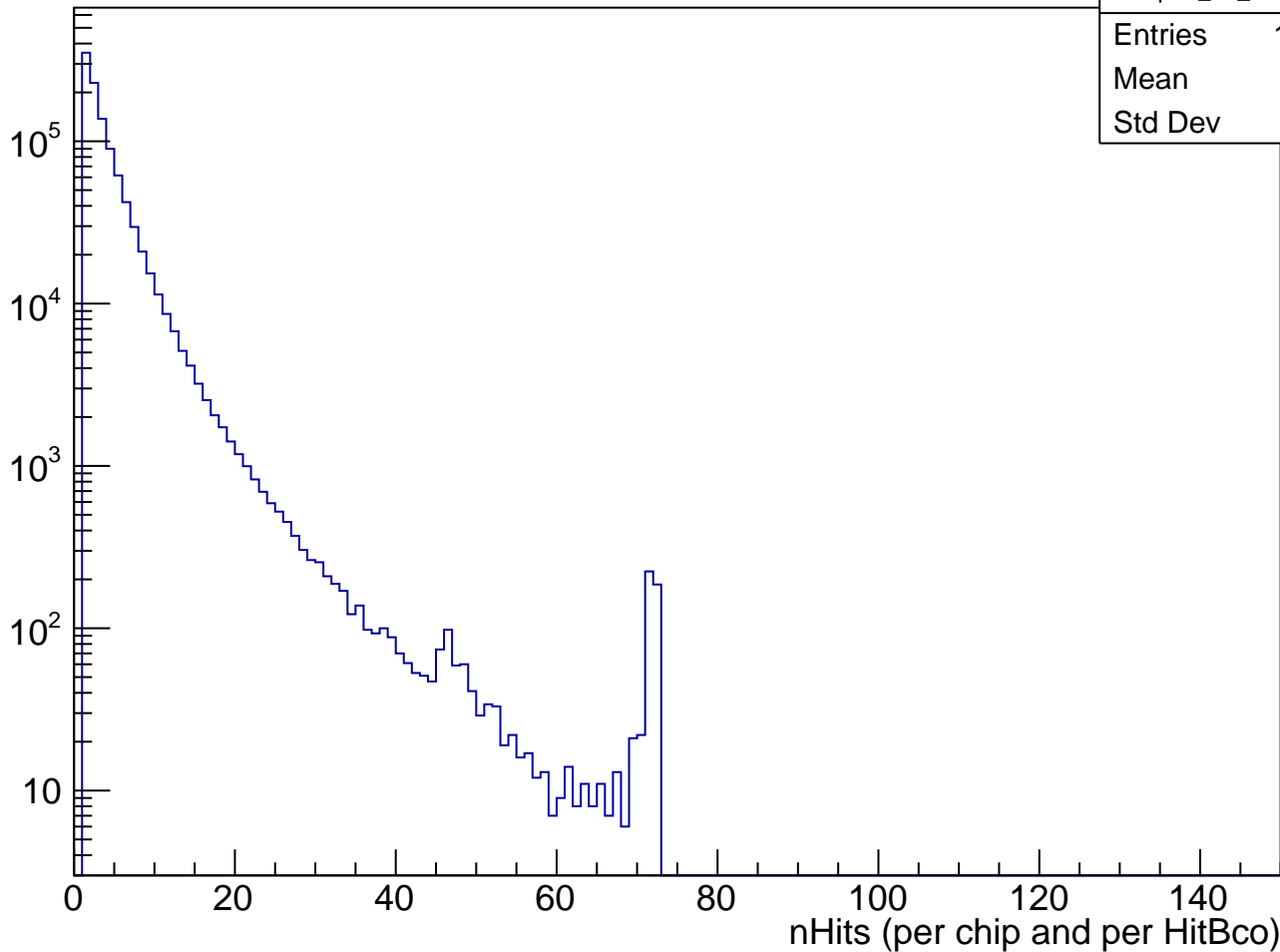
Entries 915815

Mean 3.119

Std Dev 3.67

nChipHit_F2_Fch13_Chip2

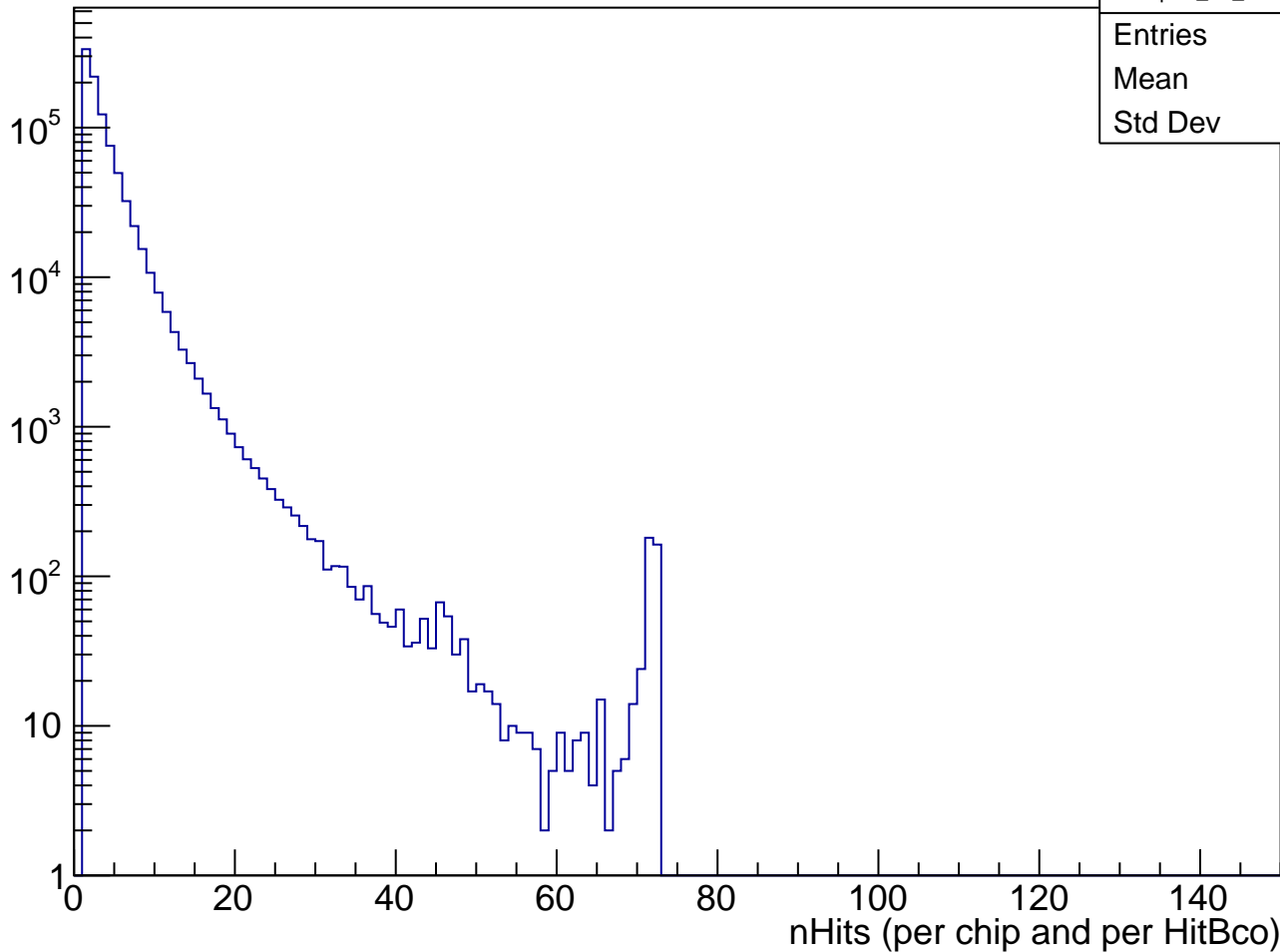
Entries



nChipHit_F2_Fch13_Chip2	
Entries	1033501
Mean	3.406
Std Dev	3.983

nChipHit_F2_Fch13_Chip20

Entries



nChipHit_F2_Fch13_Chip20

Entries 918919

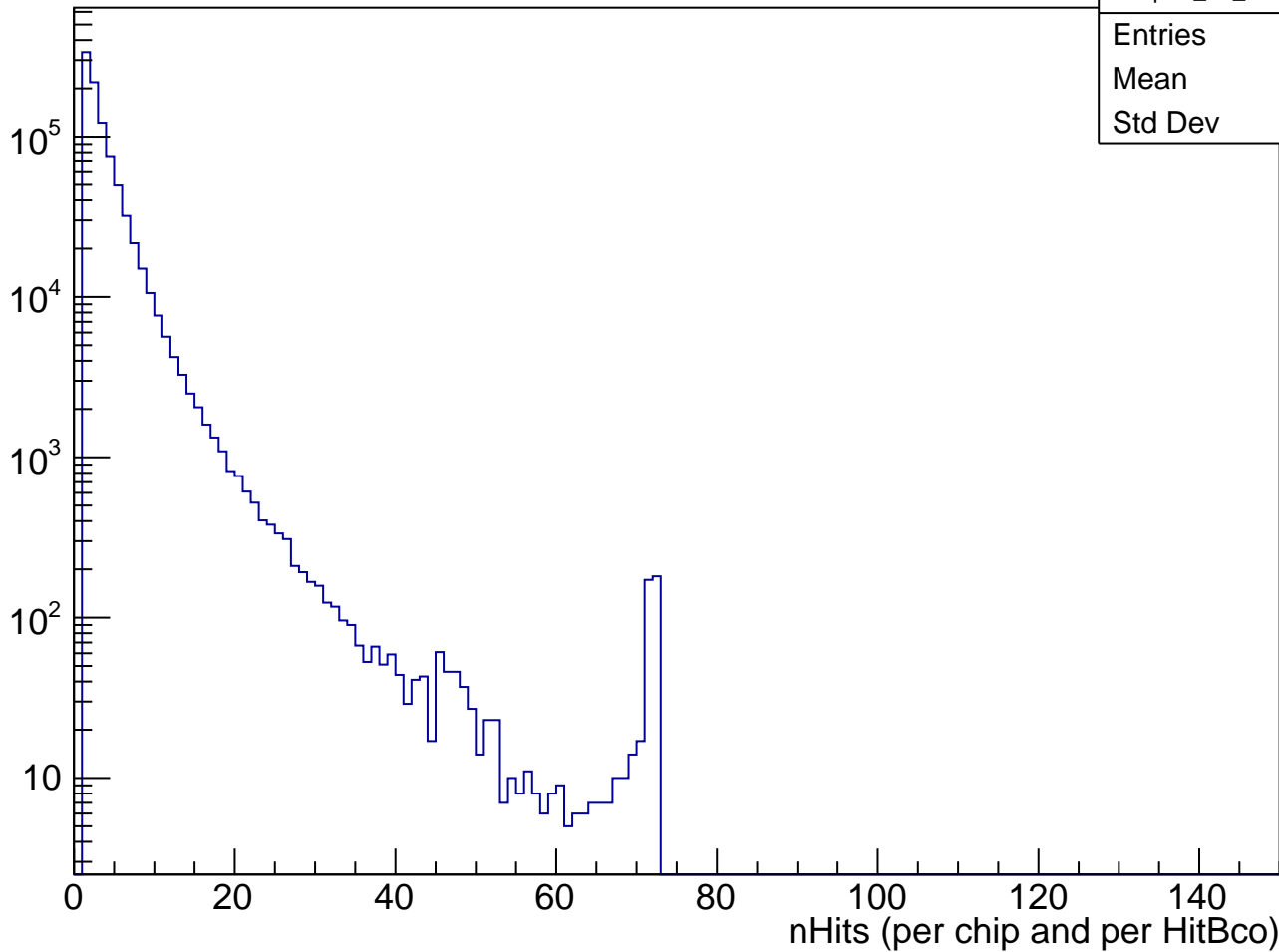
Mean 3.079

Std Dev 3.585

nHits (per chip and per HitBco)

nChipHit_F2_Fch13_Chip21

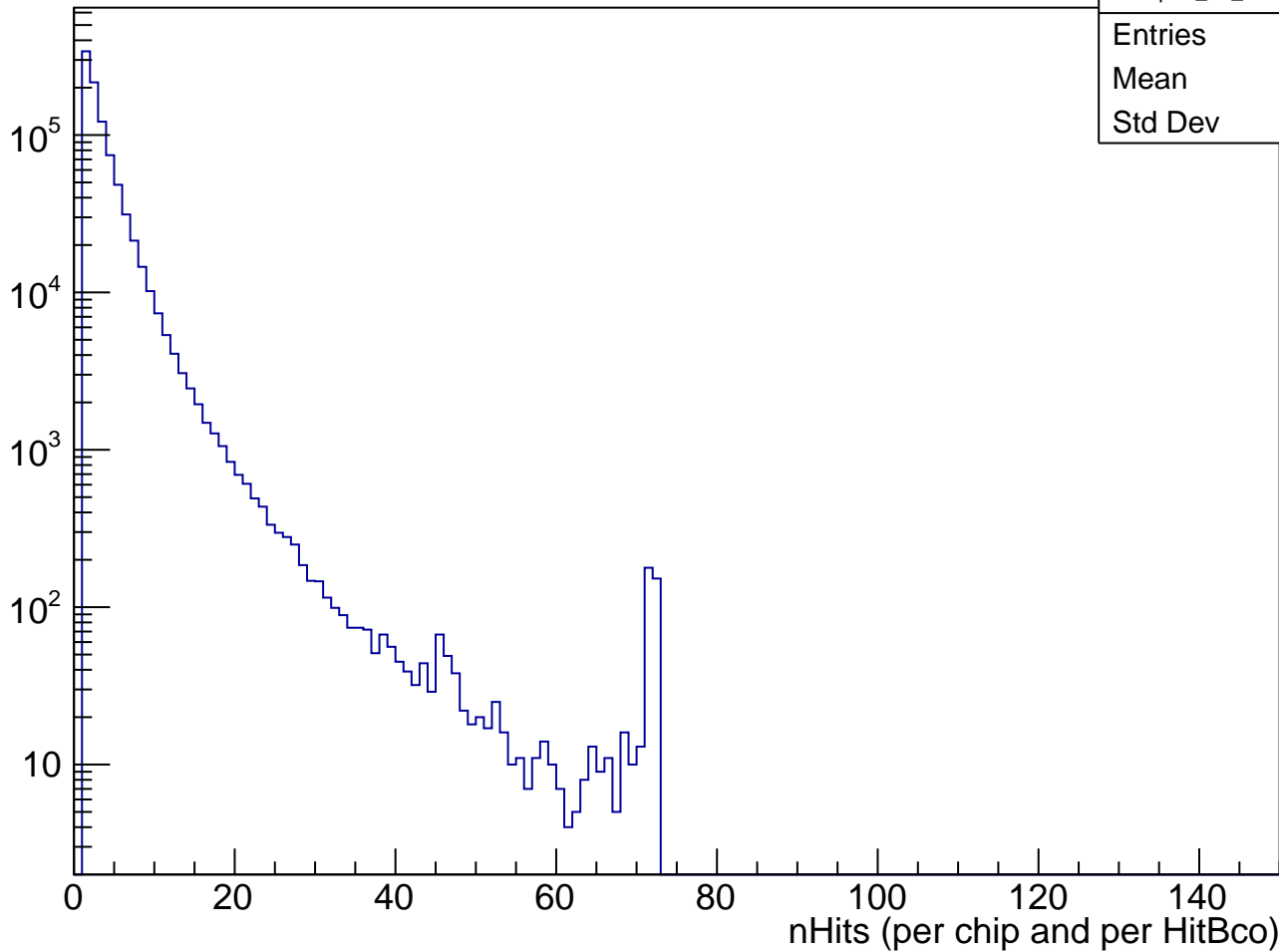
Entries



nChipHit_F2_Fch13_Chip21	
Entries	917174
Mean	3.058
Std Dev	3.568

nChipHit_F2_Fch13_Chip22

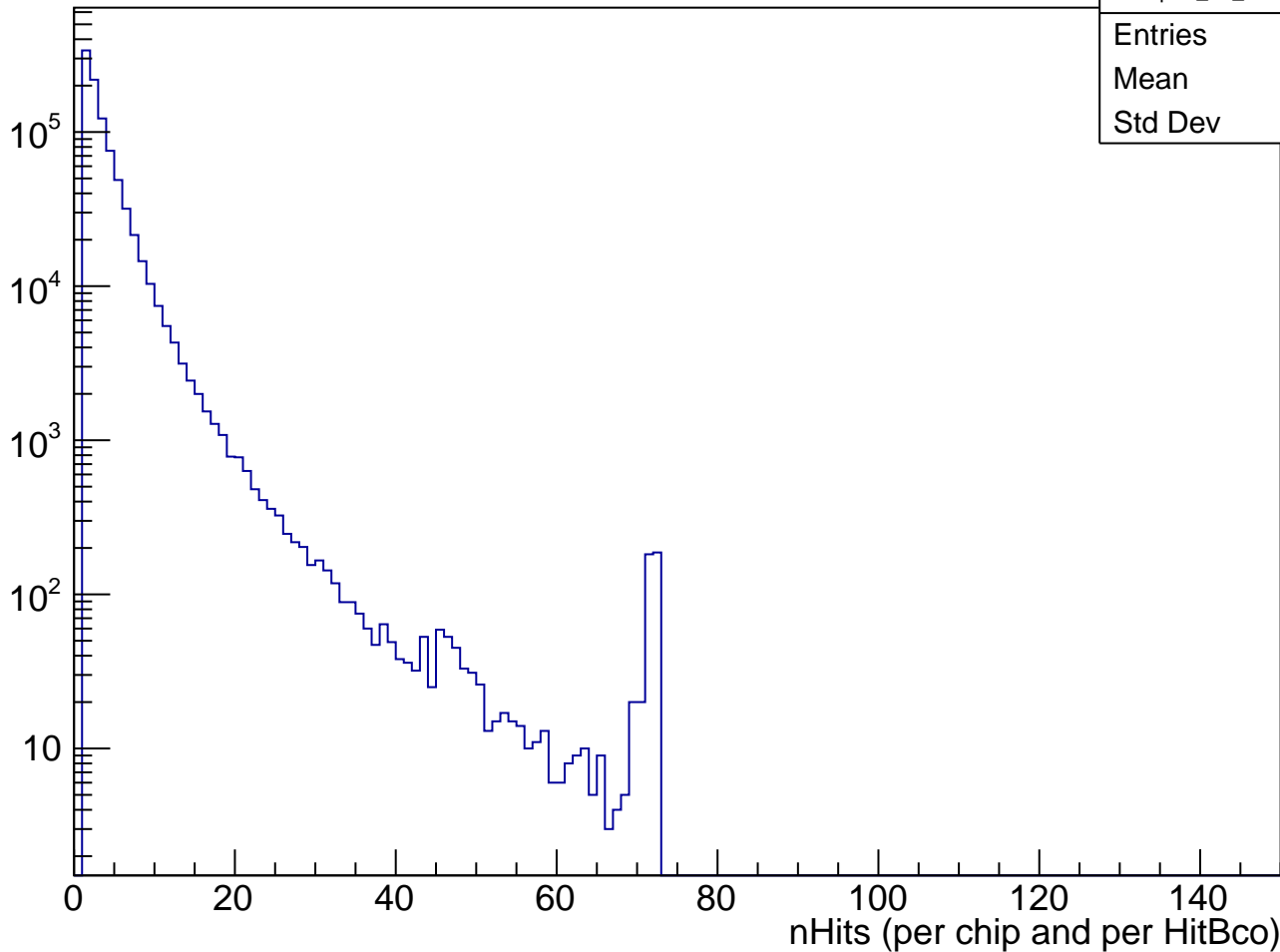
Entries



nChipHit_F2_Fch13_Chip22	
Entries	912703
Mean	3.02
Std Dev	3.53

nChipHit_F2_Fch13_Chip23

Entries



nChipHit_F2_Fch13_Chip23

Entries 917195

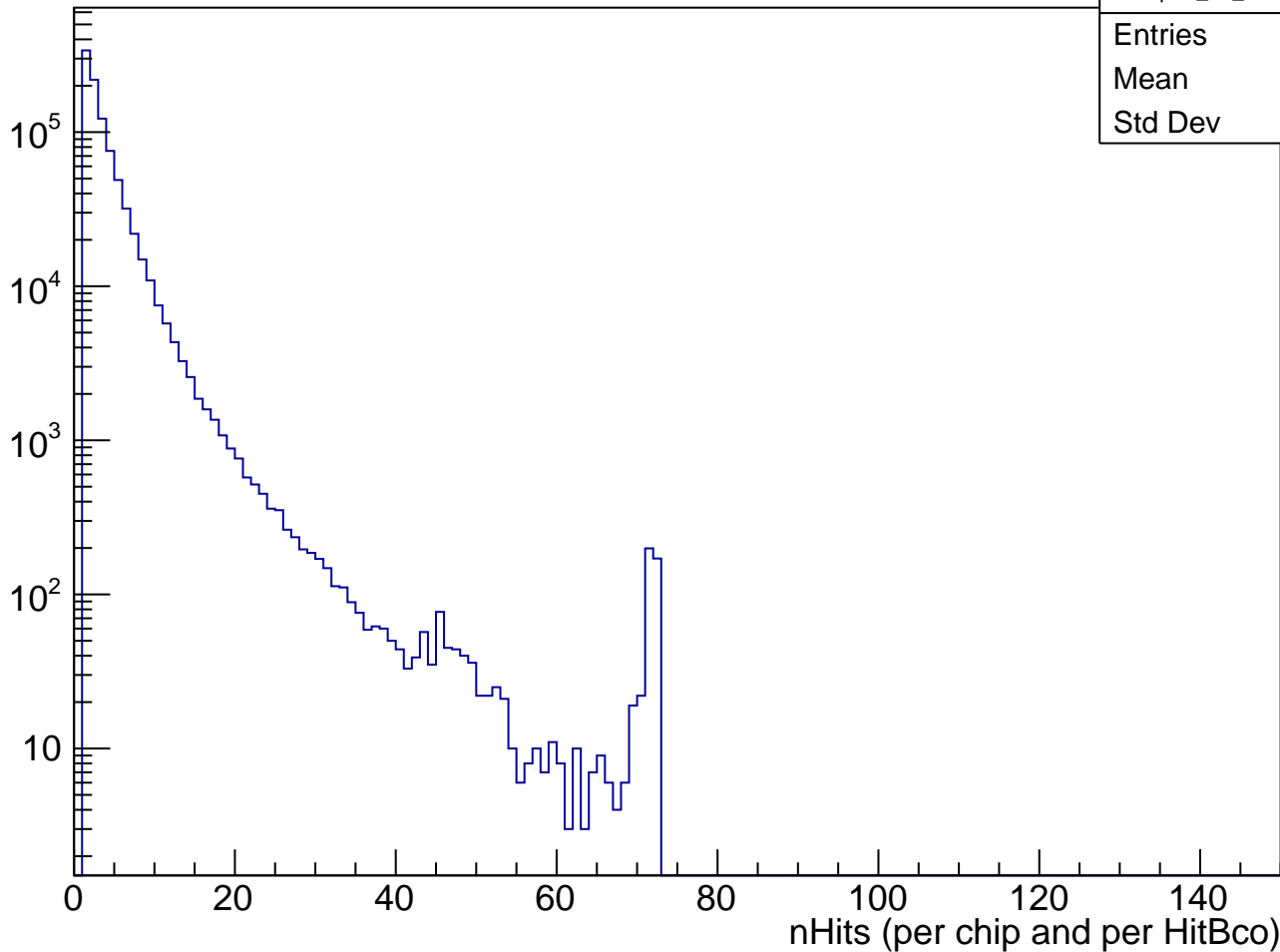
Mean 3.04

Std Dev 3.574

nHits (per chip and per HitBco)

nChipHit_F2_Fch13_Chip24

Entries



nChipHit_F2_Fch13_Chip24

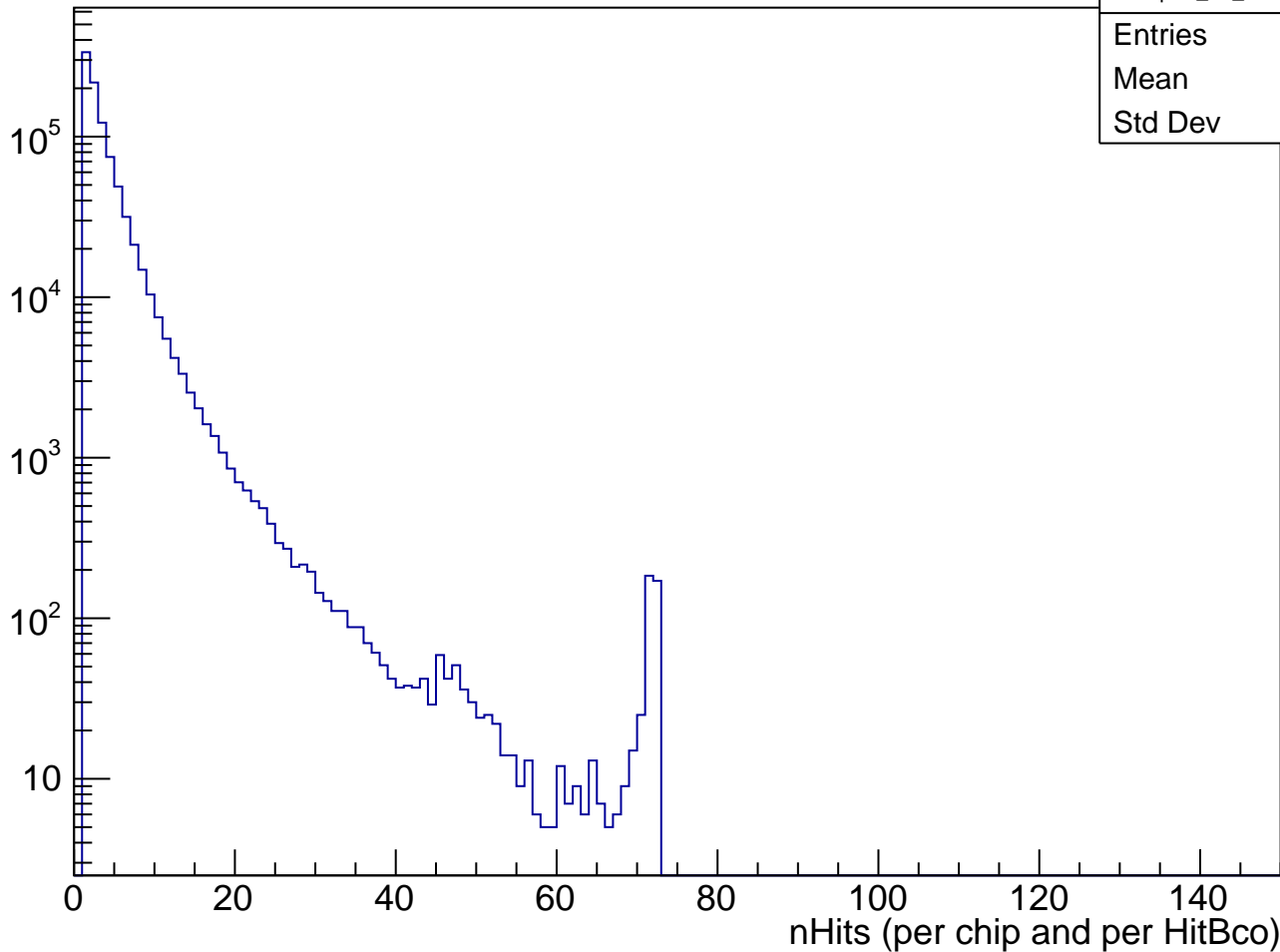
Entries 920364

Mean 3.06

Std Dev 3.611

nChipHit_F2_Fch13_Chip25

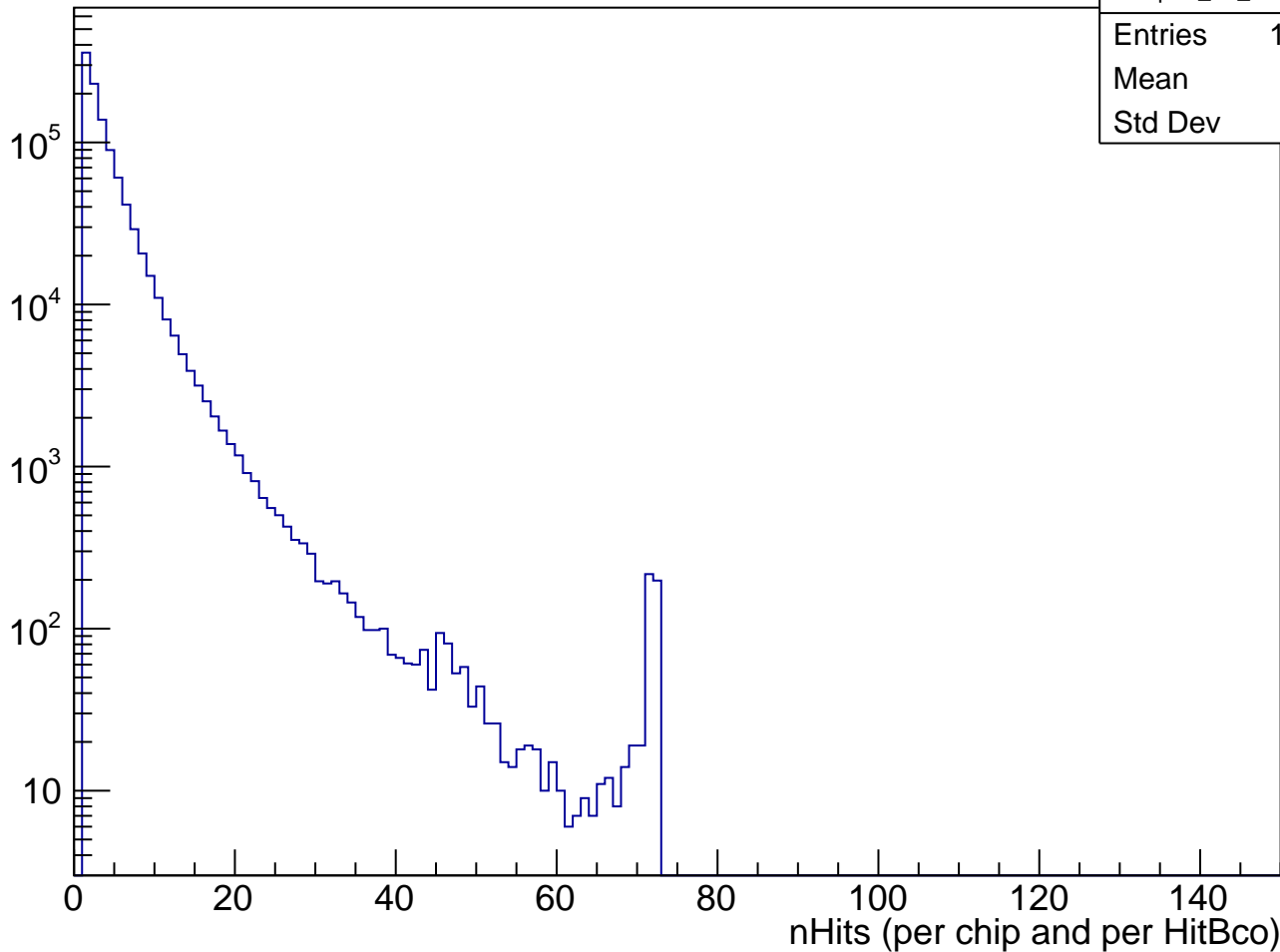
Entries



nChipHit_F2_Fch13_Chip25	
Entries	912583
Mean	3.056
Std Dev	3.599

nChipHit_F2_Fch13_Chip3

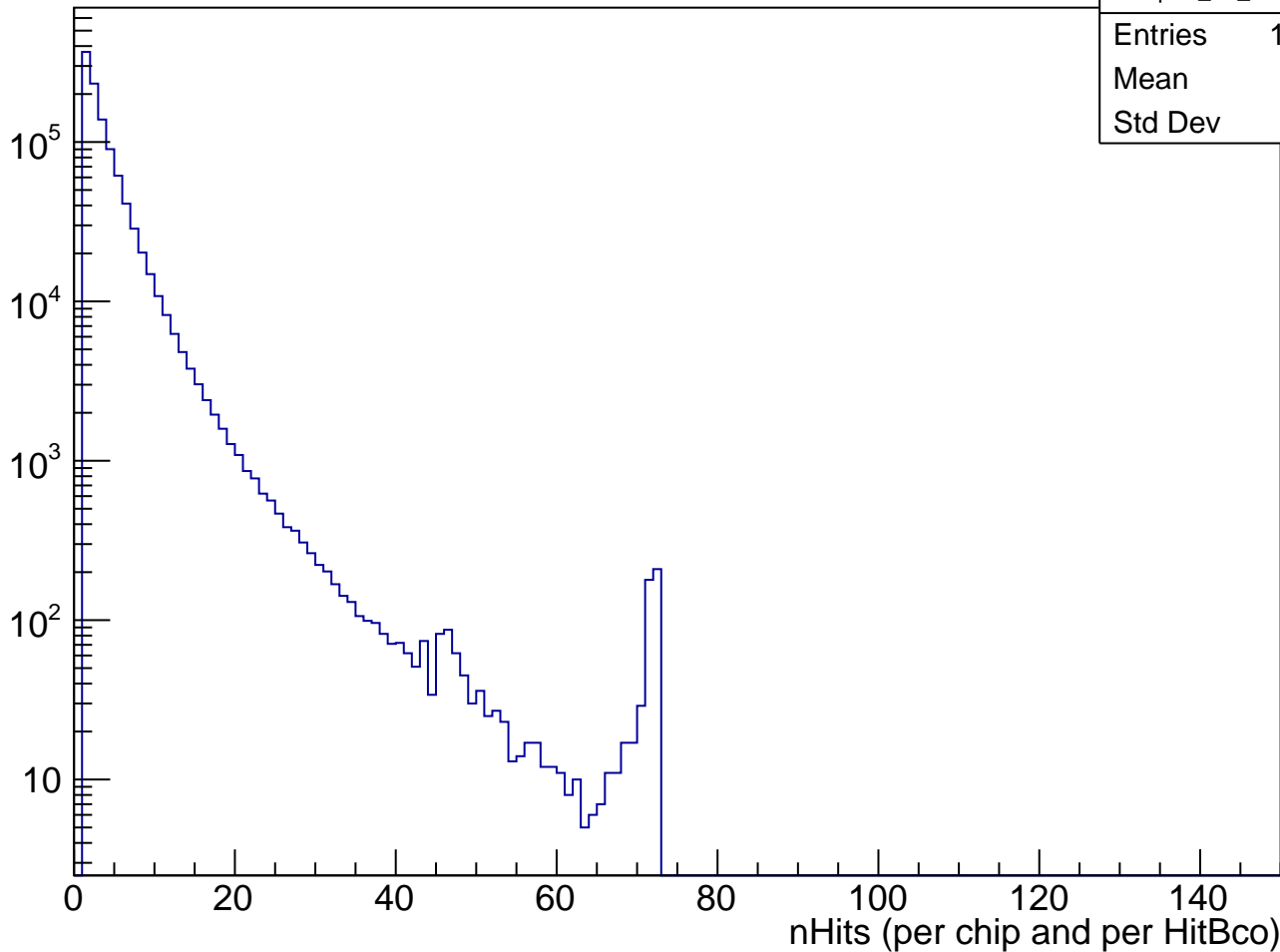
Entries



nChipHit_F2_Fch13_Chip3	
Entries	1036853
Mean	3.359
Std Dev	3.944

nChipHit_F2_Fch13_Chip4

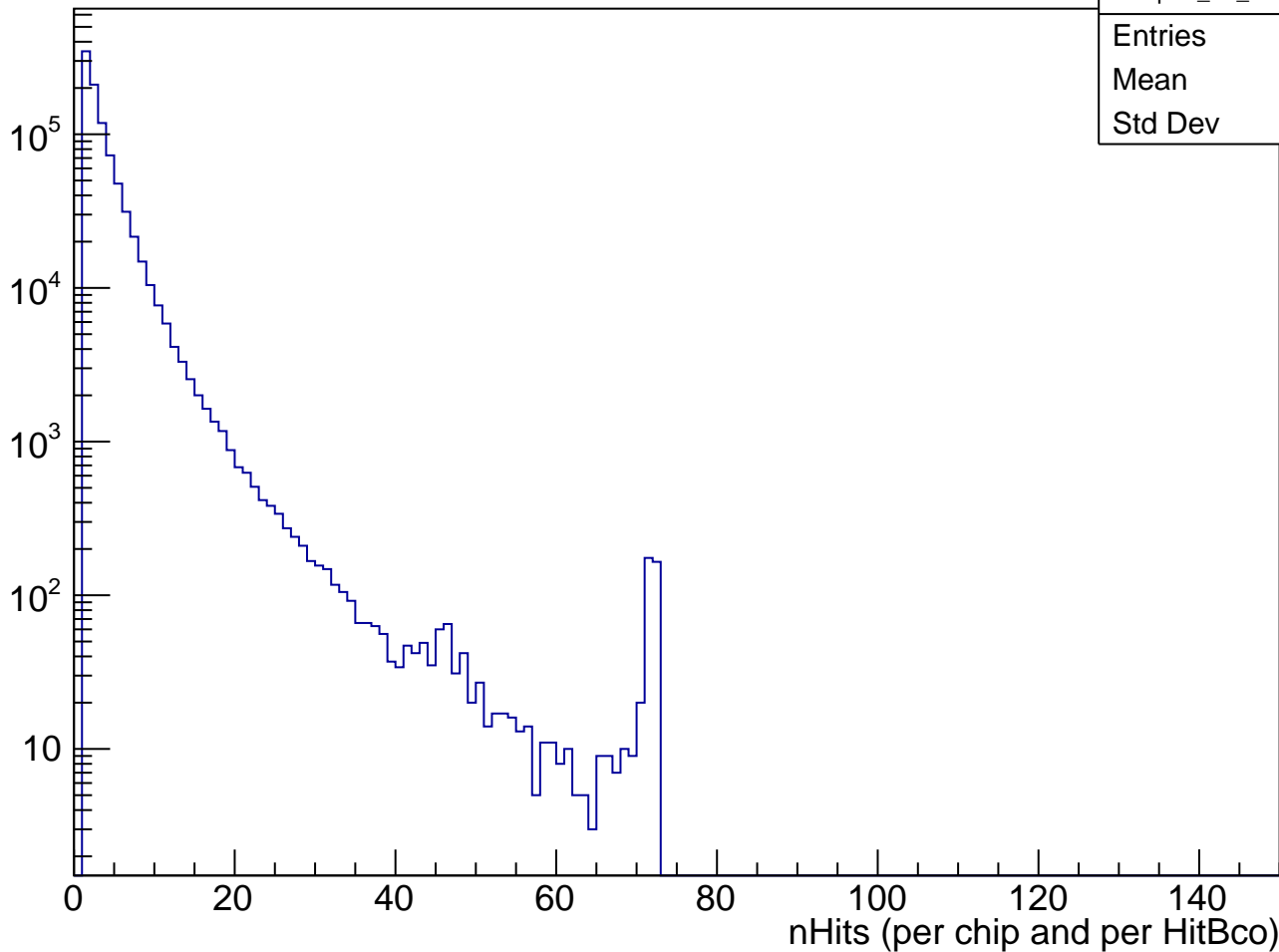
Entries



nChipHit_F2_Fch13_Chip4	
Entries	1047670
Mean	3.309
Std Dev	3.871

nChipHit_F2_Fch13_Chip5

Entries



nChipHit_F2_Fch13_Chip5

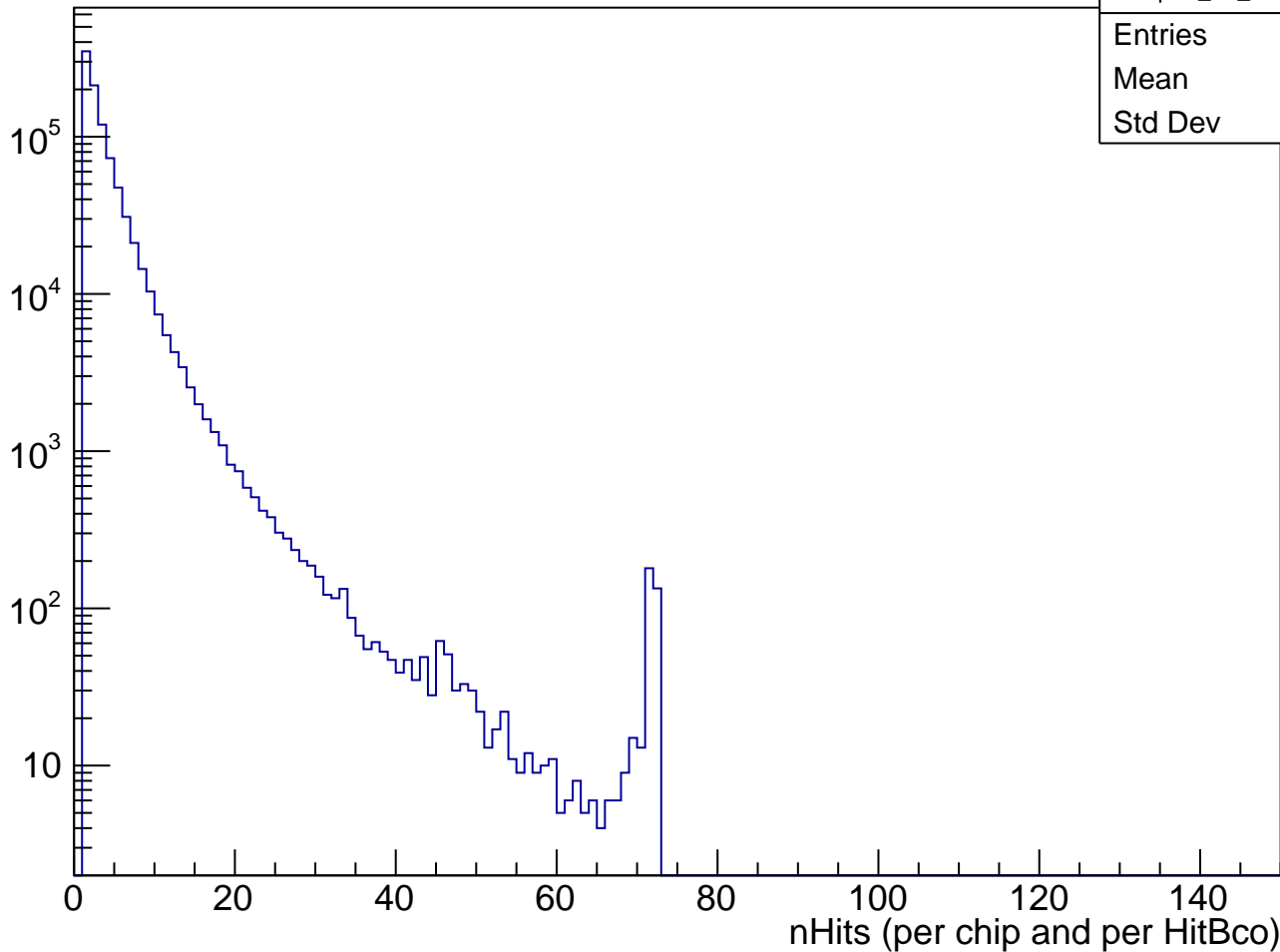
Entries 910460

Mean 3.039

Std Dev 3.6

nChipHit_F2_Fch13_Chip6

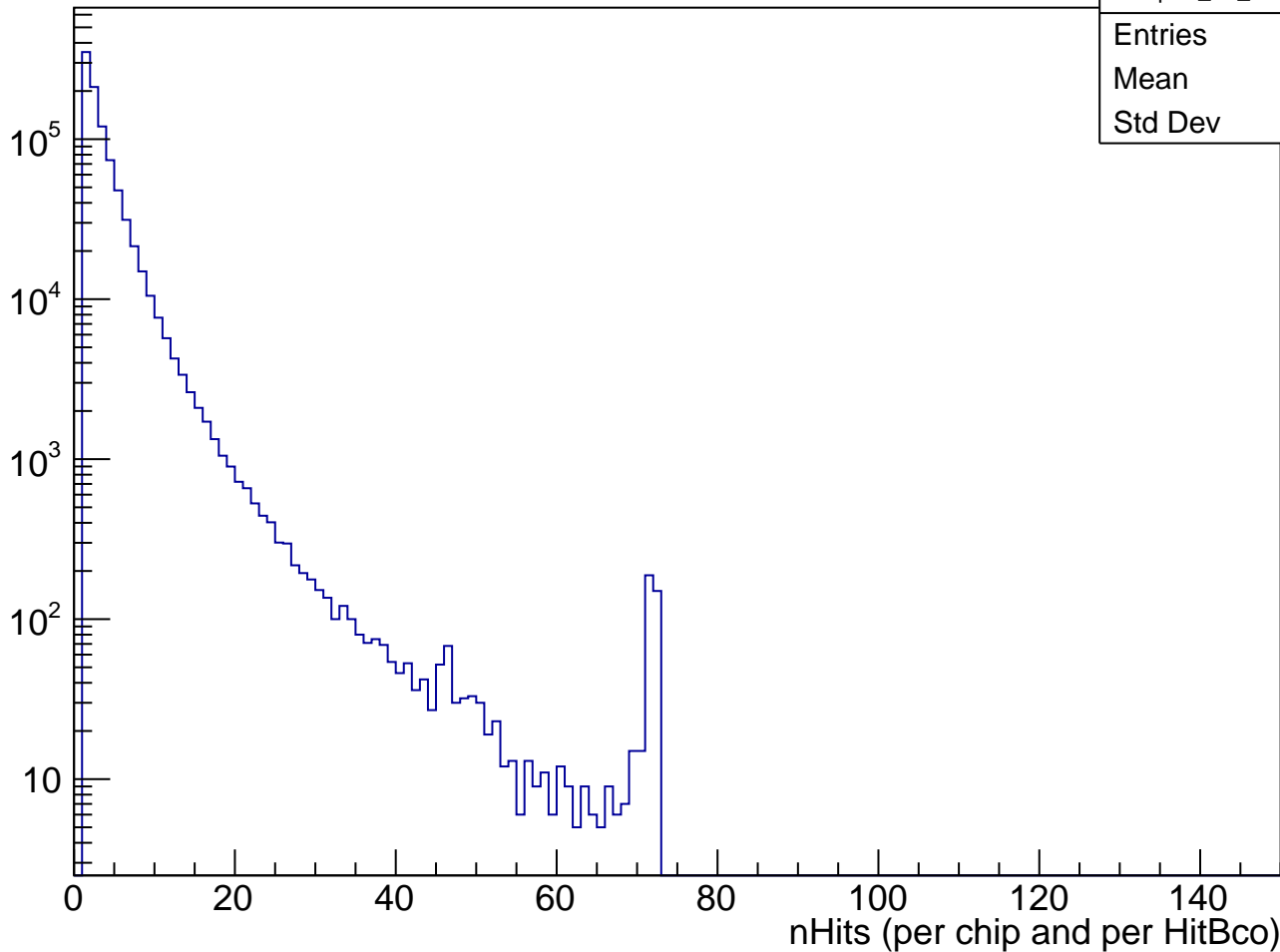
Entries



nChipHit_F2_Fch13_Chip6	
Entries	914193
Mean	3.013
Std Dev	3.545

nChipHit_F2_Fch13_Chip7

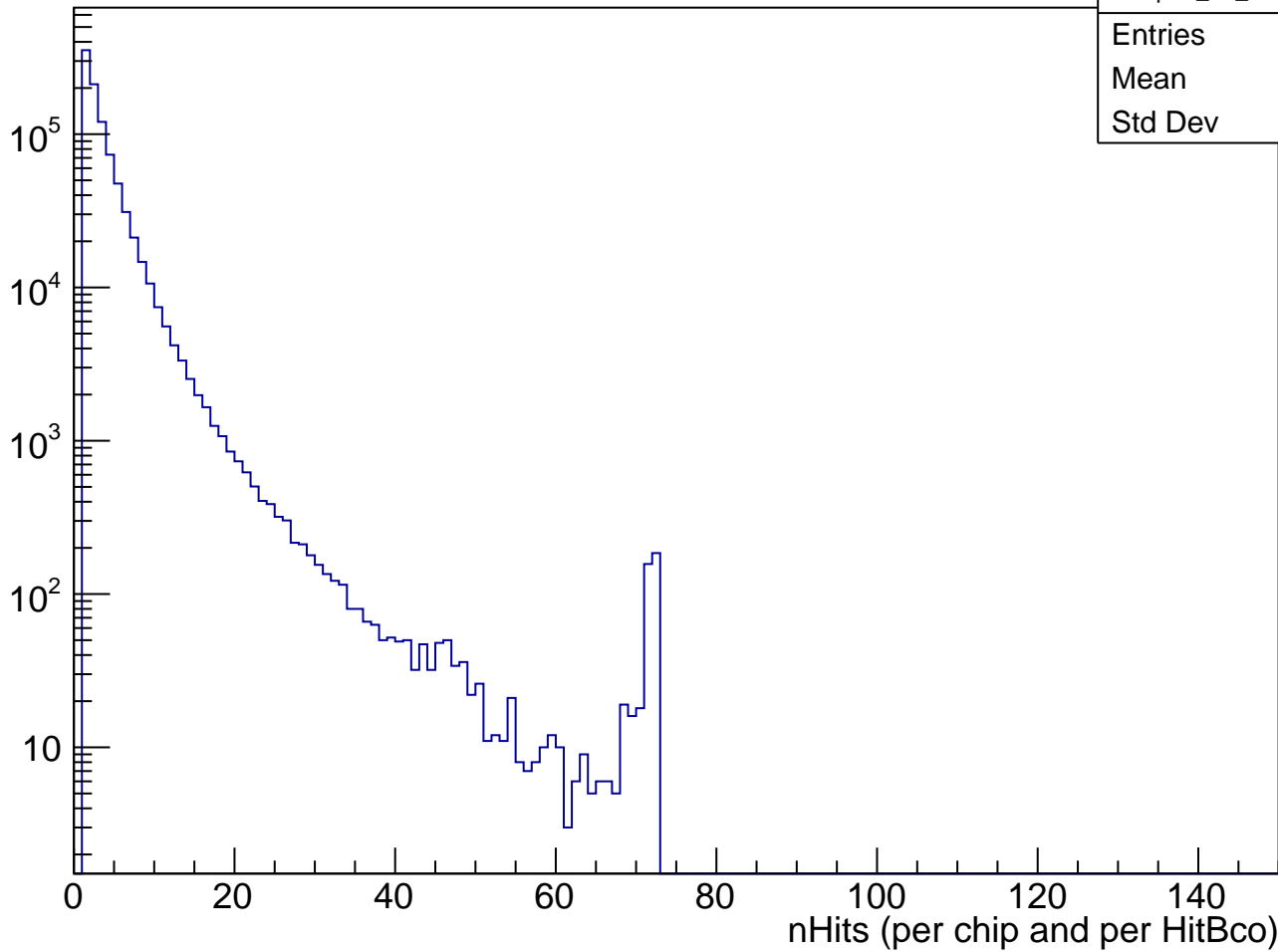
Entries



nChipHit_F2_Fch13_Chip7	
Entries	918959
Mean	3.036
Std Dev	3.598

nChipHit_F2_Fch13_Chip8

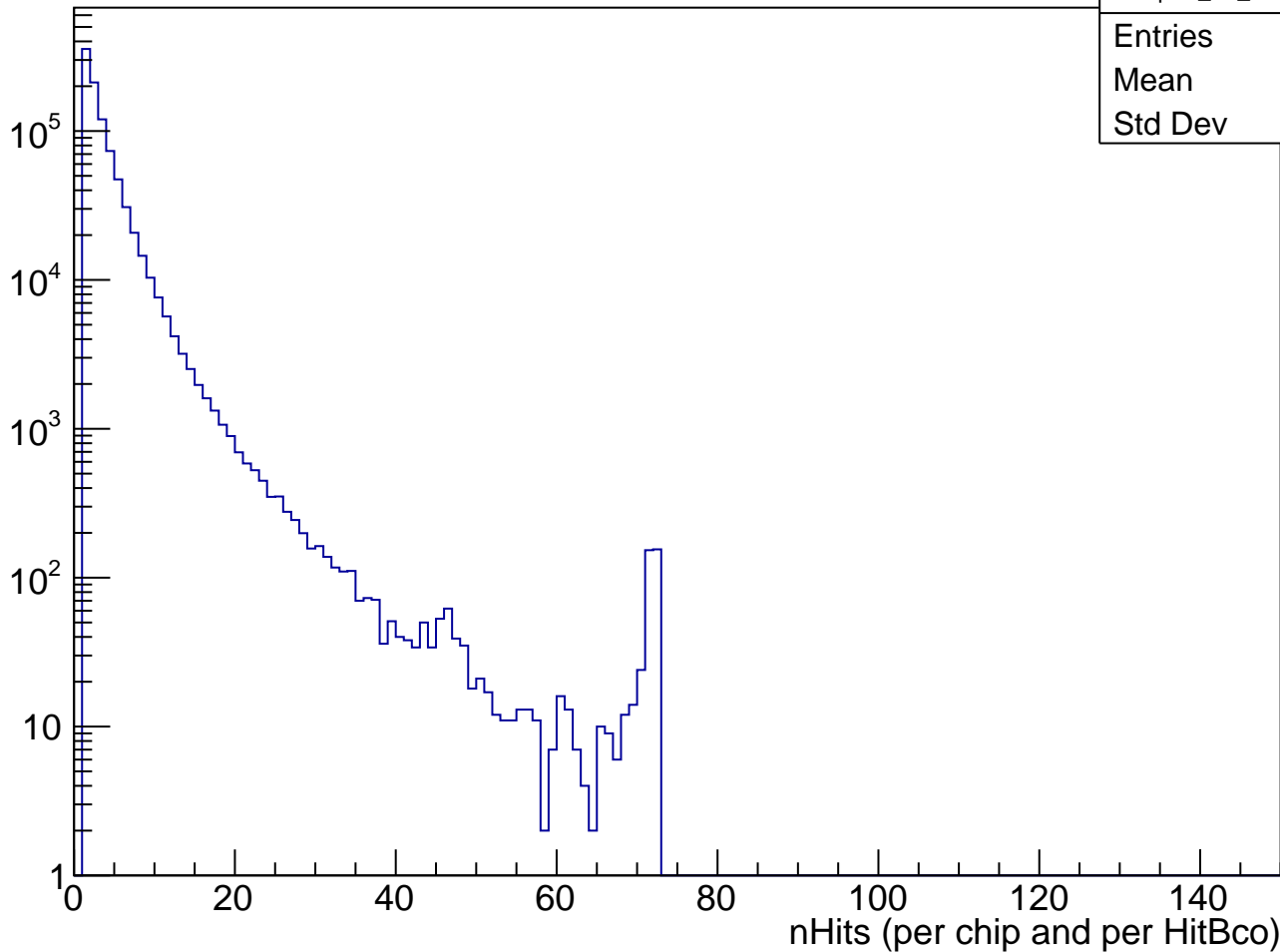
Entries



nChipHit_F2_Fch13_Chip8	
Entries	919202
Mean	3.014
Std Dev	3.571

nChipHit_F2_Fch13_Chip9

Entries

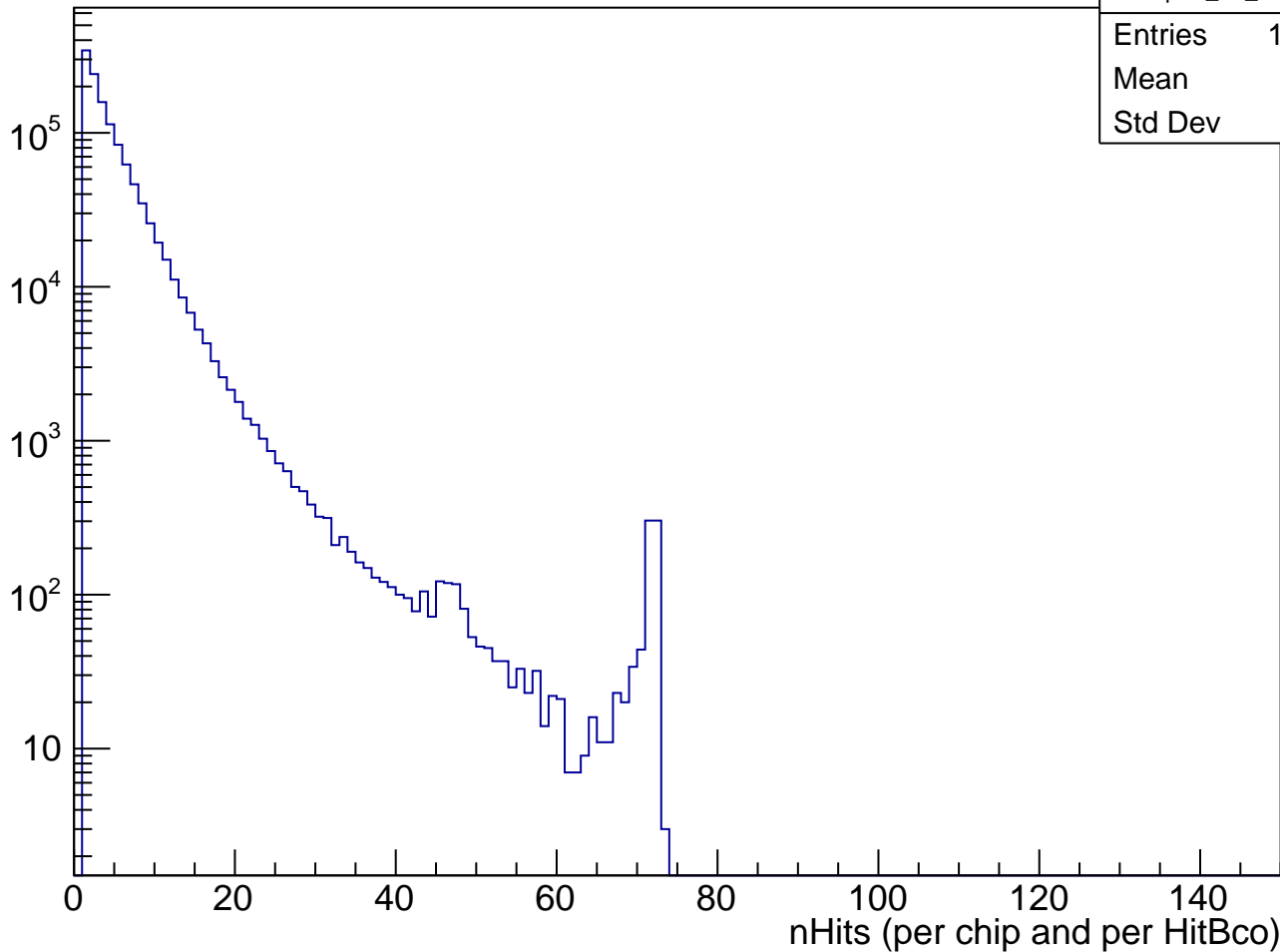


nChipHit_F2_Fch13_Chip9	
Entries	920531
Mean	3.001
Std Dev	3.55

nHits (per chip and per HitBco)

nChipHit_F2_Fch1_Chip0

Entries



nChipHit_F2_Fch1_Chip0

Entries 1201048

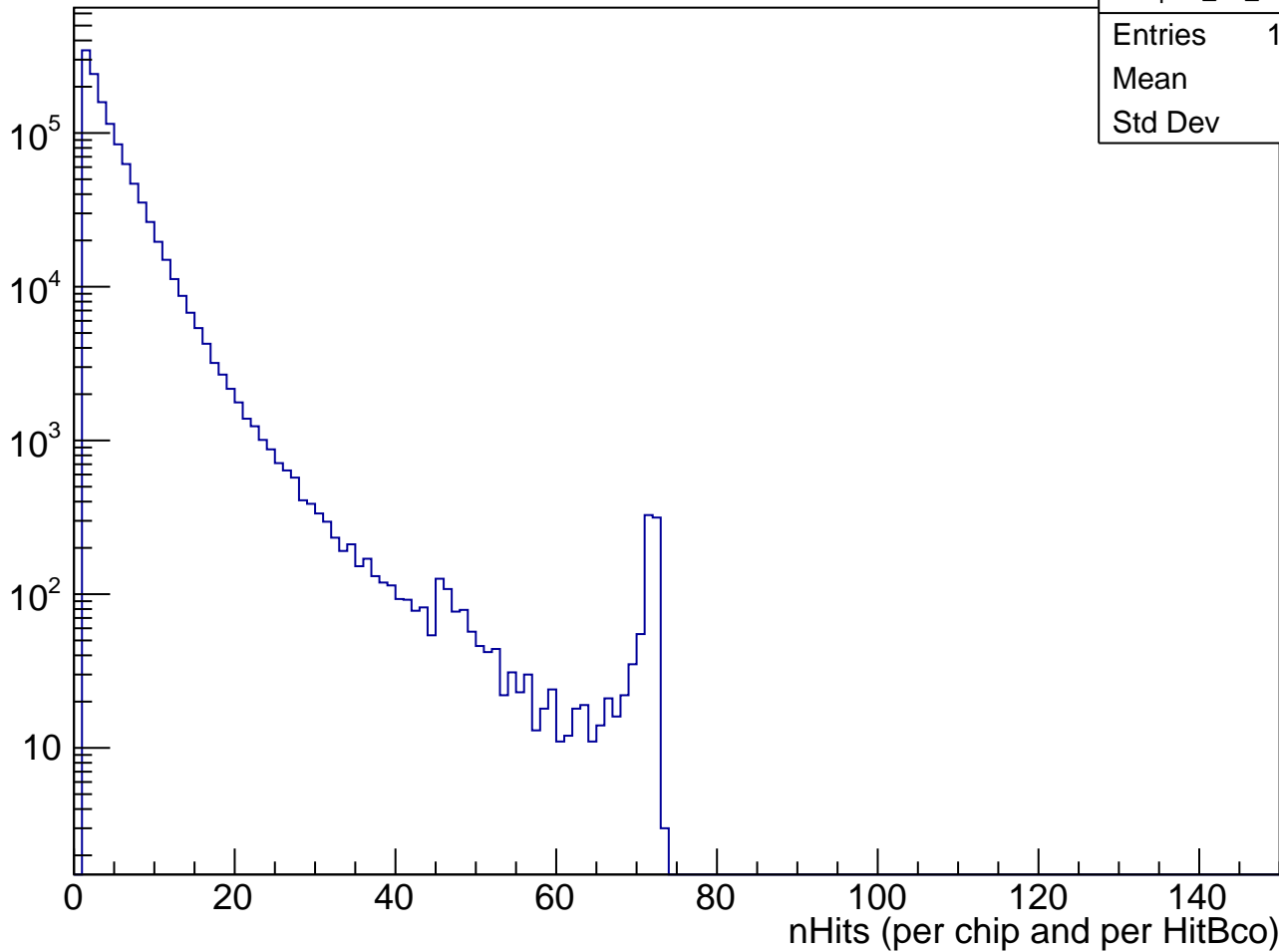
Mean 3.965

Std Dev 4.419

nHits (per chip and per HitBco)

nChipHit_F2_Fch1_Chip1

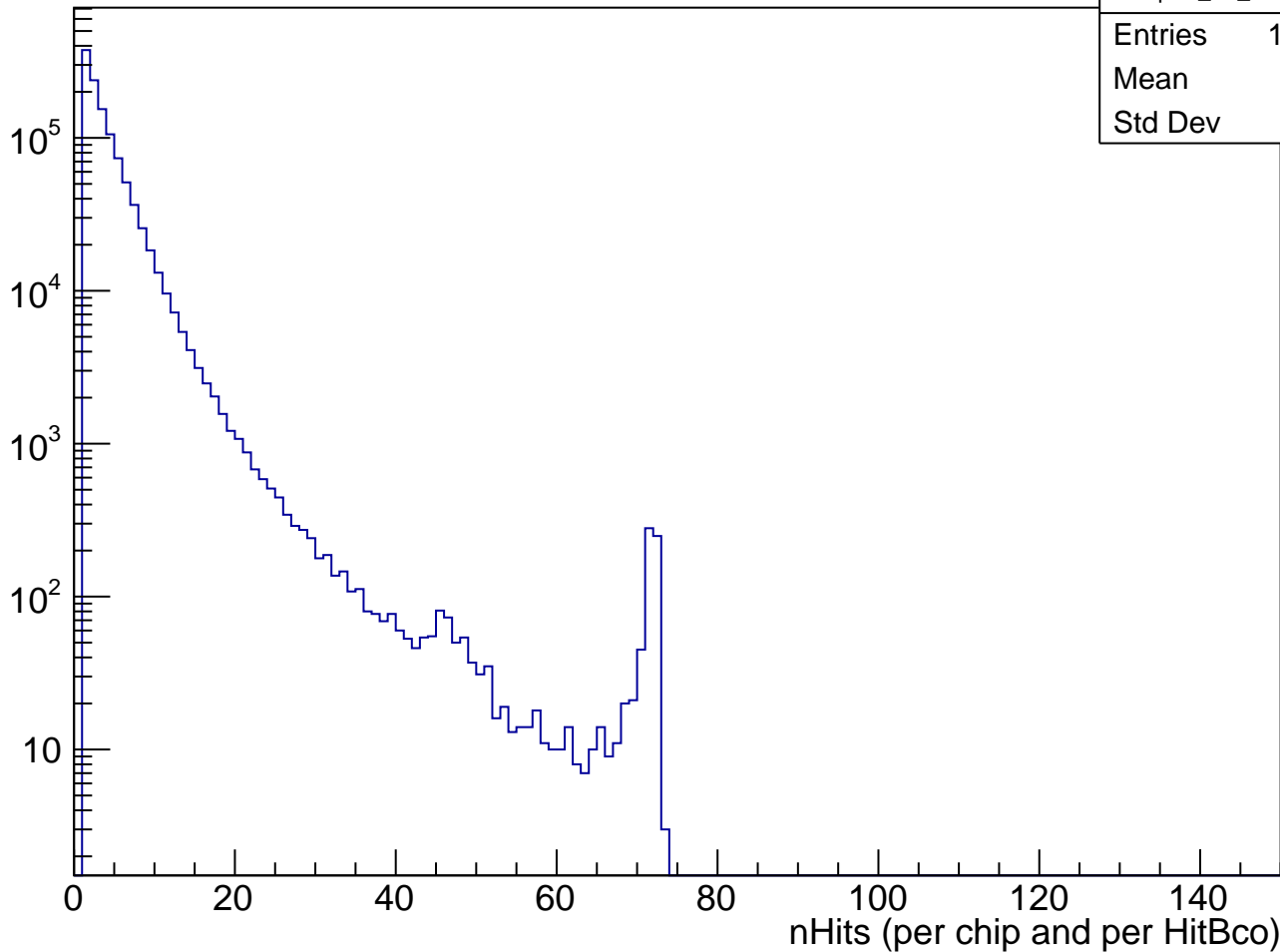
Entries



nChipHit_F2_Fch1_Chip1	
Entries	1208115
Mean	3.968
Std Dev	4.421

nChipHit_F2_Fch1_Chip10

Entries

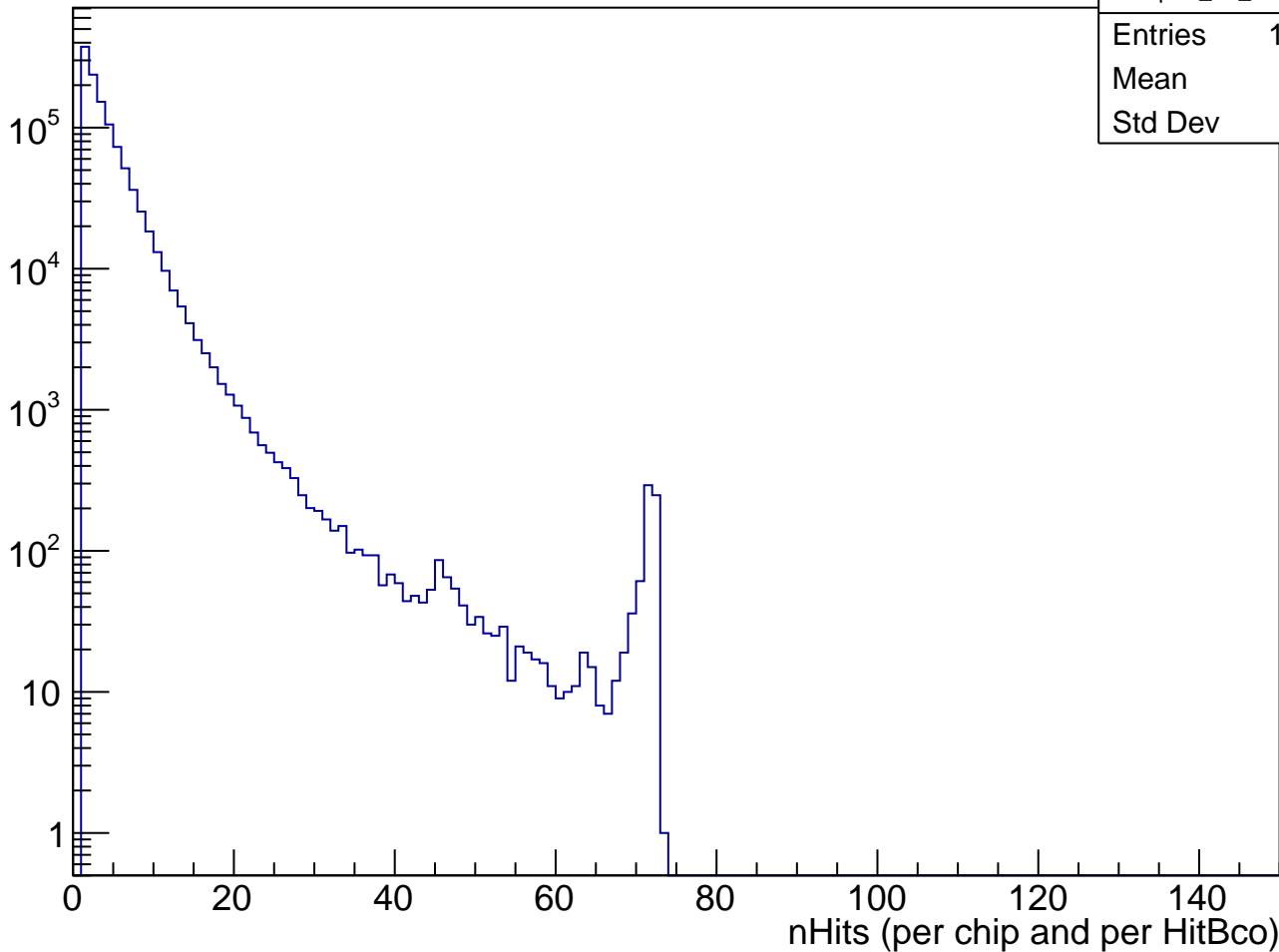


nChipHit_F2_Fch1_Chip10	
Entries	1136106
Mean	3.438
Std Dev	3.866

nHits (per chip and per HitBco)

nChipHit_F2_Fch1_Chip11

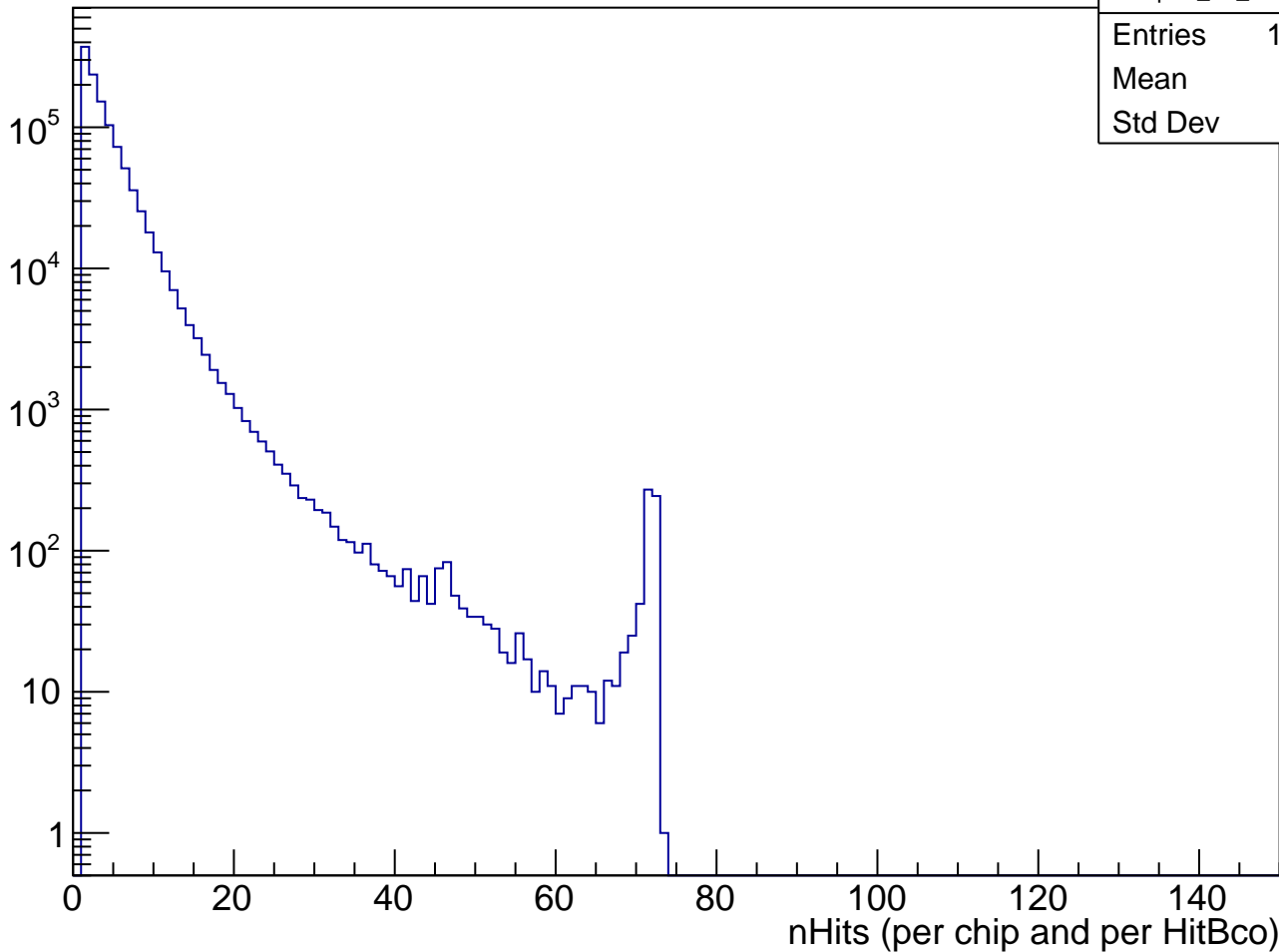
Entries



nChipHit_F2_Fch1_Chip11	
Entries	1131593
Mean	3.441
Std Dev	3.891

nChipHit_F2_Fch1_Chip12

Entries

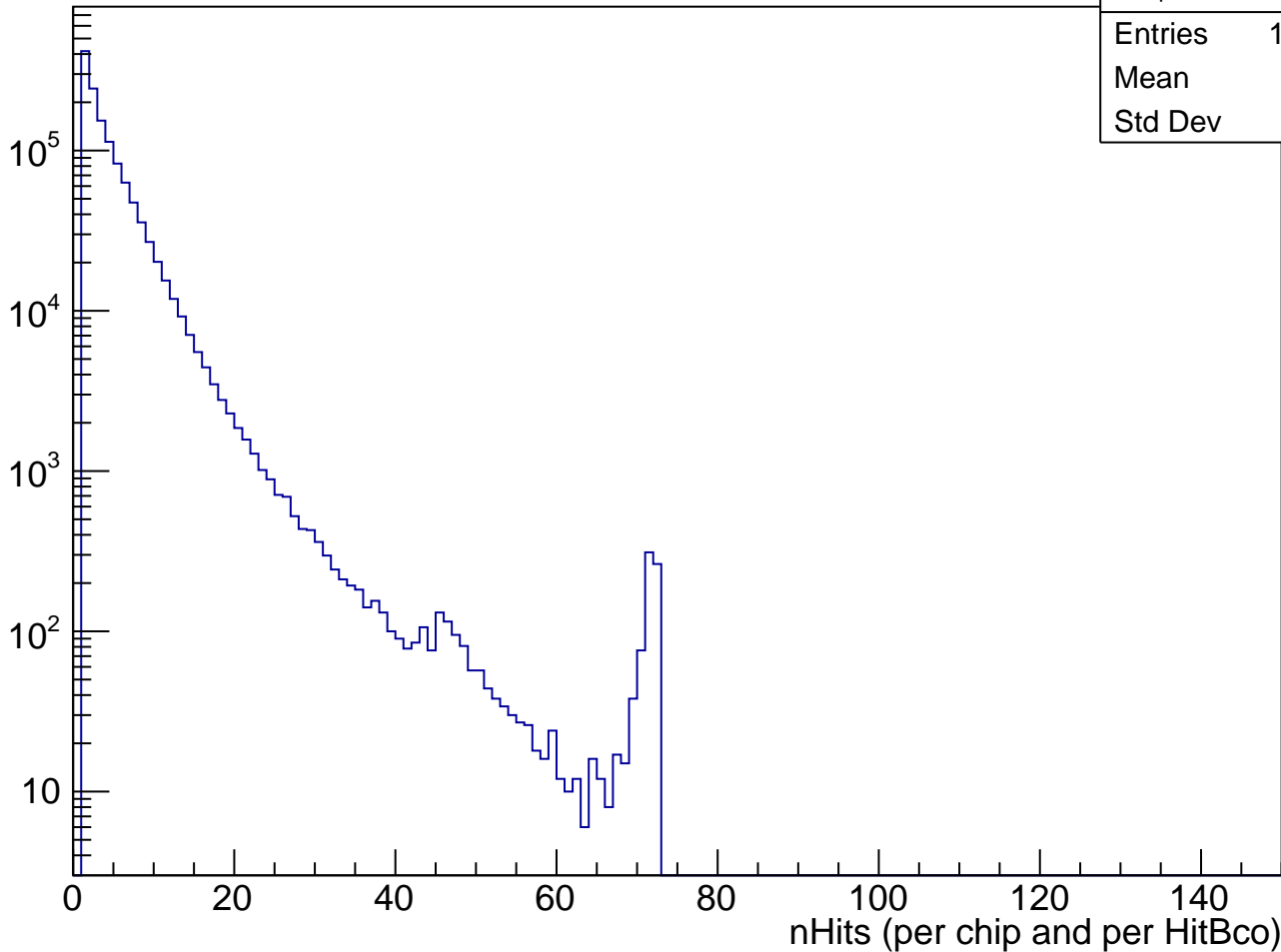


nChipHit_F2_Fch1_Chip12	
Entries	1123662
Mean	3.433
Std Dev	3.863

nHits (per chip and per HitBco)

nChipHit_F2_Fch1_Chip13

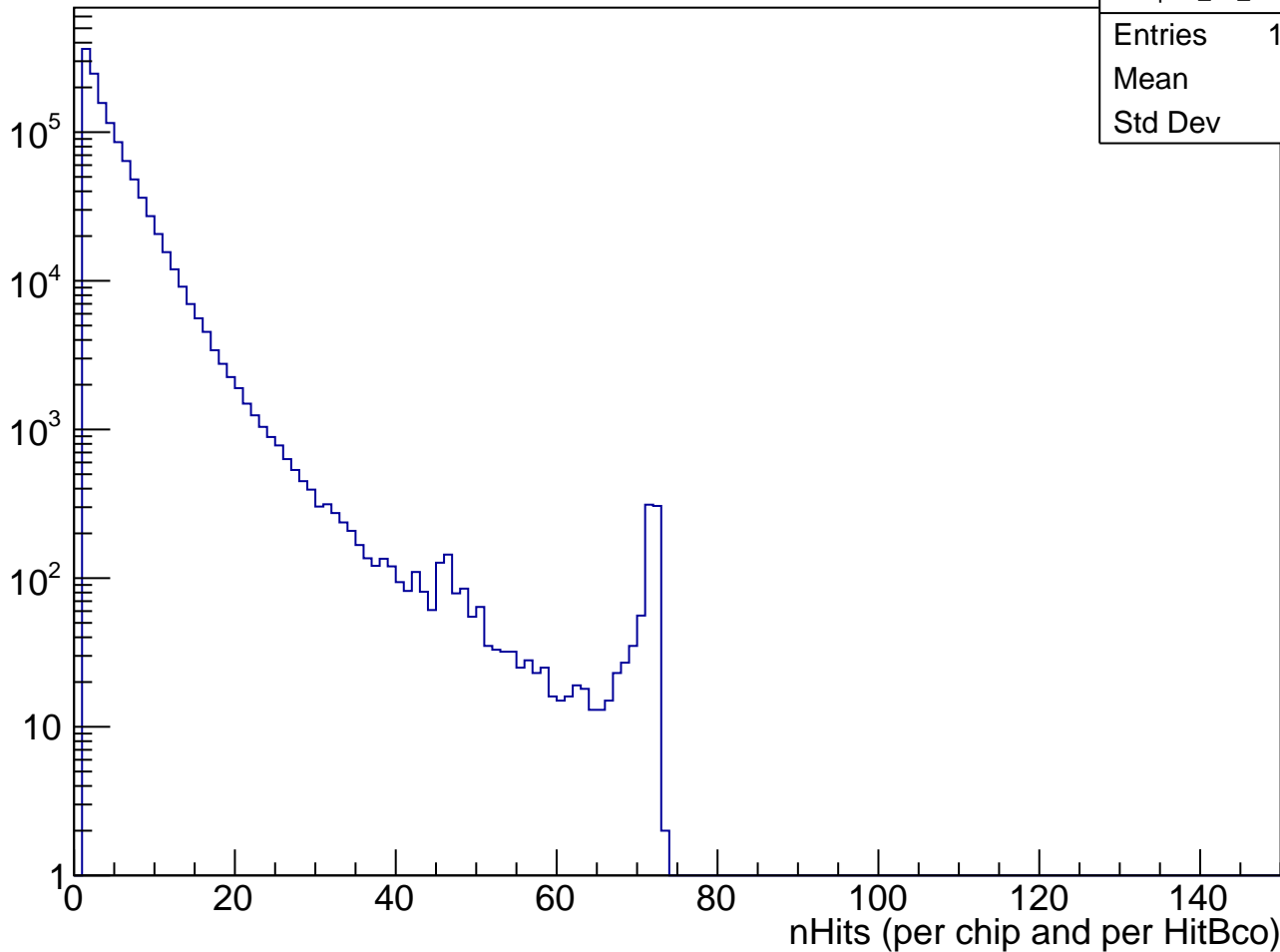
Entries



nChipHit_F2_Fch1_Chip13	
Entries	1279399
Mean	3.836
Std Dev	4.38

nChipHit_F2_Fch1_Chip14

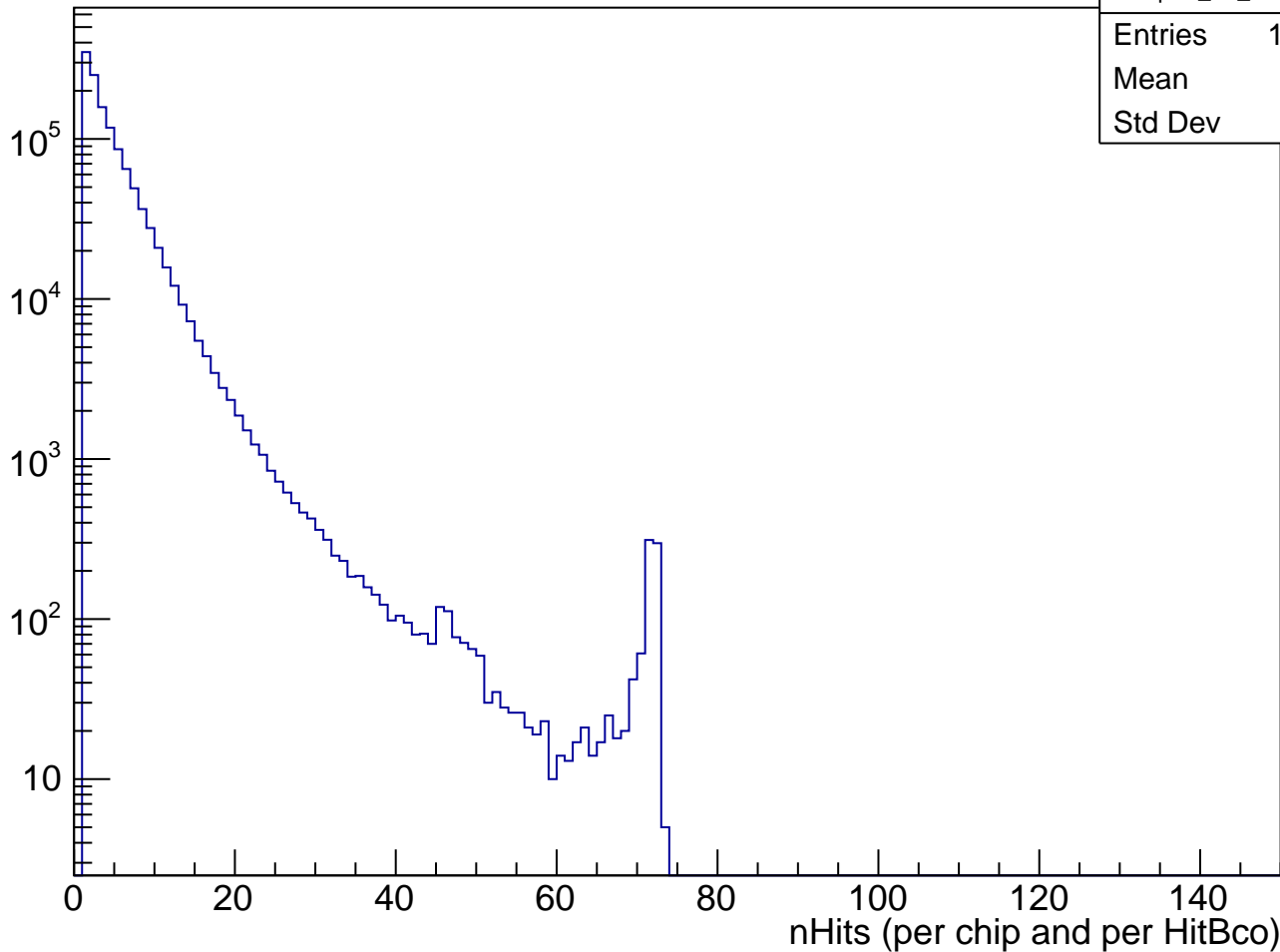
Entries



nChipHit_F2_Fch1_Chip14	
Entries	1240406
Mean	3.967
Std Dev	4.439

nChipHit_F2_Fch1_Chip15

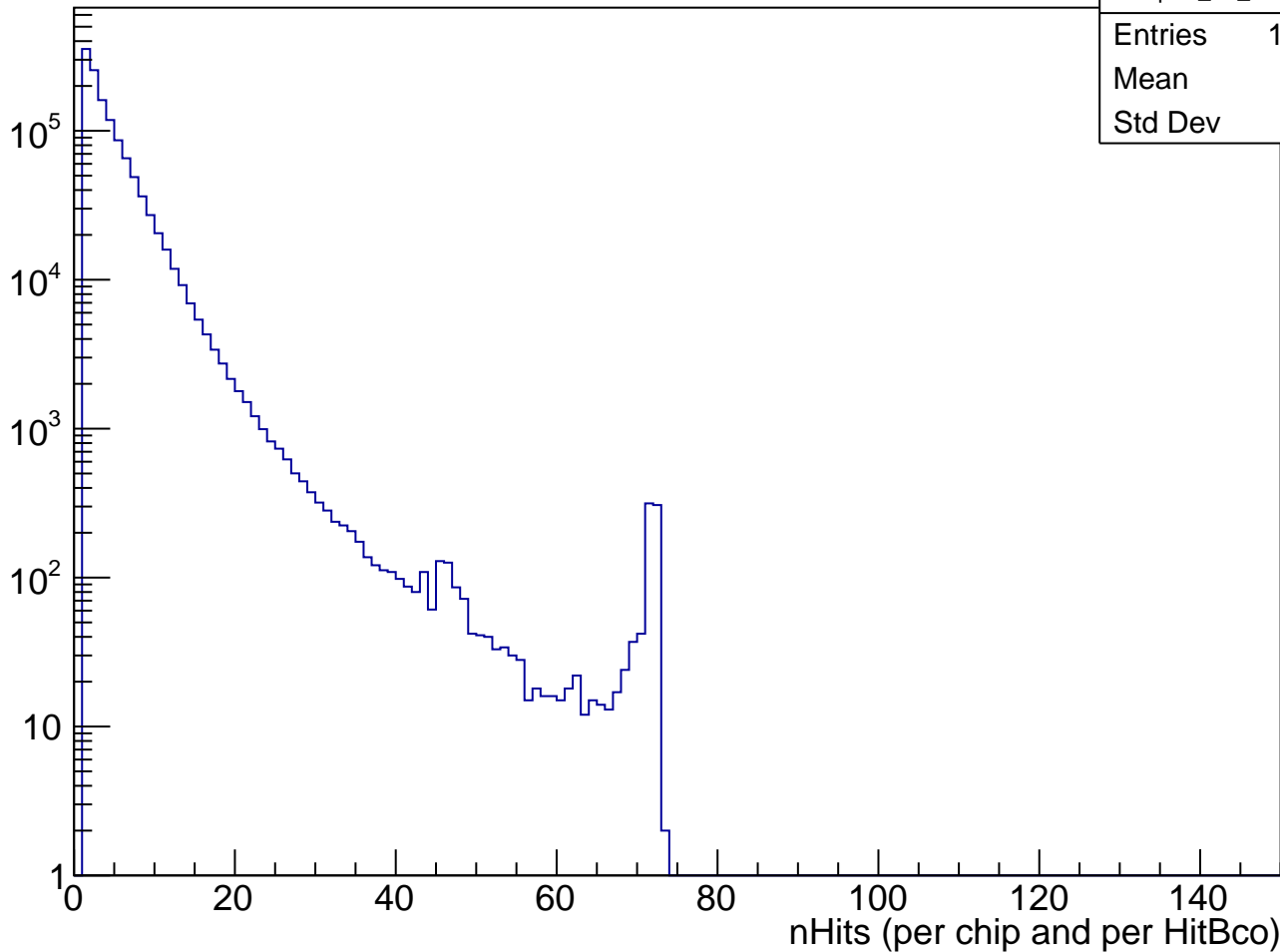
Entries



nChipHit_F2_Fch1_Chip15	
Entries	1237217
Mean	4.004
Std Dev	4.429

nChipHit_F2_Fch1_Chip16

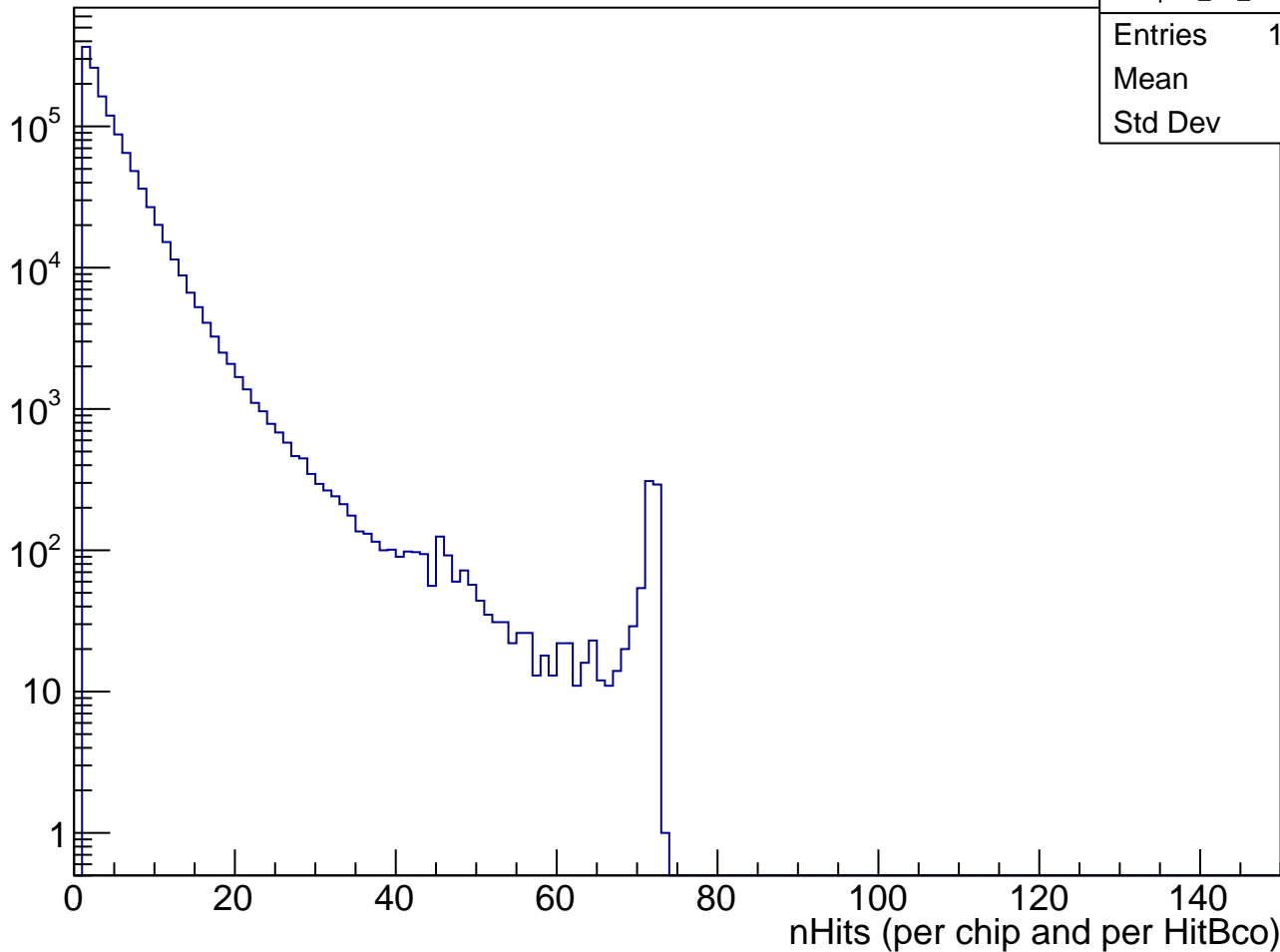
Entries



nChipHit_F2_Fch1_Chip16	
Entries	1247928
Mean	3.961
Std Dev	4.374

nChipHit_F2_Fch1_Chip17

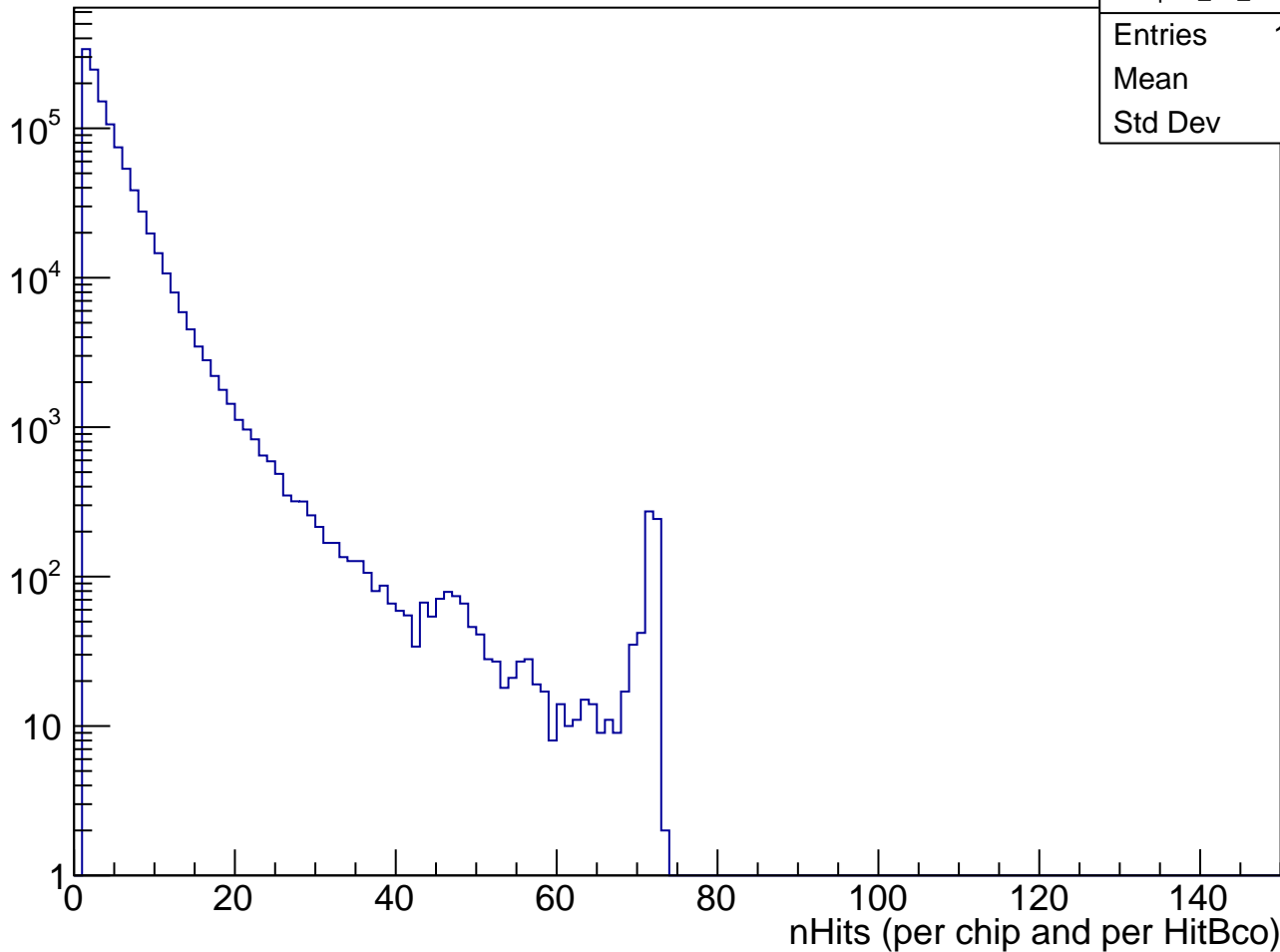
Entries



nChipHit_F2_Fch1_Chip17	
Entries	1262546
Mean	3.887
Std Dev	4.282

nChipHit_F2_Fch1_Chip18

Entries

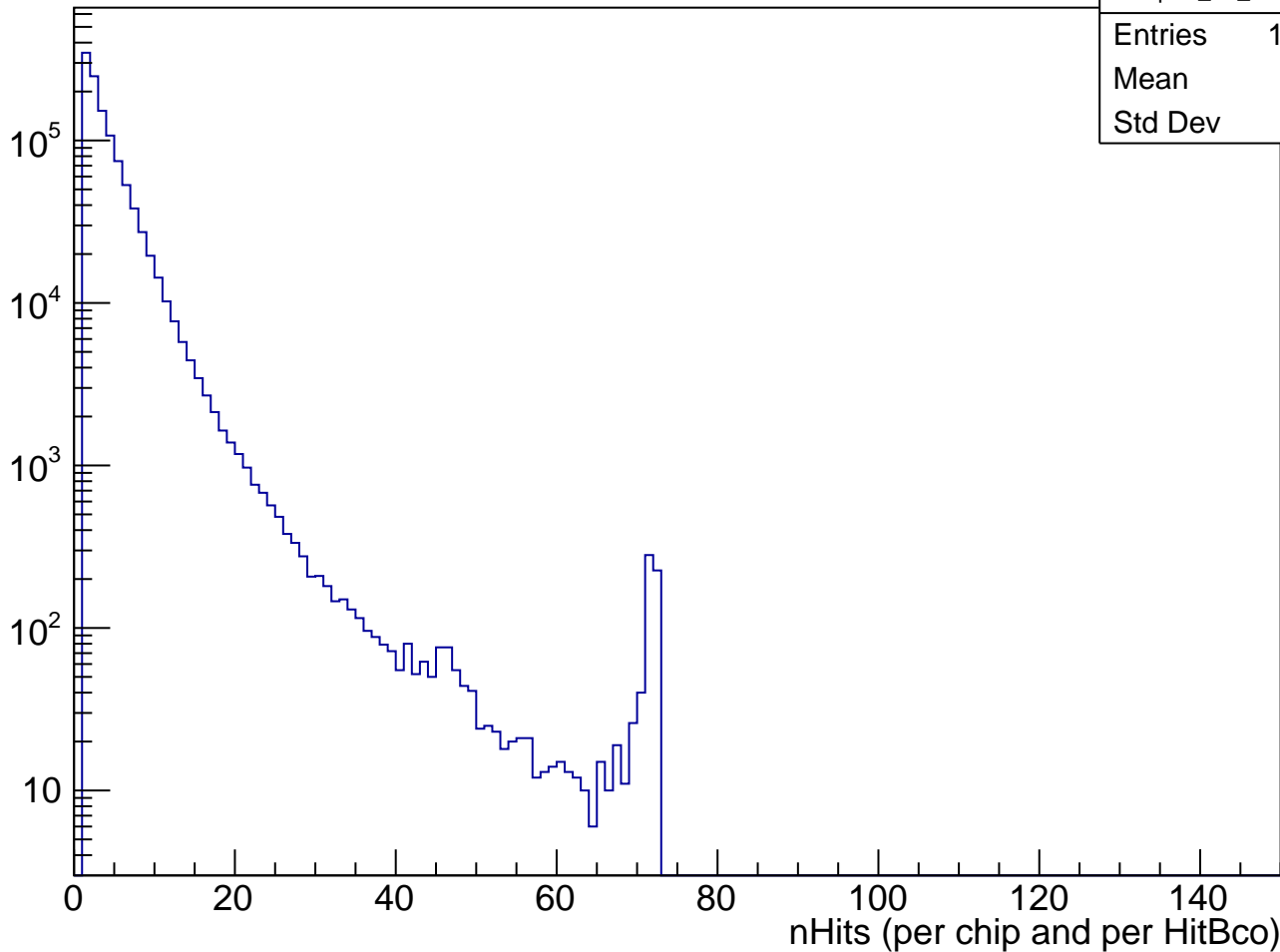


nChipHit_F2_Fch1_Chip18	
Entries	1121841
Mean	3.598
Std Dev	3.998

nHits (per chip and per HitBco)

nChipHit_F2_Fch1_Chip19

Entries

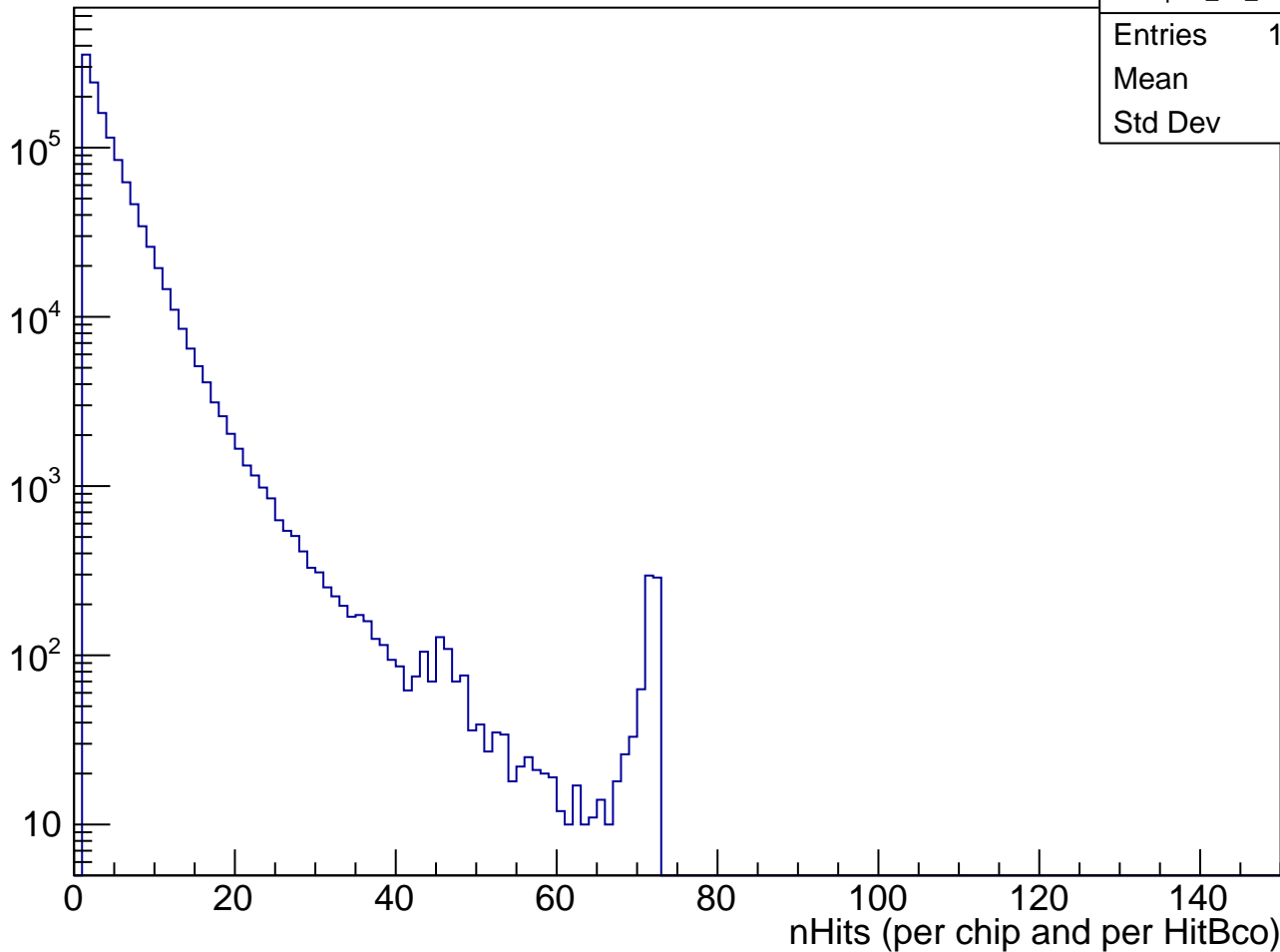


nChipHit_F2_Fch1_Chip19	
Entries	1129444
Mean	3.555
Std Dev	3.939

nHits (per chip and per HitBco)

nChipHit_F2_Fch1_Chip2

Entries



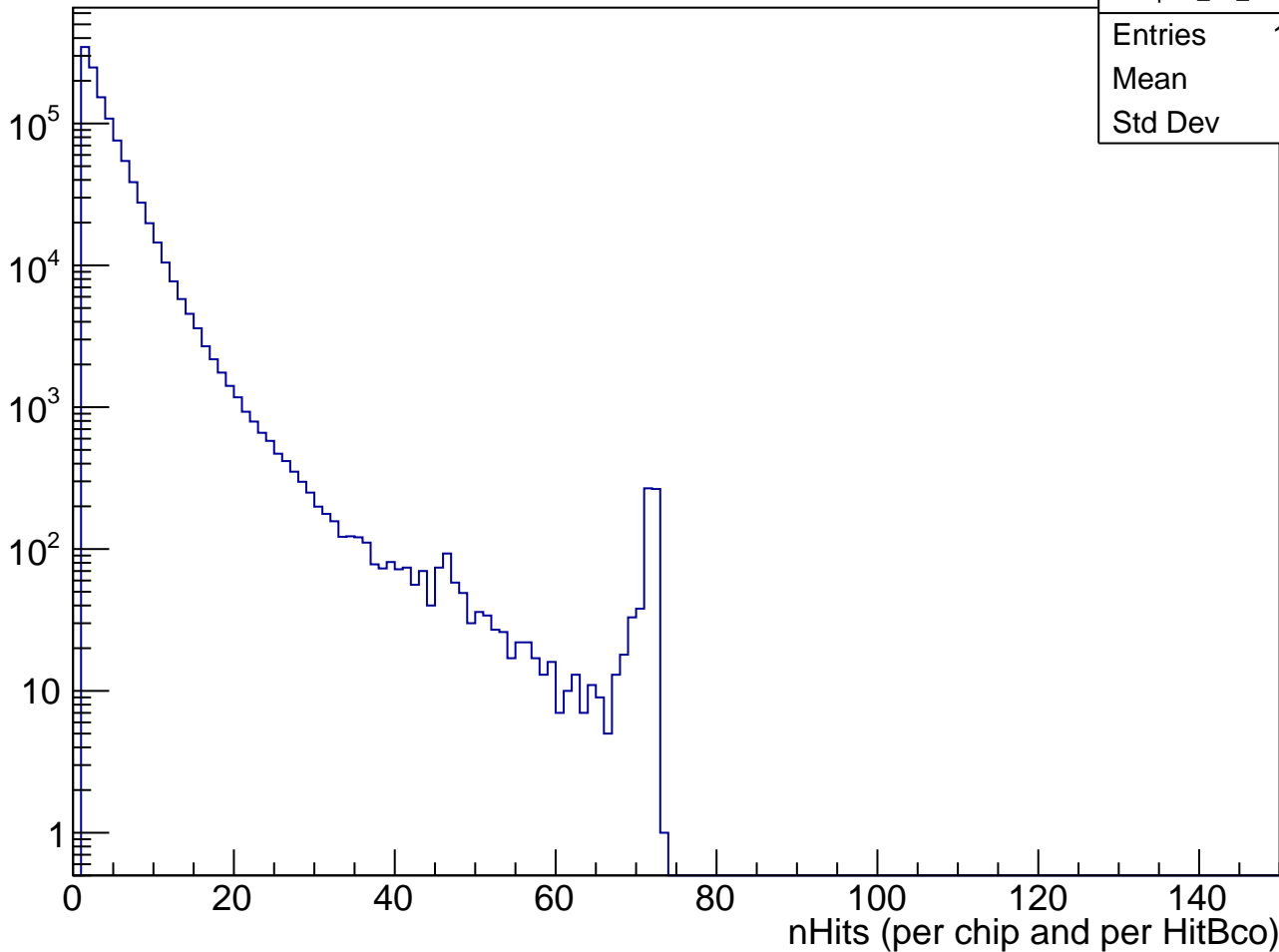
nChipHit_F2_Fch1_Chip2

Entries	1214598
Mean	3.901
Std Dev	4.32

nHits (per chip and per HitBco)

nChipHit_F2_Fch1_Chip20

Entries

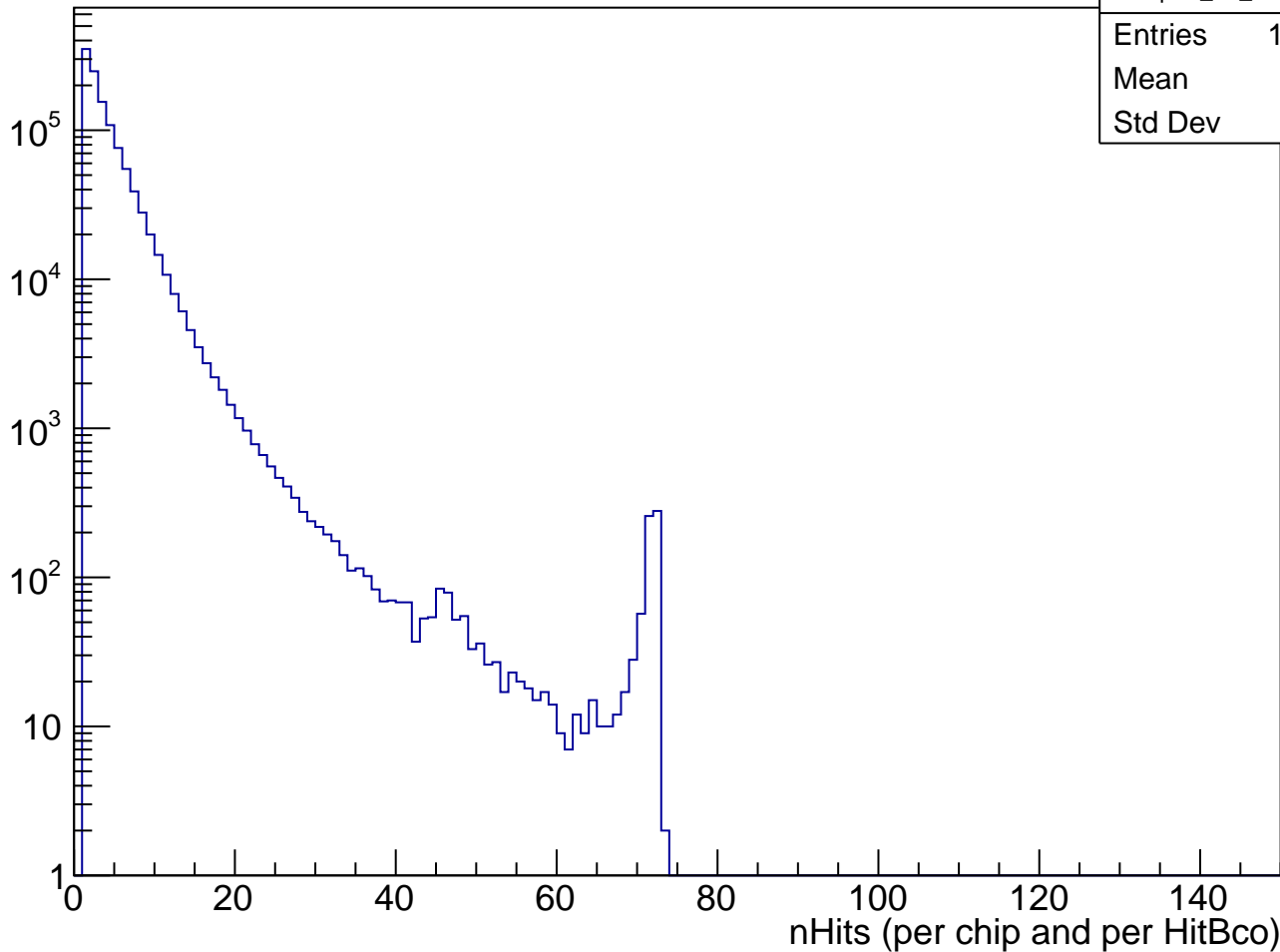


nChipHit_F2_Fch1_Chip20	
Entries	1135271
Mean	3.578
Std Dev	3.971

nHits (per chip and per HitBco)

nChipHit_F2_Fch1_Chip21

Entries

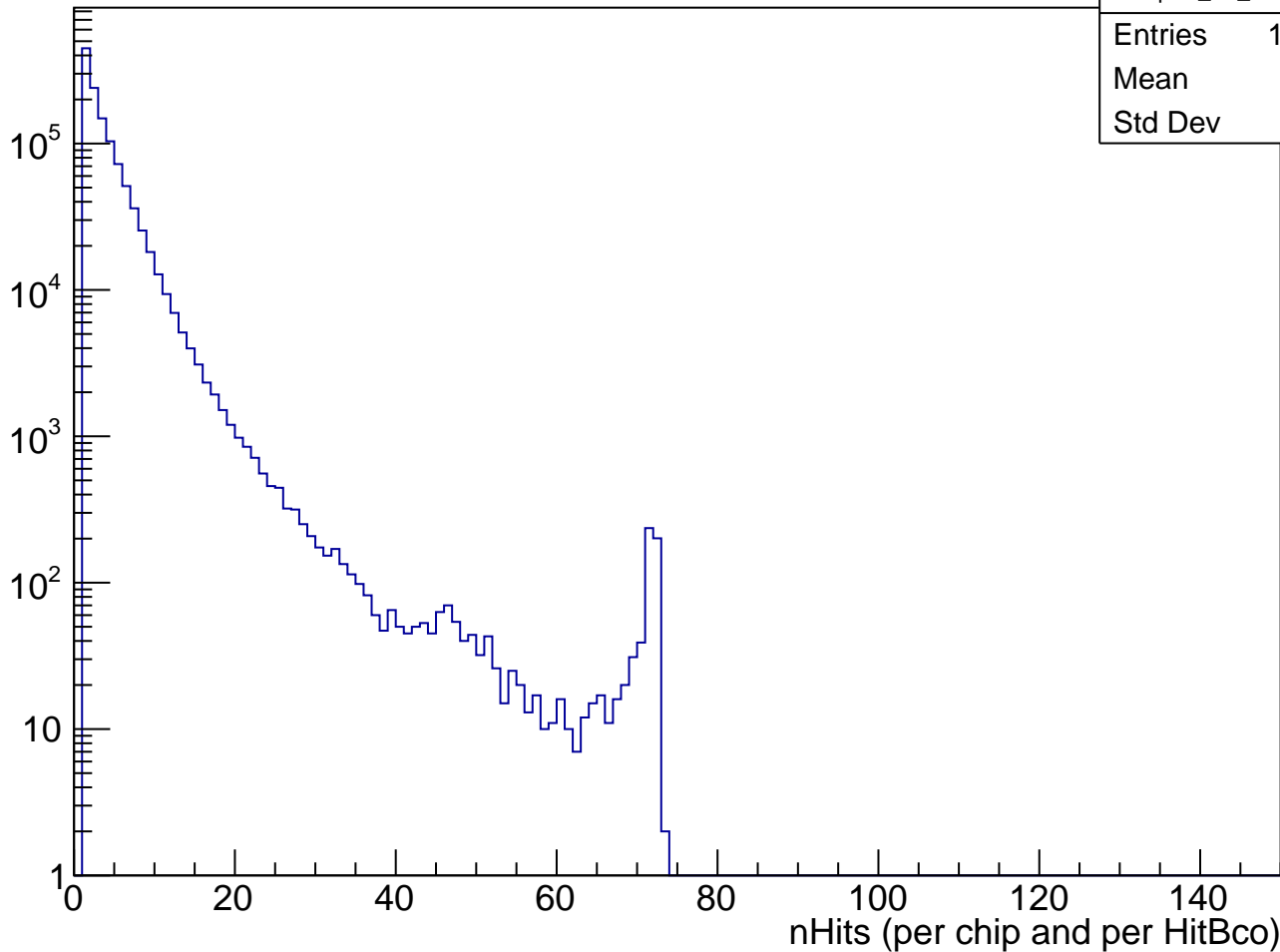


nChipHit_F2_Fch1_Chip21	
Entries	1144806
Mean	3.577
Std Dev	3.97

nHits (per chip and per HitBco)

nChipHit_F2_Fch1_Chip22

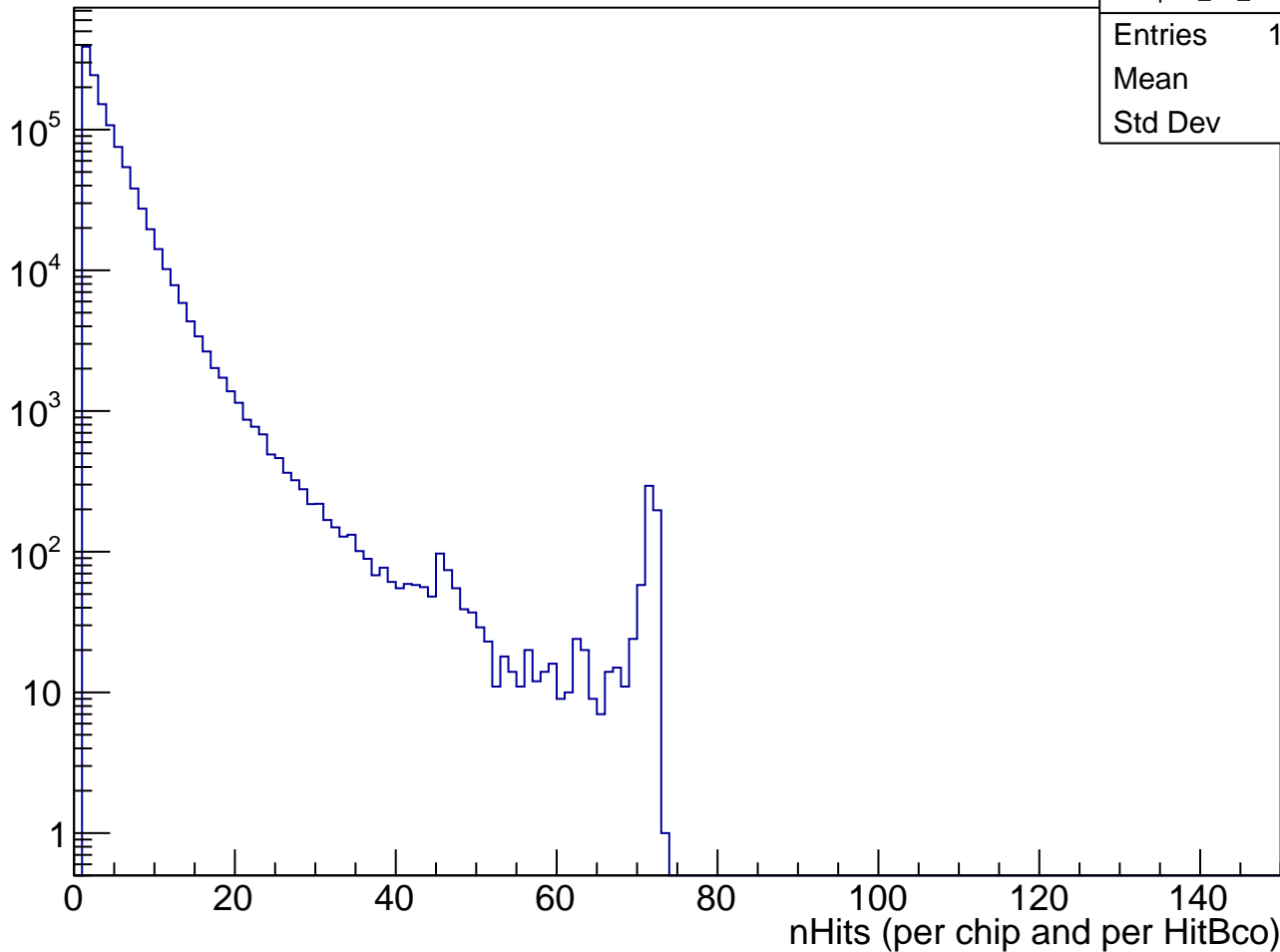
Entries



nChipHit_F2_Fch1_Chip22	
Entries	1198764
Mean	3.264
Std Dev	3.733

nChipHit_F2_Fch1_Chip23

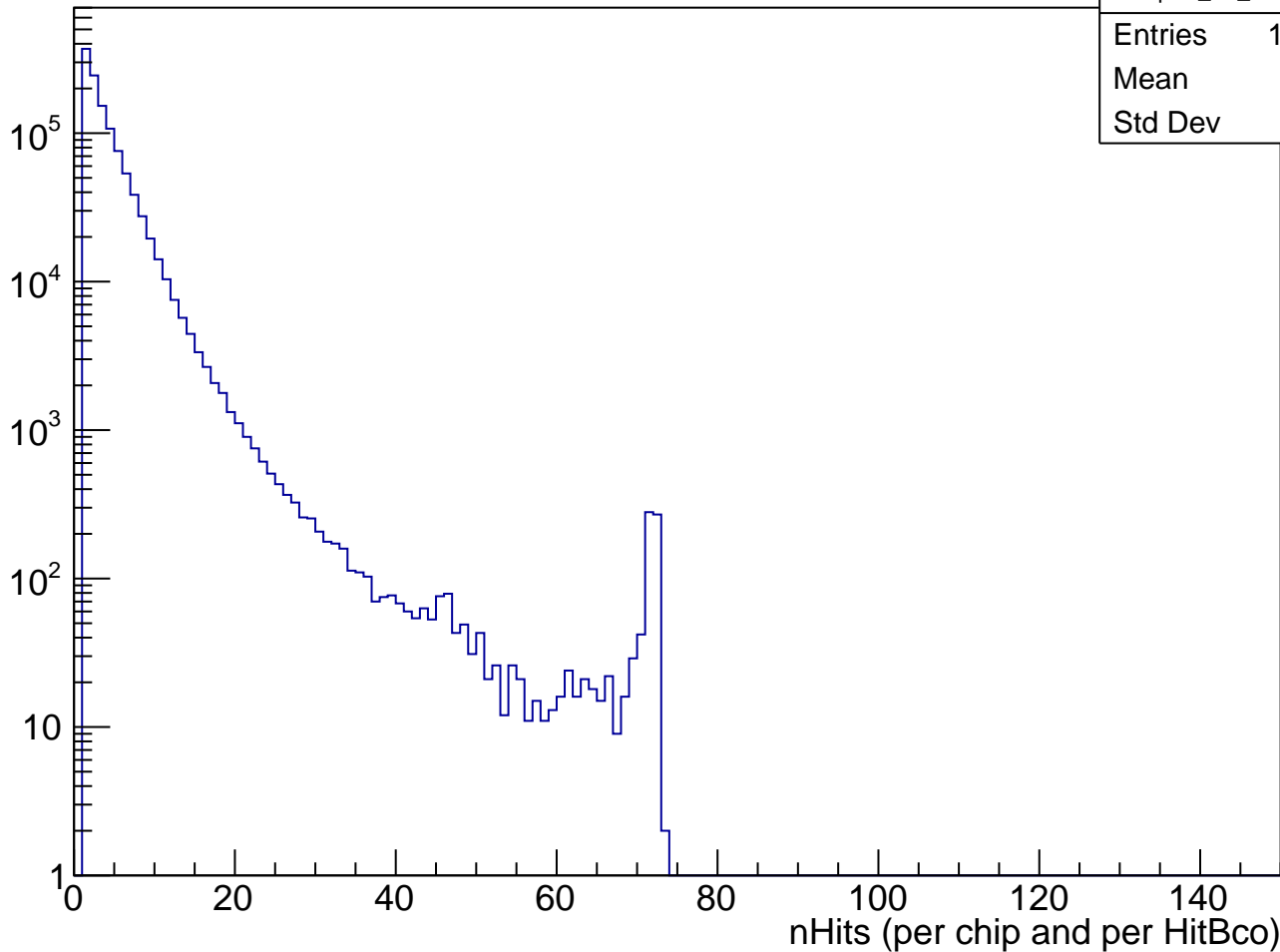
Entries



nChipHit_F2_Fch1_Chip23	
Entries	1167824
Mean	3.465
Std Dev	3.878

nChipHit_F2_Fch1_Chip24

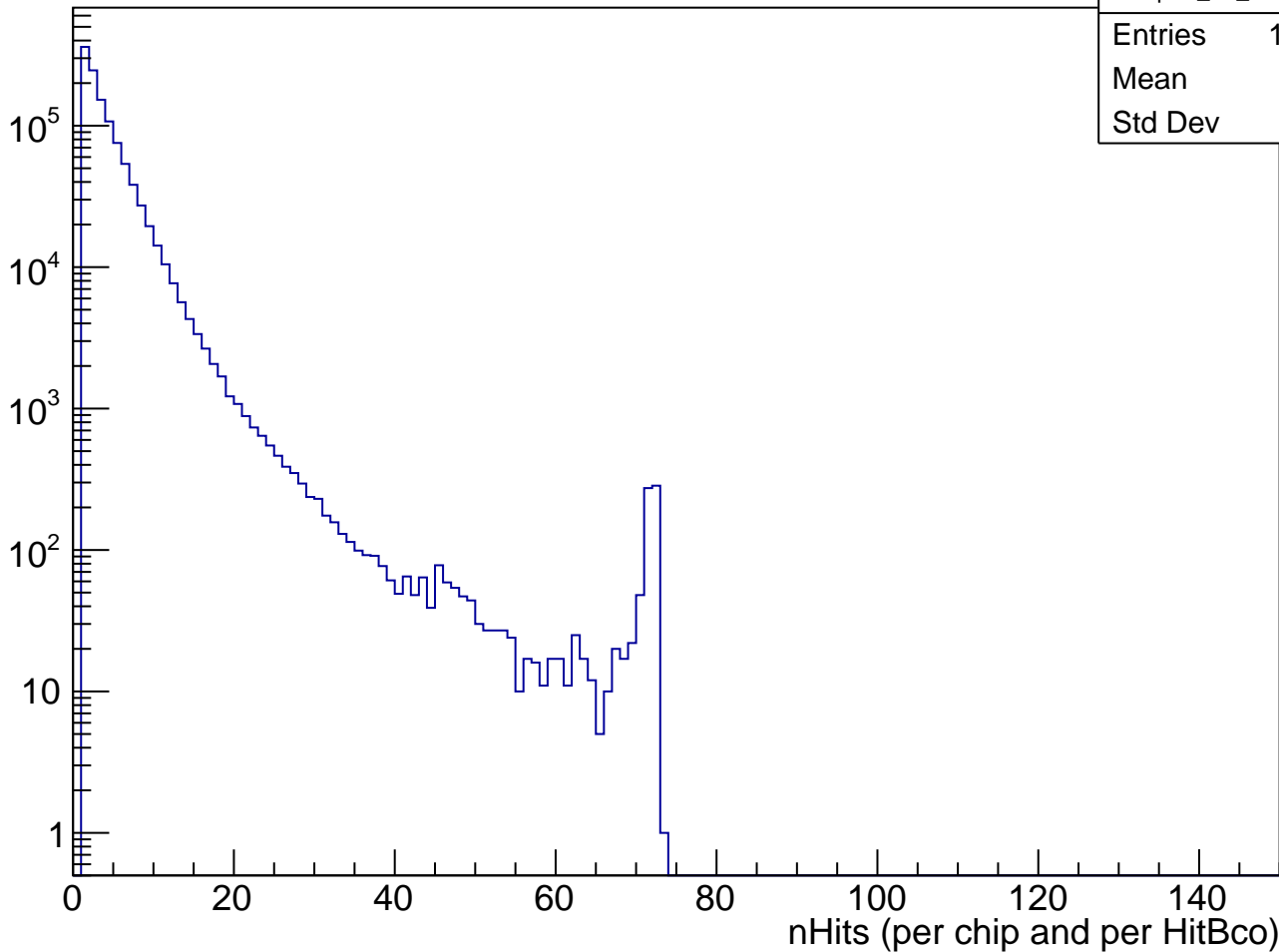
Entries



nChipHit_F2_Fch1_Chip24	
Entries	1151195
Mean	3.509
Std Dev	3.944

nChipHit_F2_Fch1_Chip25

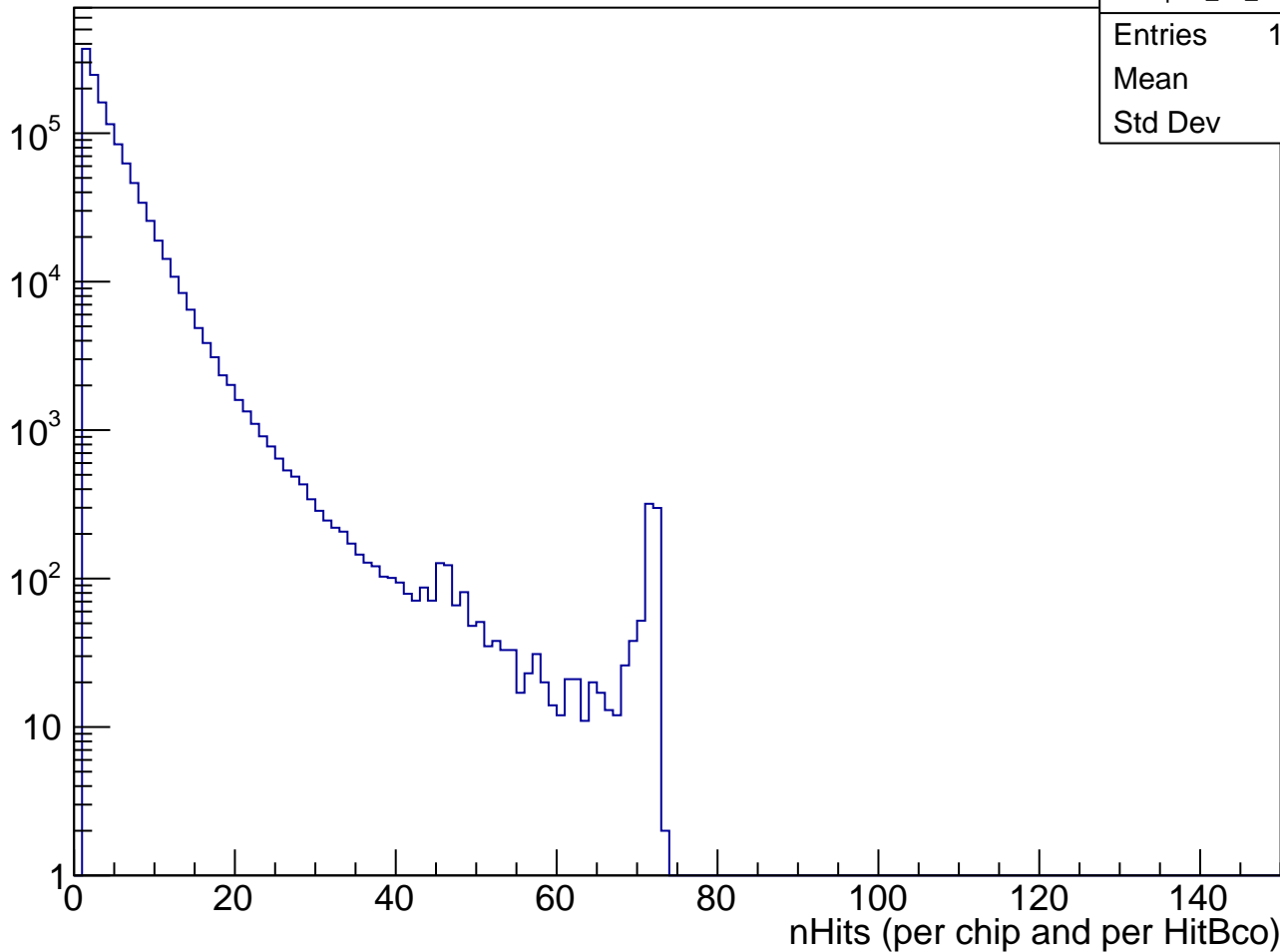
Entries



nChipHit_F2_Fch1_Chip25	
Entries	1142390
Mean	3.523
Std Dev	3.943

nChipHit_F2_Fch1_Chip3

Entries



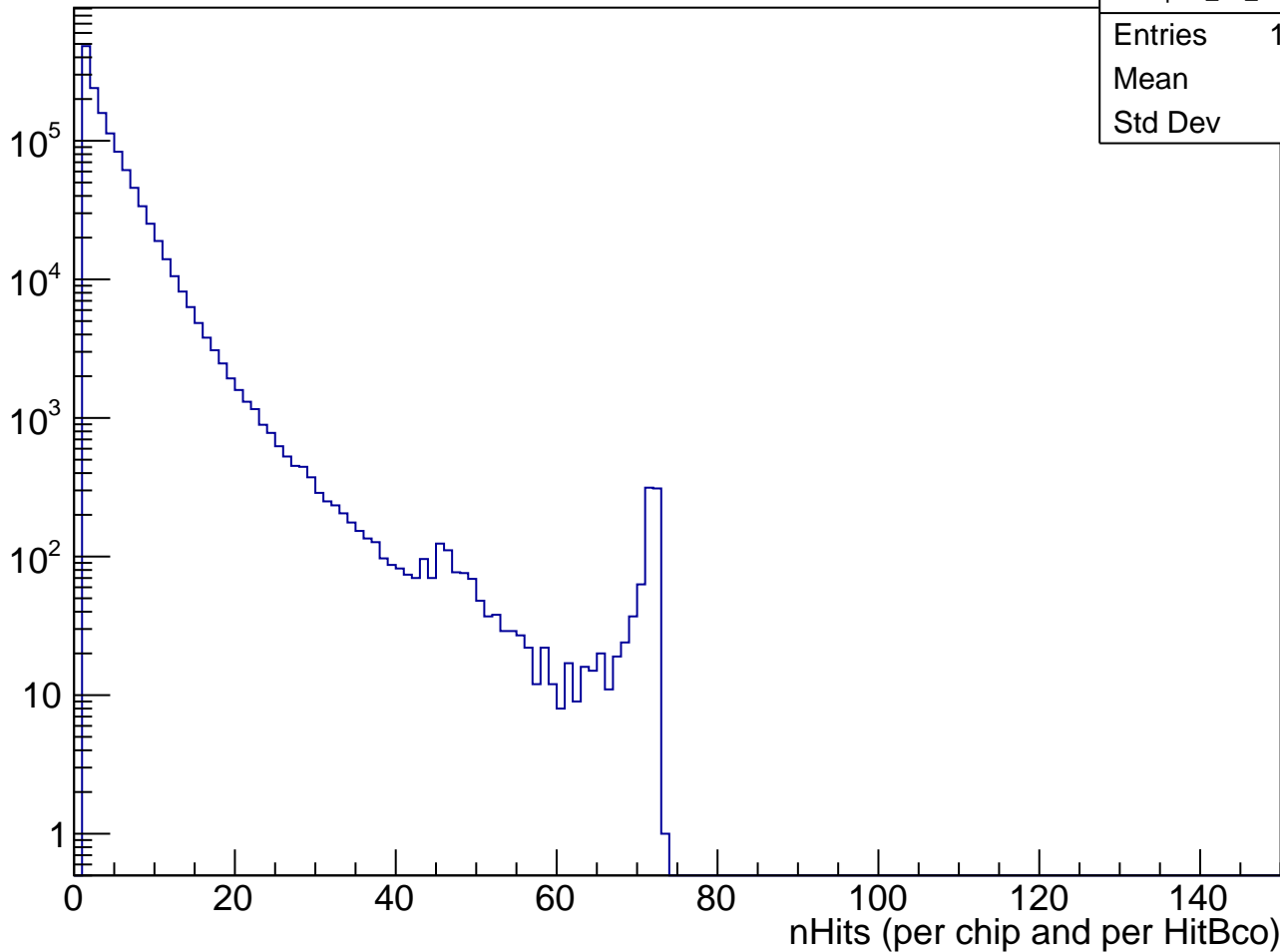
nChipHit_F2_Fch1_Chip3

Entries	1233209
Mean	3.839
Std Dev	4.301

nHits (per chip and per HitBco)

nChipHit_F2_Fch1_Chip4

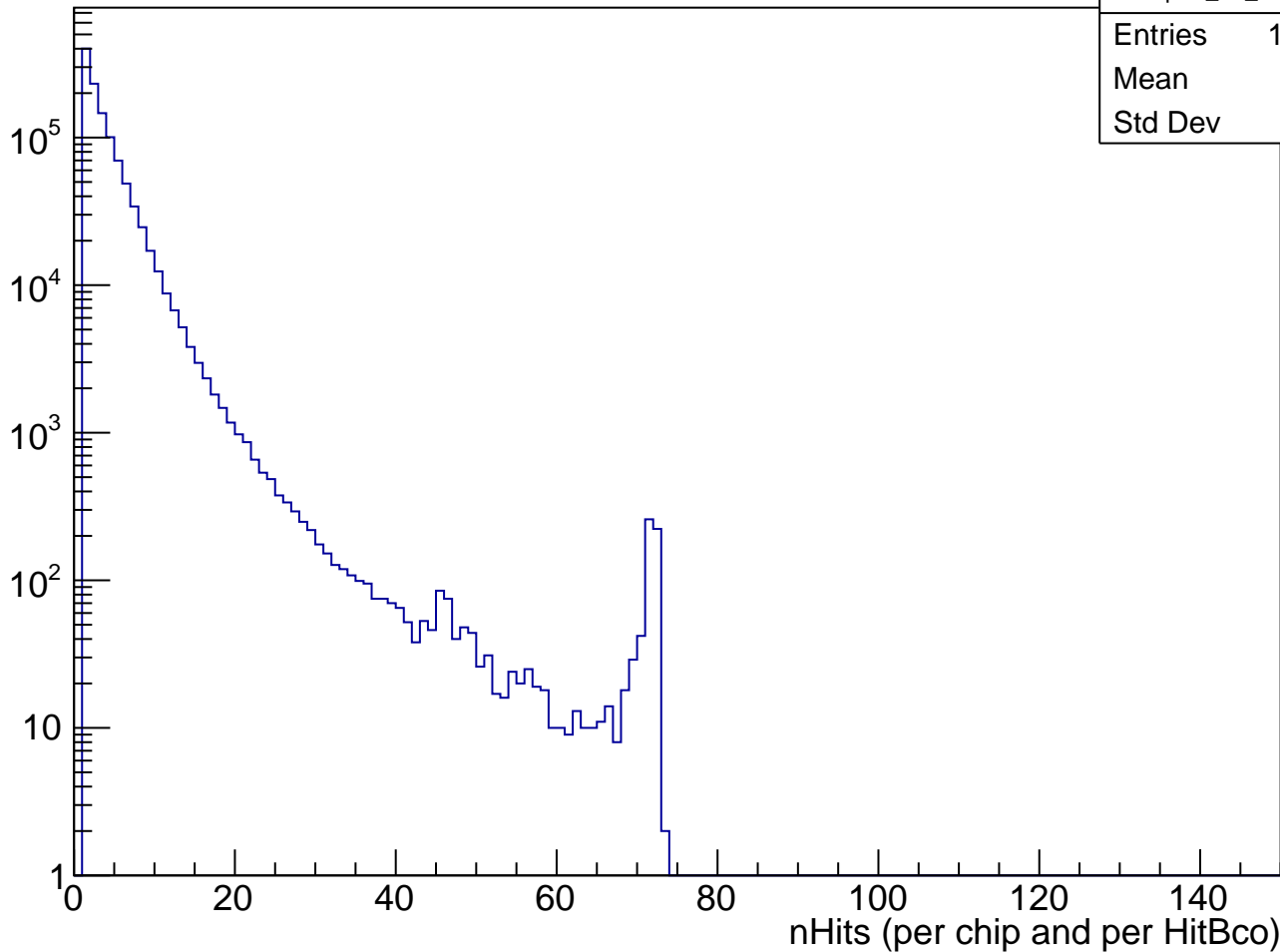
Entries



nChipHit_F2_Fch1_Chip4	
Entries	1329050
Mean	3.599
Std Dev	4.206

nChipHit_F2_Fch1_Chip5

Entries



nChipHit_F2_Fch1_Chip5

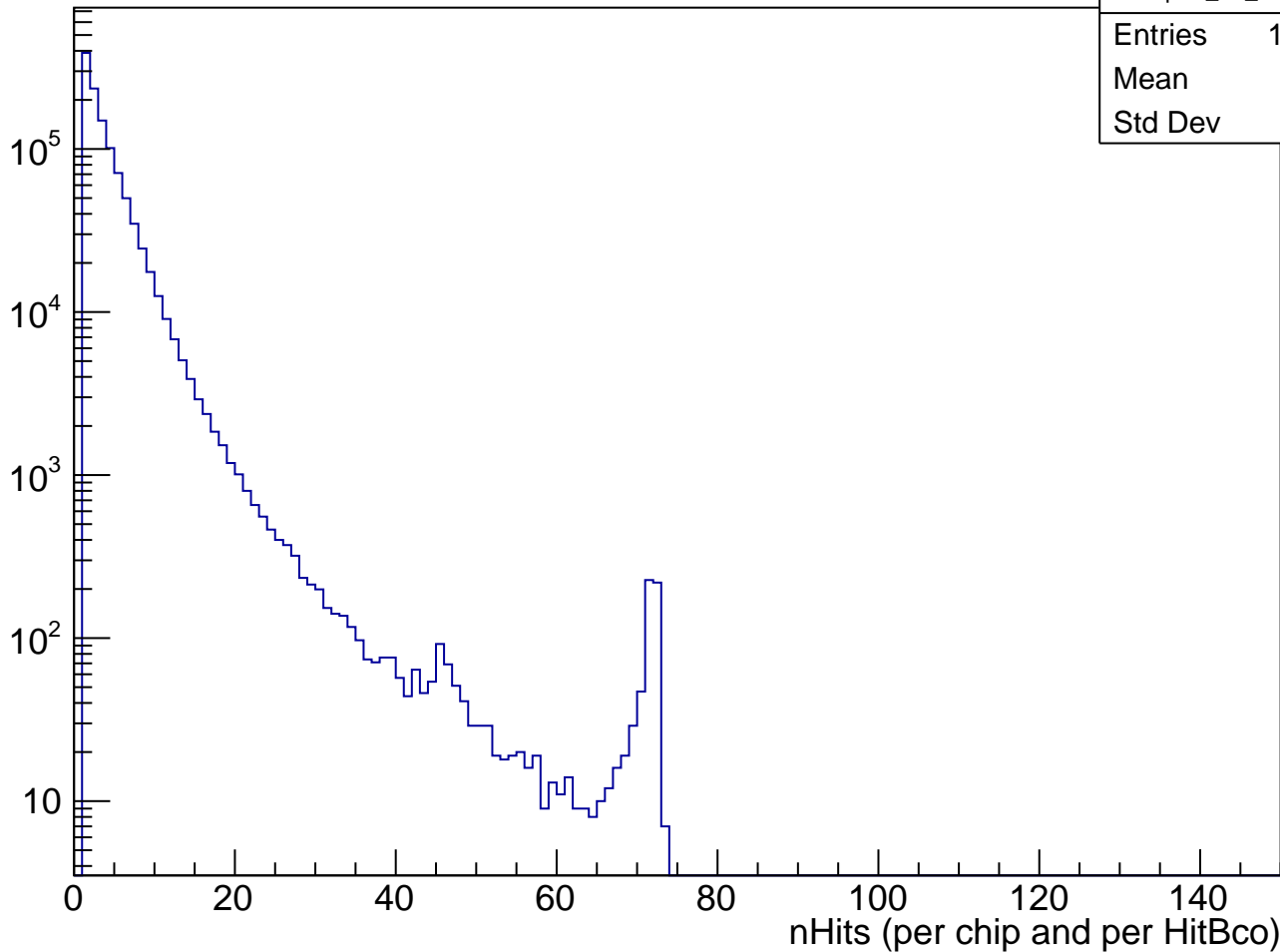
Entries 1128019

Mean 3.329

Std Dev 3.809

nChipHit_F2_Fch1_Chip6

Entries



nChipHit_F2_Fch1_Chip6

Entries 1126994

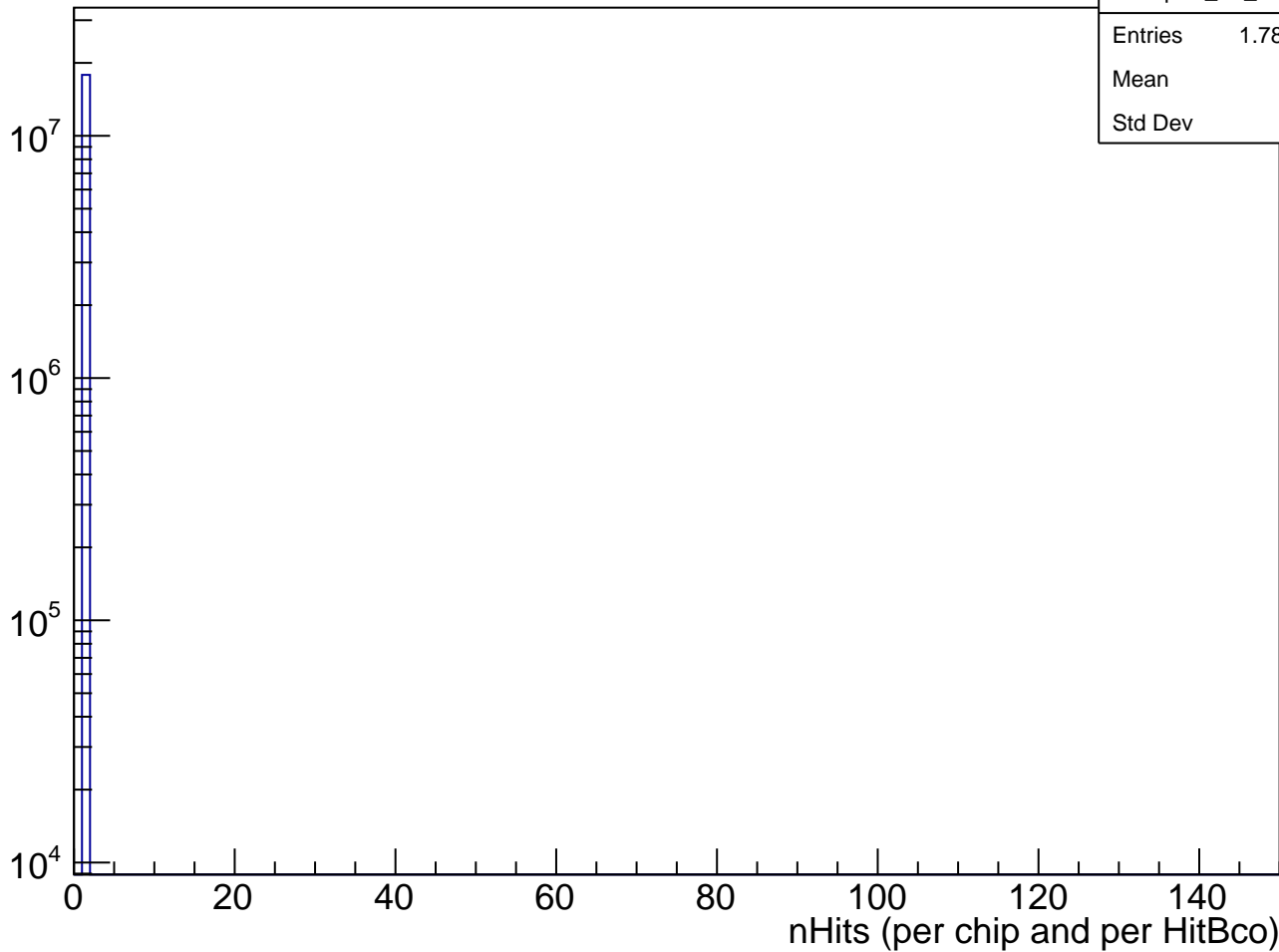
Mean 3.362

Std Dev 3.801

nHits (per chip and per HitBco)

nChipHit_F2_Fch1_Chip7

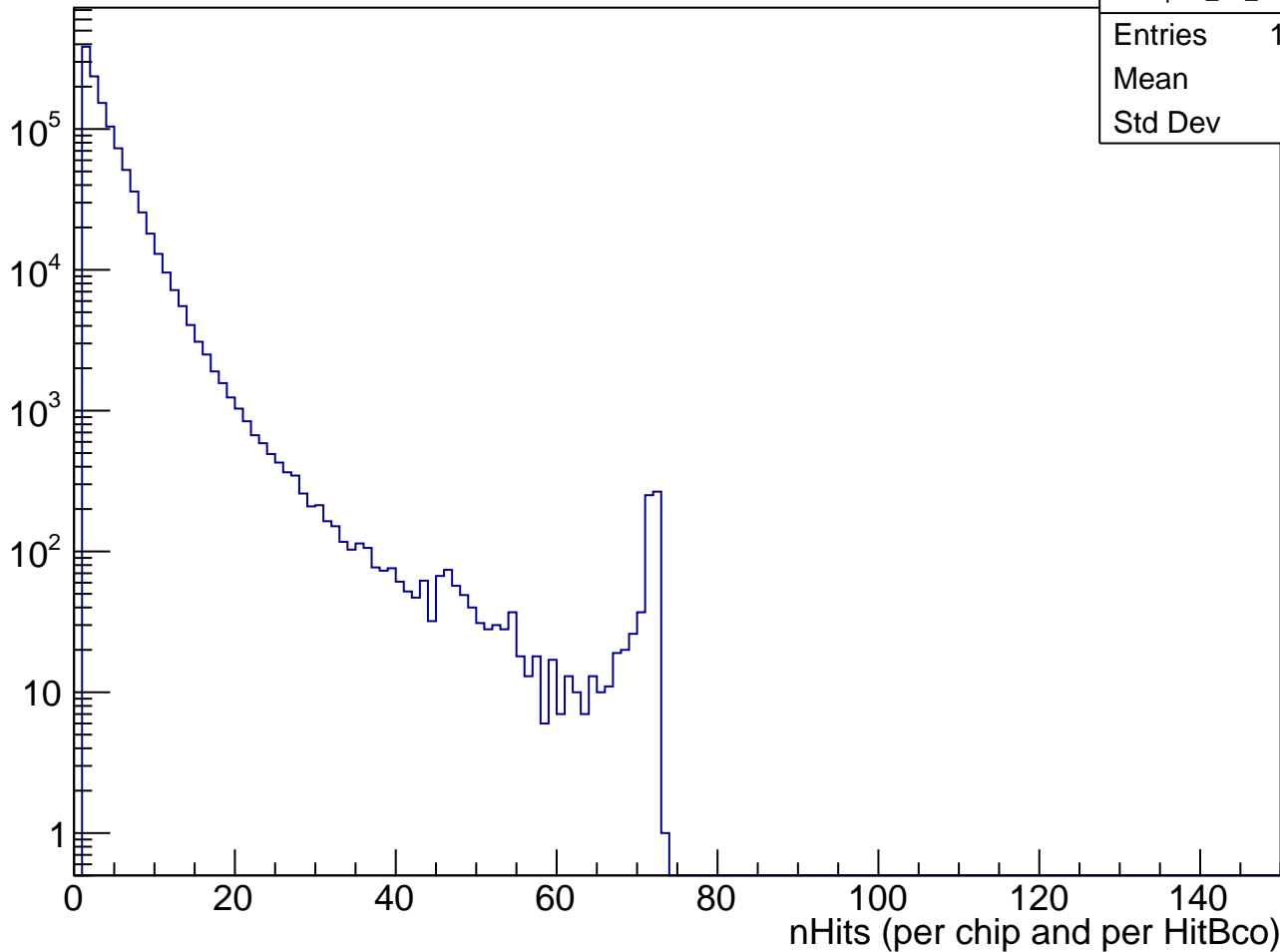
Entries



nChipHit_F2_Fch1_Chip7	
Entries	1.7846886e+07
Mean	1
Std Dev	0

nChipHit_F2_Fch1_Chip8

Entries



nChipHit_F2_Fch1_Chip8

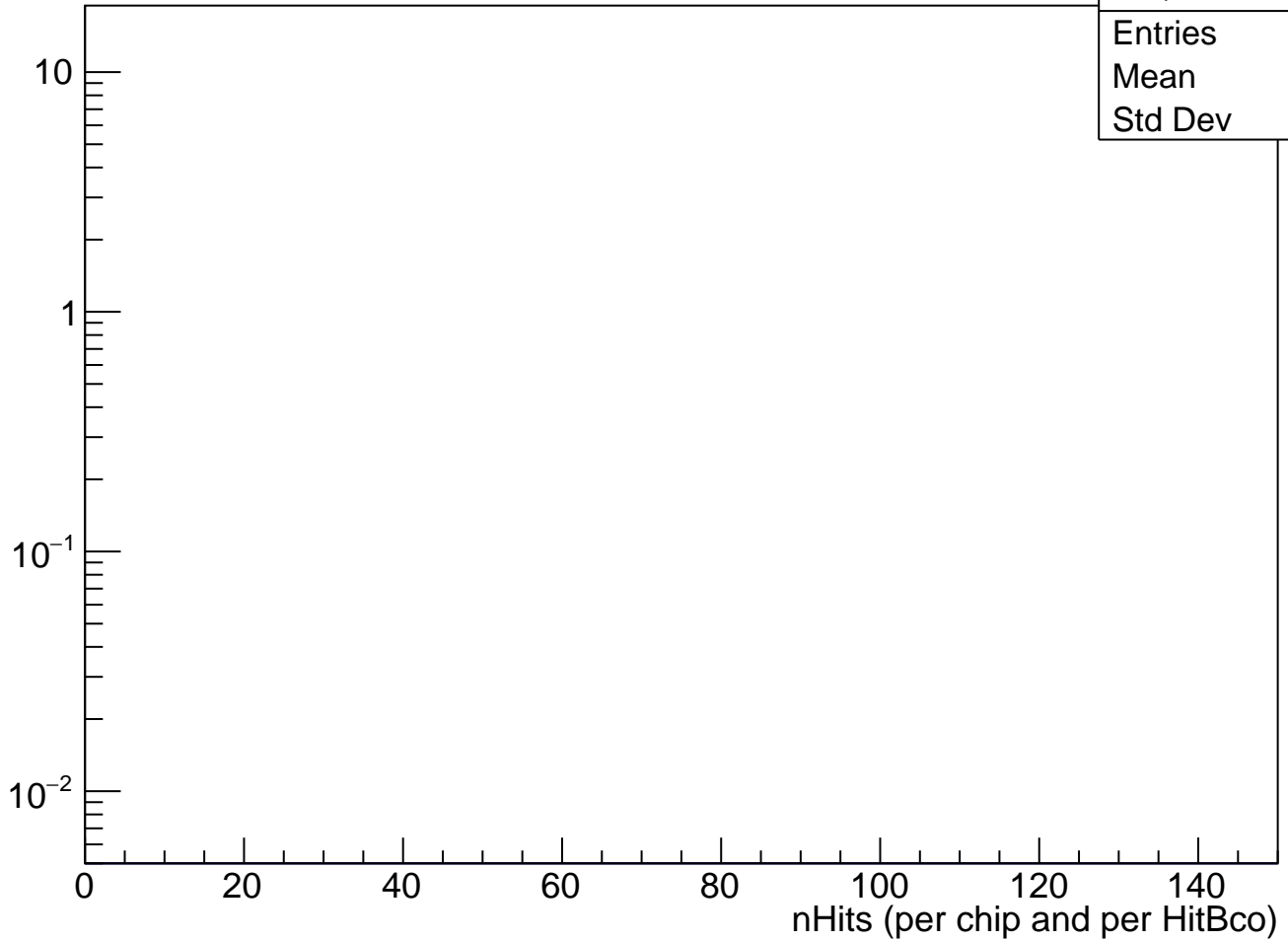
Entries 1139653

Mean 3.415

Std Dev 3.864

nChipHit_F2_Fch1_Chip9

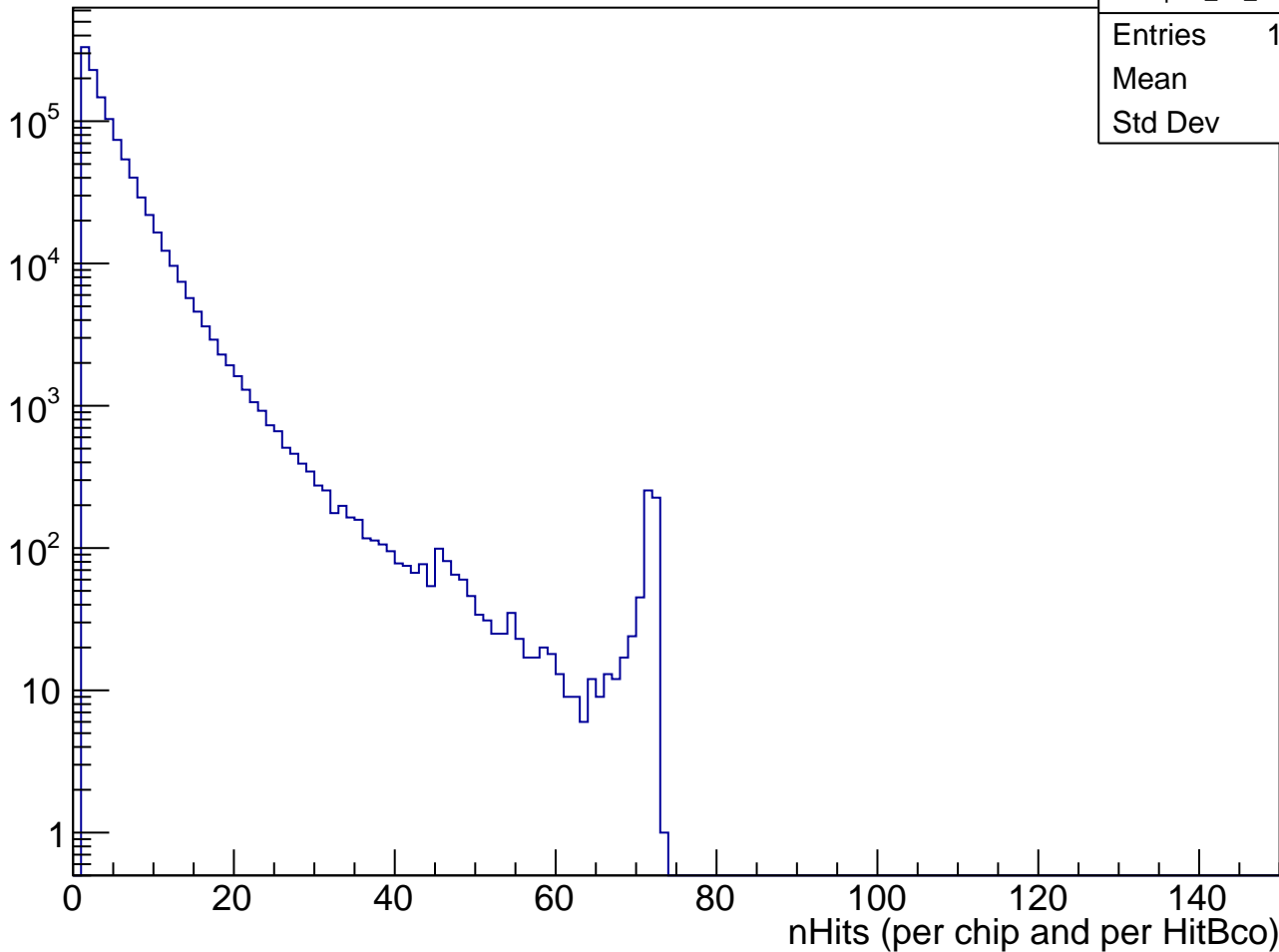
Entries



nChipHit_F2_Fch1_Chip9	
Entries	0
Mean	0
Std Dev	0

nChipHit_F2_Fch2_Chip0

Entries



nChipHit_F2_Fch2_Chip0

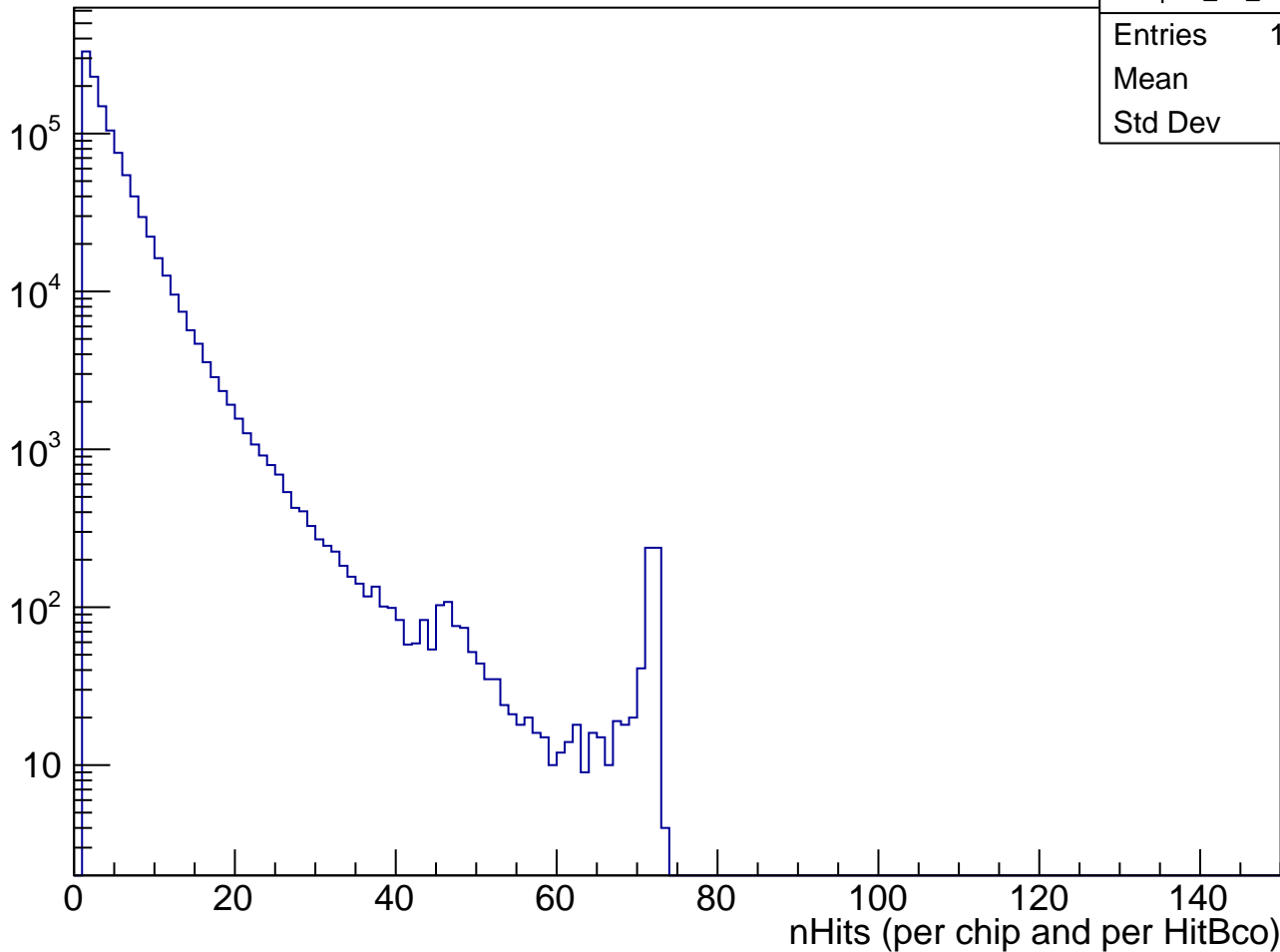
Entries 1108287

Mean 3.812

Std Dev 4.264

nChipHit_F2_Fch2_Chip1

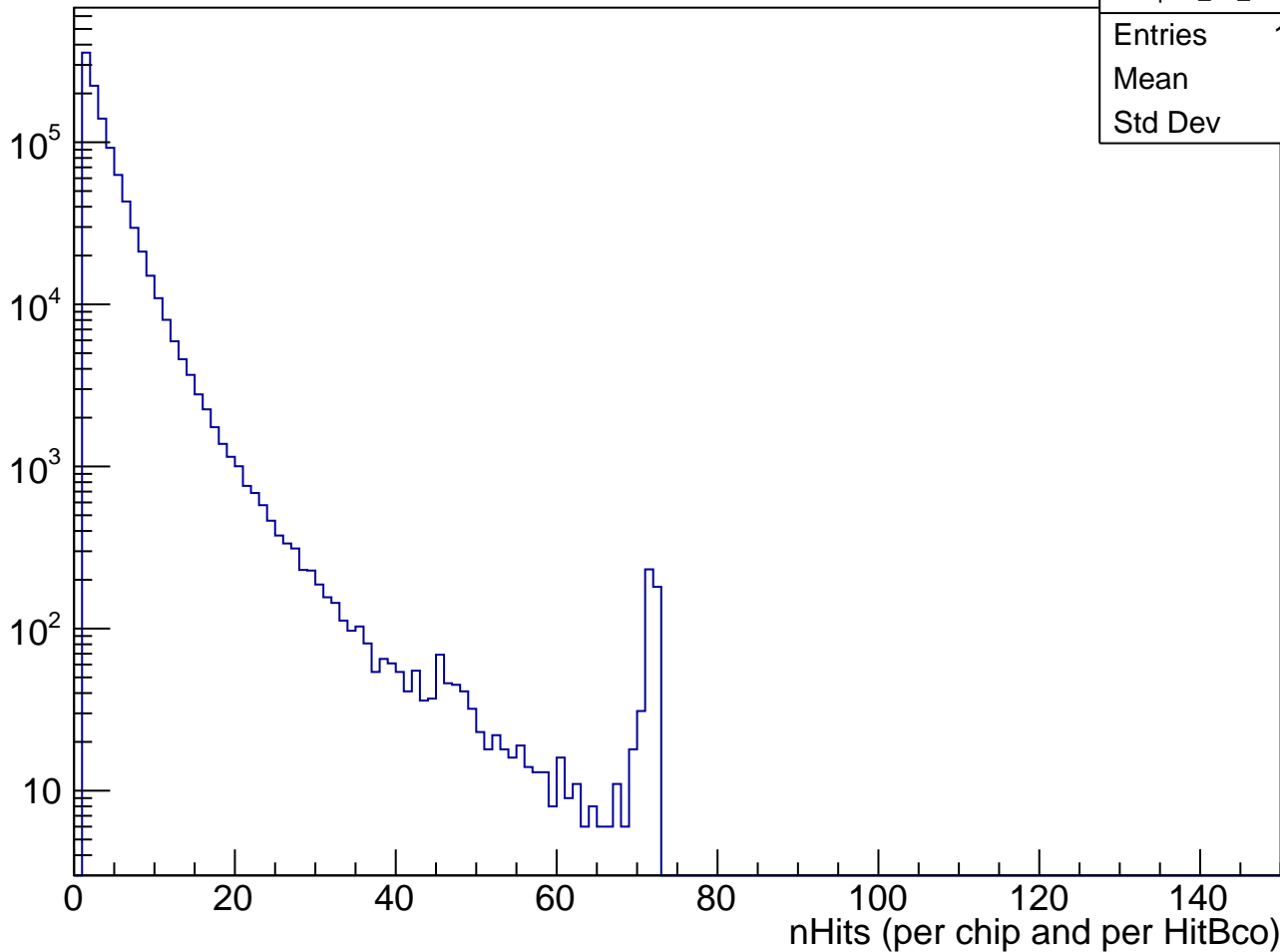
Entries



nChipHit_F2_Fch2_Chip1	
Entries	1113910
Mean	3.821
Std Dev	4.273

nChipHit_F2_Fch2_Chip10

Entries

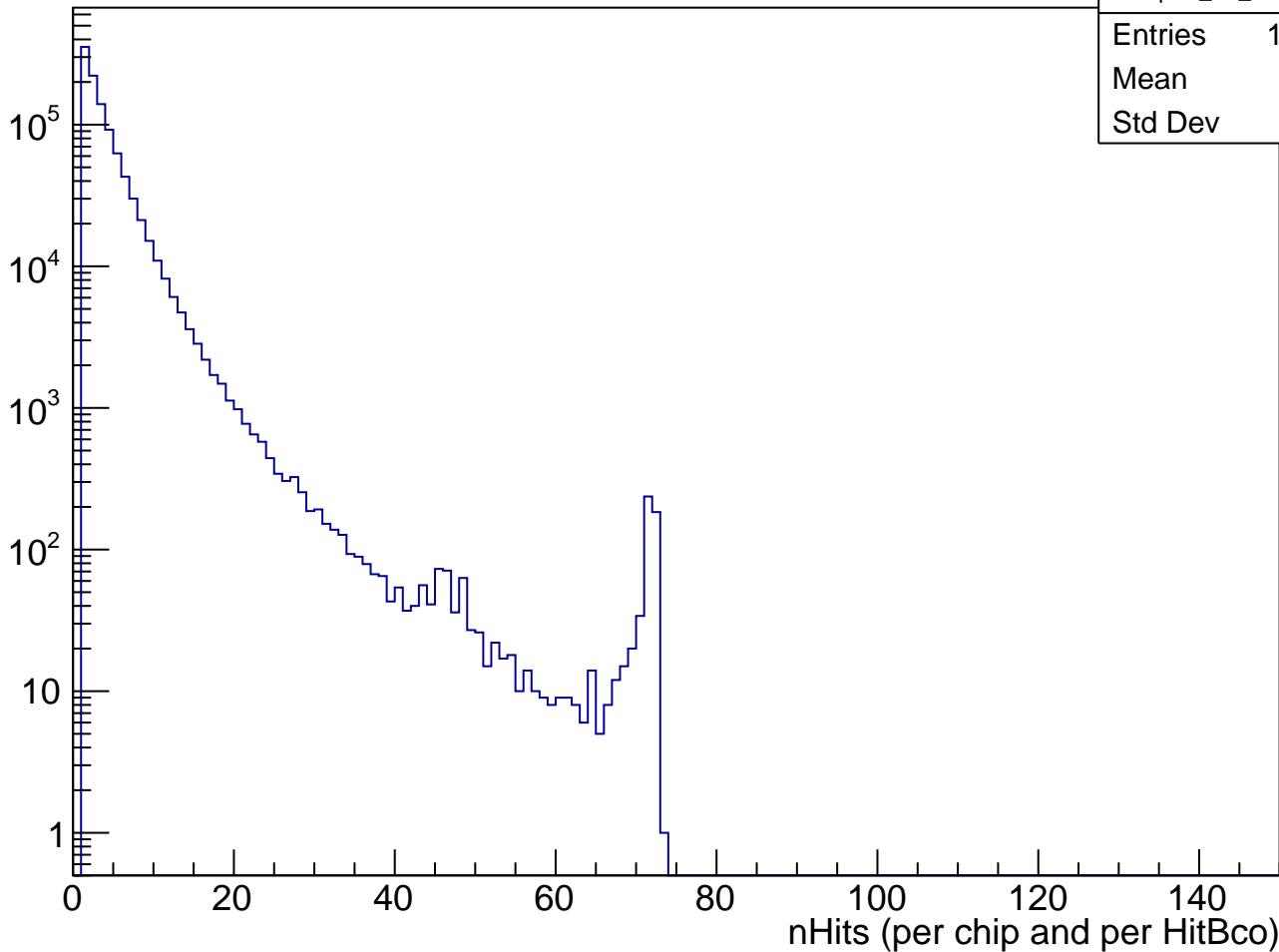


nChipHit_F2_Fch2_Chip10	
Entries	1033911
Mean	3.316
Std Dev	3.767

nHits (per chip and per HitBco)

nChipHit_F2_Fch2_Chip11

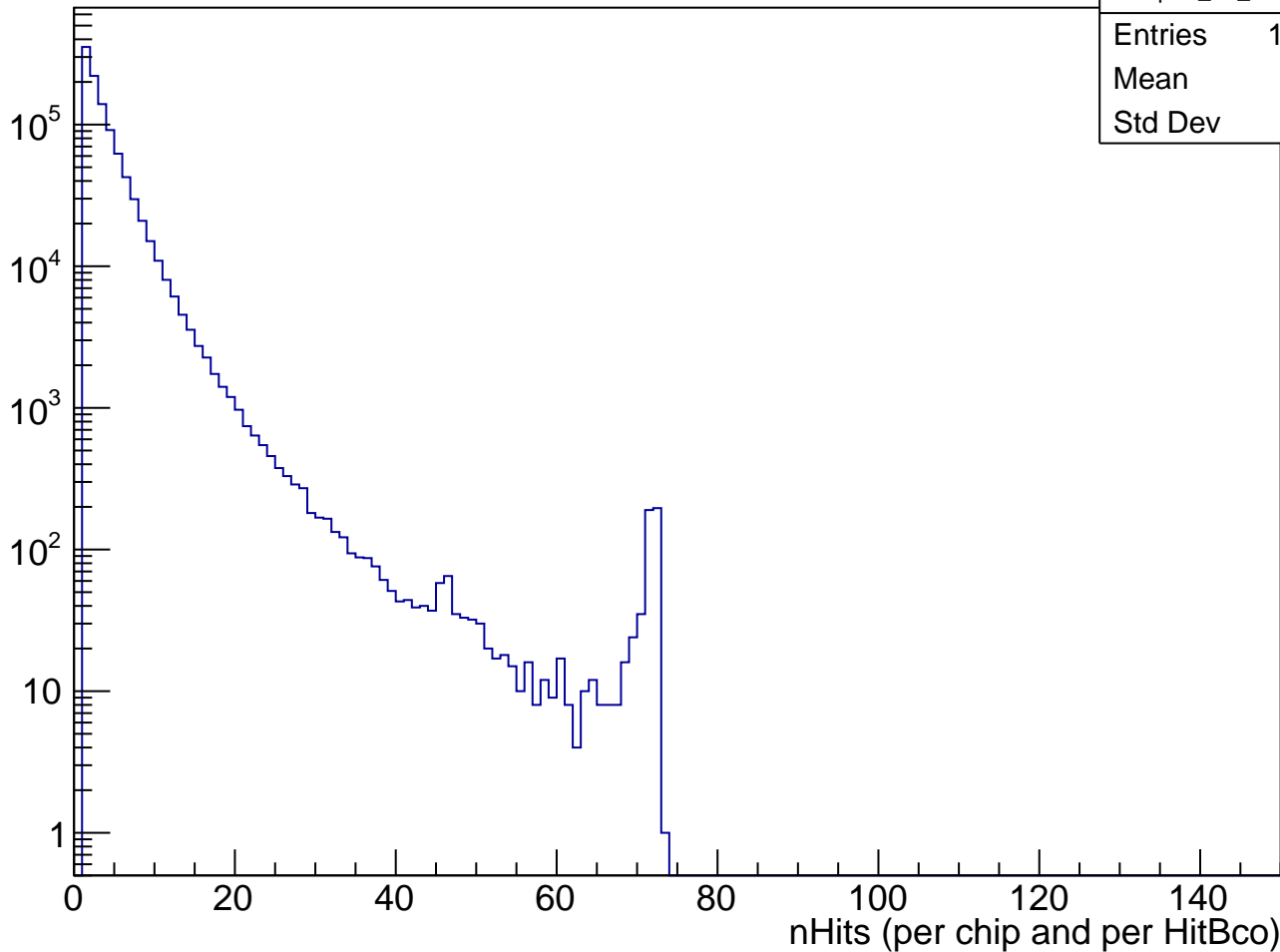
Entries



nChipHit_F2_Fch2_Chip11	
Entries	1029279
Mean	3.33
Std Dev	3.786

nChipHit_F2_Fch2_Chip12

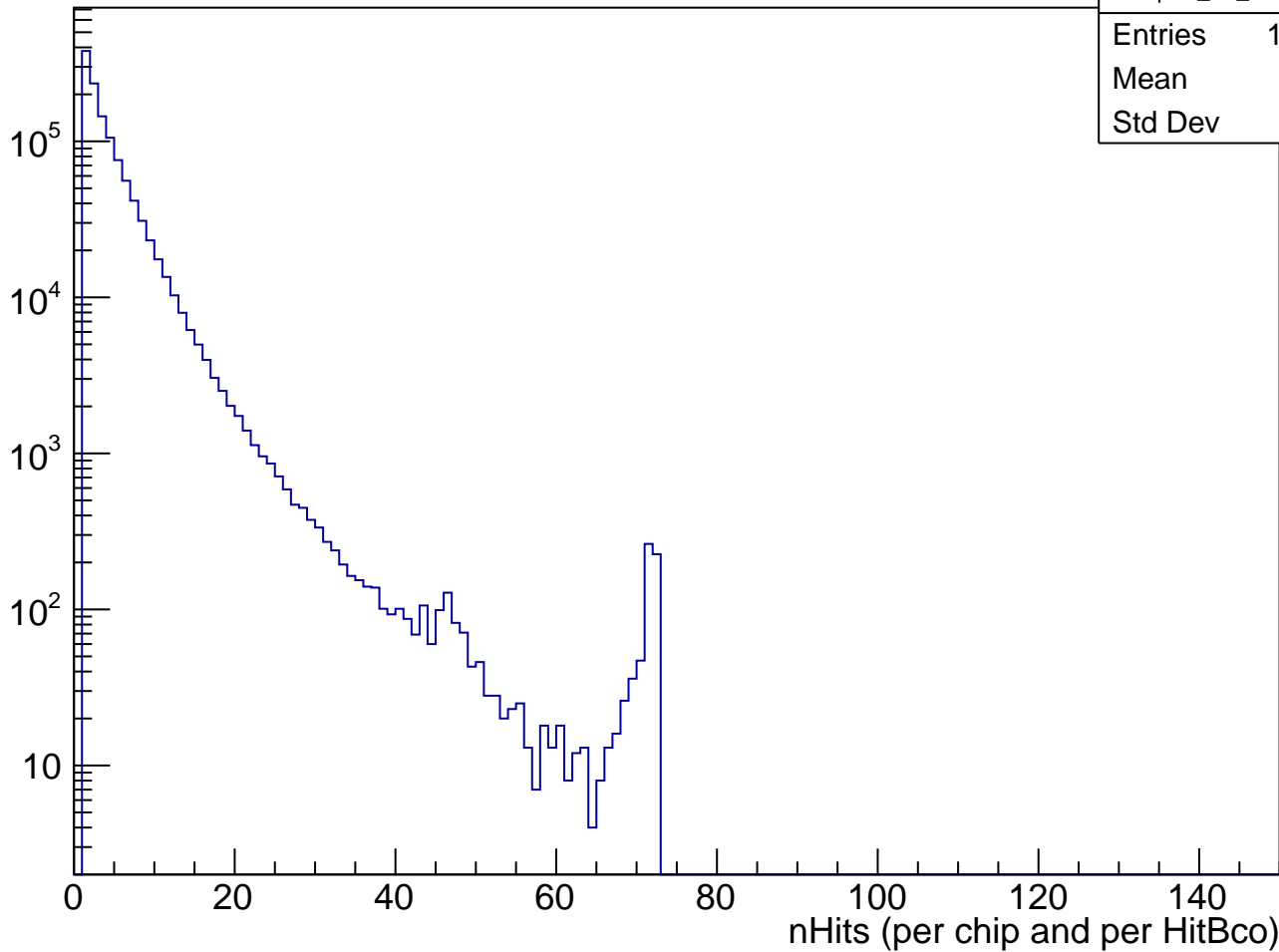
Entries



nChipHit_F2_Fch2_Chip12	
Entries	1024959
Mean	3.32
Std Dev	3.758

nChipHit_F2_Fch2_Chip13

Entries

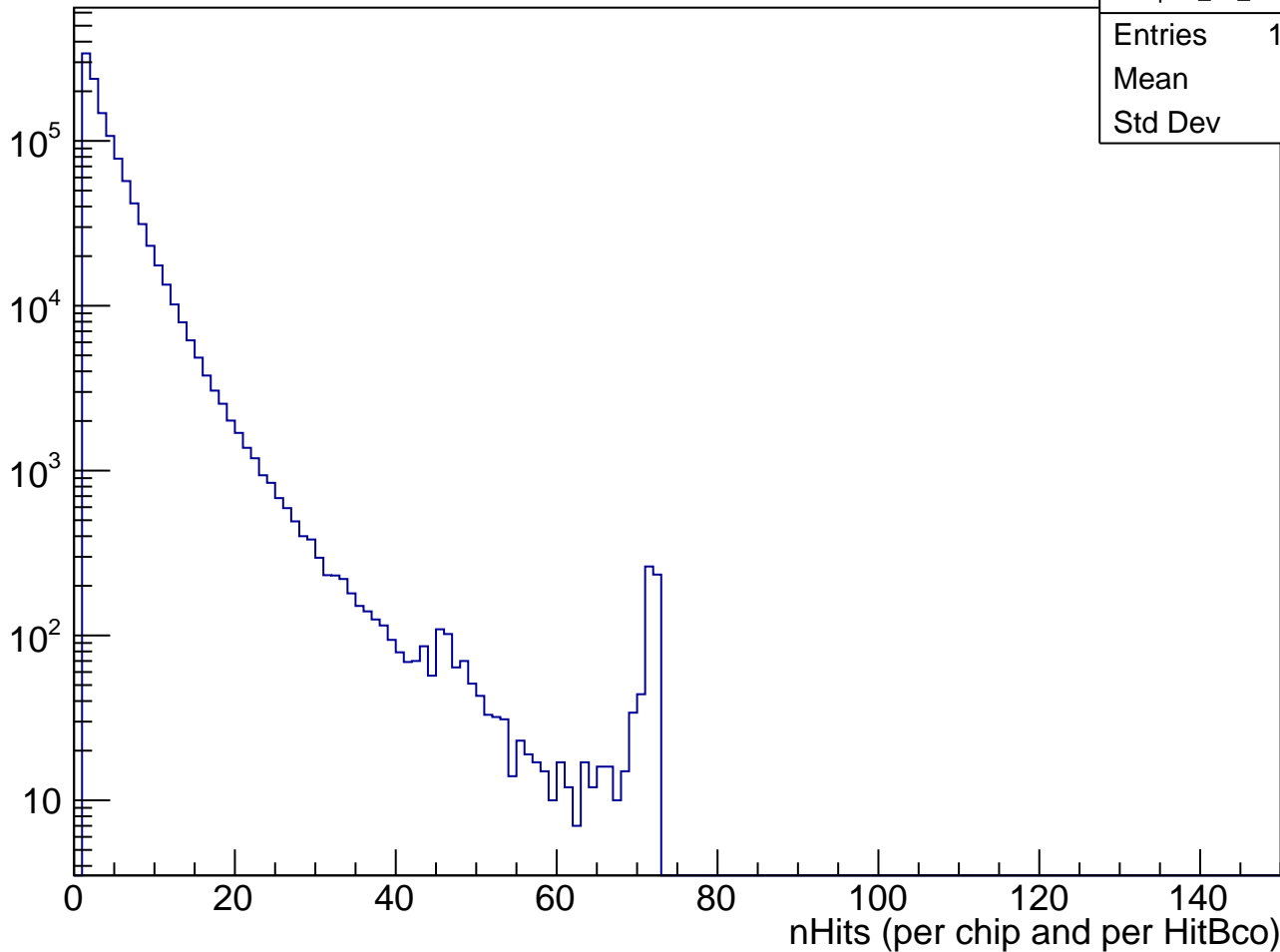


nChipHit_F2_Fch2_Chip13	
Entries	1176222
Mean	3.773
Std Dev	4.306

nHits (per chip and per HitBco)

nChipHit_F2_Fch2_Chip14

Entries

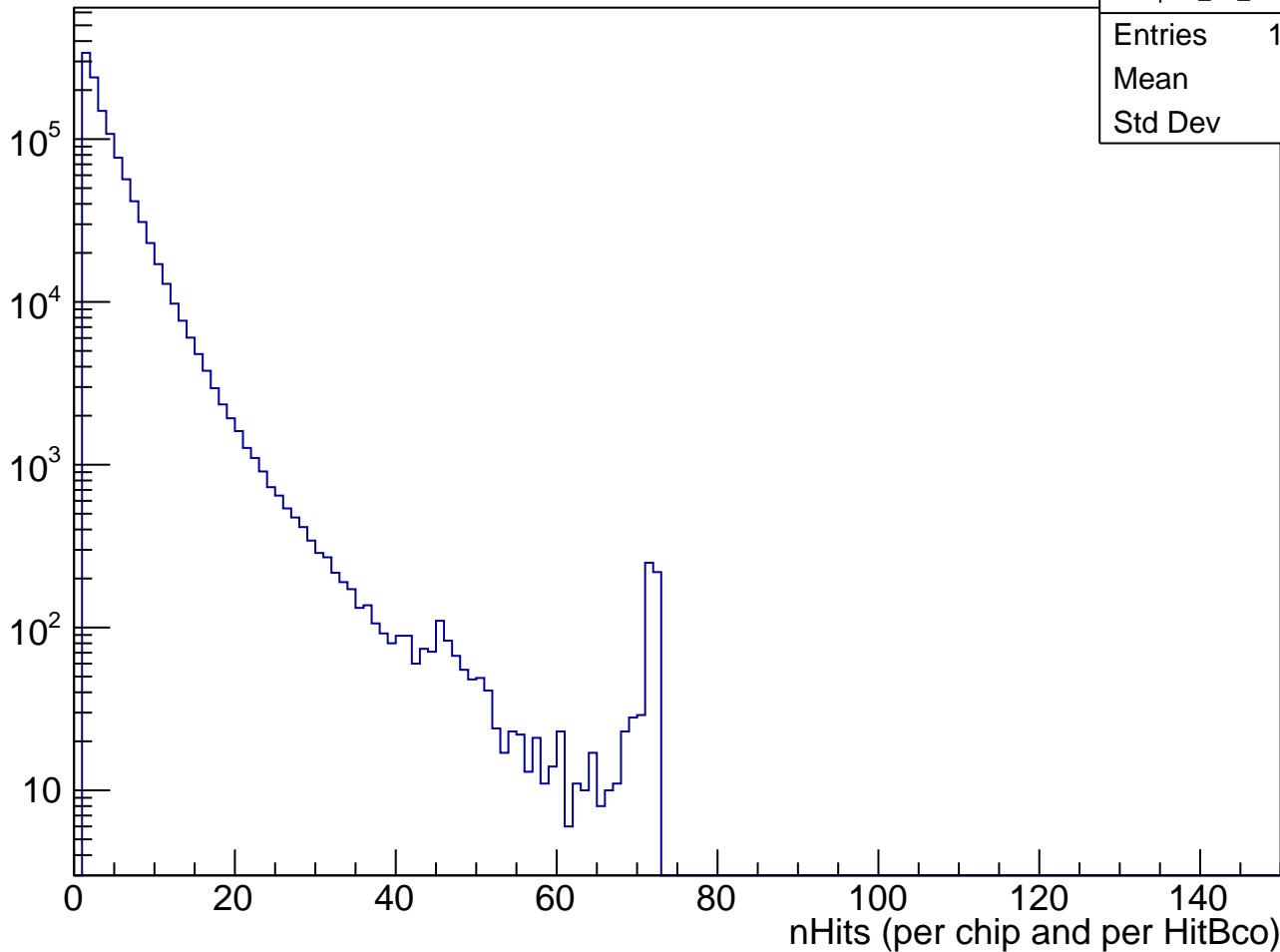


nChipHit_F2_Fch2_Chip14	
Entries	1146846
Mean	3.86
Std Dev	4.312

nHits (per chip and per HitBco)

nChipHit_F2_Fch2_Chip15

Entries

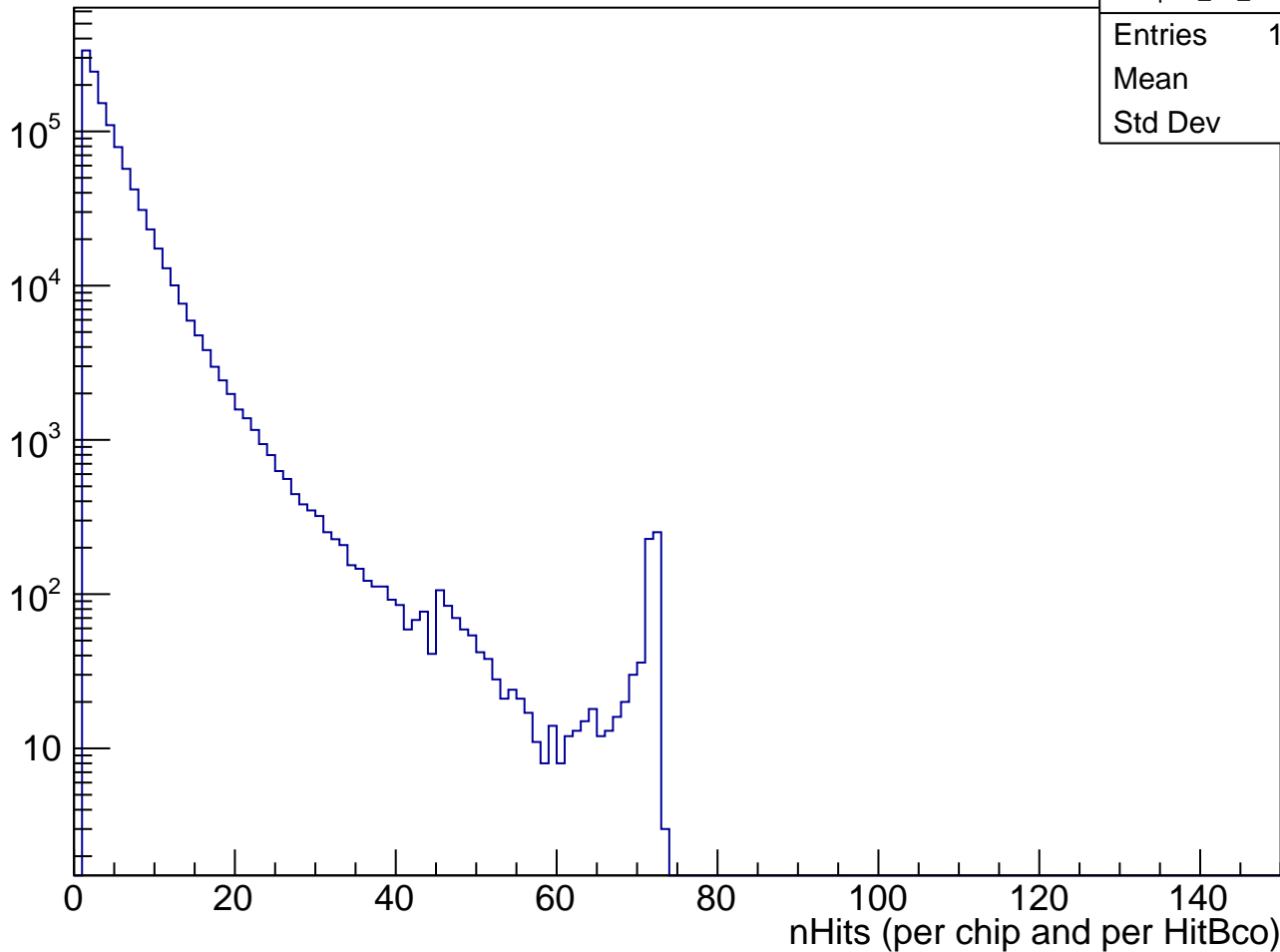


nChipHit_F2_Fch2_Chip15	
Entries	1144347
Mean	3.821
Std Dev	4.236

nHits (per chip and per HitBco)

nChipHit_F2_Fch2_Chip16

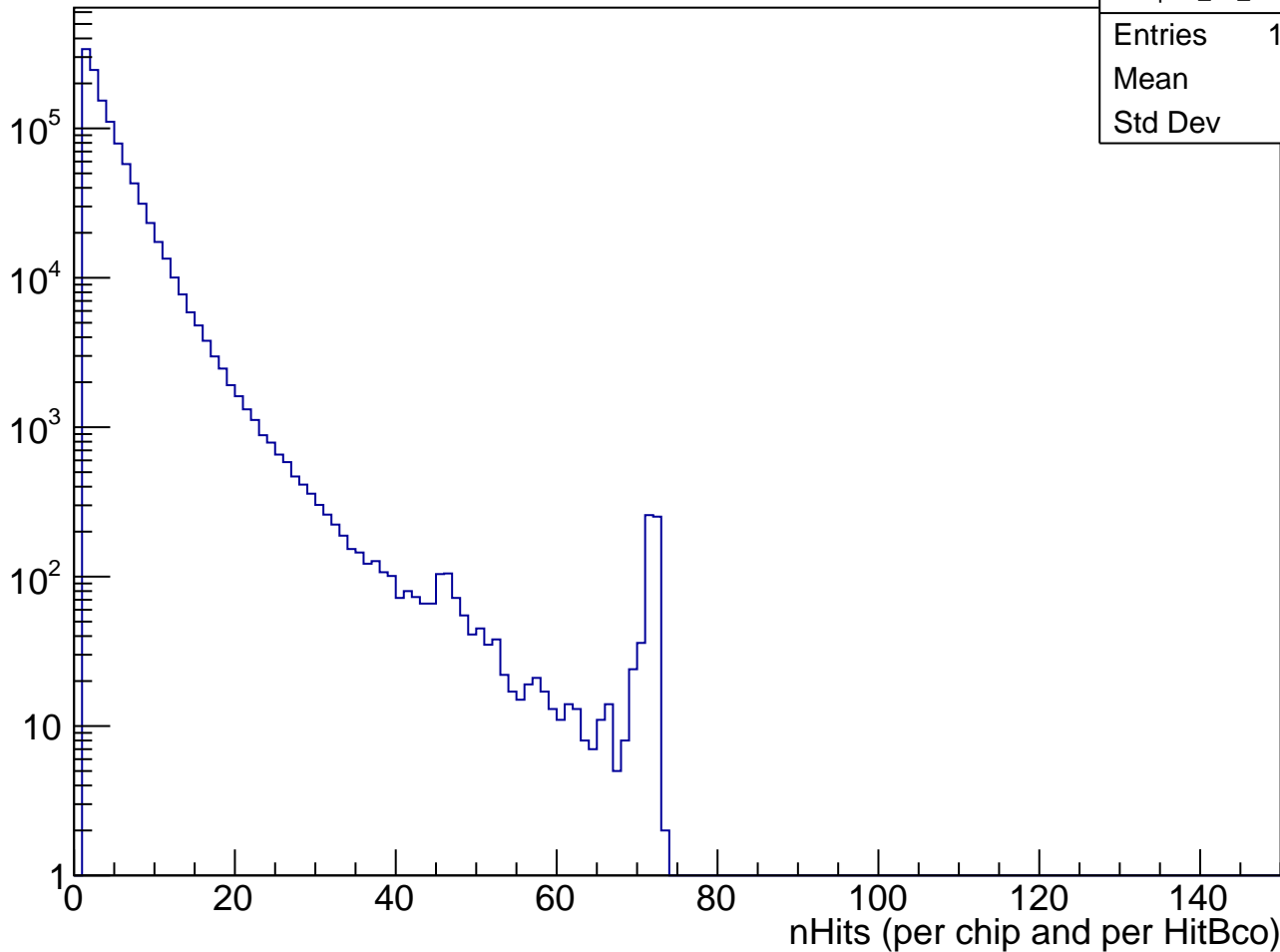
Entries



nChipHit_F2_Fch2_Chip16	
Entries	1154992
Mean	3.836
Std Dev	4.24

nChipHit_F2_Fch2_Chip17

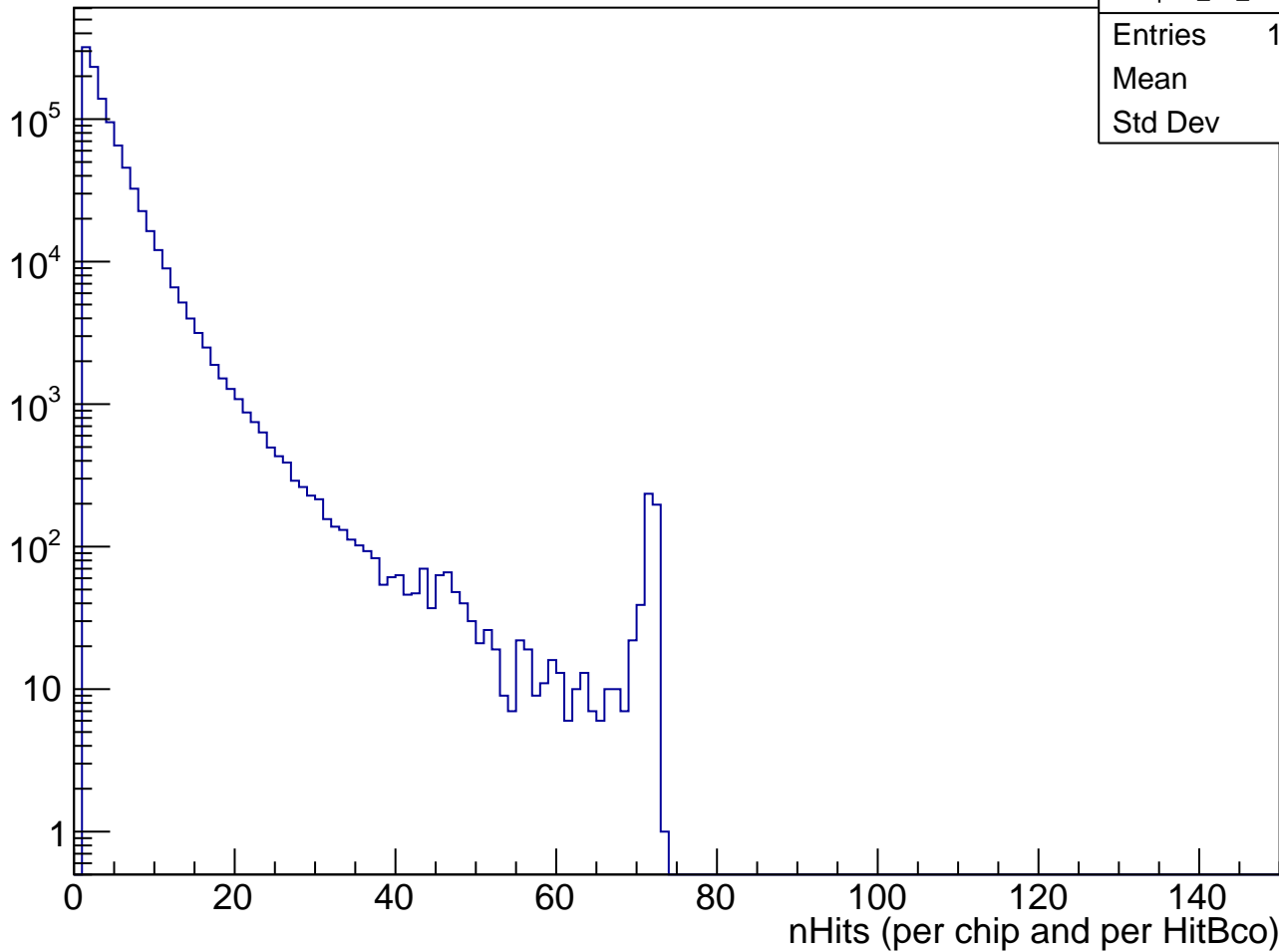
Entries



nChipHit_F2_Fch2_Chip17	
Entries	1165742
Mean	3.83
Std Dev	4.235

nChipHit_F2_Fch2_Chip18

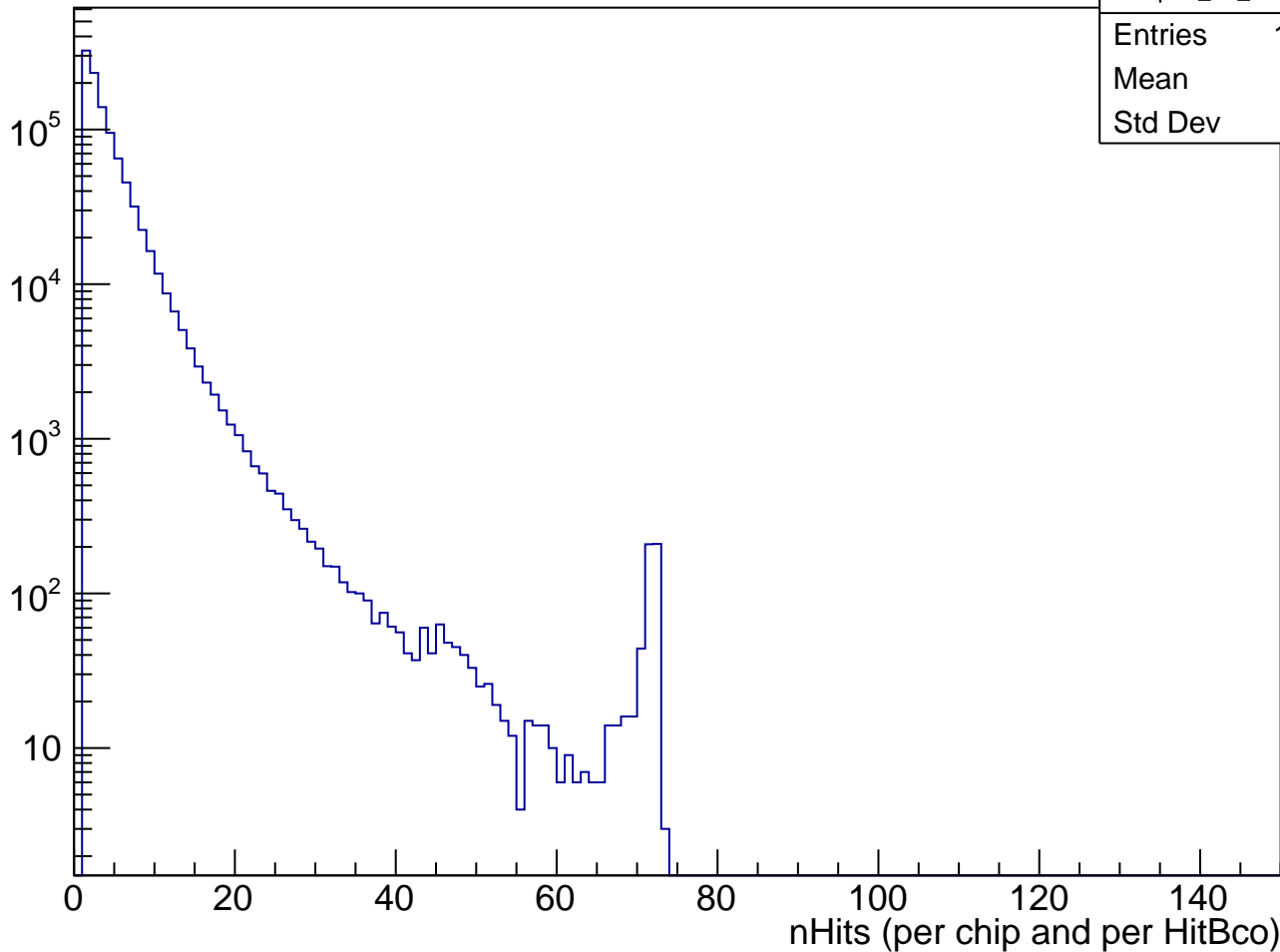
Entries



nChipHit_F2_Fch2_Chip18	
Entries	1023592
Mean	3.483
Std Dev	3.894

nChipHit_F2_Fch2_Chip19

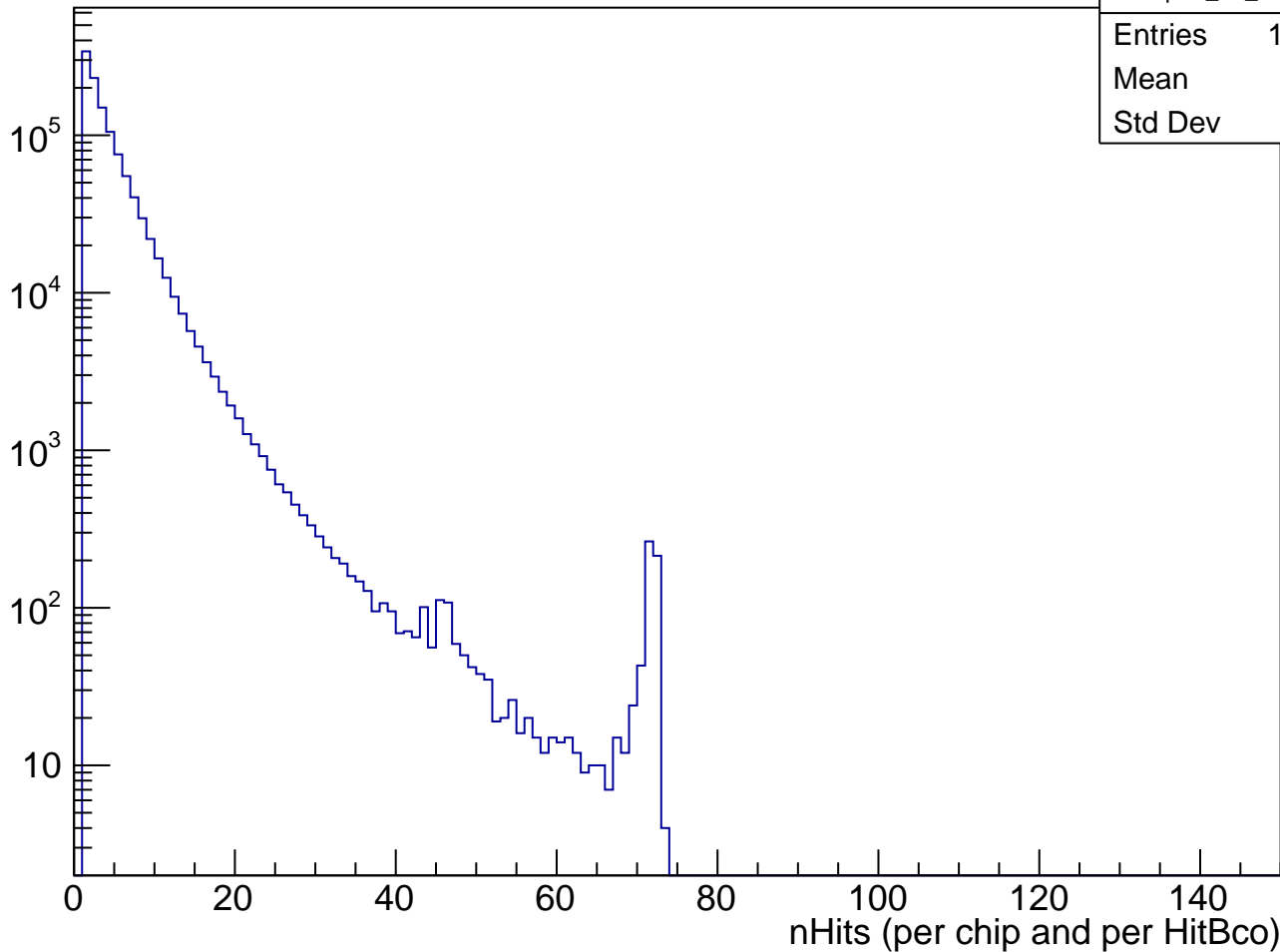
Entries



nChipHit_F2_Fch2_Chip19	
Entries	1025841
Mean	3.449
Std Dev	3.844

nChipHit_F2_Fch2_Chip2

Entries



nChipHit_F2_Fch2_Chip2

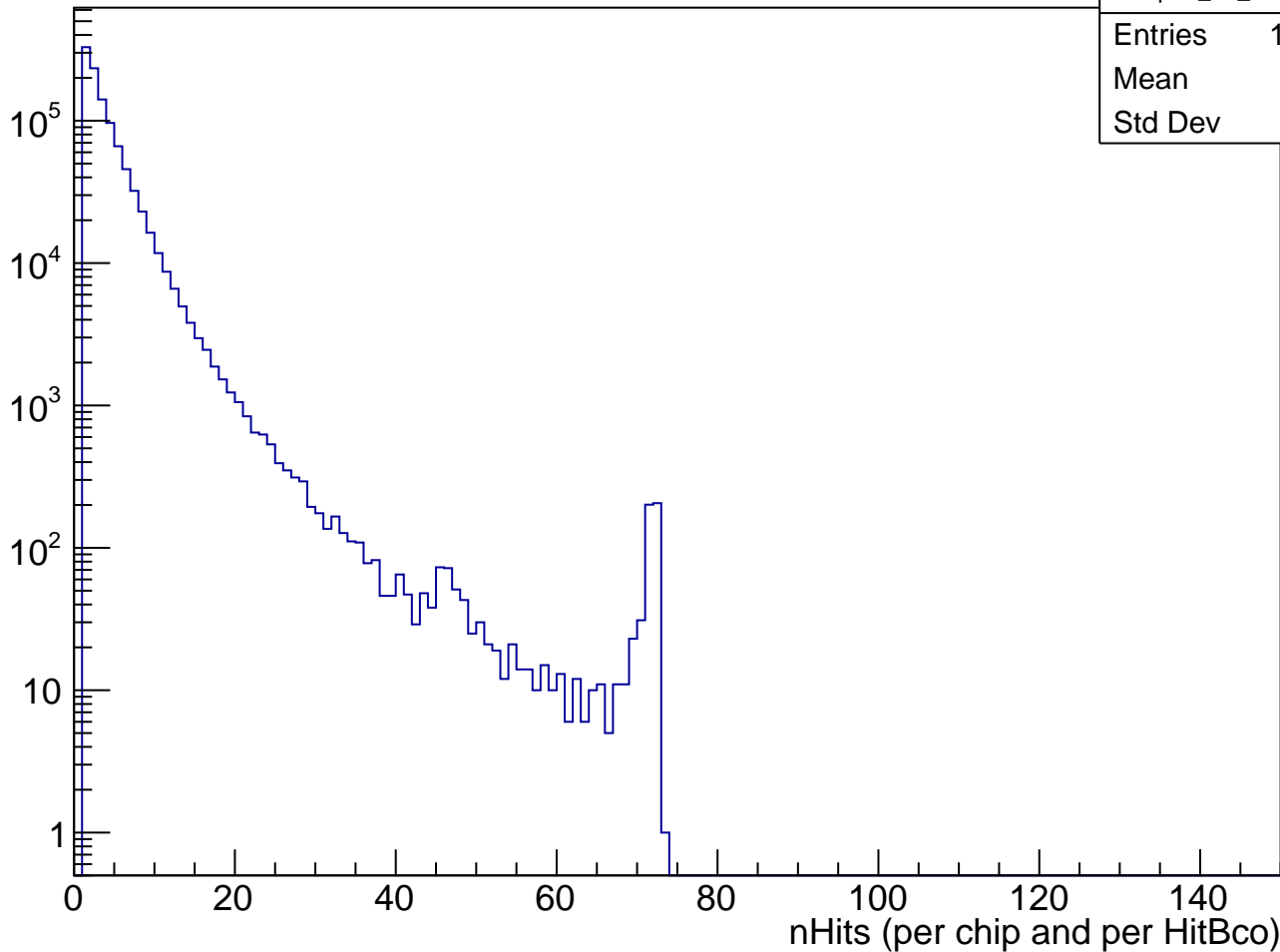
Entries 1127598

Mean 3.791

Std Dev 4.233

nChipHit_F2_Fch2_Chip20

Entries

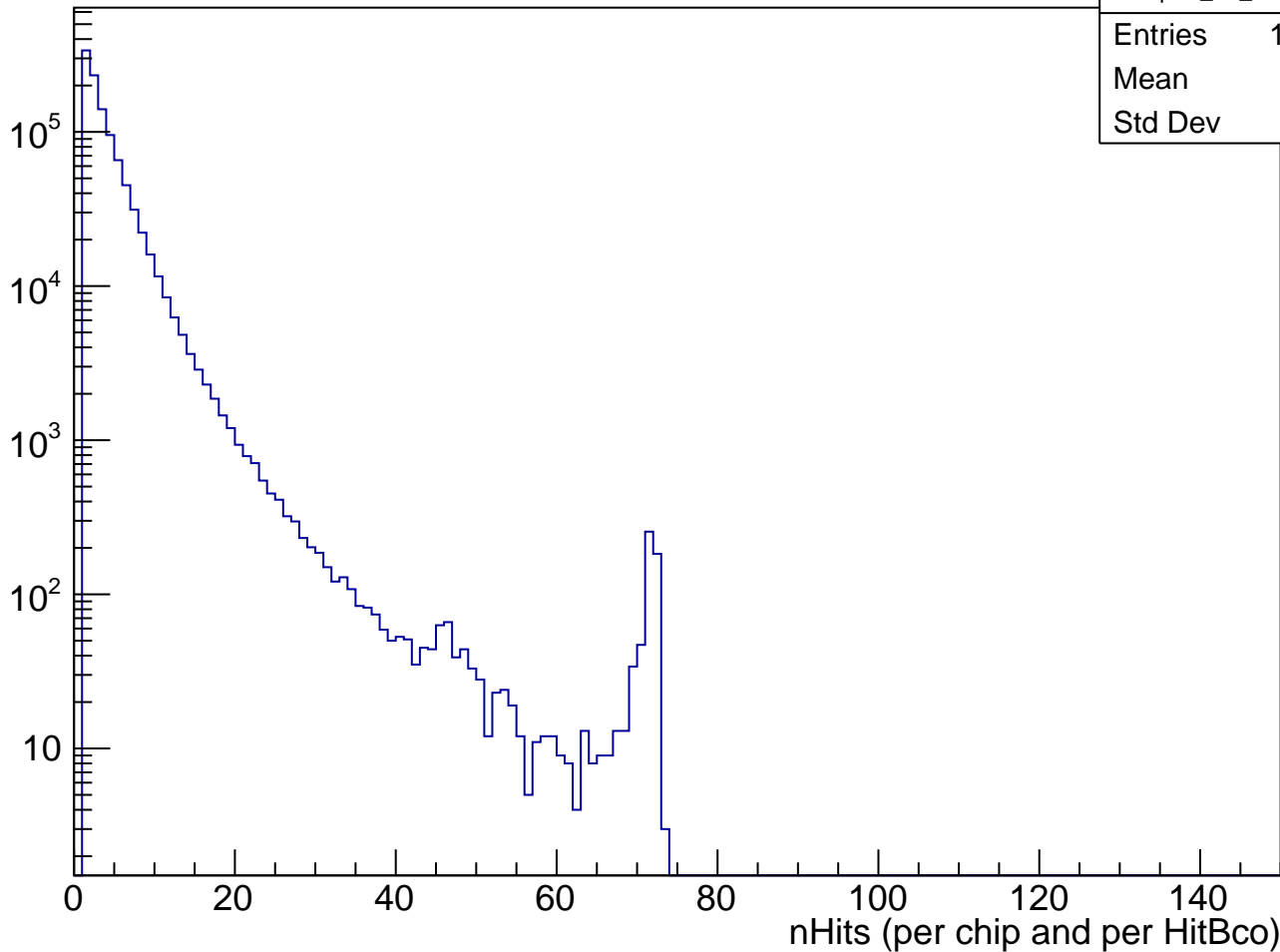


nChipHit_F2_Fch2_Chip20	
Entries	1036273
Mean	3.444
Std Dev	3.829

nHits (per chip and per HitBco)

nChipHit_F2_Fch2_Chip21

Entries

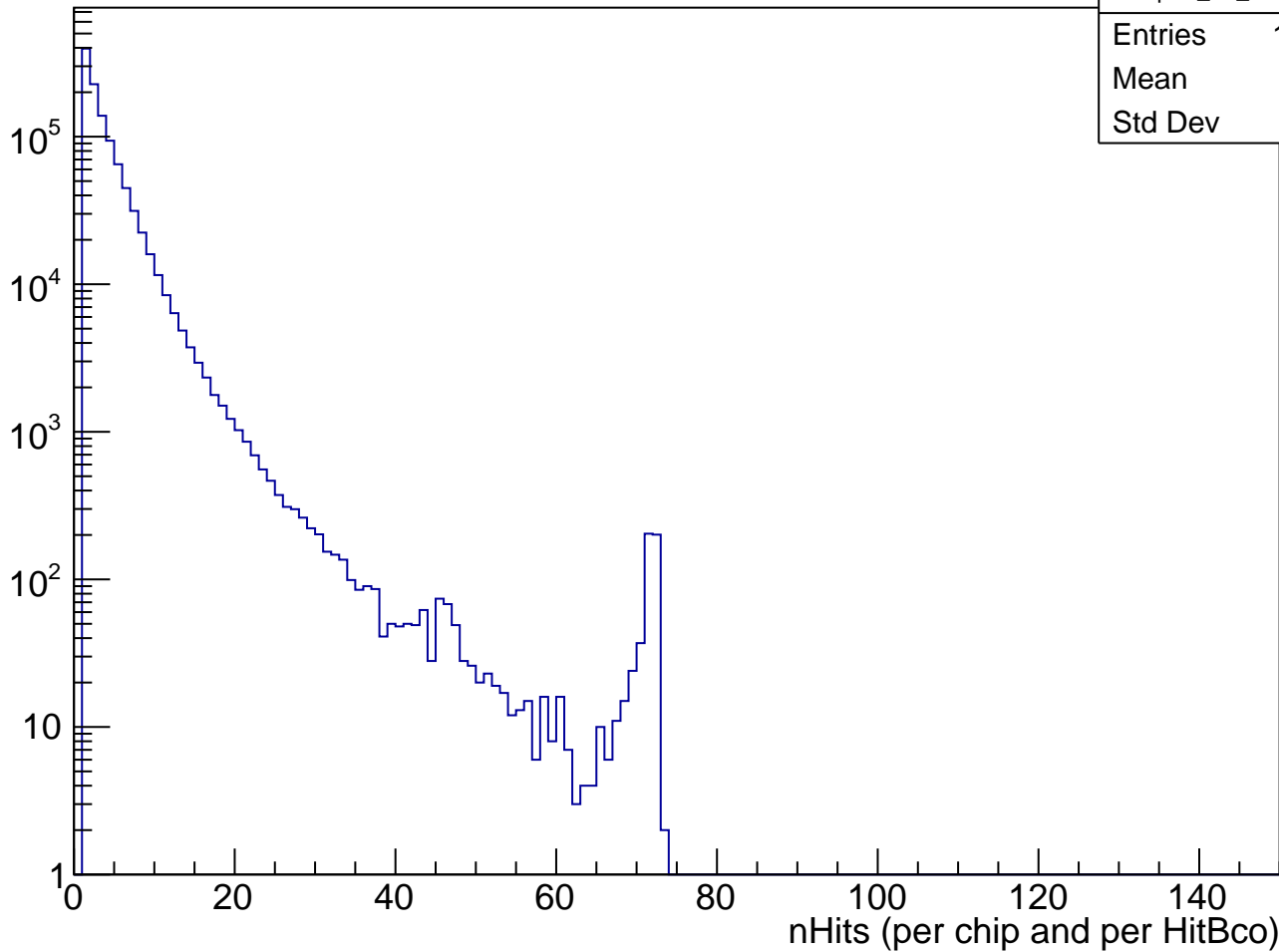


nChipHit_F2_Fch2_Chip21	
Entries	1038183
Mean	3.393
Std Dev	3.816

nHits (per chip and per HitBco)

nChipHit_F2_Fch2_Chip22

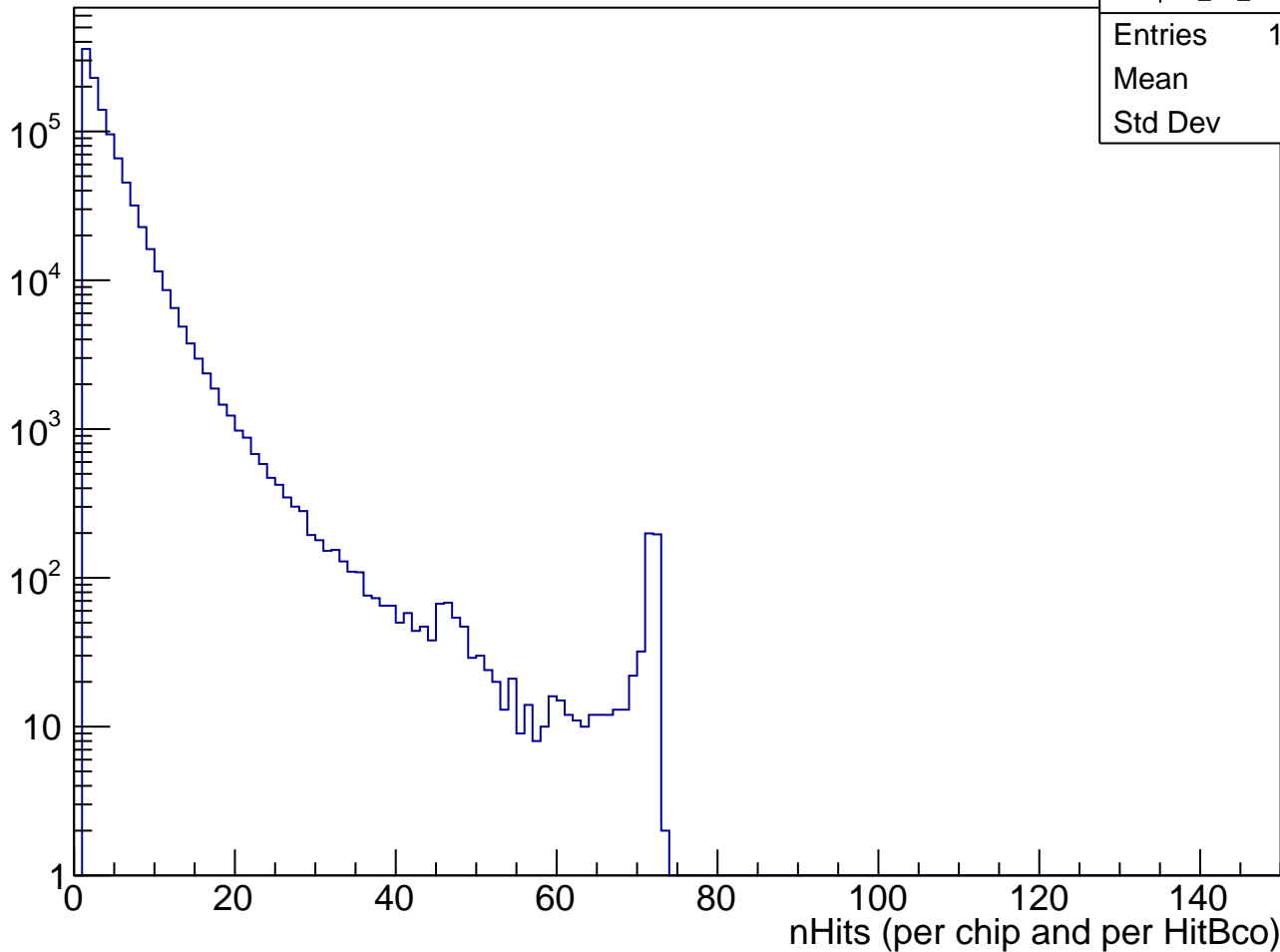
Entries



nChipHit_F2_Fch2_Chip22	
Entries	1086091
Mean	3.277
Std Dev	3.748

nChipHit_F2_Fch2_Chip23

Entries



nChipHit_F2_Fch2_Chip23

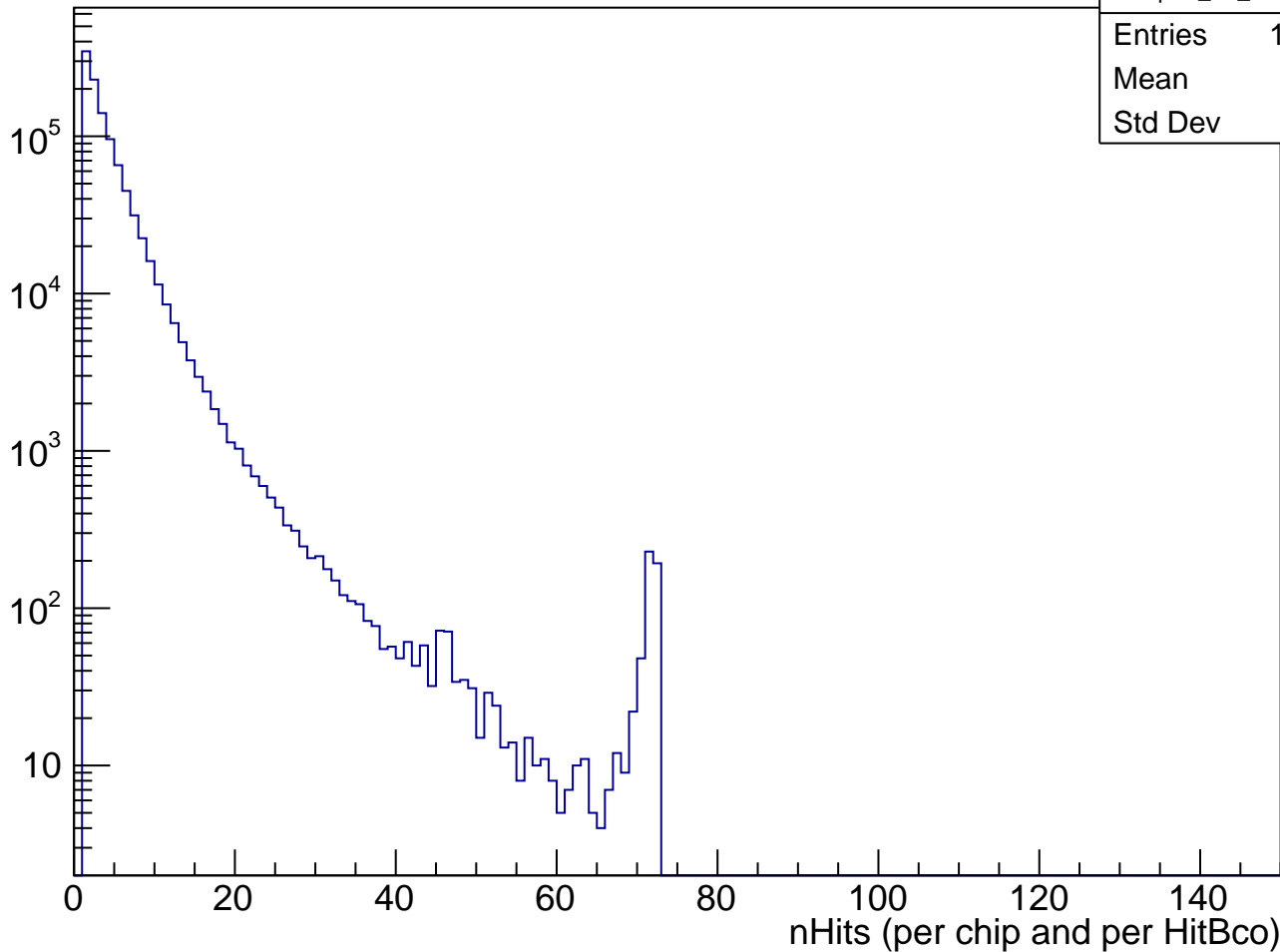
Entries 1057459

Mean 3.37

Std Dev 3.807

nChipHit_F2_Fch2_Chip24

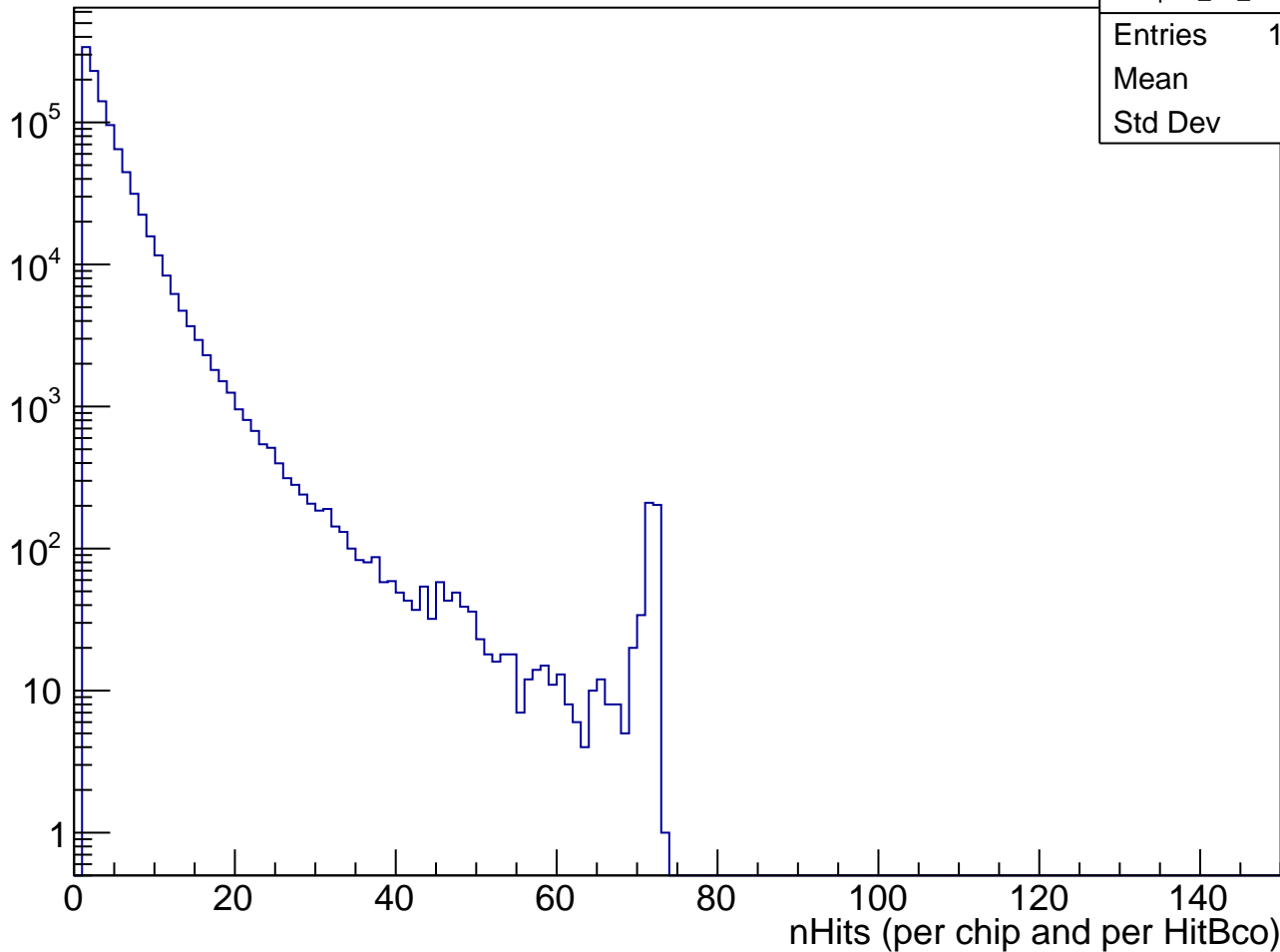
Entries



nChipHit_F2_Fch2_Chip24	
Entries	1045063
Mean	3.39
Std Dev	3.815

nChipHit_F2_Fch2_Chip25

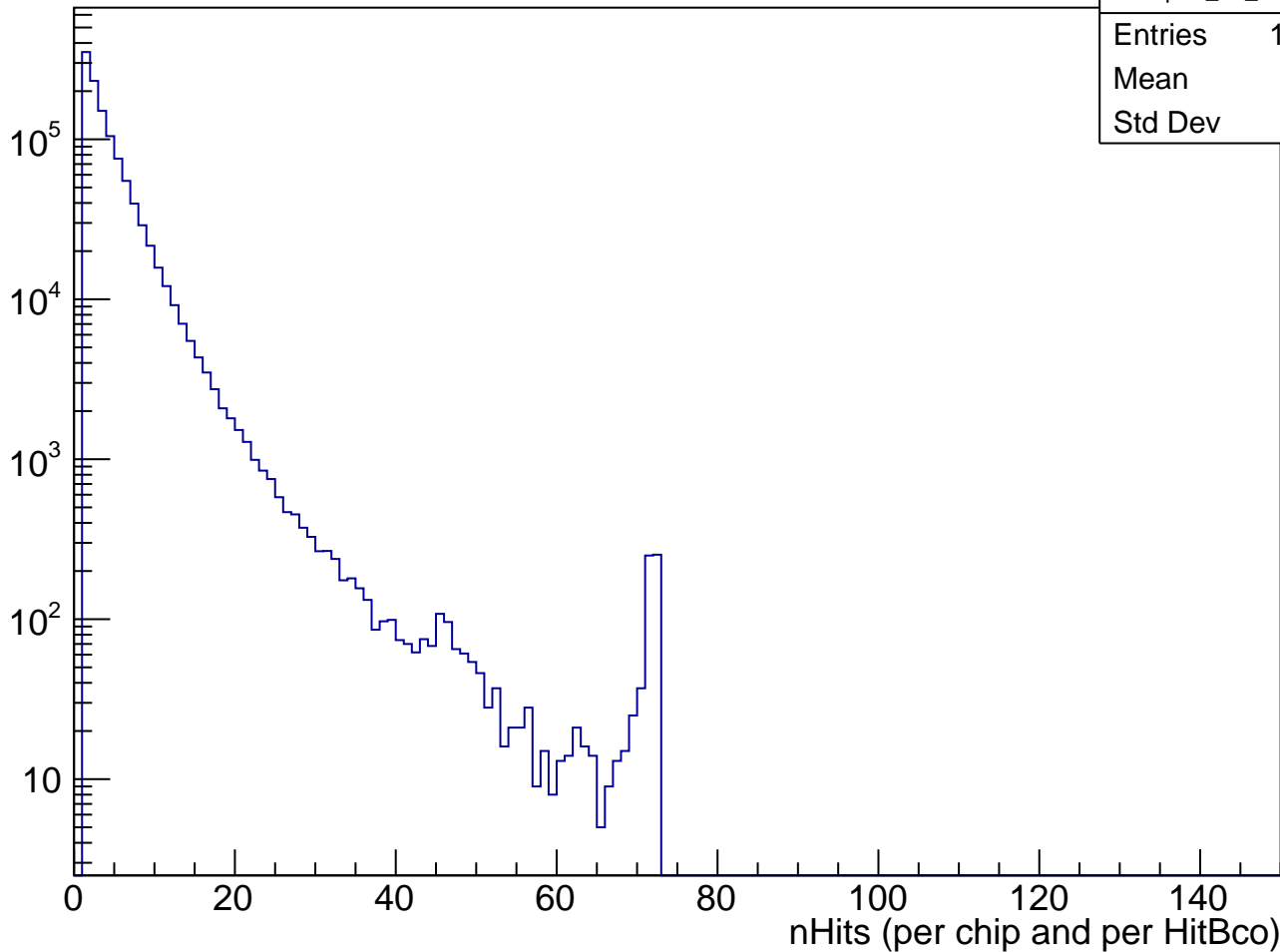
Entries



nChipHit_F2_Fch2_Chip25	
Entries	1036352
Mean	3.389
Std Dev	3.784

nChipHit_F2_Fch2_Chip3

Entries



nChipHit_F2_Fch2_Chip3

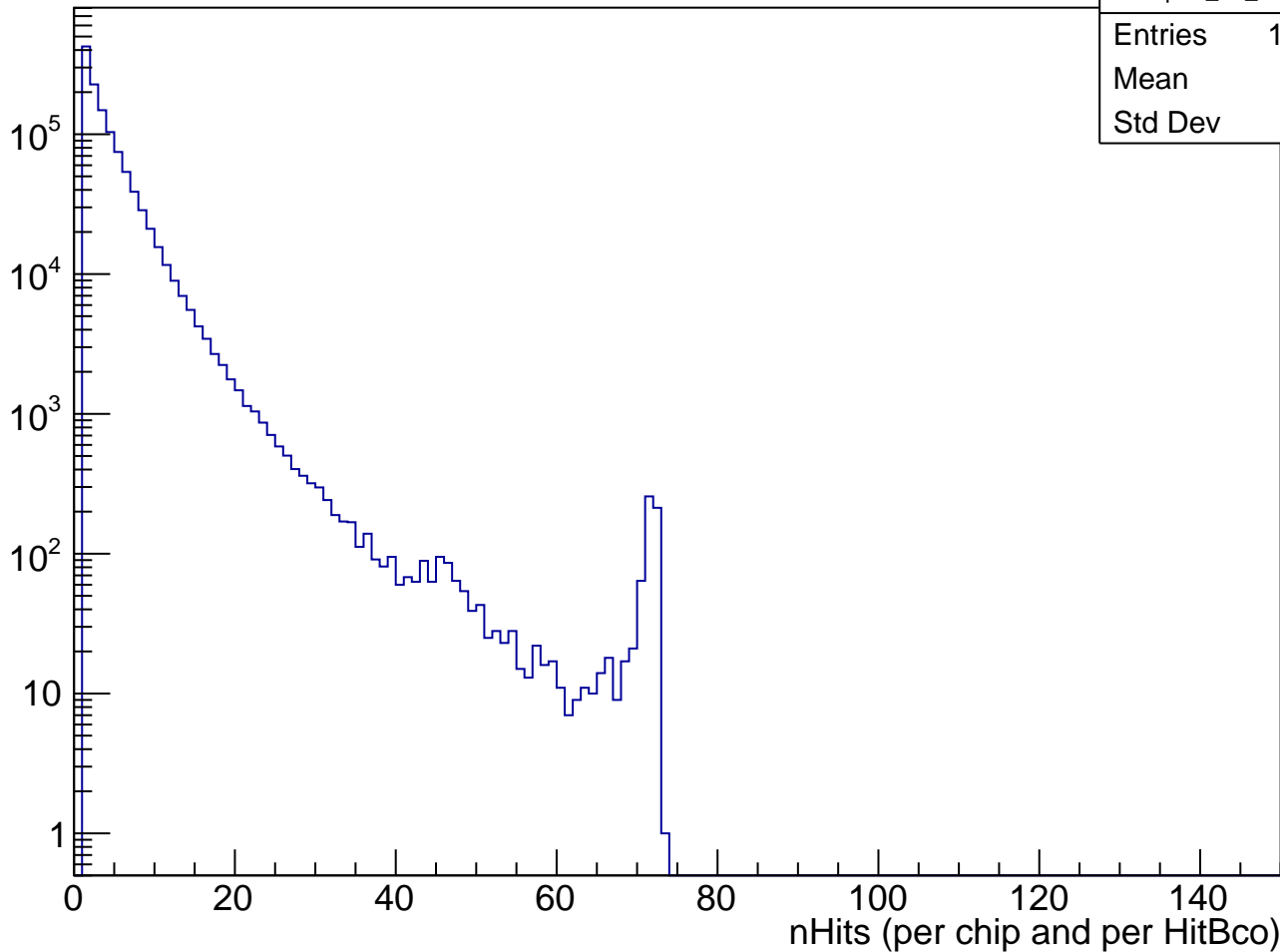
Entries 1134120

Mean 3.73

Std Dev 4.204

nChipHit_F2_Fch2_Chip4

Entries



nChipHit_F2_Fch2_Chip4

Entries 1194535

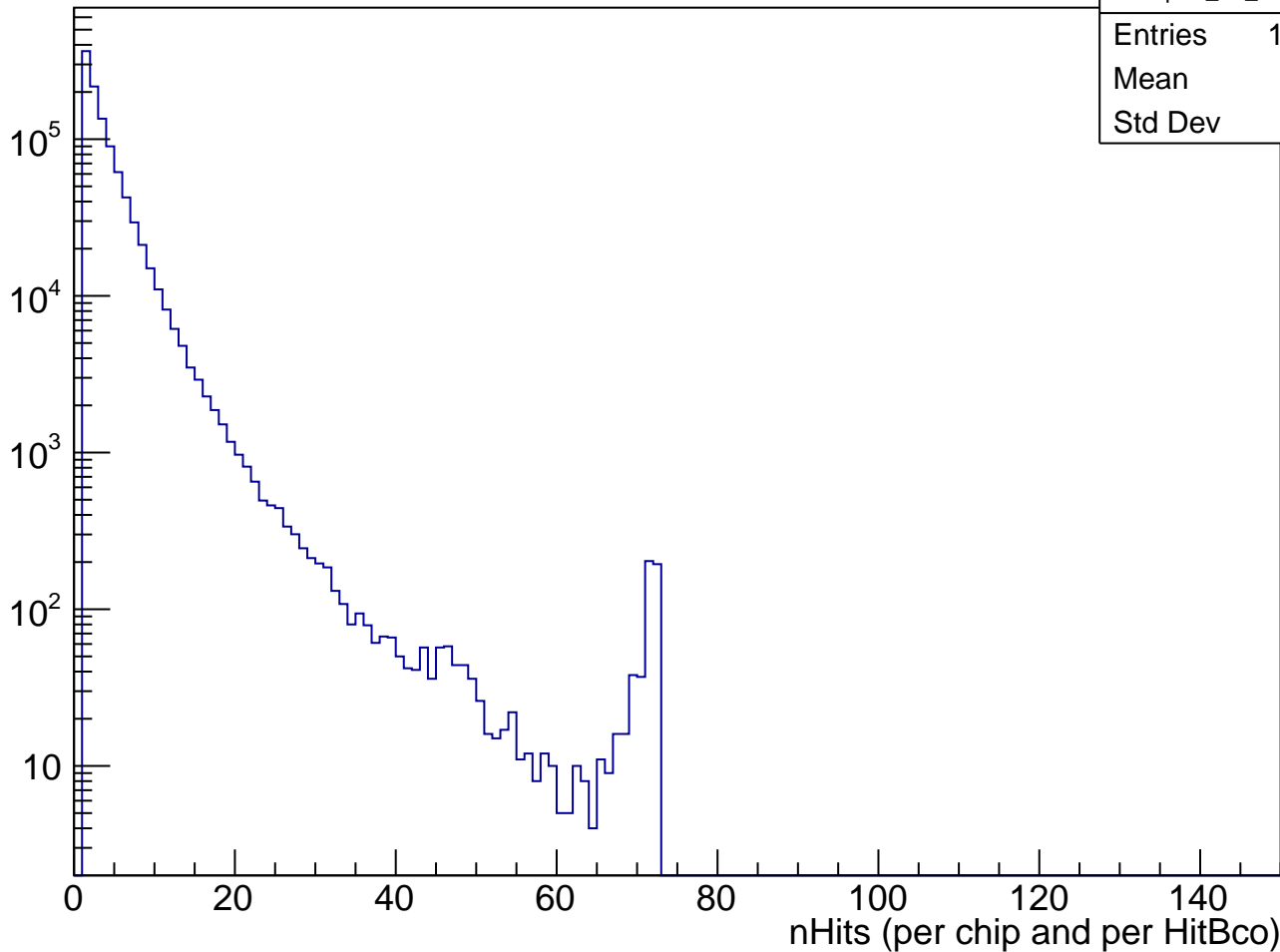
Mean 3.546

Std Dev 4.102

nHits (per chip and per HitBco)

nChipHit_F2_Fch2_Chip5

Entries



nChipHit_F2_Fch2_Chip5

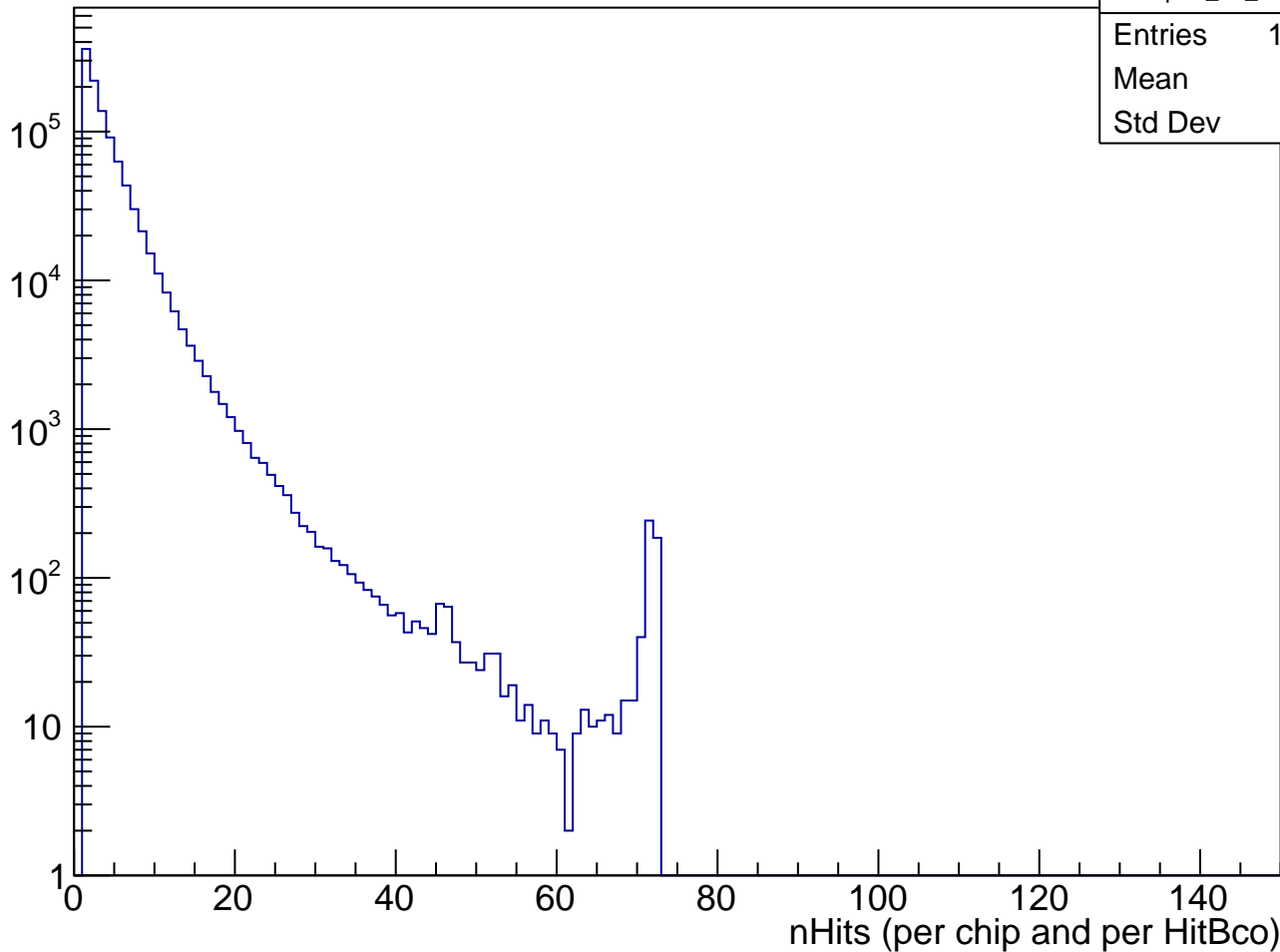
Entries 1026995

Mean 3.312

Std Dev 3.802

nChipHit_F2_Fch2_Chip6

Entries



nChipHit_F2_Fch2_Chip6

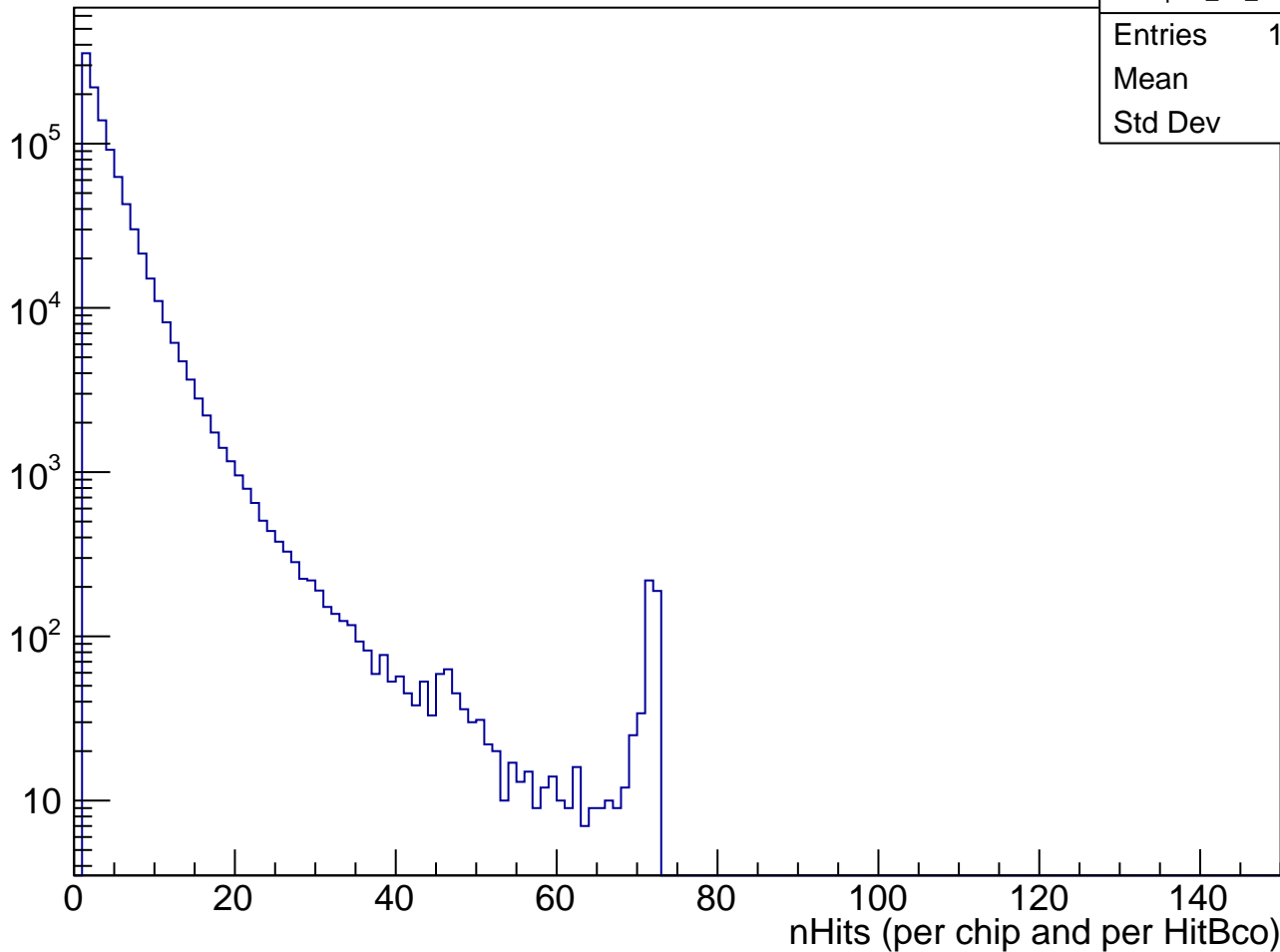
Entries 1032004

Mean 3.332

Std Dev 3.809

nChipHit_F2_Fch2_Chip7

Entries



nChipHit_F2_Fch2_Chip7

Entries 1027887

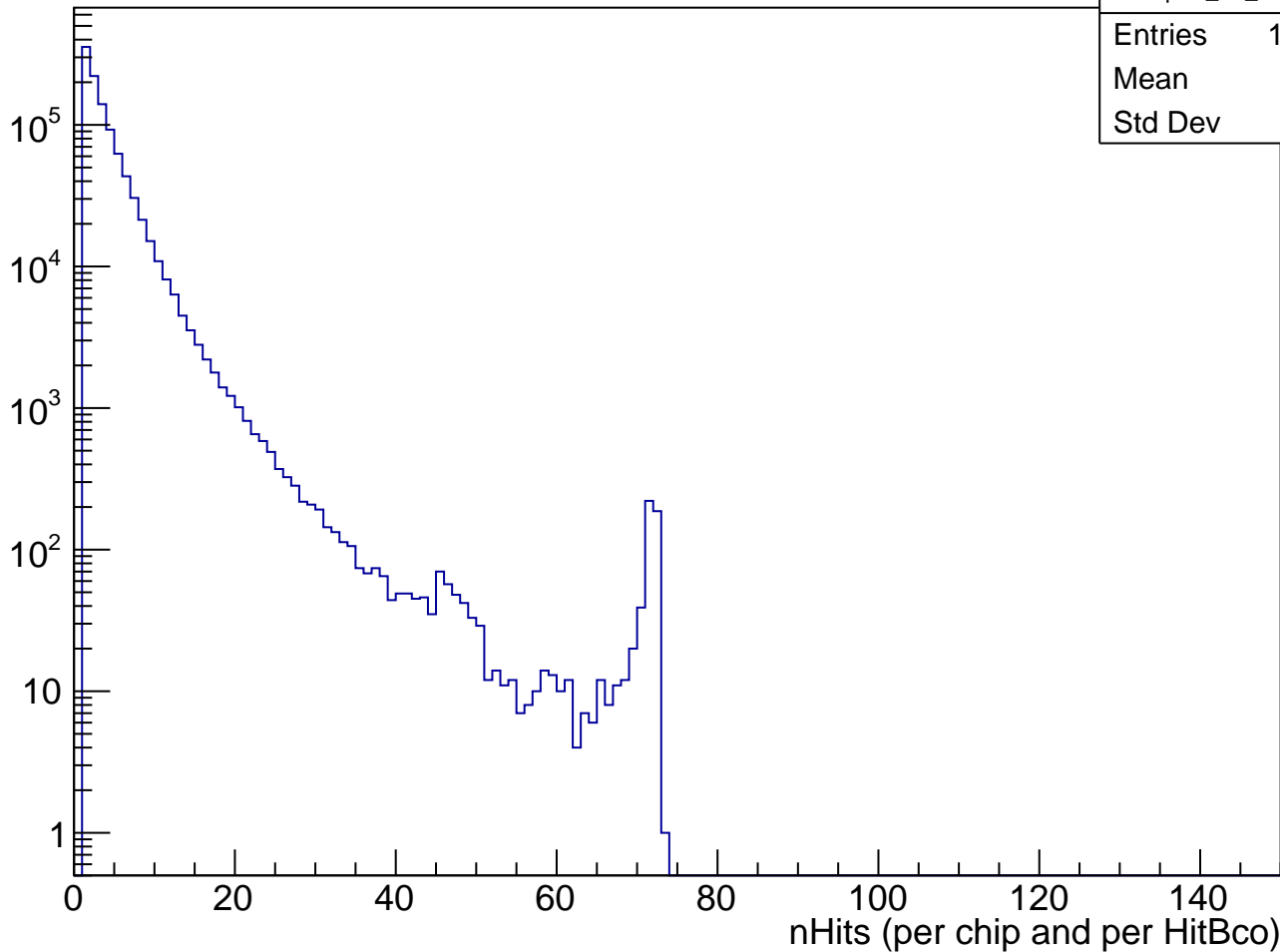
Mean 3.329

Std Dev 3.785

nHits (per chip and per HitBco)

nChipHit_F2_Fch2_Chip8

Entries



nChipHit_F2_Fch2_Chip8

Entries 1031727

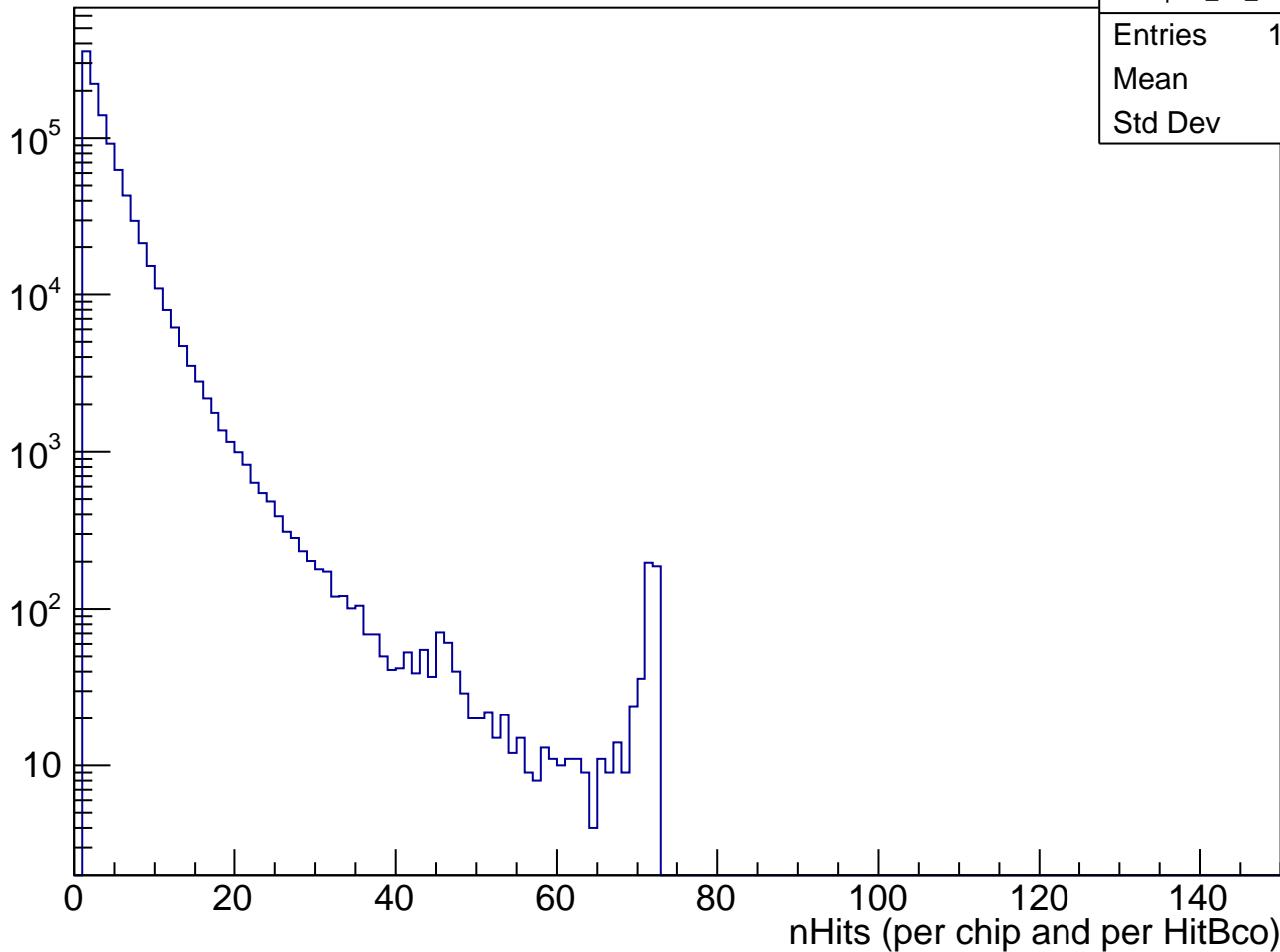
Mean 3.328

Std Dev 3.767

nHits (per chip and per HitBco)

nChipHit_F2_Fch2_Chip9

Entries



nChipHit_F2_Fch2_Chip9

Entries 1031867

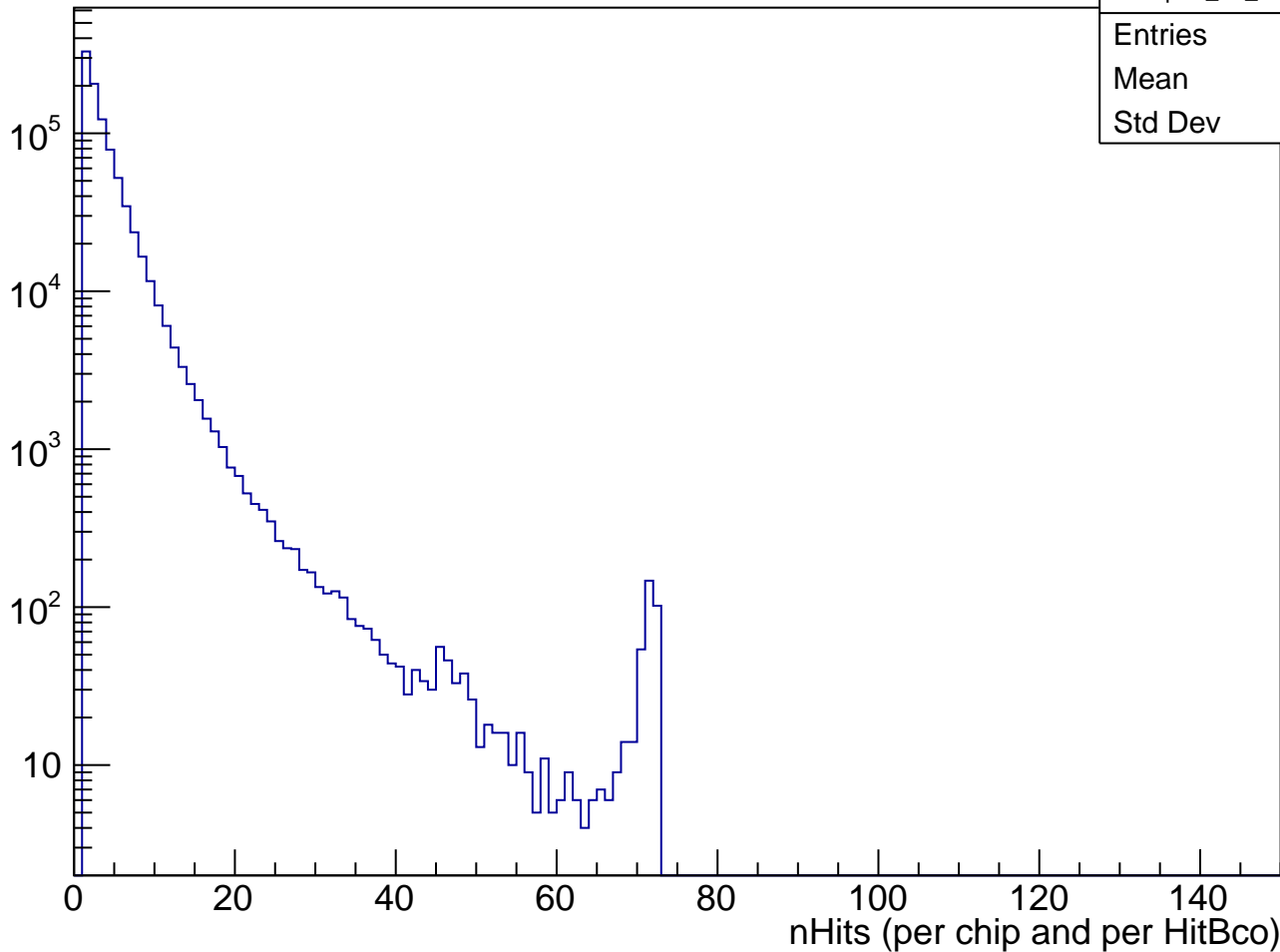
Mean 3.317

Std Dev 3.747

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip0

Entries



nChipHit_F2_Fch3_Chip0

Entries 911816

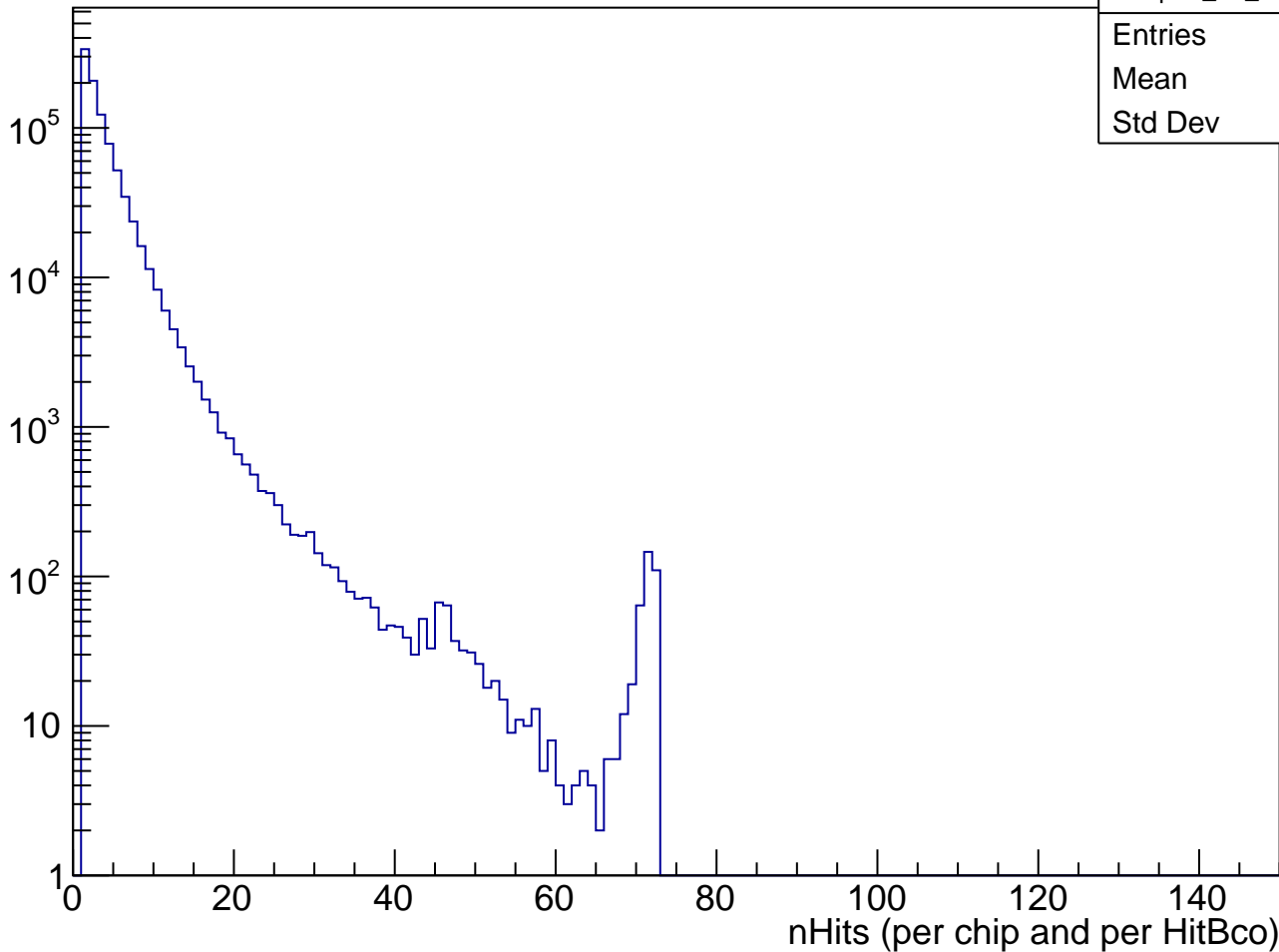
Mean 3.121

Std Dev 3.521

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip1

Entries

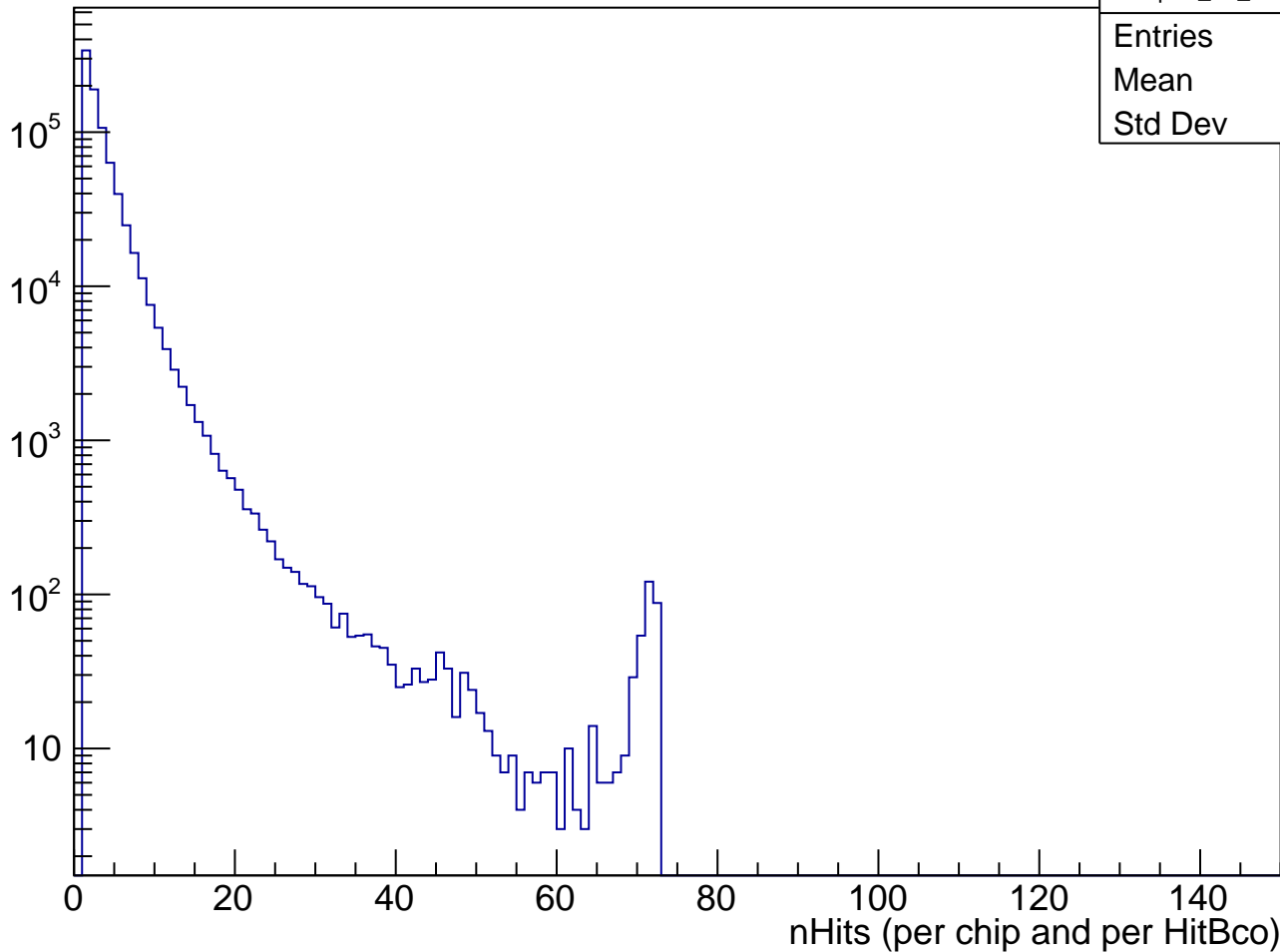


nChipHit_F2_Fch3_Chip1	
Entries	918890
Mean	3.104
Std Dev	3.53

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip10

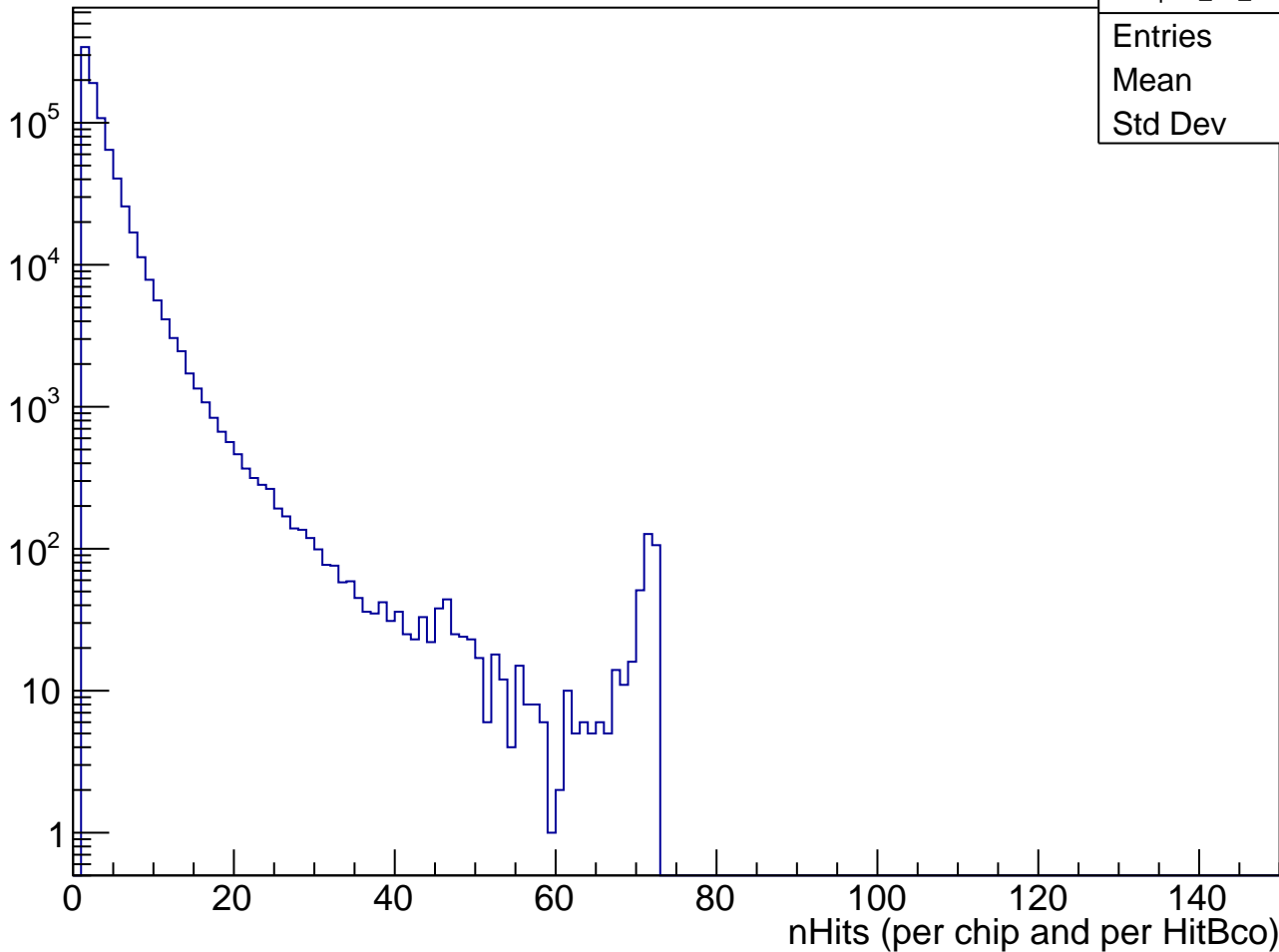
Entries



nChipHit_F2_Fch3_Chip10	
Entries	822571
Mean	2.779
Std Dev	3.234

nChipHit_F2_Fch3_Chip11

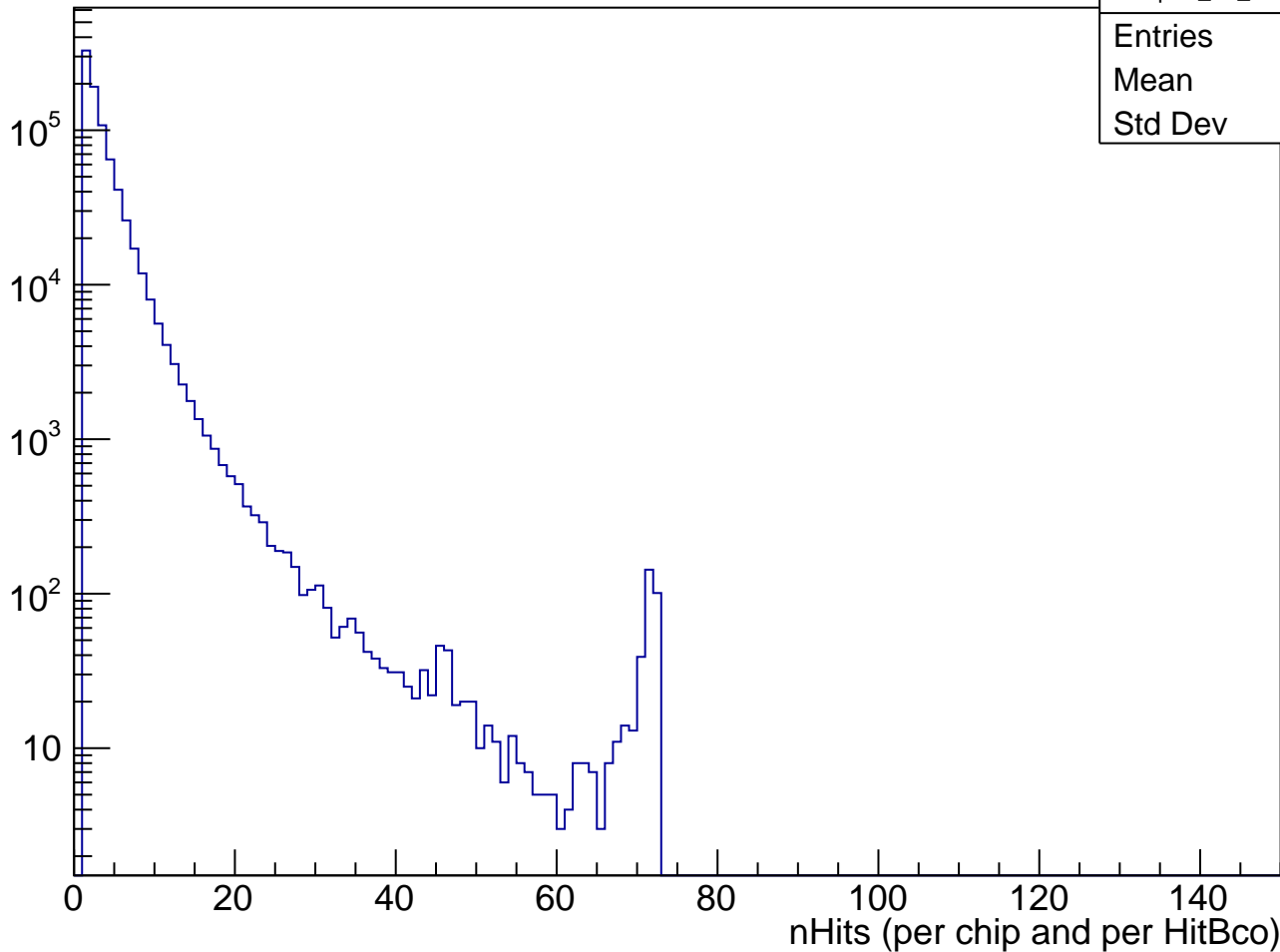
Entries



nChipHit_F2_Fch3_Chip11	
Entries	832191
Mean	2.798
Std Dev	3.255

nChipHit_F2_Fch3_Chip12

Entries

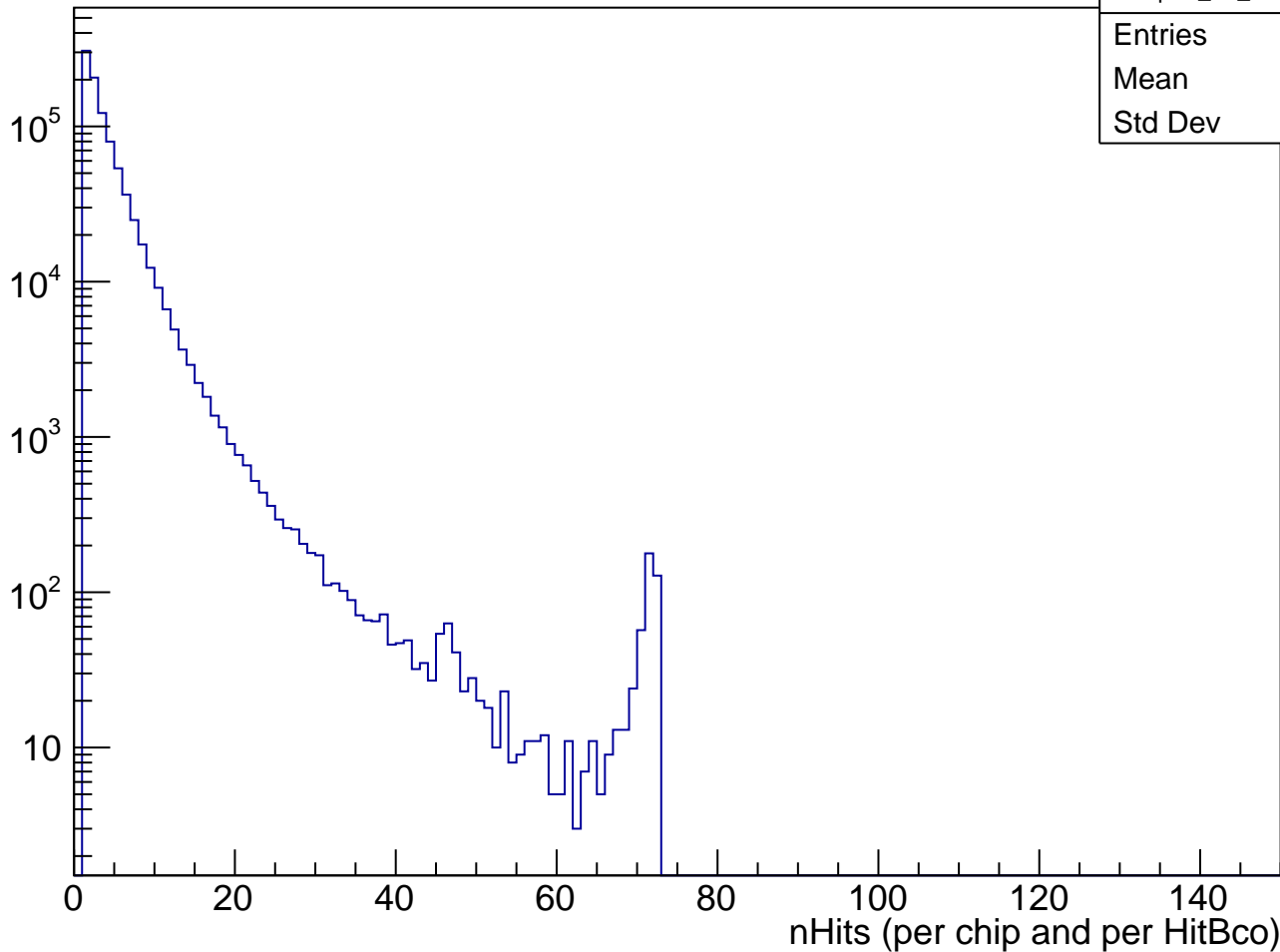


nChipHit_F2_Fch3_Chip12	
Entries	820971
Mean	2.833
Std Dev	3.26

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip13

Entries

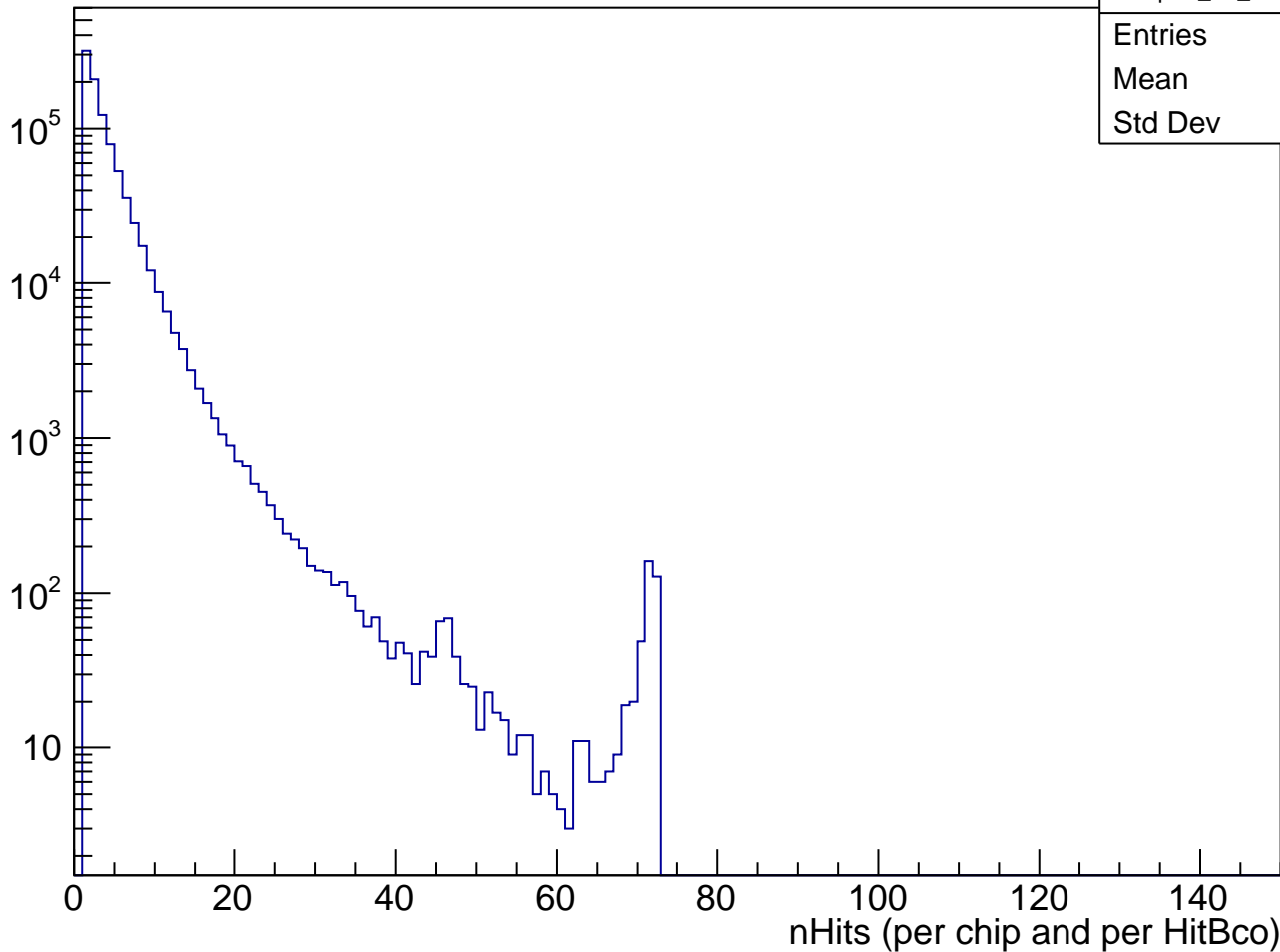


nChipHit_F2_Fch3_Chip13	
Entries	900417
Mean	3.254
Std Dev	3.685

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip14

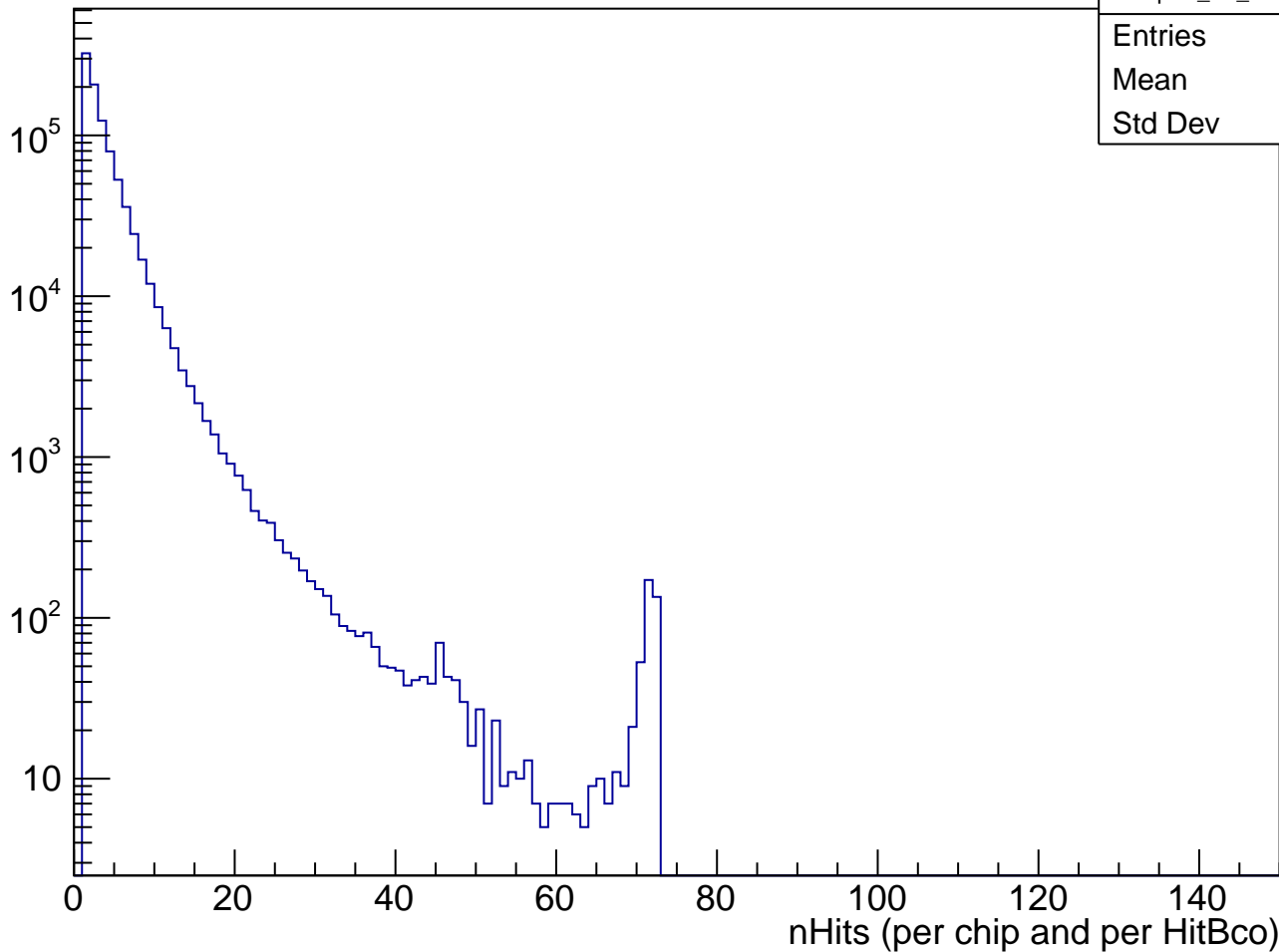
Entries



nChipHit_F2_Fch3_Chip14	
Entries	909968
Mean	3.202
Std Dev	3.624

nChipHit_F2_Fch3_Chip15

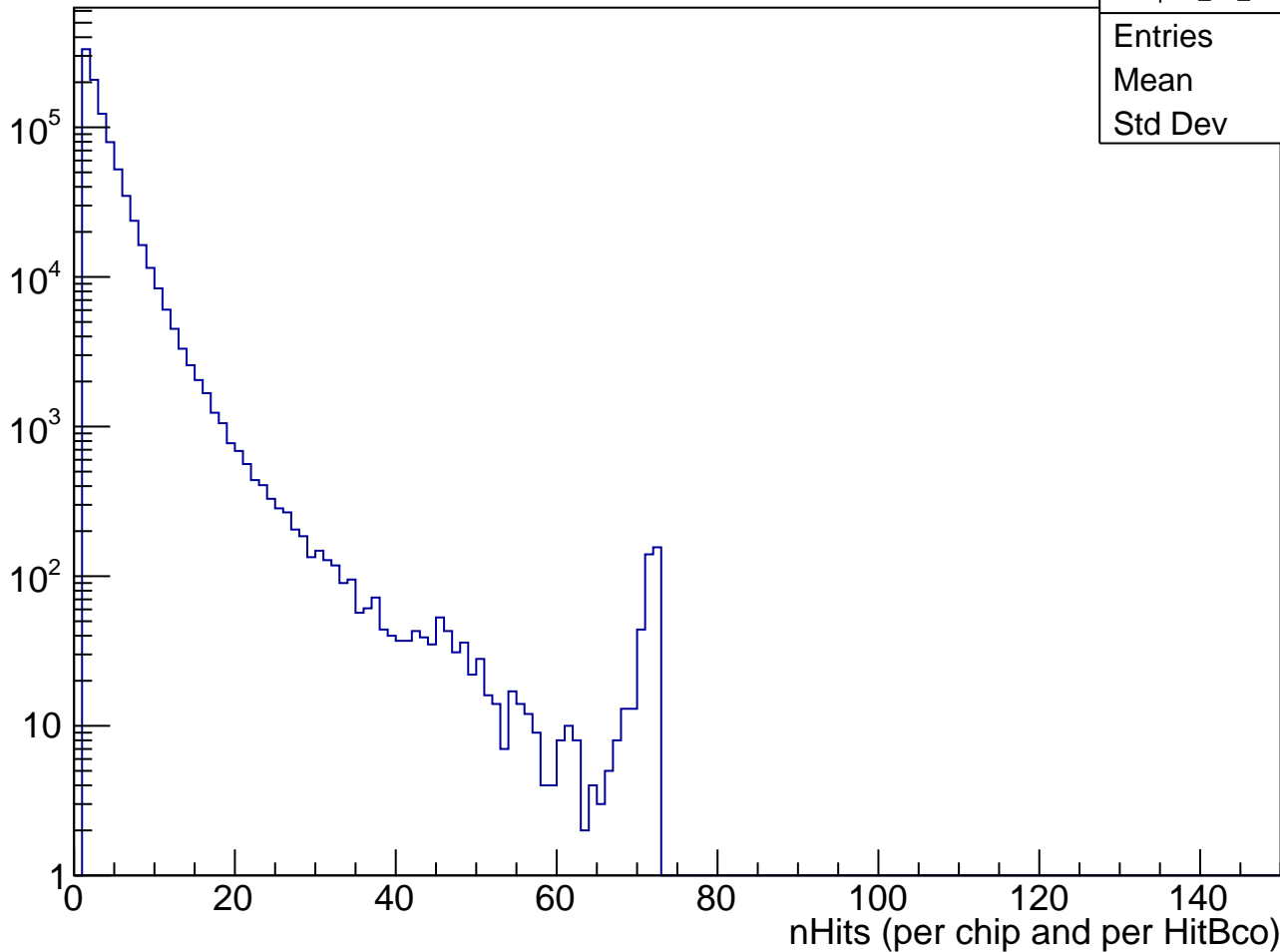
Entries



nChipHit_F2_Fch3_Chip15	
Entries	914895
Mean	3.18
Std Dev	3.621

nChipHit_F2_Fch3_Chip16

Entries

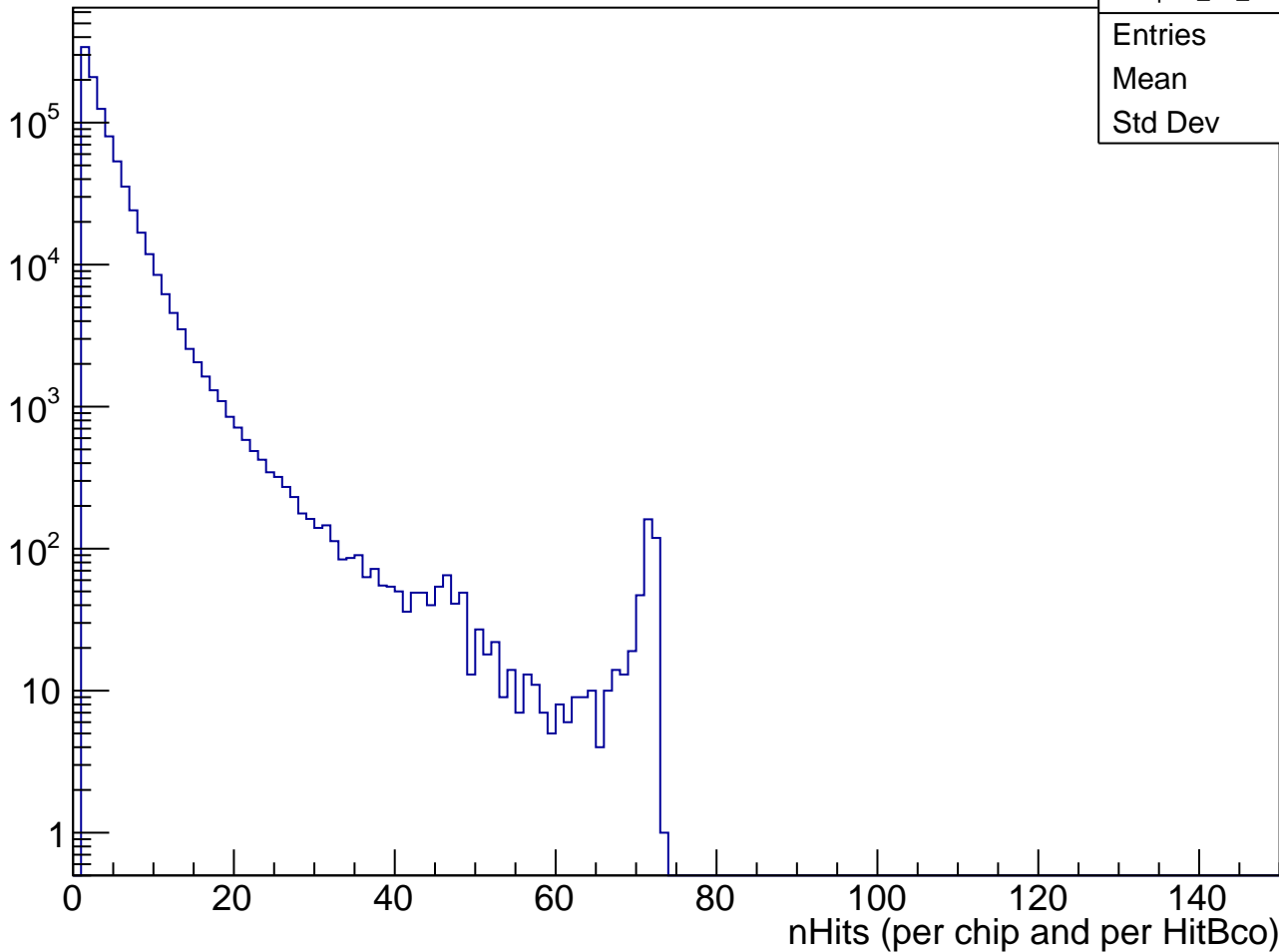


nChipHit_F2_Fch3_Chip16	
Entries	918941
Mean	3.117
Std Dev	3.532

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip17

Entries

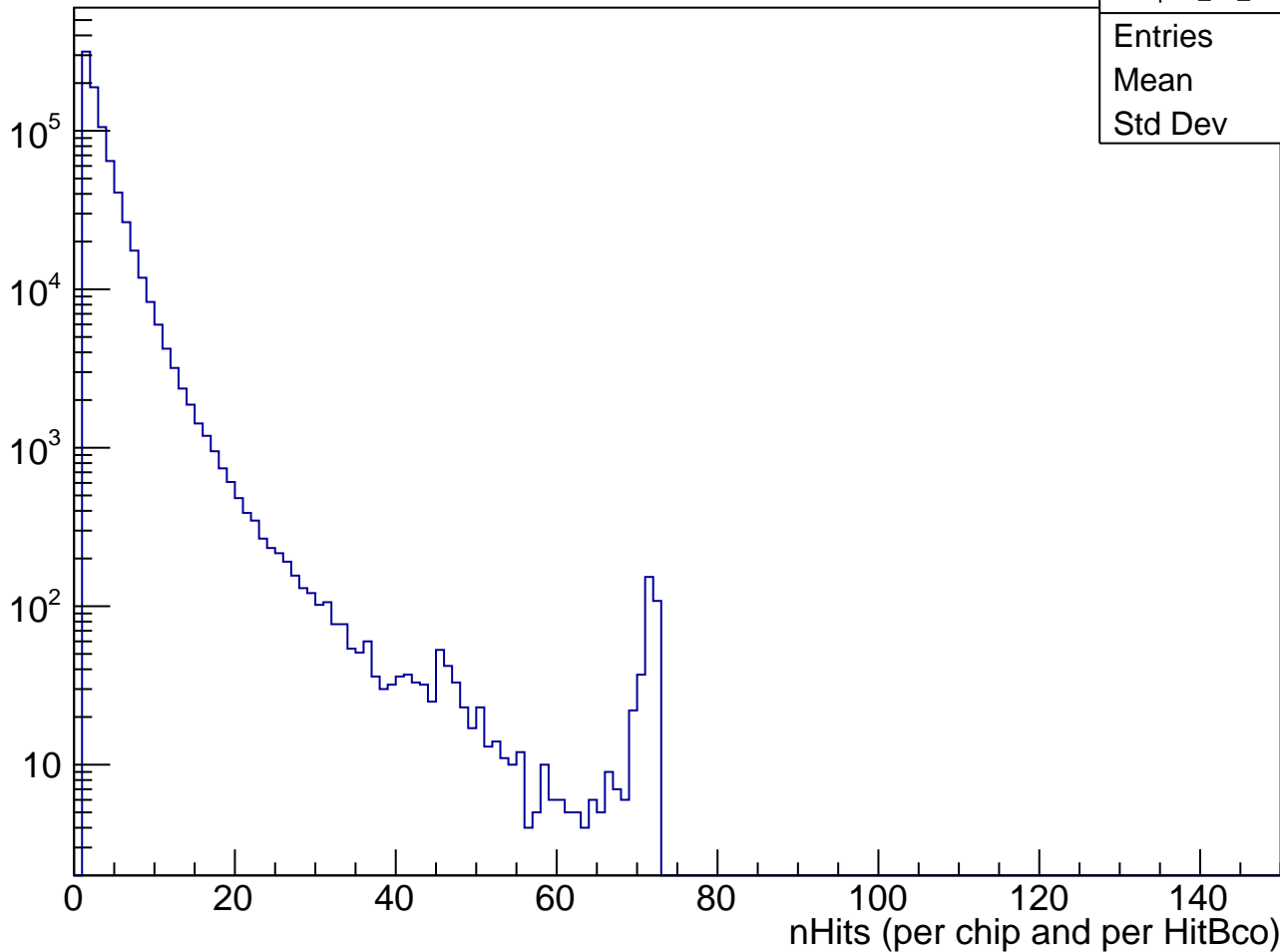


nChipHit_F2_Fch3_Chip17	
Entries	934057
Mean	3.126
Std Dev	3.577

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip18

Entries

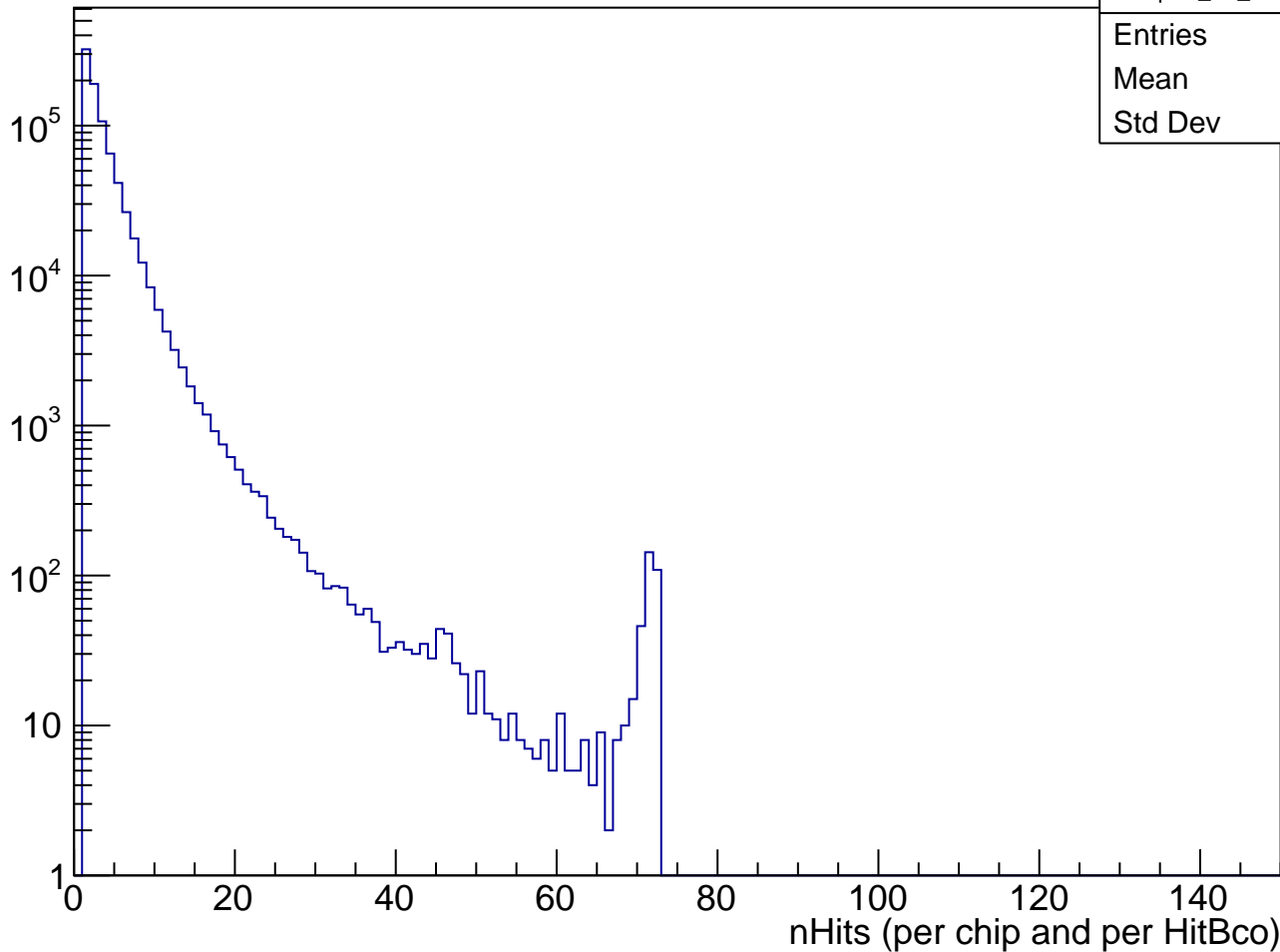


nChipHit_F2_Fch3_Chip18	
Entries	805691
Mean	2.893
Std Dev	3.369

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip19

Entries

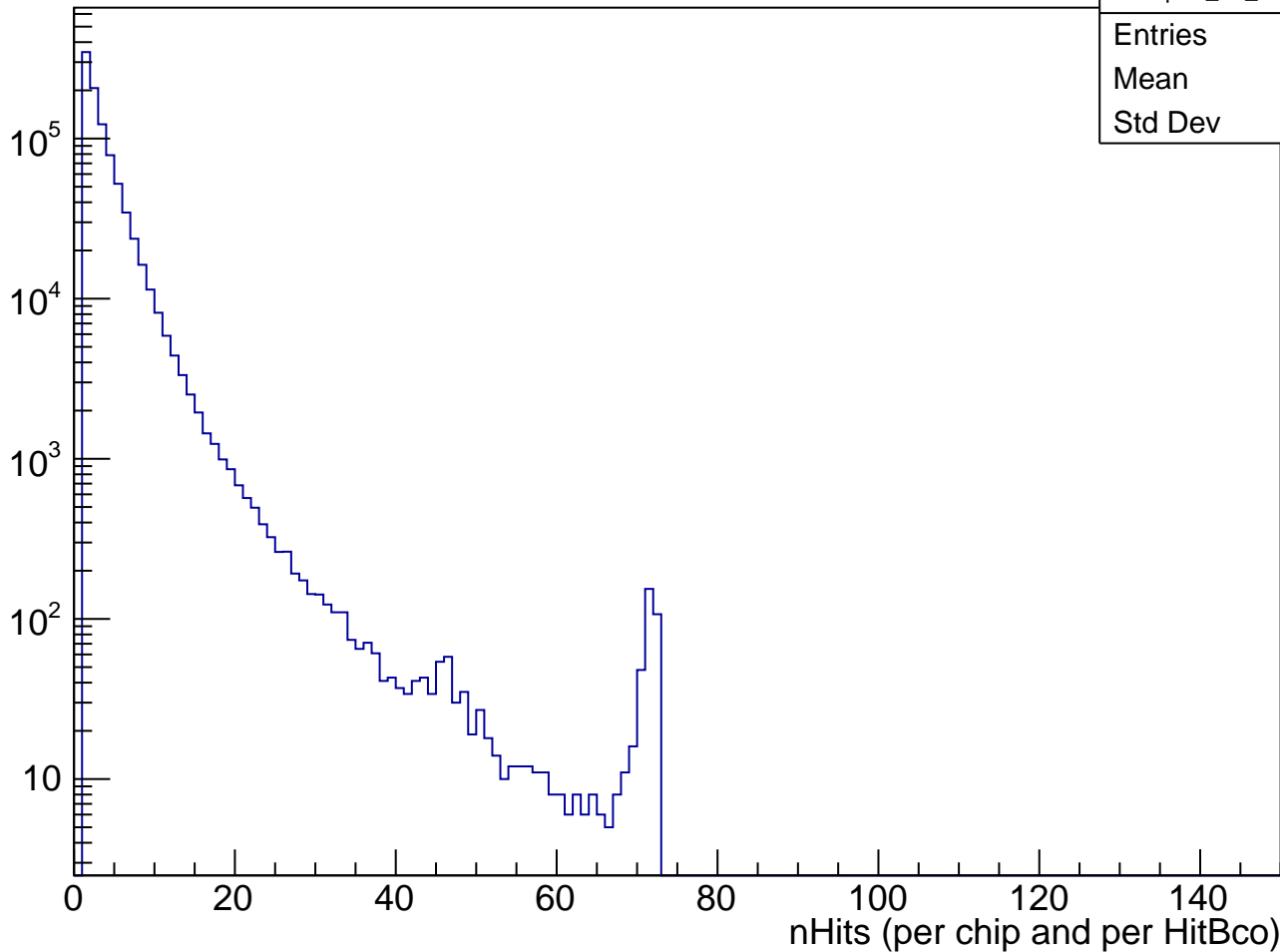


nChipHit_F2_Fch3_Chip19	
Entries	817194
Mean	2.884
Std Dev	3.354

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip2

Entries



nChipHit_F2_Fch3_Chip2

Entries 928927

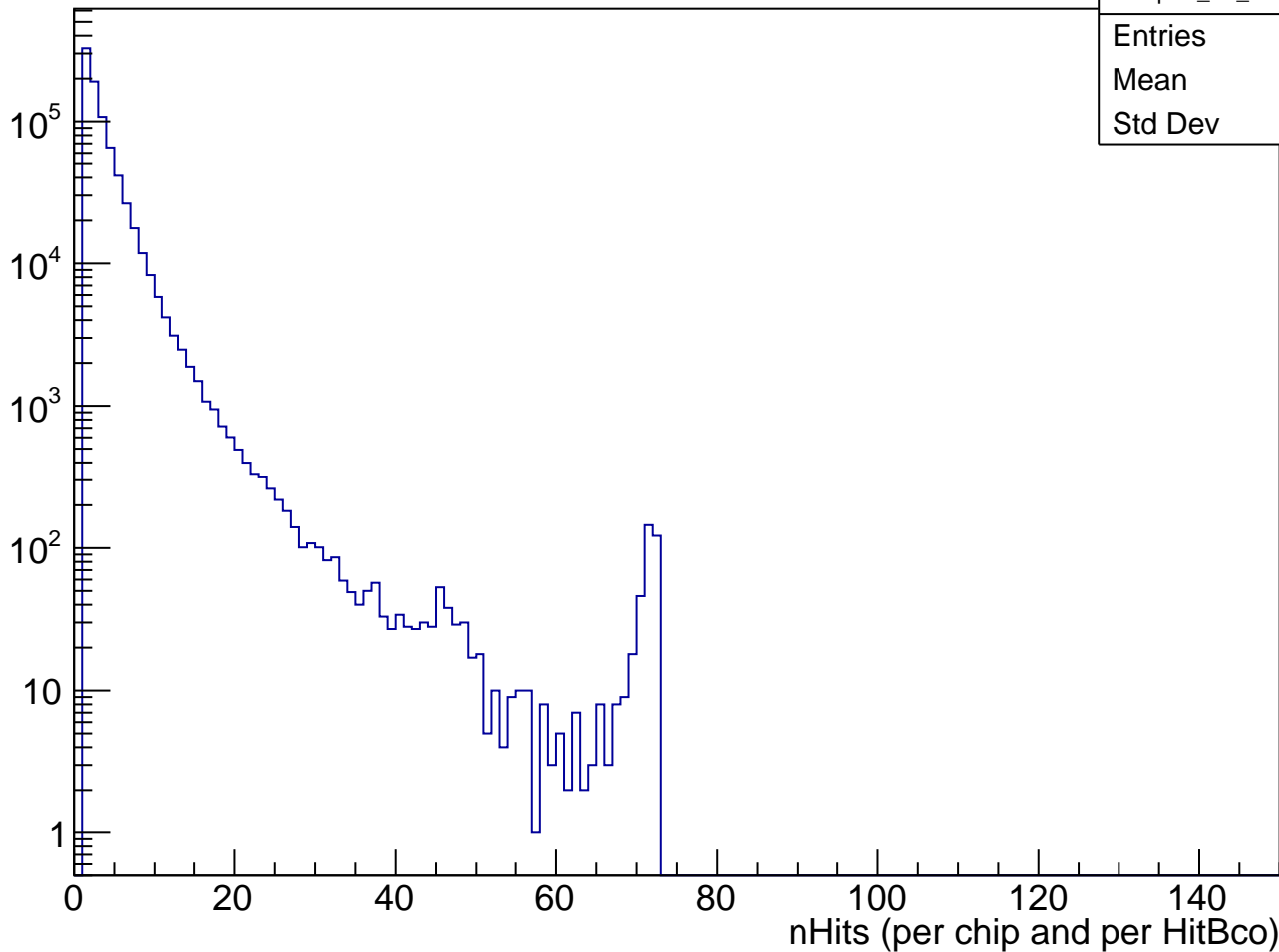
Mean 3.074

Std Dev 3.495

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip20

Entries



nChipHit_F2_Fch3_Chip20

Entries 821754

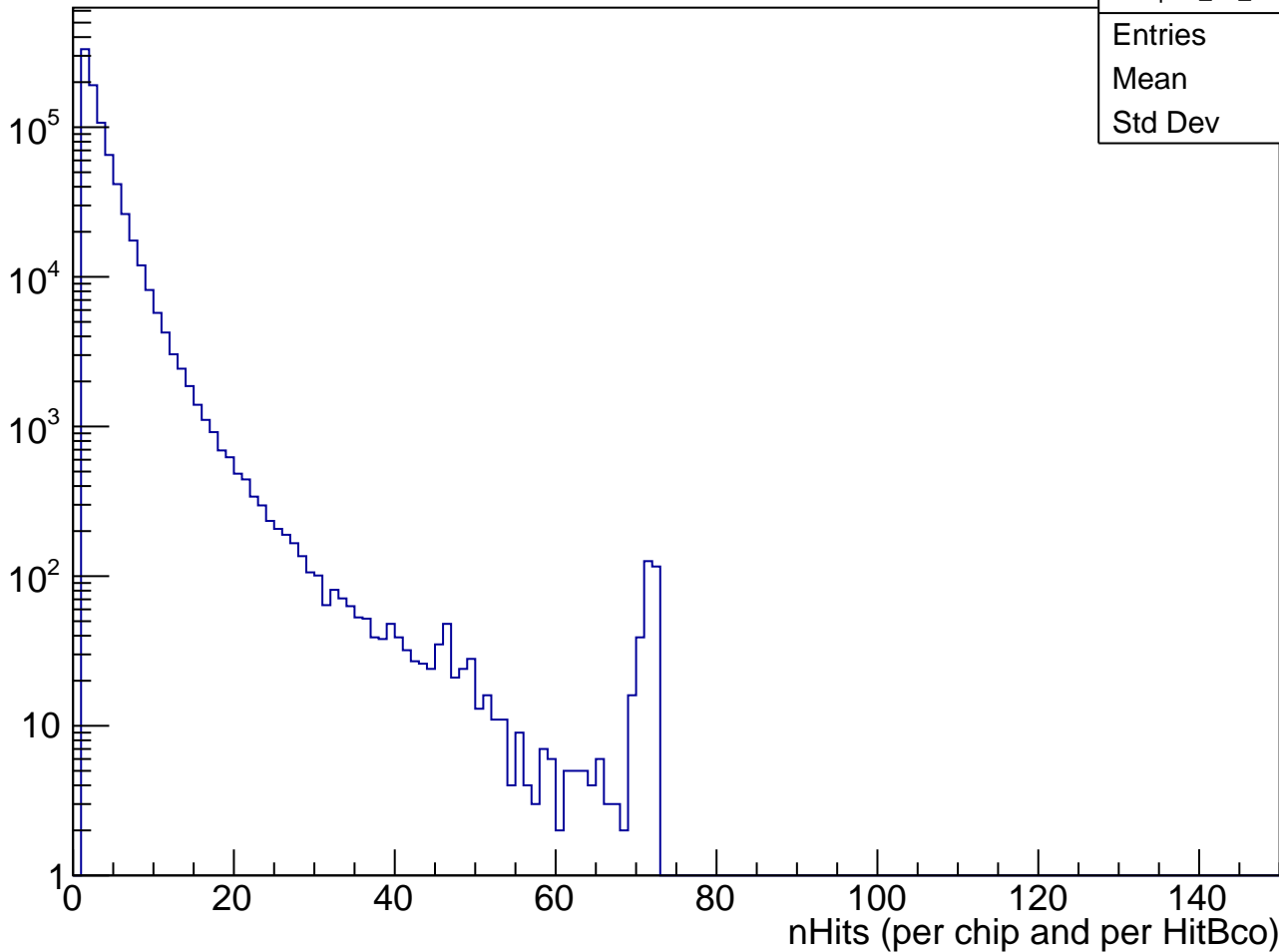
Mean 2.864

Std Dev 3.32

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip21

Entries

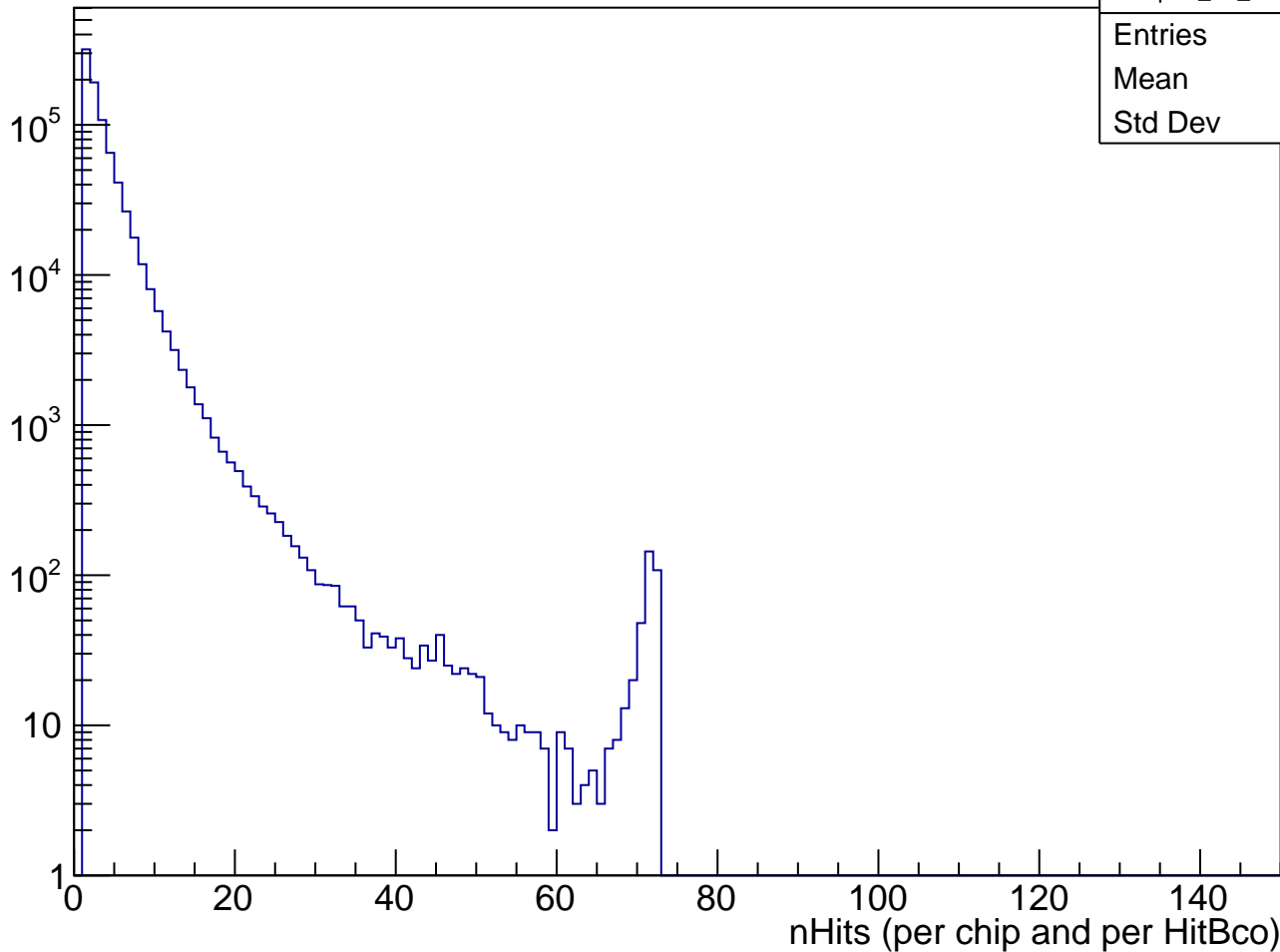


nChipHit_F2_Fch3_Chip21	
Entries	826946
Mean	2.845
Std Dev	3.28

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip22

Entries

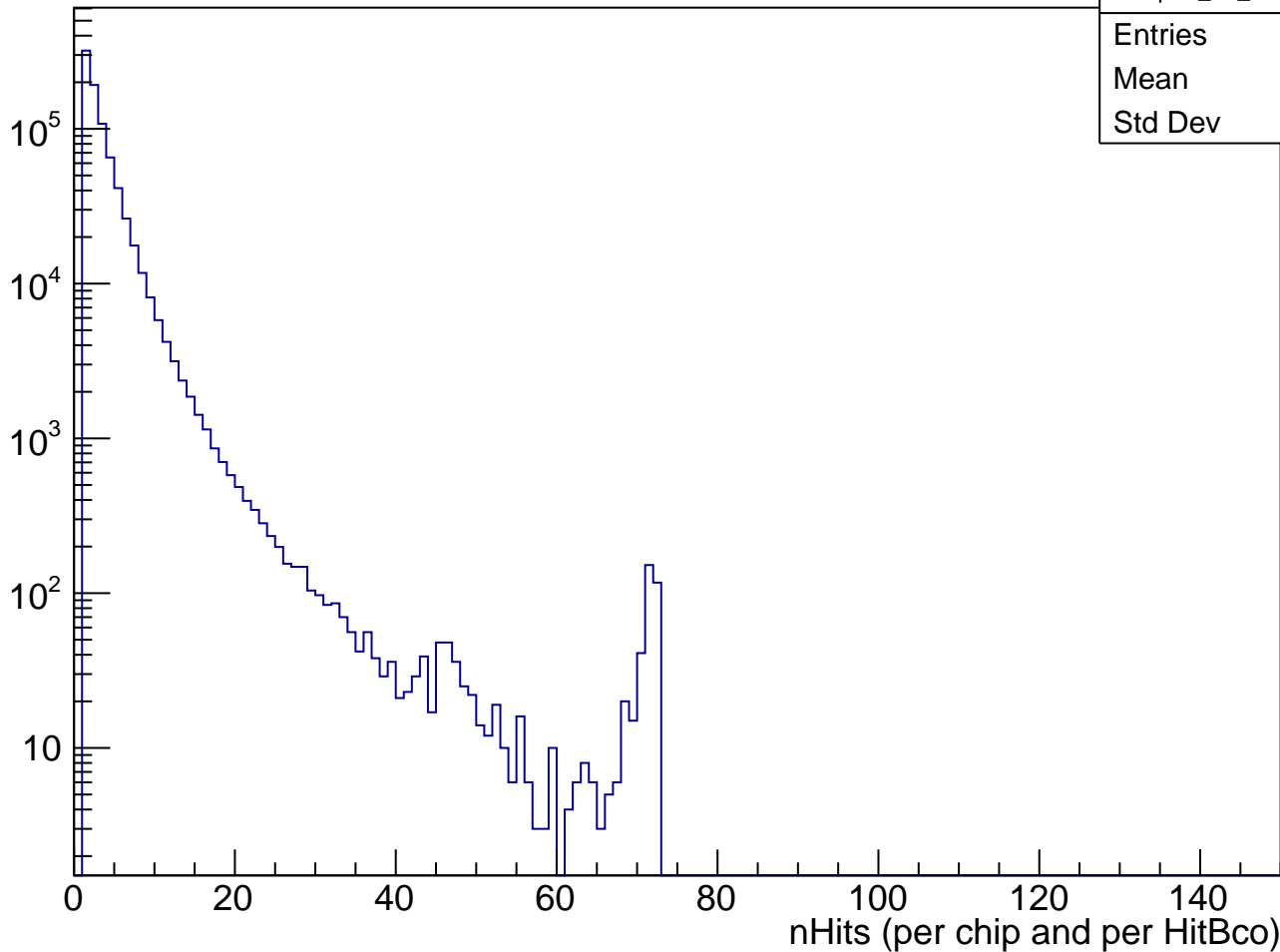


nChipHit_F2_Fch3_Chip22	
Entries	814375
Mean	2.871
Std Dev	3.313

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip23

Entries



nChipHit_F2_Fch3_Chip23

Entries 816976

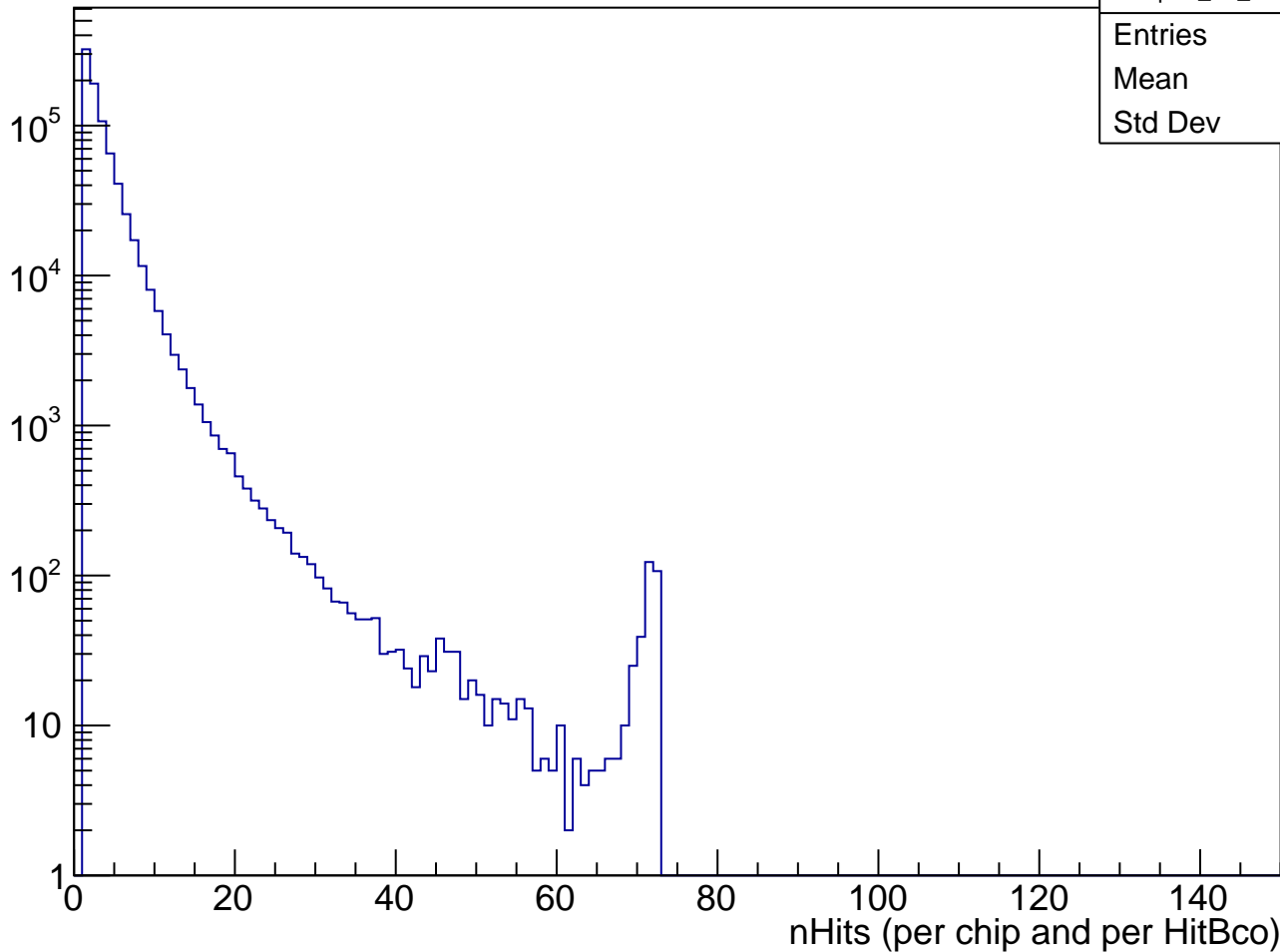
Mean 2.871

Std Dev 3.33

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip24

Entries

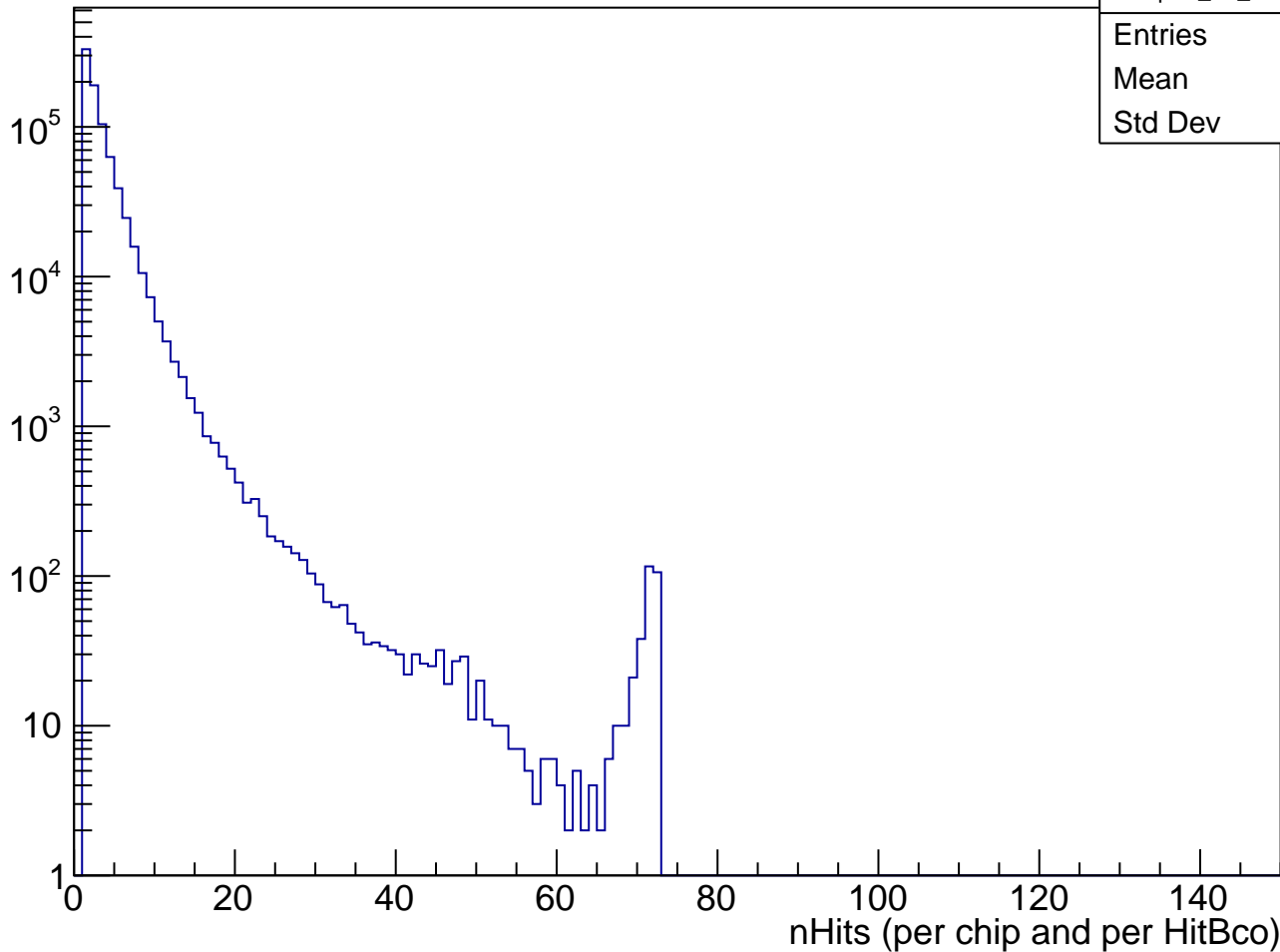


nChipHit_F2_Fch3_Chip24	
Entries	814203
Mean	2.848
Std Dev	3.28

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip25

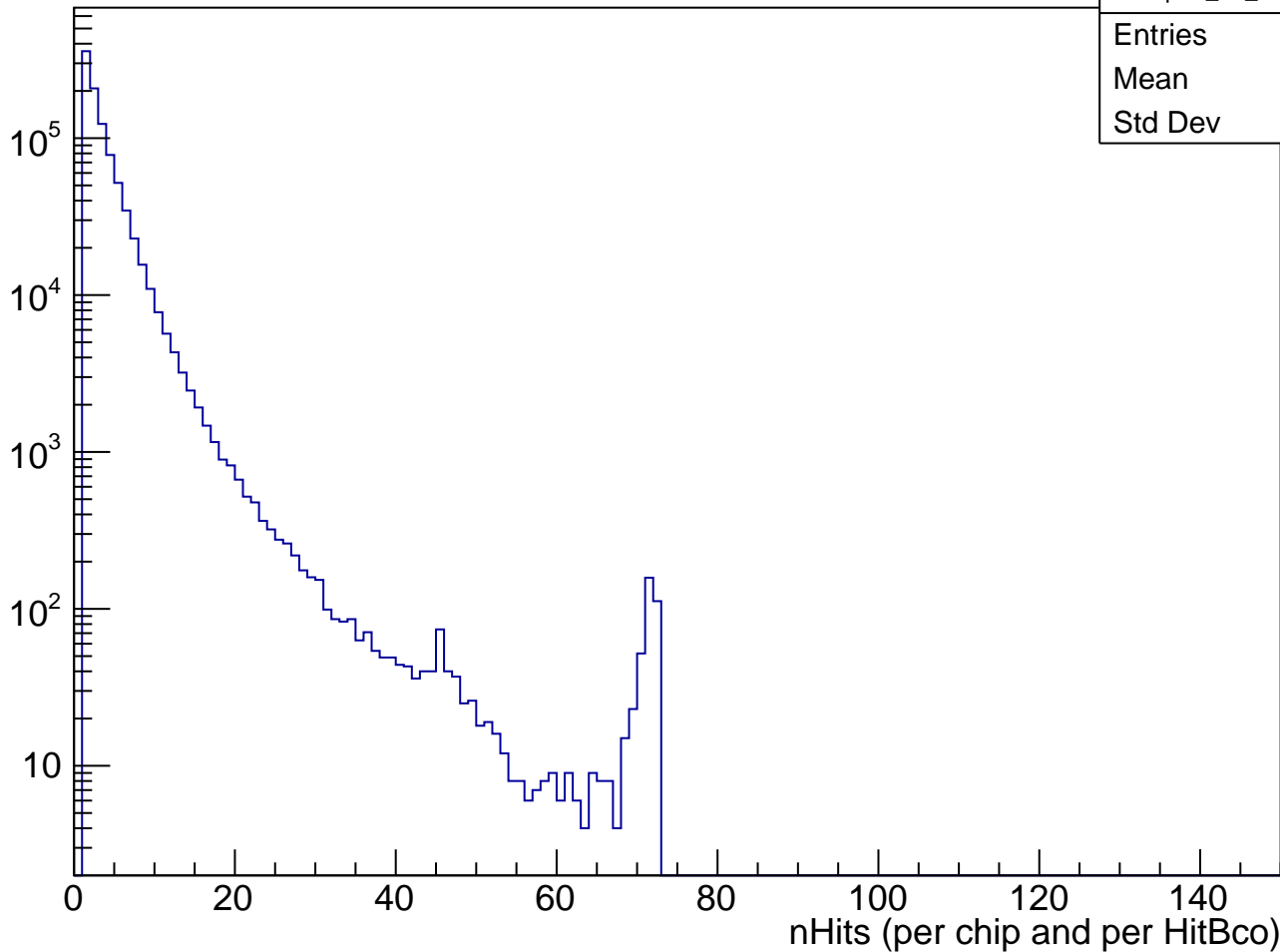
Entries



nChipHit_F2_Fch3_Chip25	
Entries	806955
Mean	2.755
Std Dev	3.159

nChipHit_F2_Fch3_Chip3

Entries



nChipHit_F2_Fch3_Chip3

Entries 938184

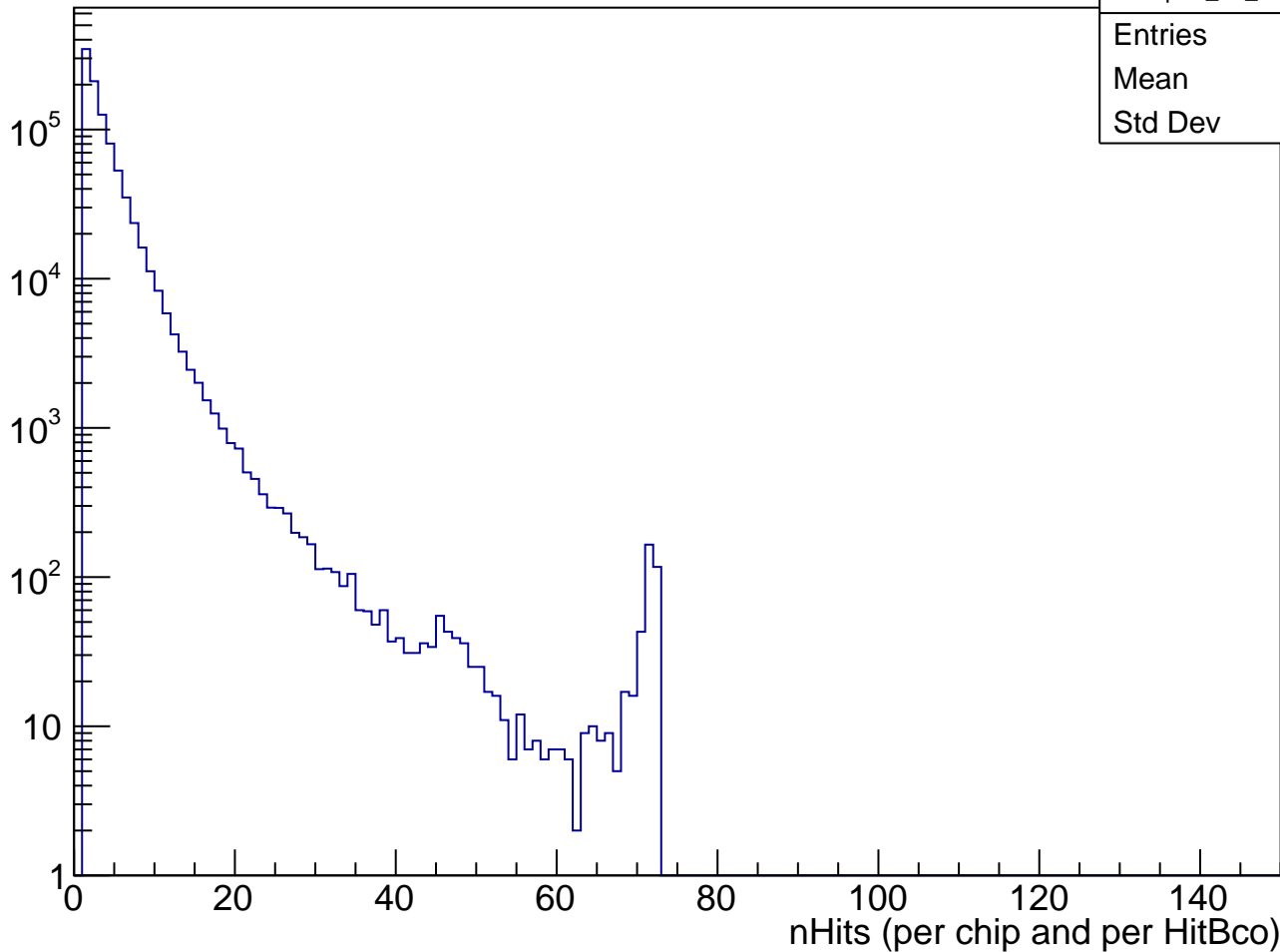
Mean 3.025

Std Dev 3.467

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip4

Entries



nChipHit_F2_Fch3_Chip4

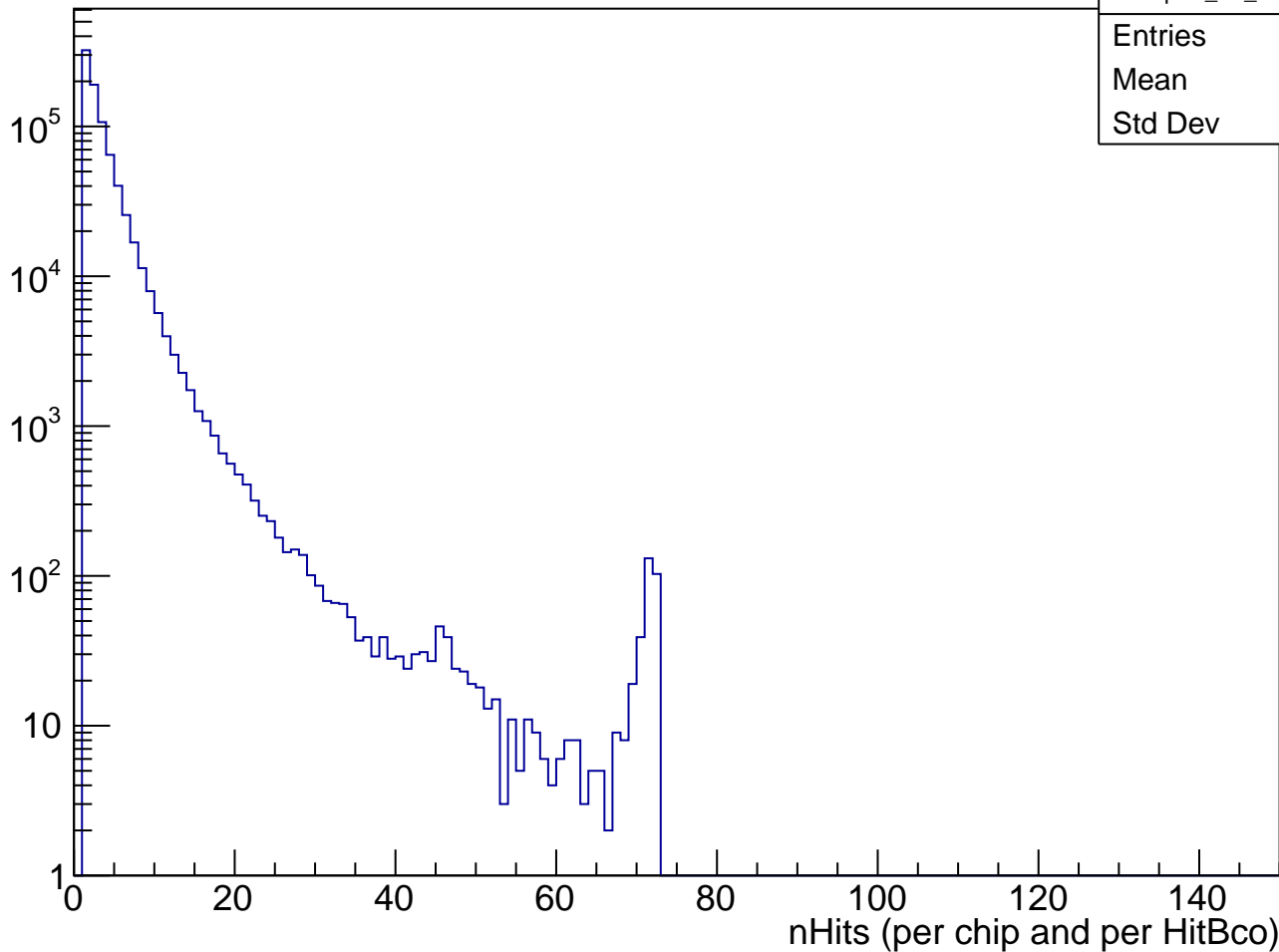
Entries 938264

Mean 3.069

Std Dev 3.473

nChipHit_F2_Fch3_Chip5

Entries



nChipHit_F2_Fch3_Chip5

Entries 810643

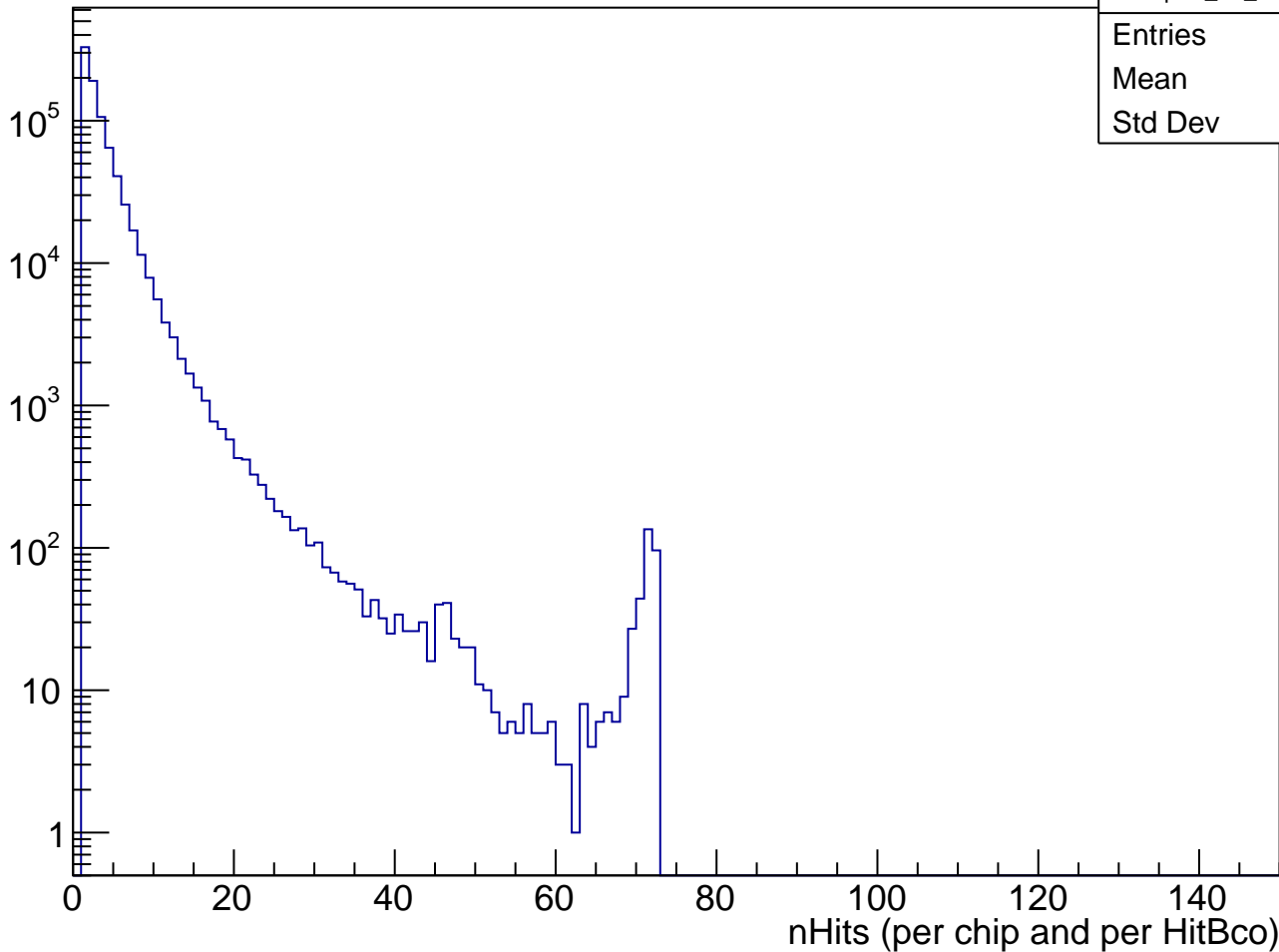
Mean 2.83

Std Dev 3.249

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip6

Entries



nChipHit_F2_Fch3_Chip6

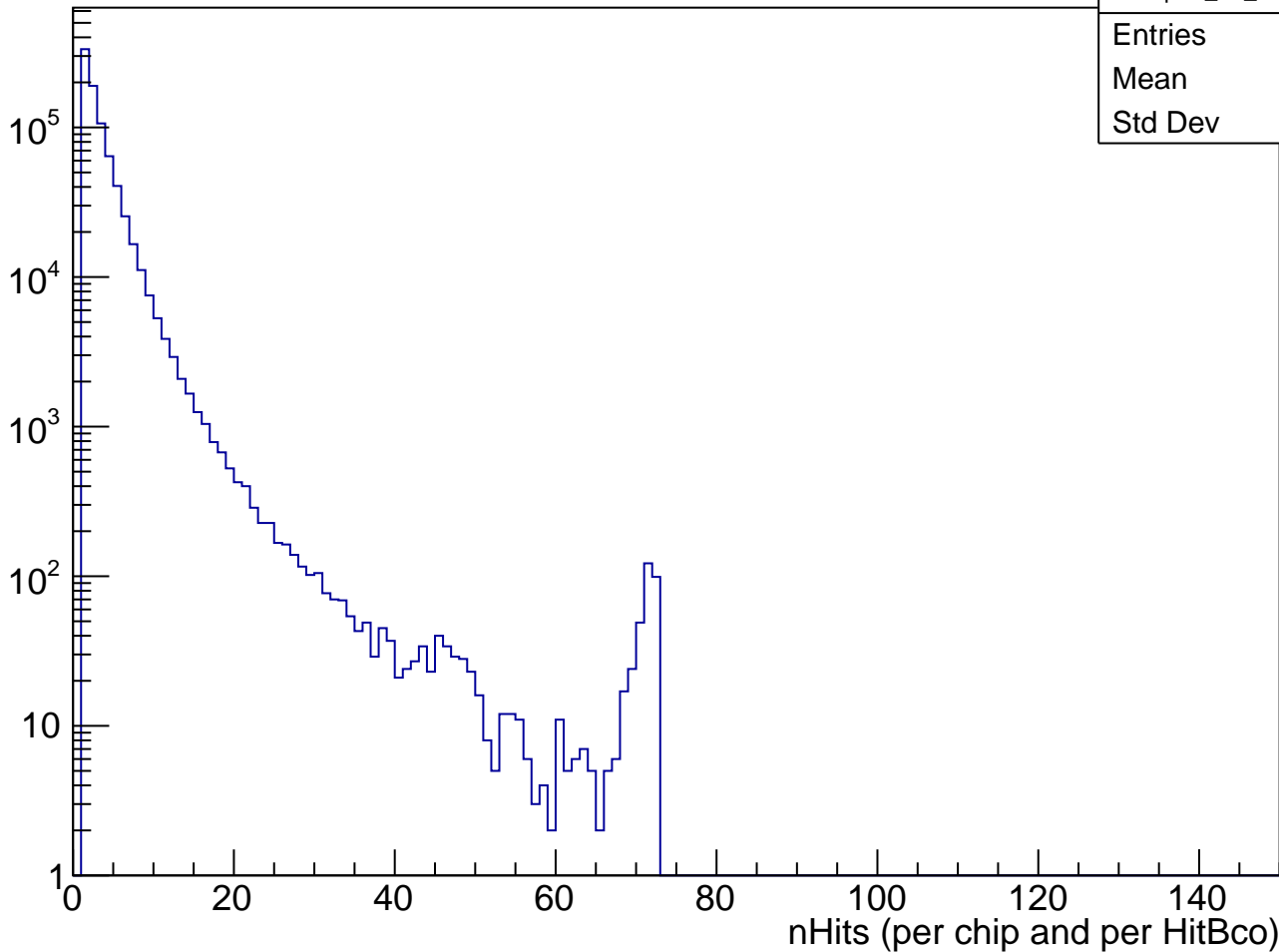
Entries 816874

Mean 2.814

Std Dev 3.227

nChipHit_F2_Fch3_Chip7

Entries

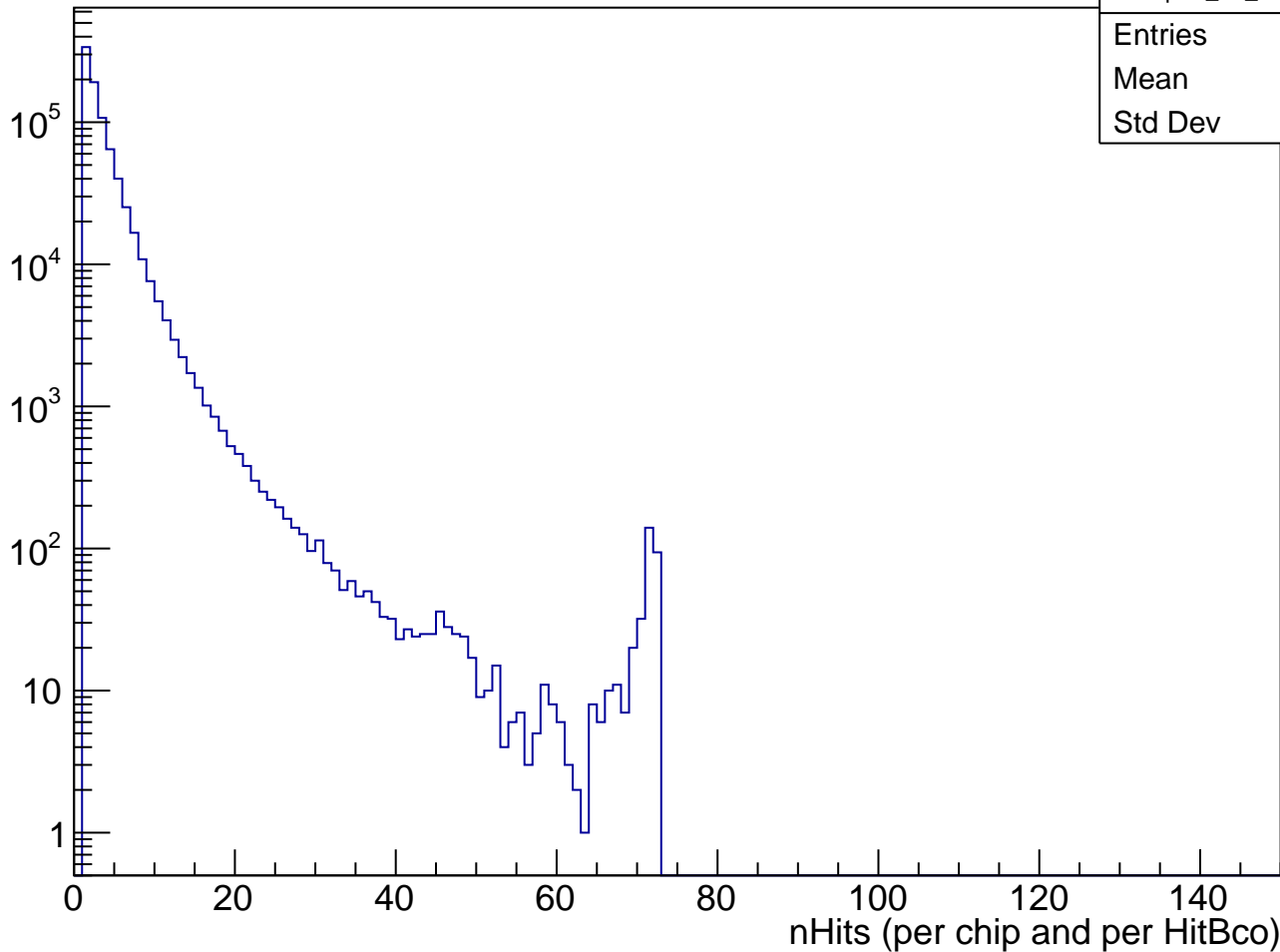


nChipHit_F2_Fch3_Chip7	
Entries	818496
Mean	2.789
Std Dev	3.219

nHits (per chip and per HitBco)

nChipHit_F2_Fch3_Chip8

Entries



nChipHit_F2_Fch3_Chip8

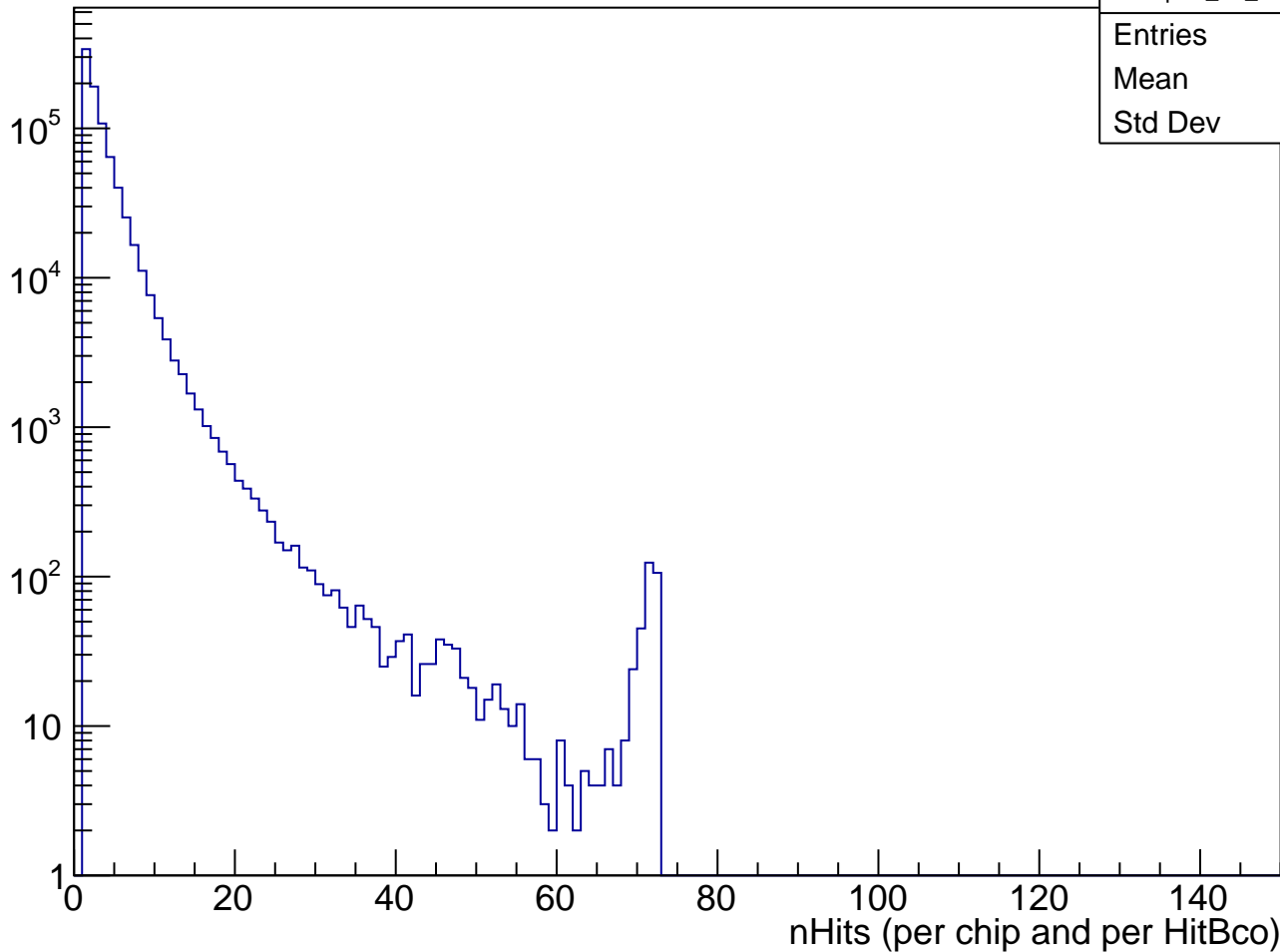
Entries 825985

Mean 2.782

Std Dev 3.201

nChipHit_F2_Fch3_Chip9

Entries



nChipHit_F2_Fch3_Chip9

Entries 826440

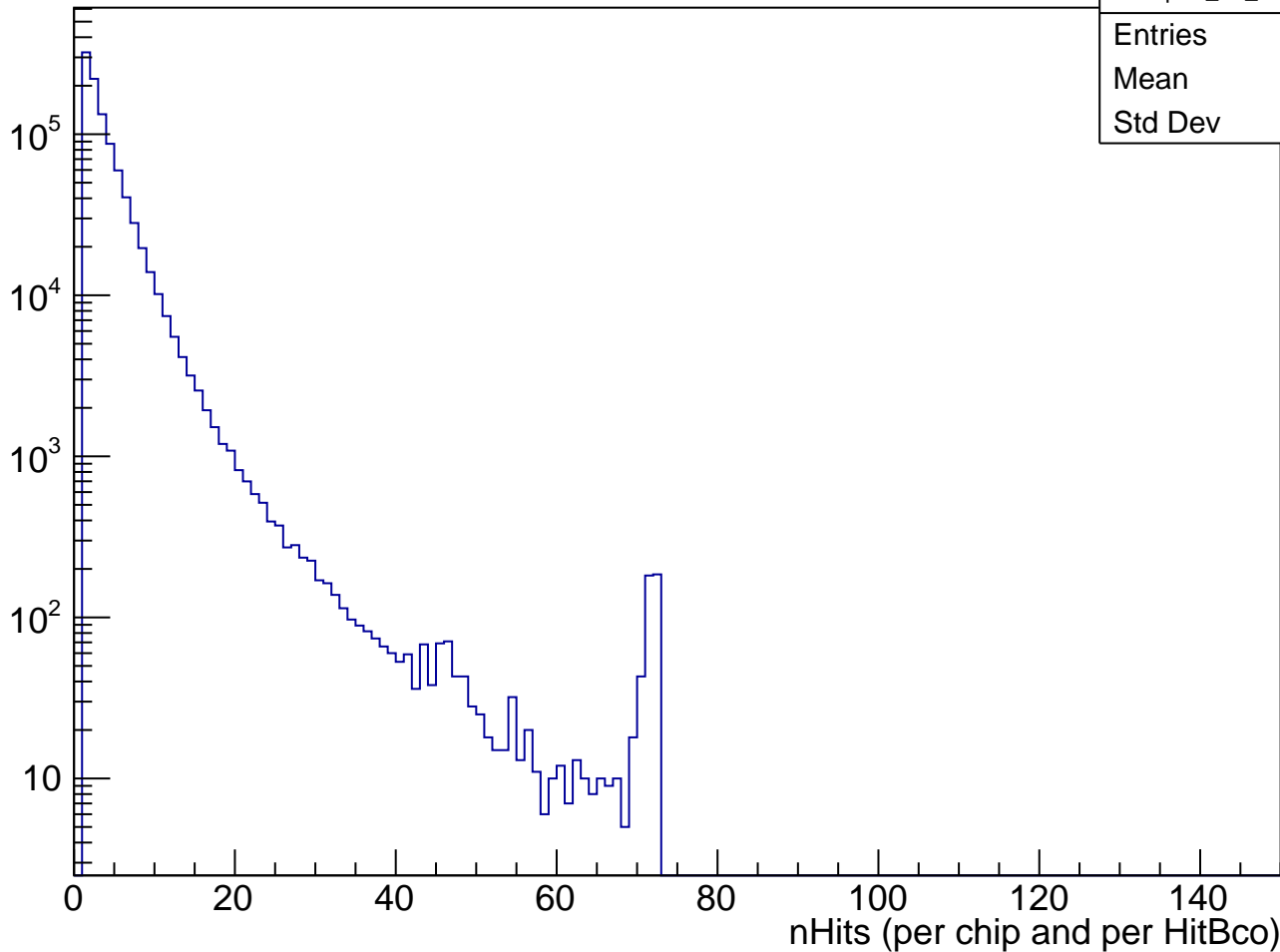
Mean 2.782

Std Dev 3.225

nHits (per chip and per HitBco)

nChipHit_F2_Fch4_Chip0

Entries



nChipHit_F2_Fch4_Chip0

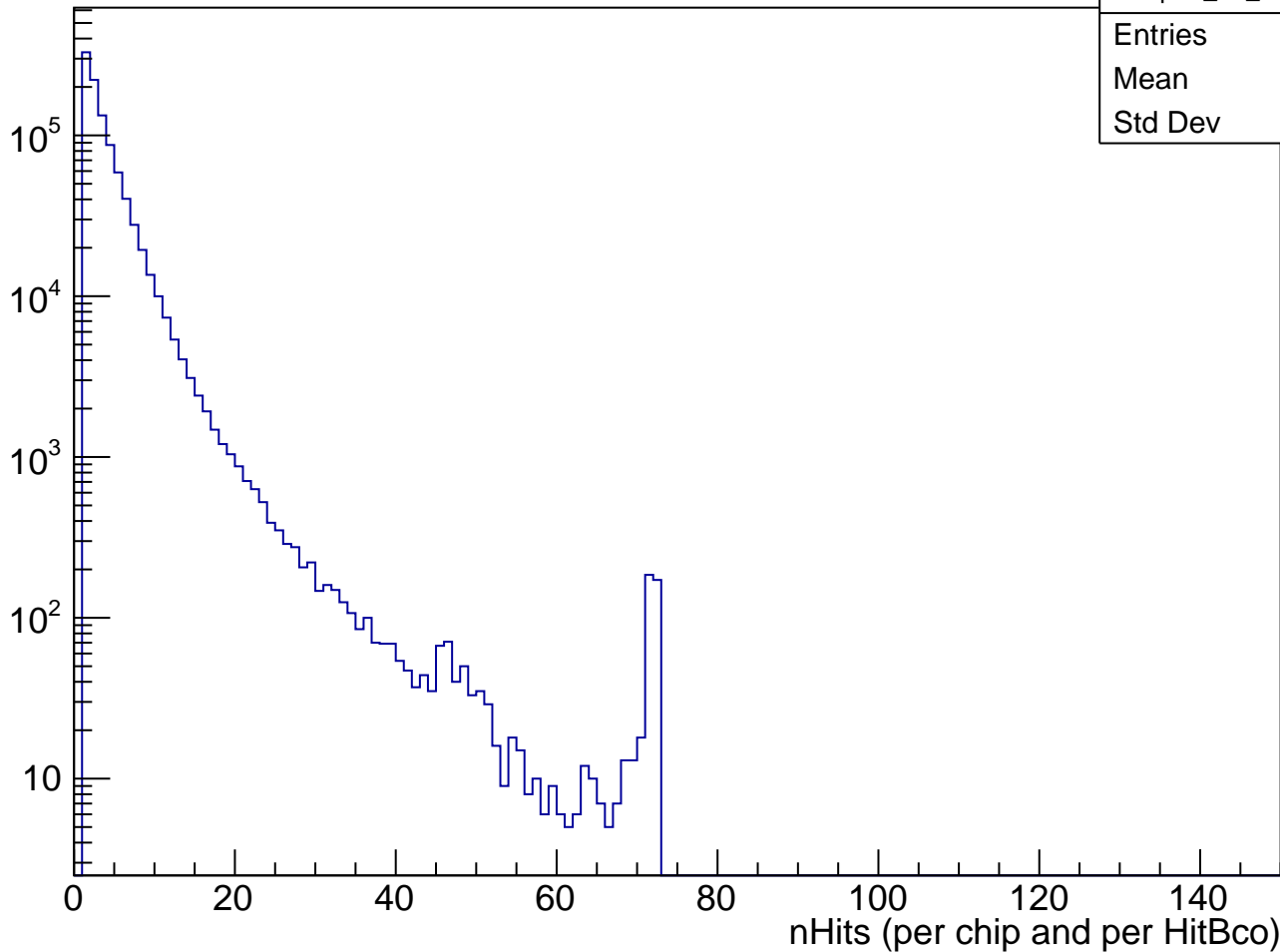
Entries 970387

Mean 3.32

Std Dev 3.761

nChipHit_F2_Fch4_Chip1

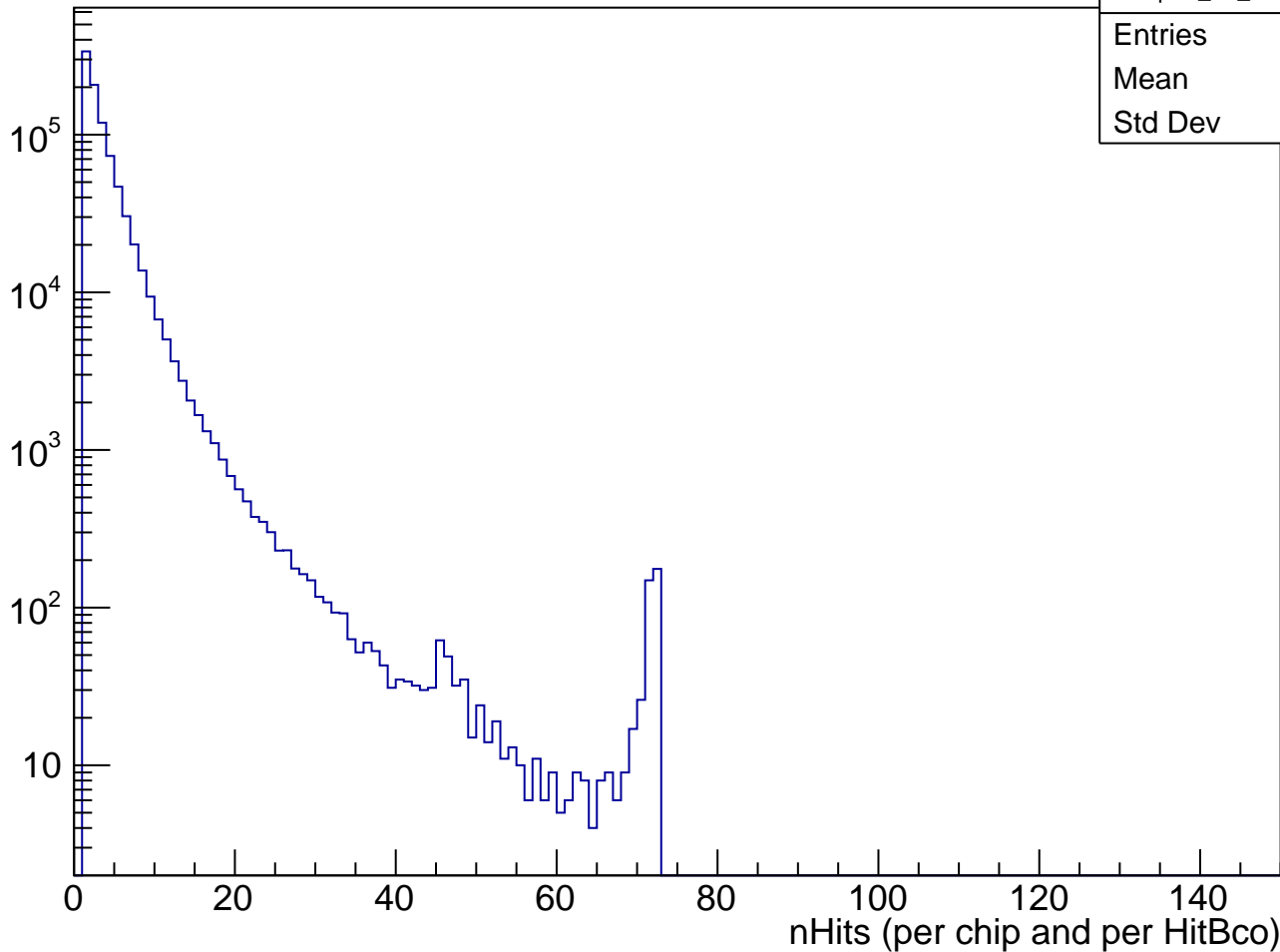
Entries



nChipHit_F2_Fch4_Chip1	
Entries	974608
Mean	3.287
Std Dev	3.708

nChipHit_F2_Fch4_Chip10

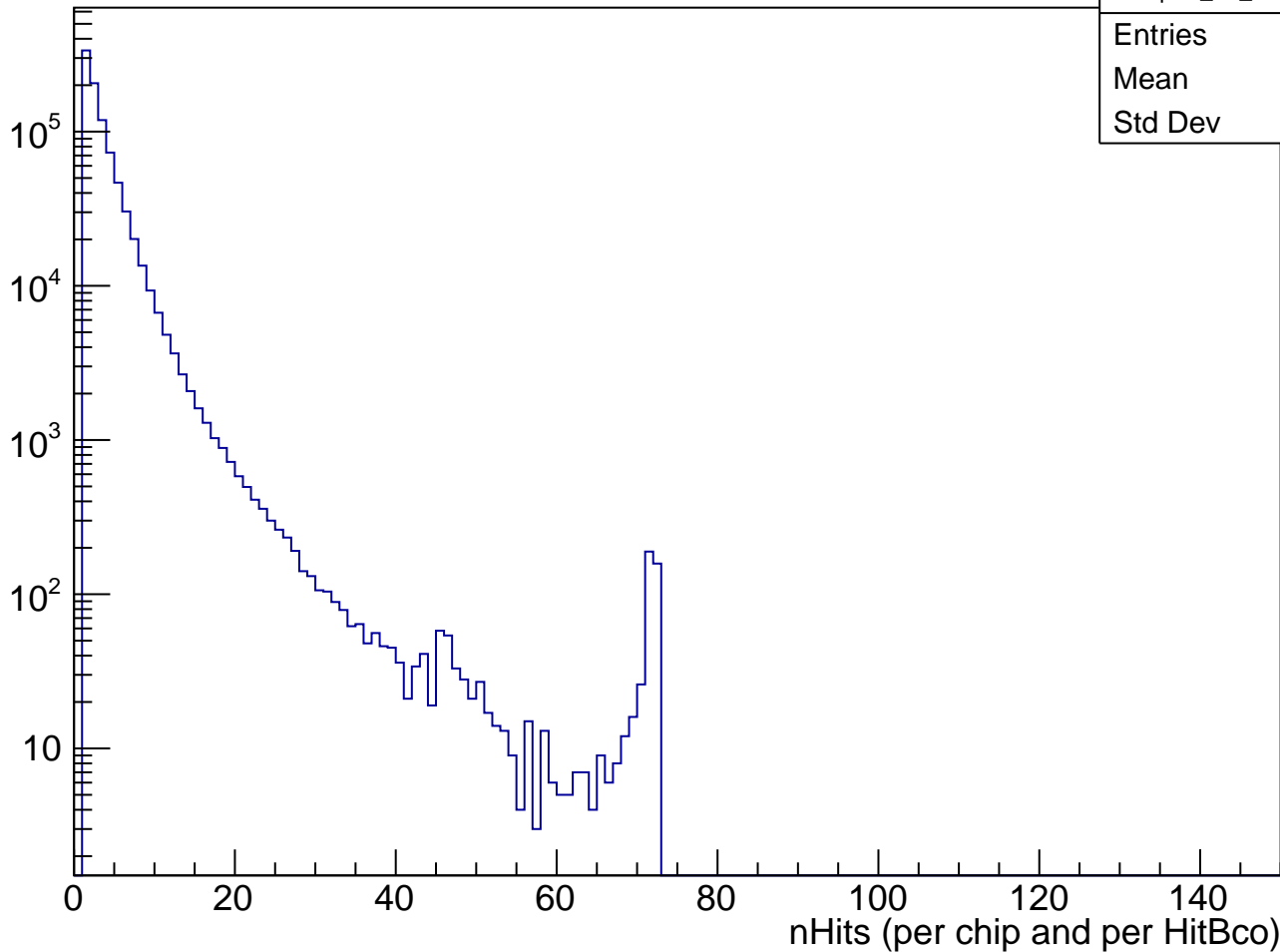
Entries



nChipHit_F2_Fch4_Chip10	
Entries	887909
Mean	2.956
Std Dev	3.426

nChipHit_F2_Fch4_Chip11

Entries

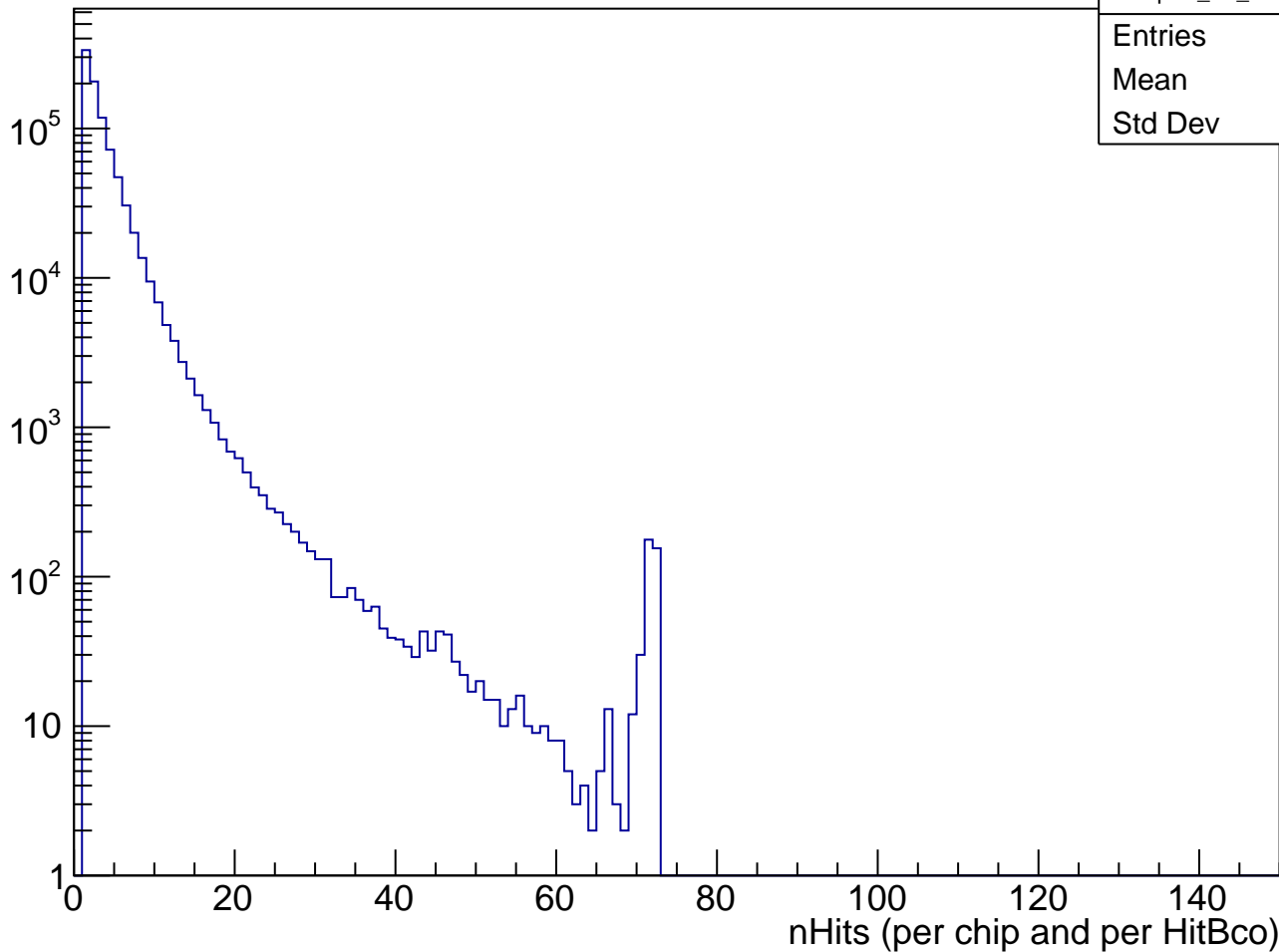


nChipHit_F2_Fch4_Chip11	
Entries	883970
Mean	2.956
Std Dev	3.441

nHits (per chip and per HitBco)

nChipHit_F2_Fch4_Chip12

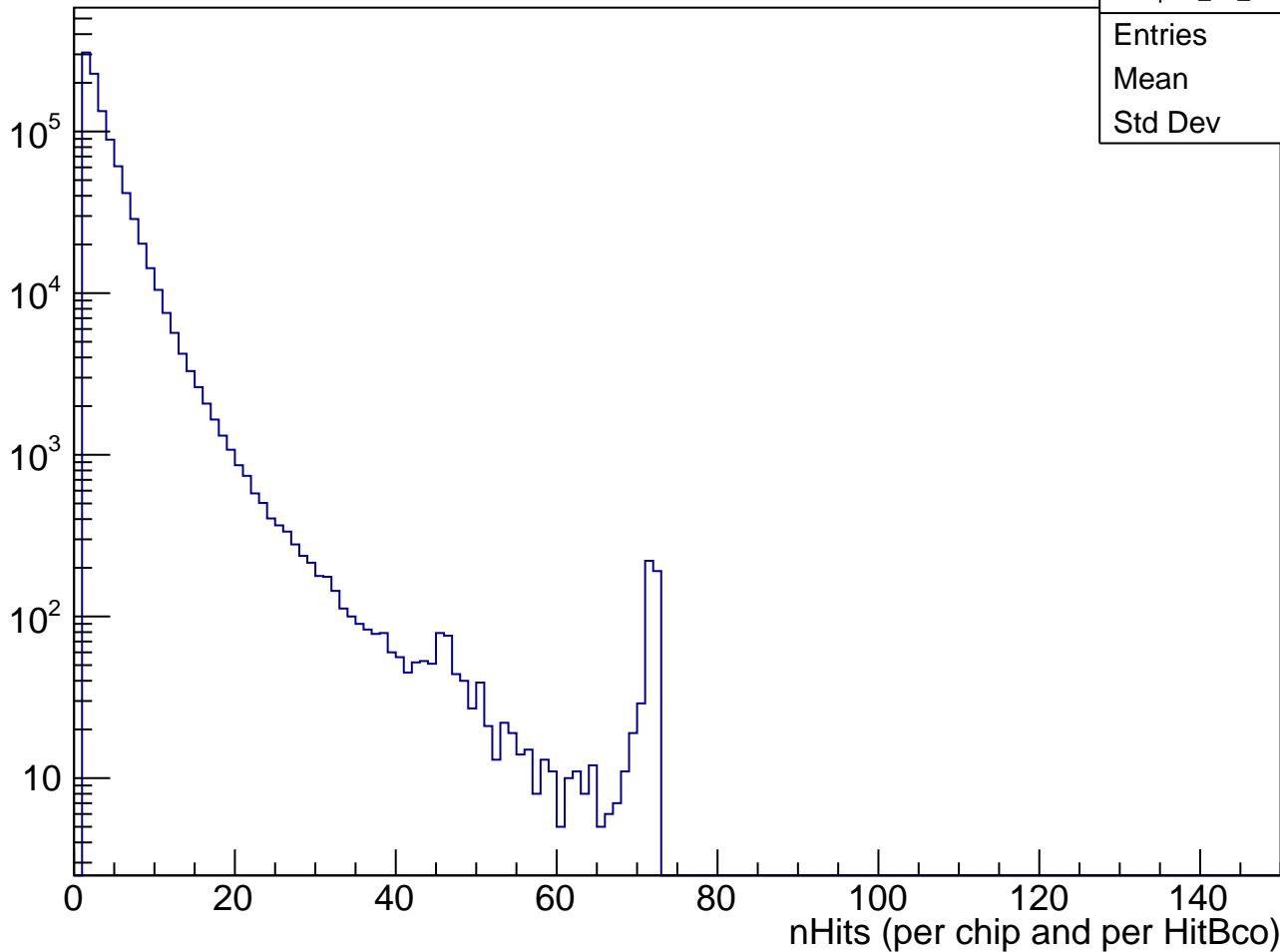
Entries



nChipHit_F2_Fch4_Chip12	
Entries	882424
Mean	2.966
Std Dev	3.437

nChipHit_F2_Fch4_Chip13

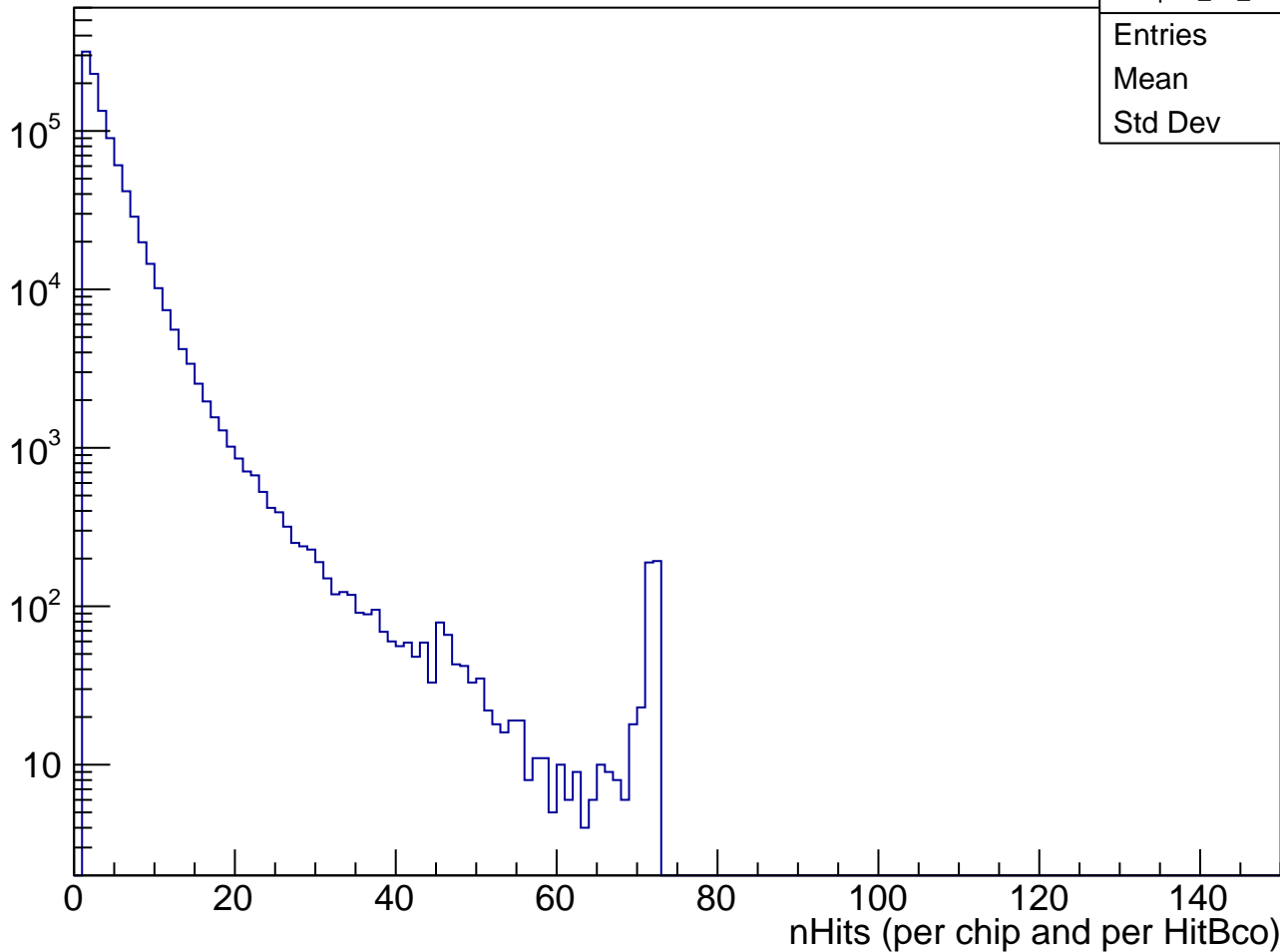
Entries



nChipHit_F2_Fch4_Chip13	
Entries	970716
Mean	3.378
Std Dev	3.81

nChipHit_F2_Fch4_Chip14

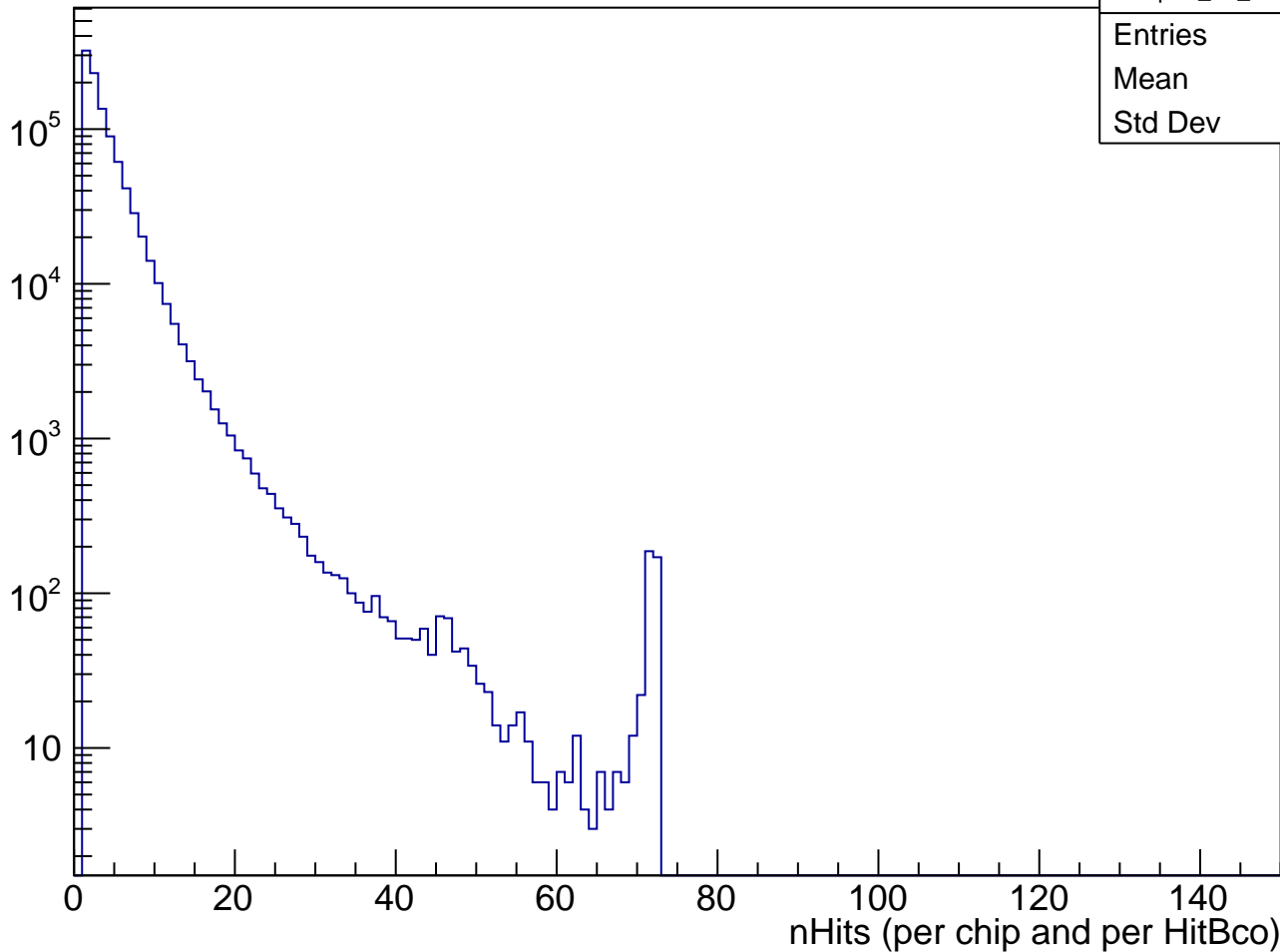
Entries



nChipHit_F2_Fch4_Chip14	
Entries	981650
Mean	3.344
Std Dev	3.758

nChipHit_F2_Fch4_Chip15

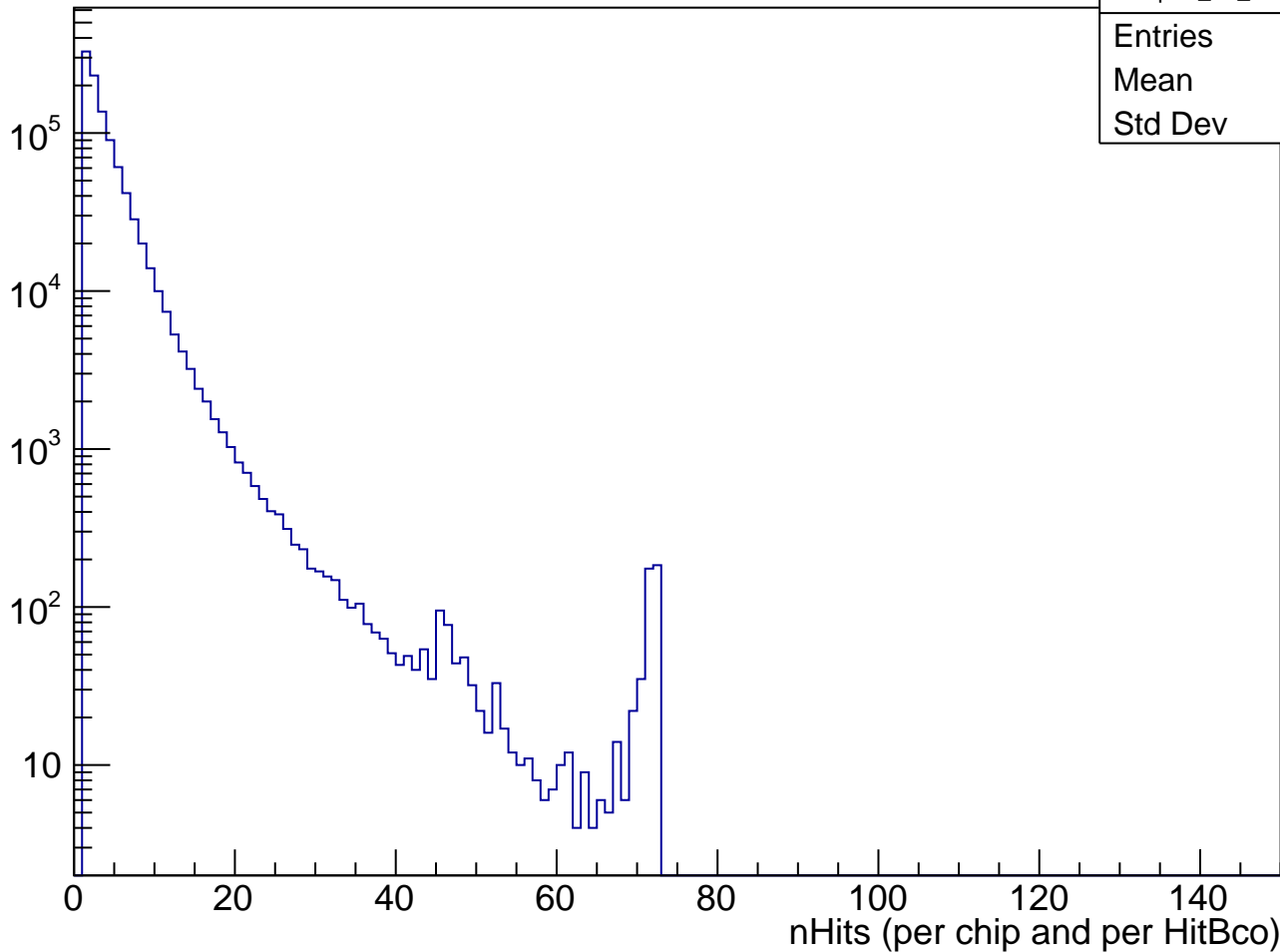
Entries



nChipHit_F2_Fch4_Chip15	
Entries	987930
Mean	3.314
Std Dev	3.687

nChipHit_F2_Fch4_Chip16

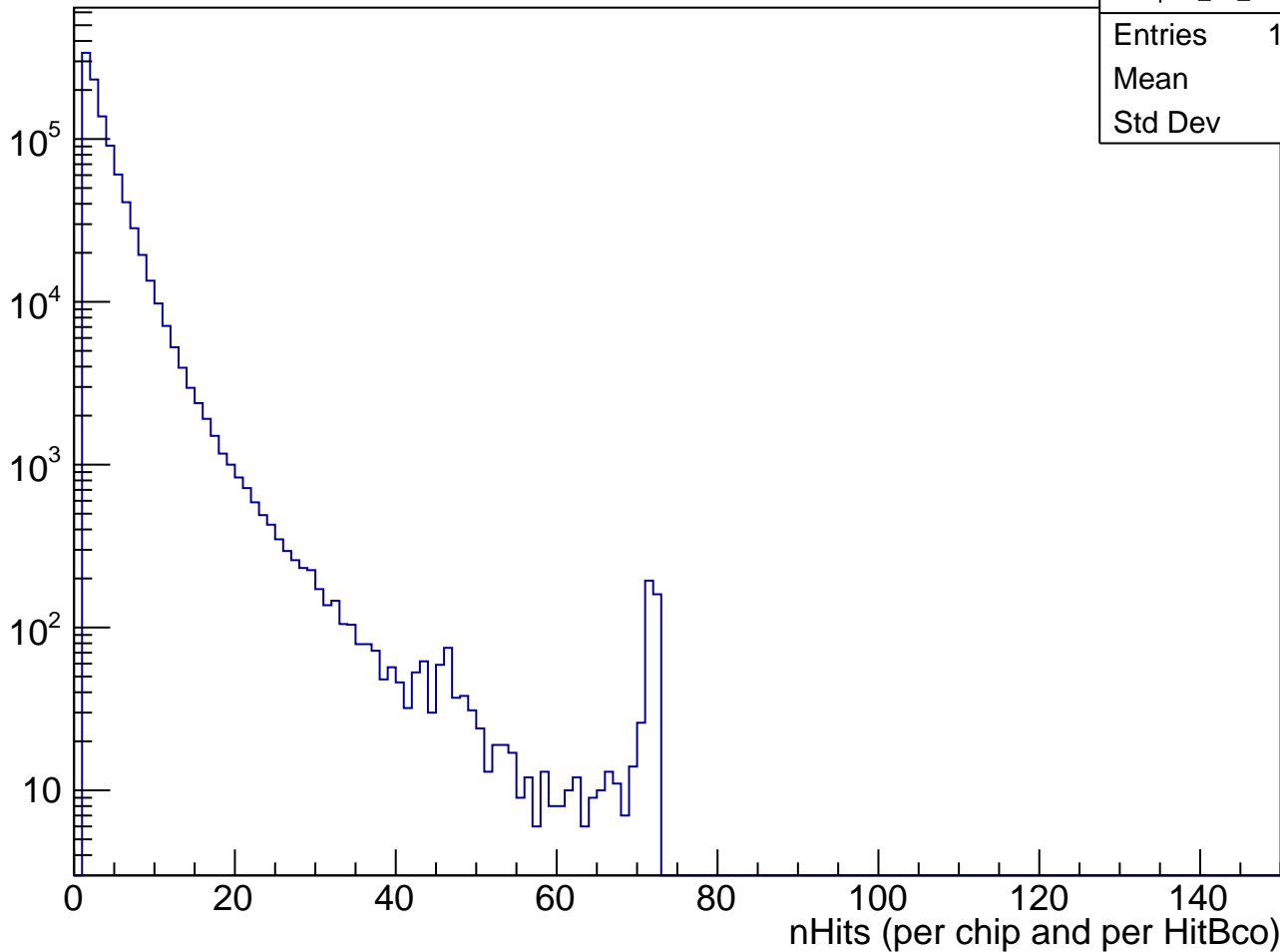
Entries



nChipHit_F2_Fch4_Chip16	
Entries	995941
Mean	3.295
Std Dev	3.695

nChipHit_F2_Fch4_Chip17

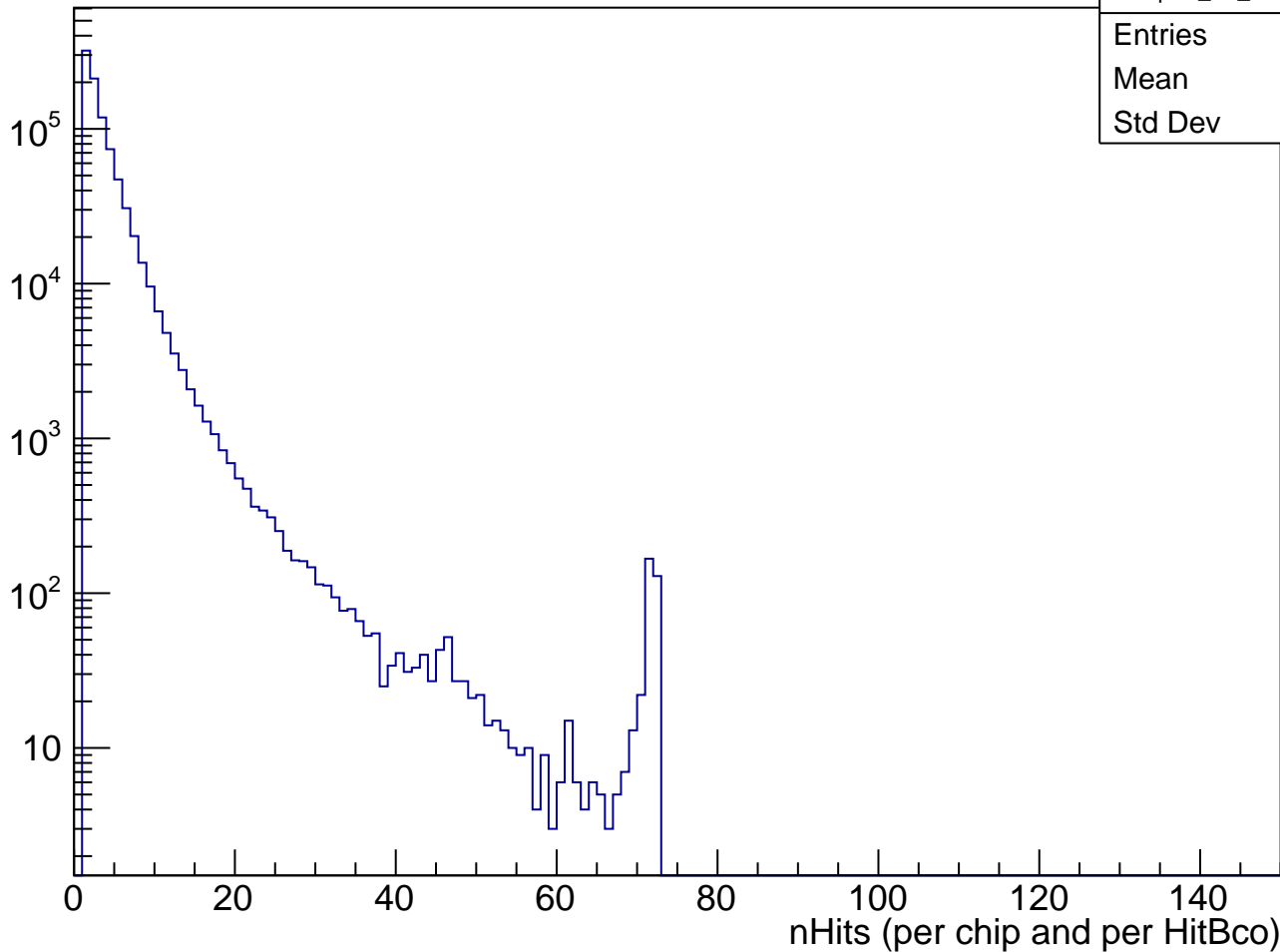
Entries



nChipHit_F2_Fch4_Chip17	
Entries	1005255
Mean	3.247
Std Dev	3.642

nChipHit_F2_Fch4_Chip18

Entries

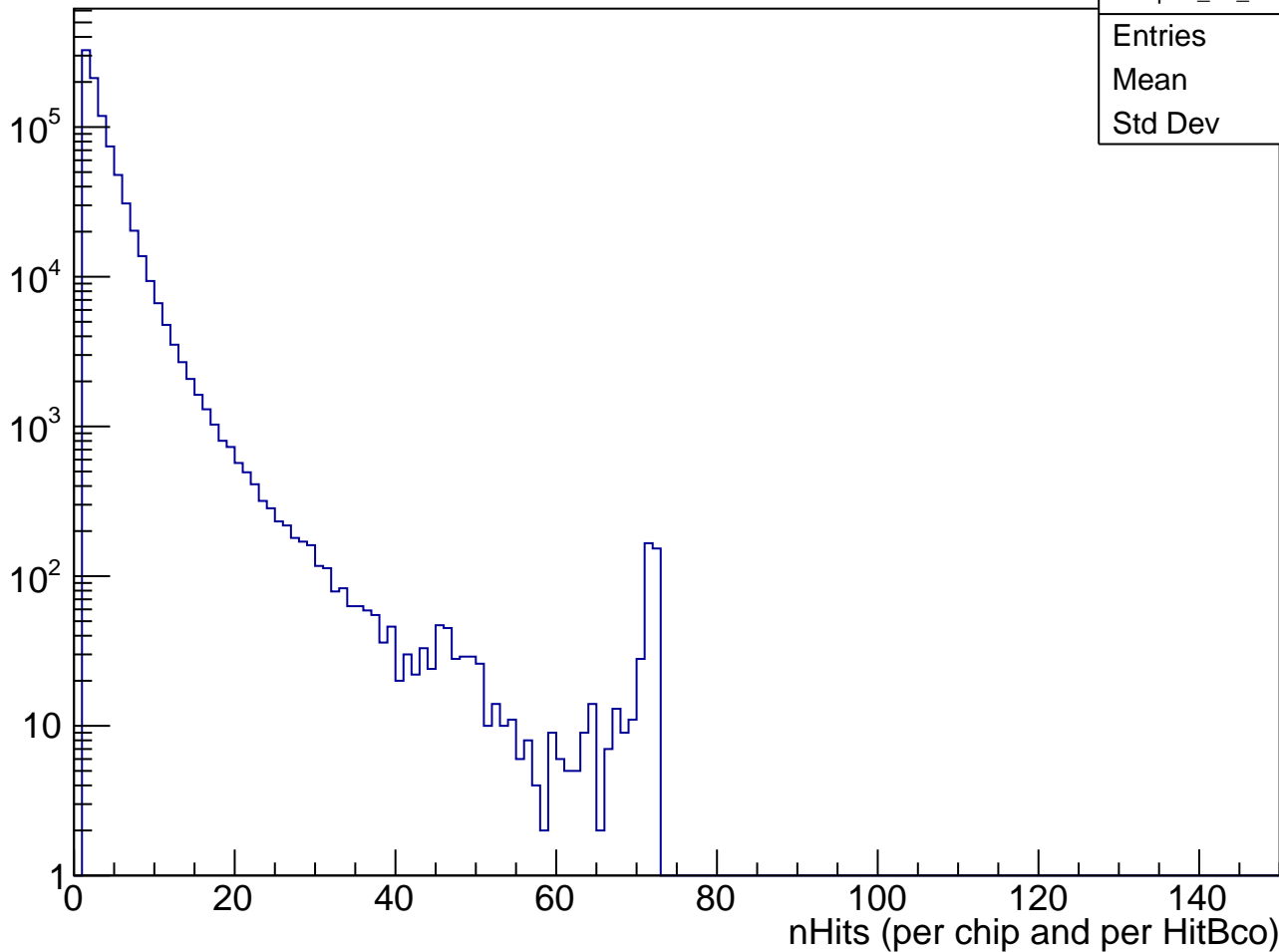


nChipHit_F2_Fch4_Chip18	
Entries	875682
Mean	2.981
Std Dev	3.382

nHits (per chip and per HitBco)

nChipHit_F2_Fch4_Chip19

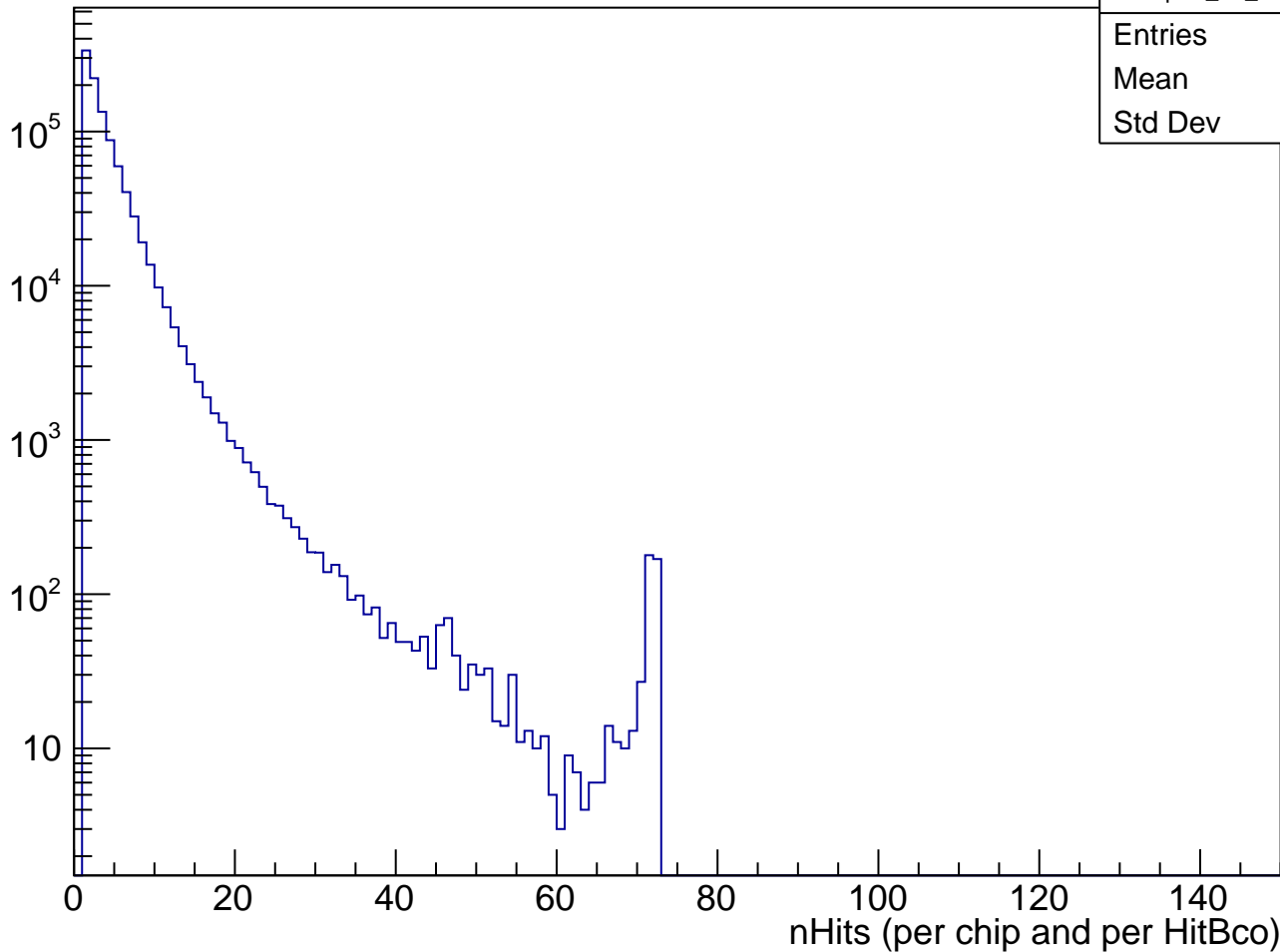
Entries



nChipHit_F2_Fch4_Chip19	
Entries	883372
Mean	2.971
Std Dev	3.397

nChipHit_F2_Fch4_Chip2

Entries



nChipHit_F2_Fch4_Chip2

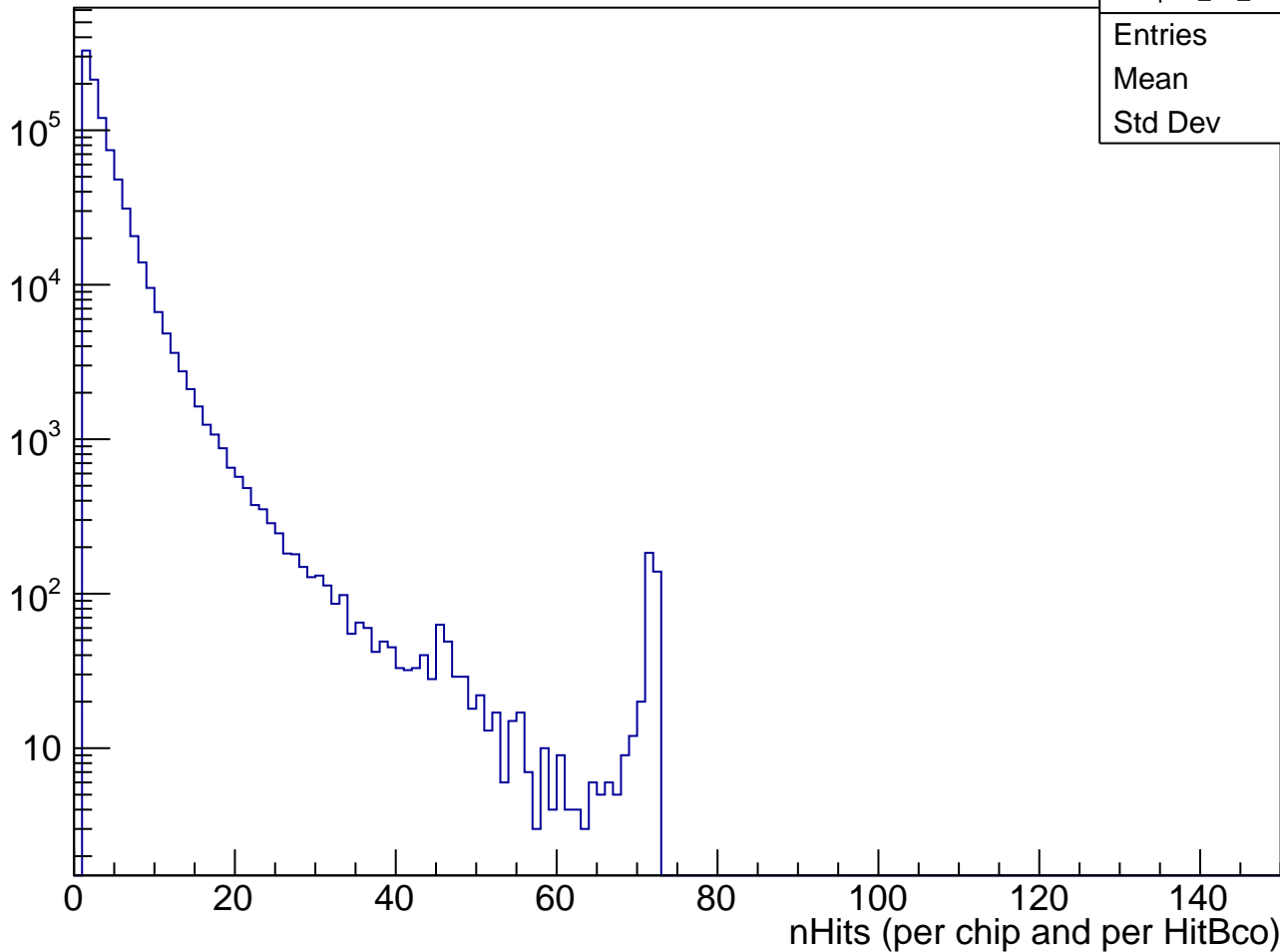
Entries 984859

Mean 3.27

Std Dev 3.691

nChipHit_F2_Fch4_Chip20

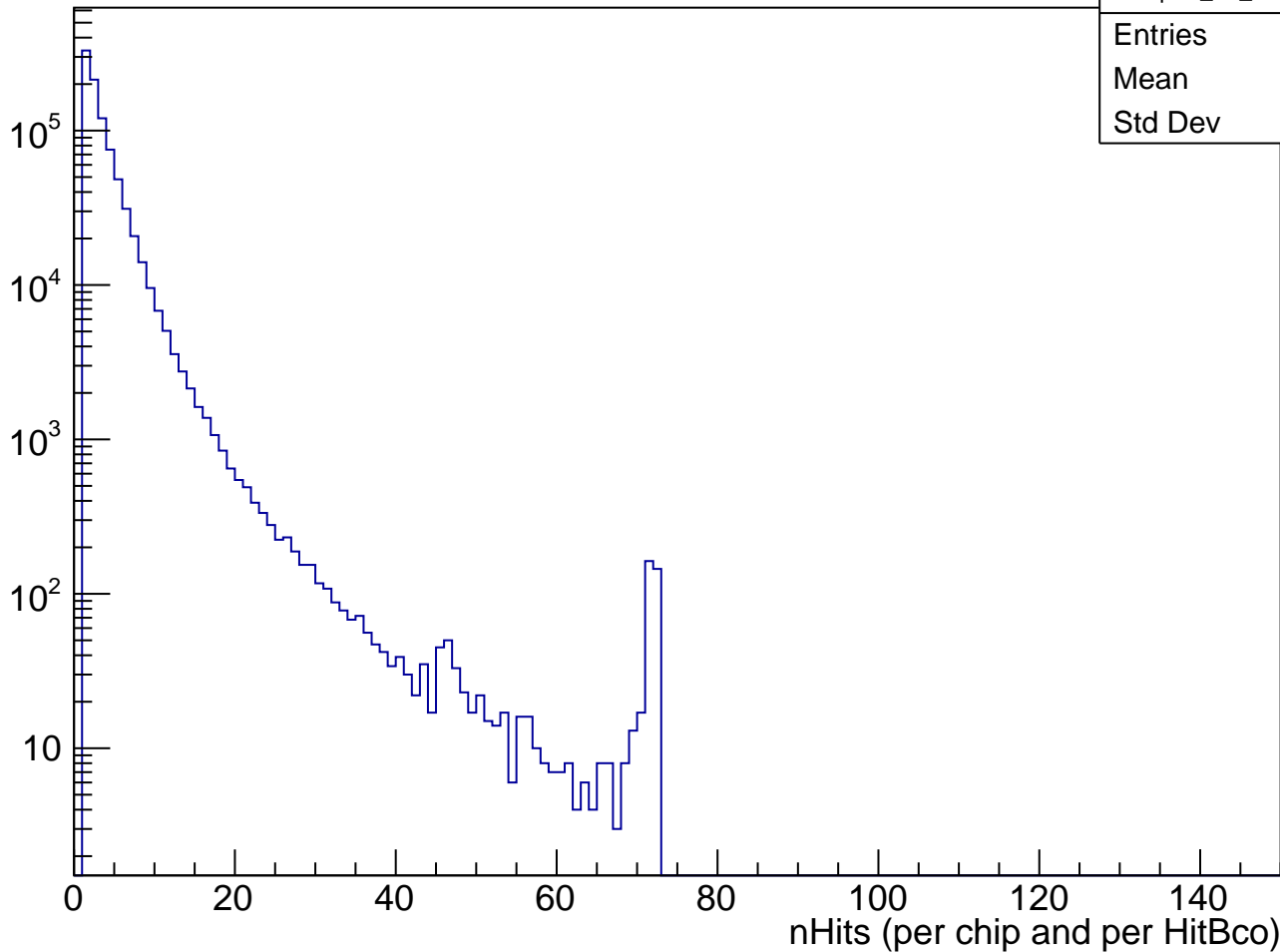
Entries



nChipHit_F2_Fch4_Chip20	
Entries	888645
Mean	2.974
Std Dev	3.394

nChipHit_F2_Fch4_Chip21

Entries

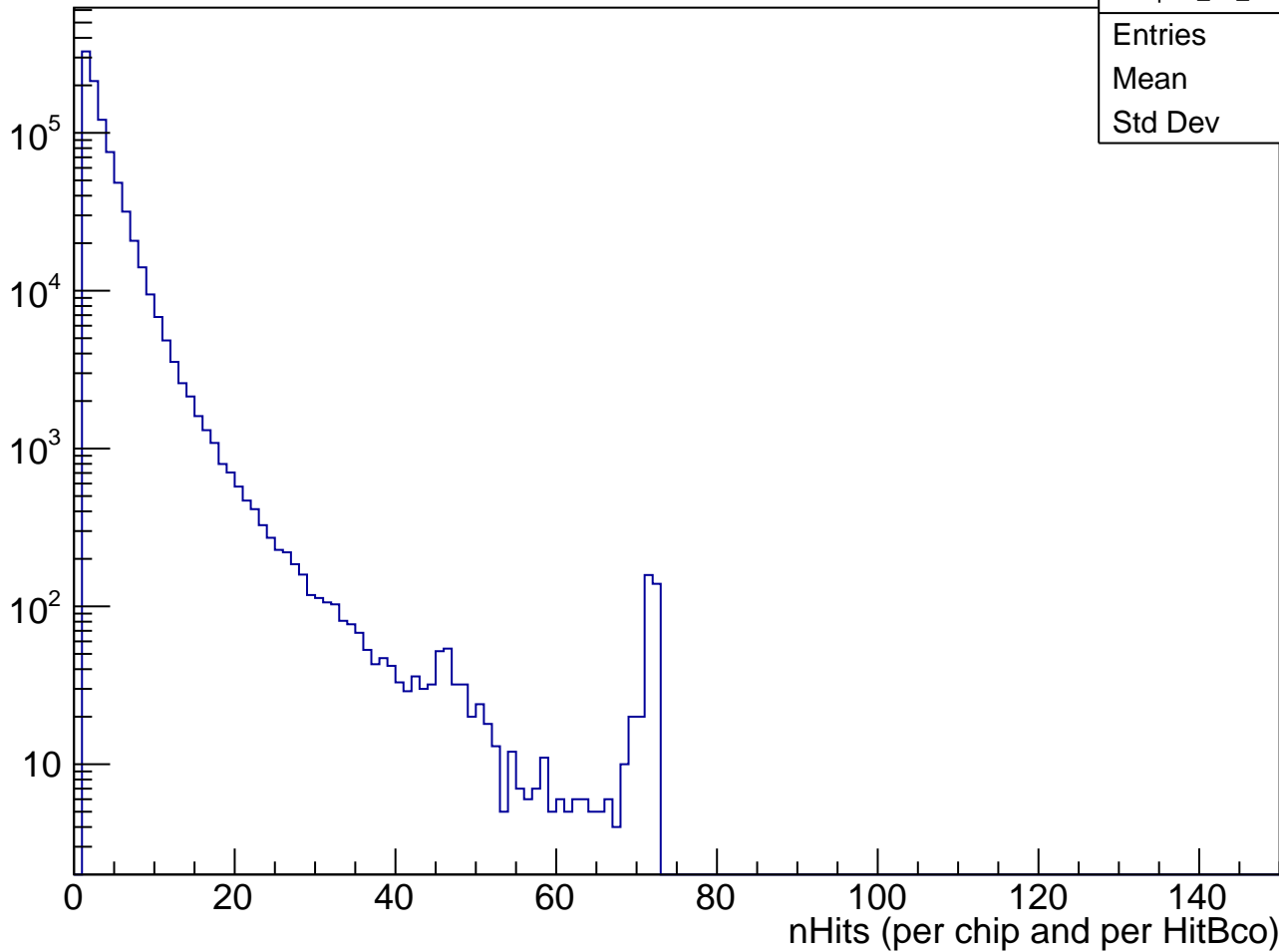


nChipHit_F2_Fch4_Chip21	
Entries	893480
Mean	2.974
Std Dev	3.378

nHits (per chip and per HitBco)

nChipHit_F2_Fch4_Chip22

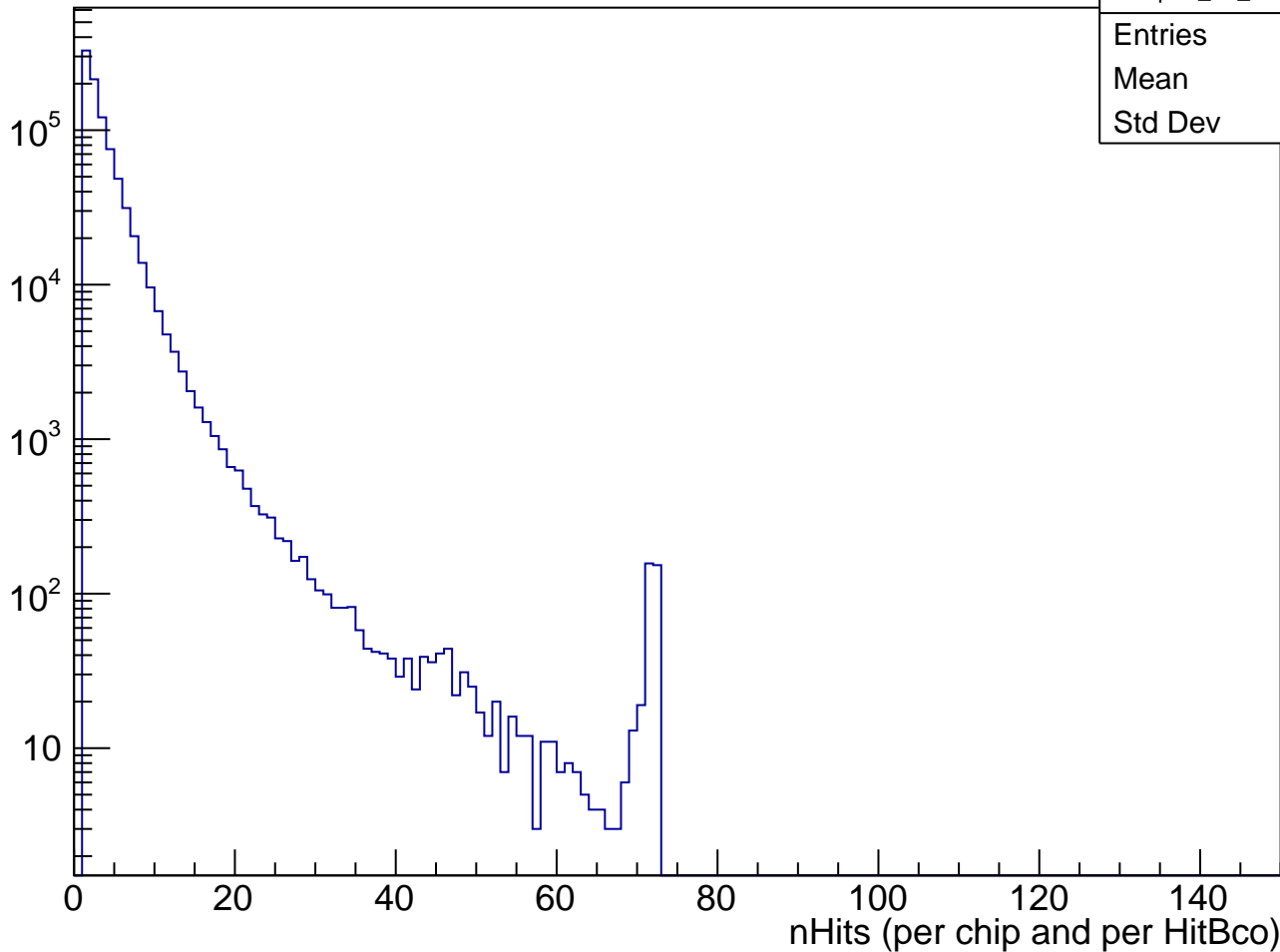
Entries



nChipHit_F2_Fch4_Chip22	
Entries	891674
Mean	2.976
Std Dev	3.372

nChipHit_F2_Fch4_Chip23

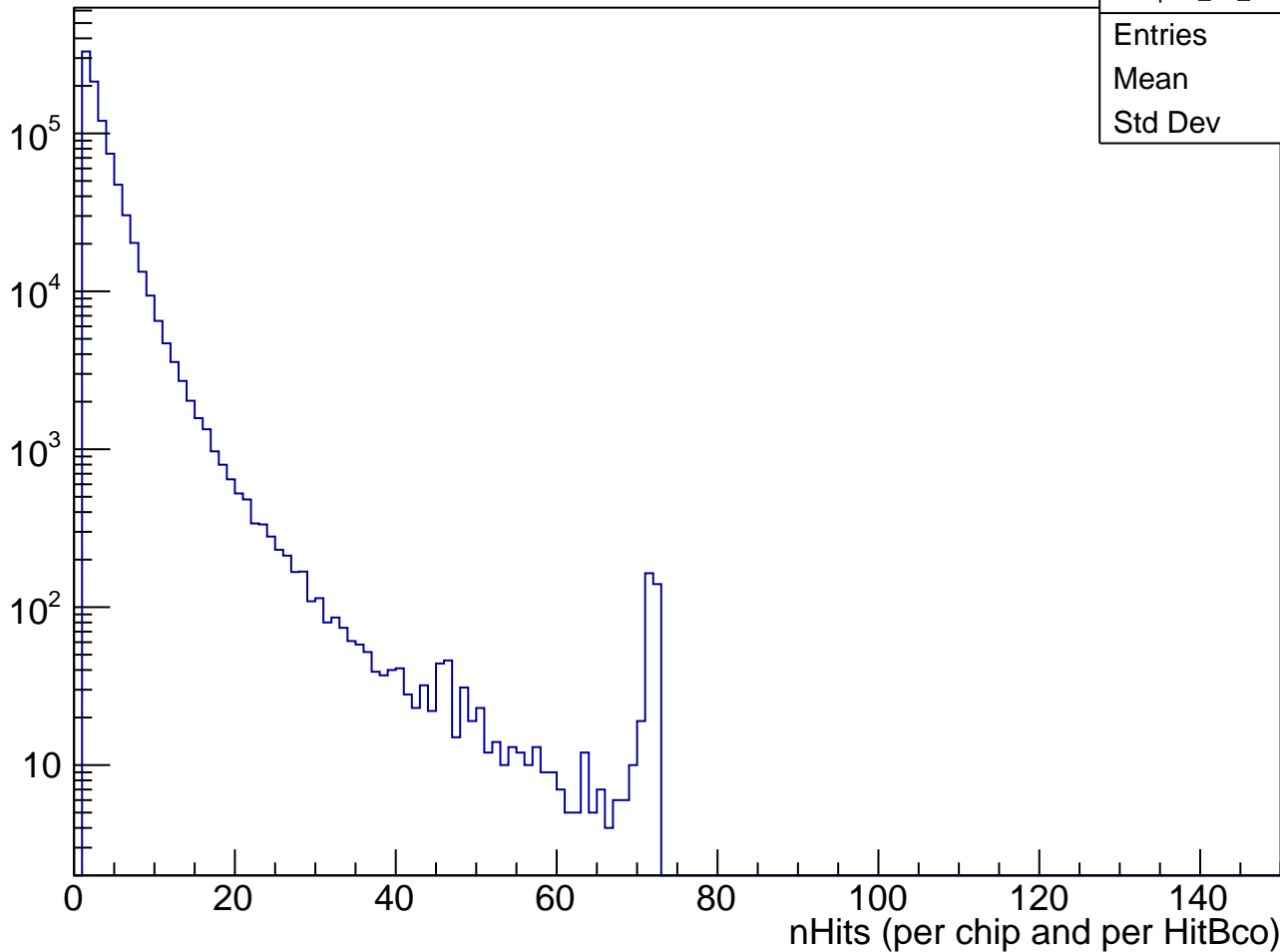
Entries



nChipHit_F2_Fch4_Chip23	
Entries	891532
Mean	2.973
Std Dev	3.365

nChipHit_F2_Fch4_Chip24

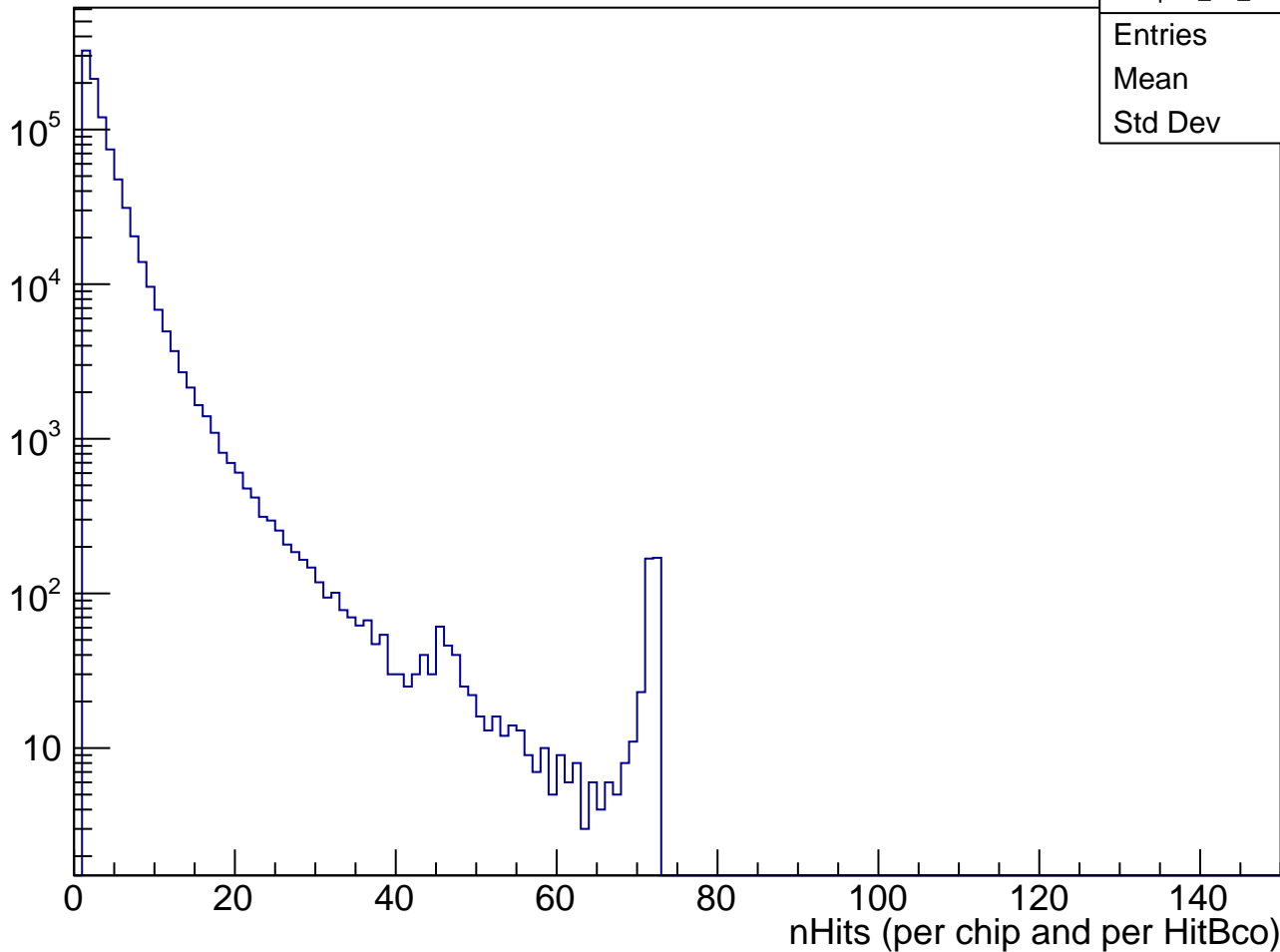
Entries



nChipHit_F2_Fch4_Chip24	
Entries	887607
Mean	2.943
Std Dev	3.334

nChipHit_F2_Fch4_Chip25

Entries

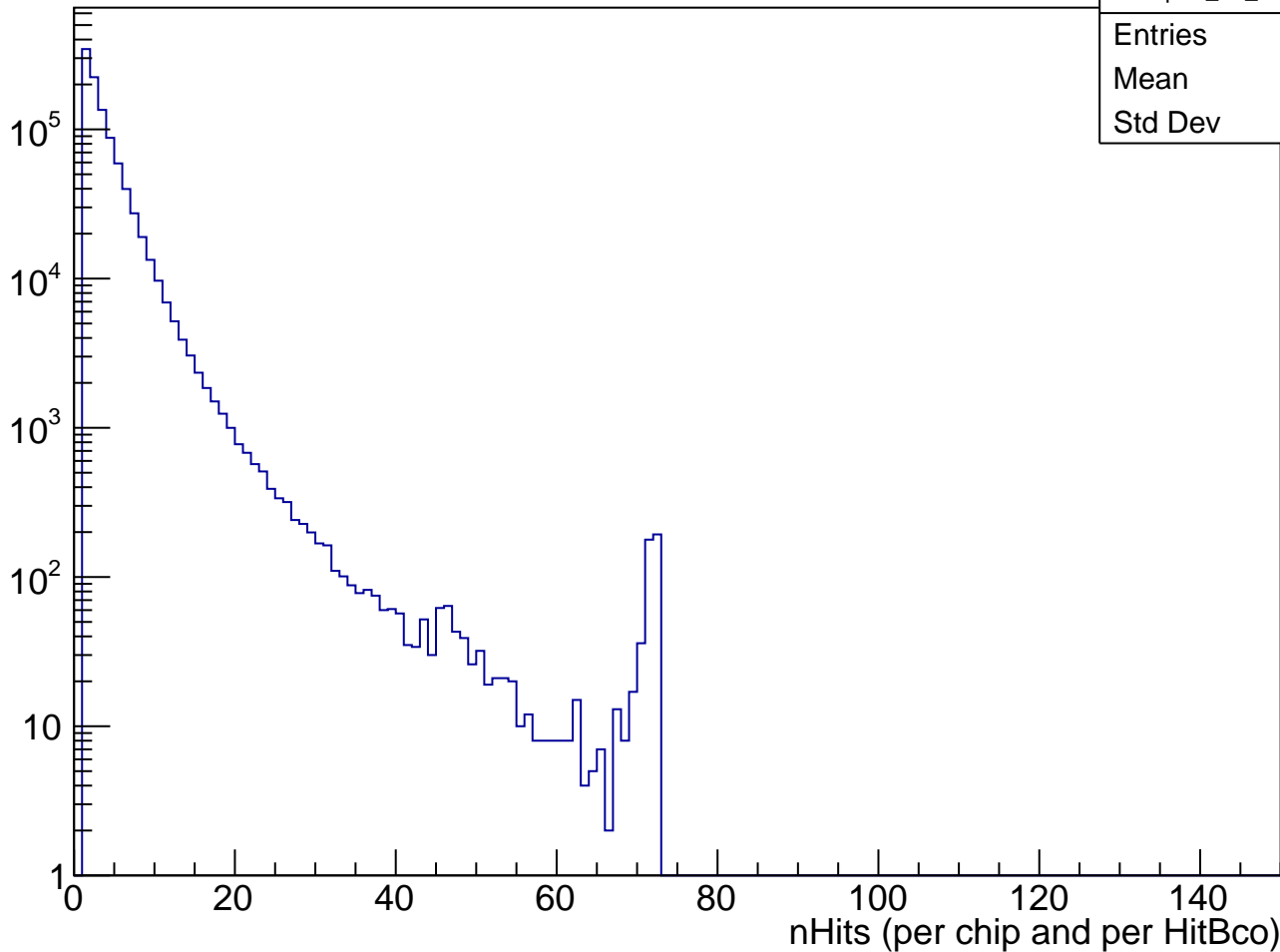


nChipHit_F2_Fch4_Chip25	
Entries	884307
Mean	2.991
Std Dev	3.429

nHits (per chip and per HitBco)

nChipHit_F2_Fch4_Chip3

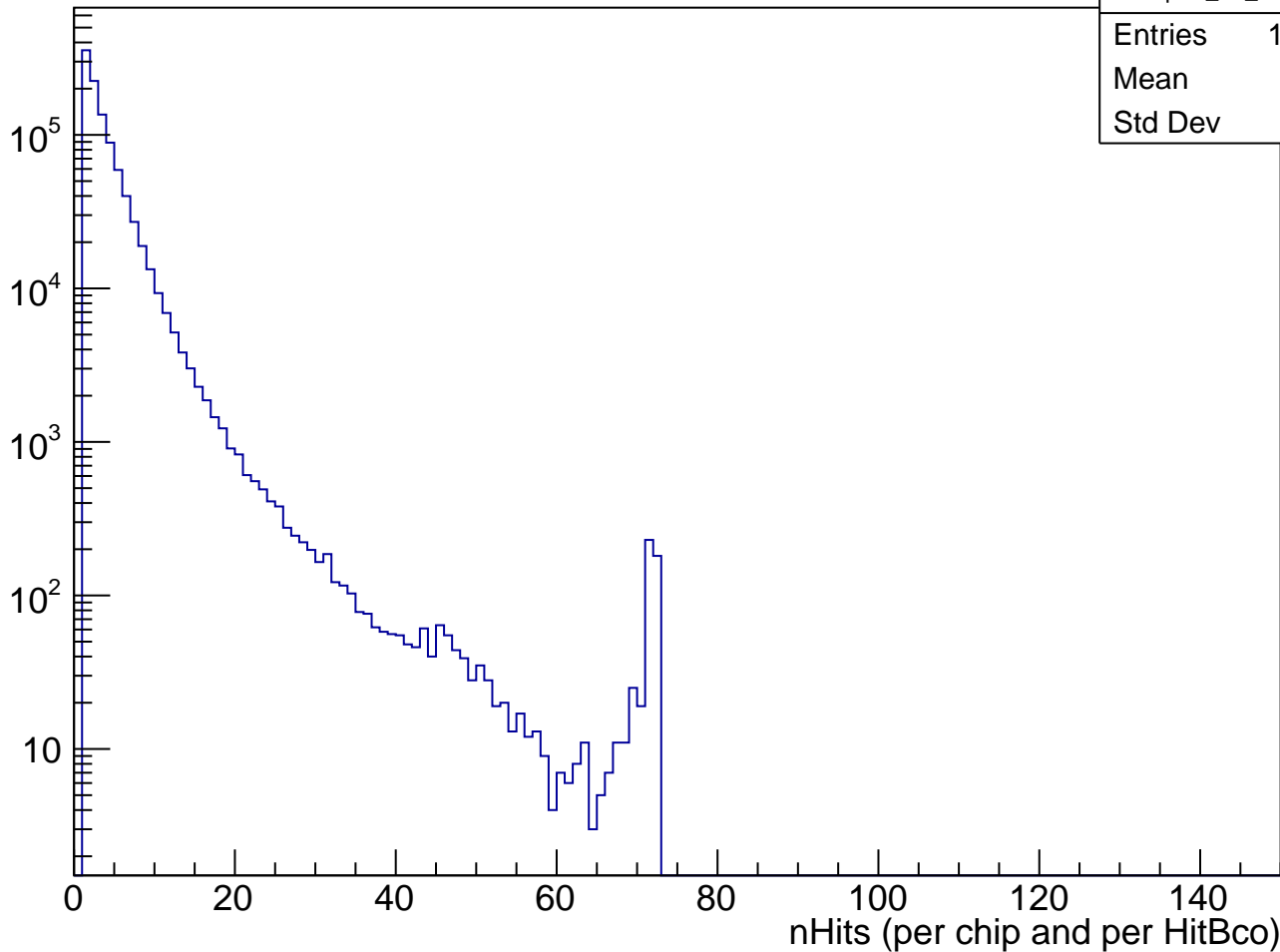
Entries



nChipHit_F2_Fch4_Chip3	
Entries	993438
Mean	3.223
Std Dev	3.66

nChipHit_F2_Fch4_Chip4

Entries



nChipHit_F2_Fch4_Chip4

Entries 1005737

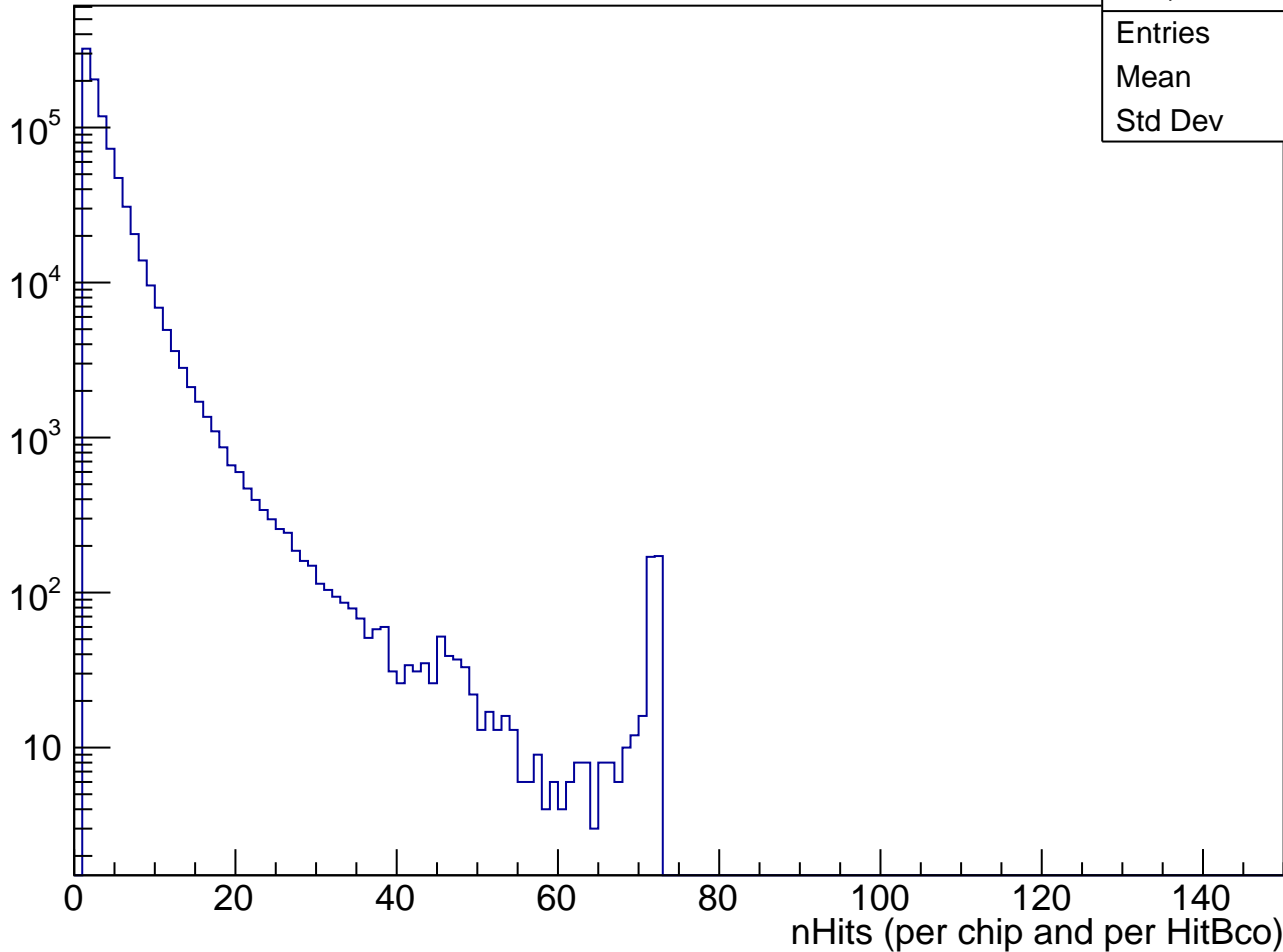
Mean 3.196

Std Dev 3.666

nHits (per chip and per HitBco)

nChipHit_F2_Fch4_Chip5

Entries



nChipHit_F2_Fch4_Chip5

Entries 871057

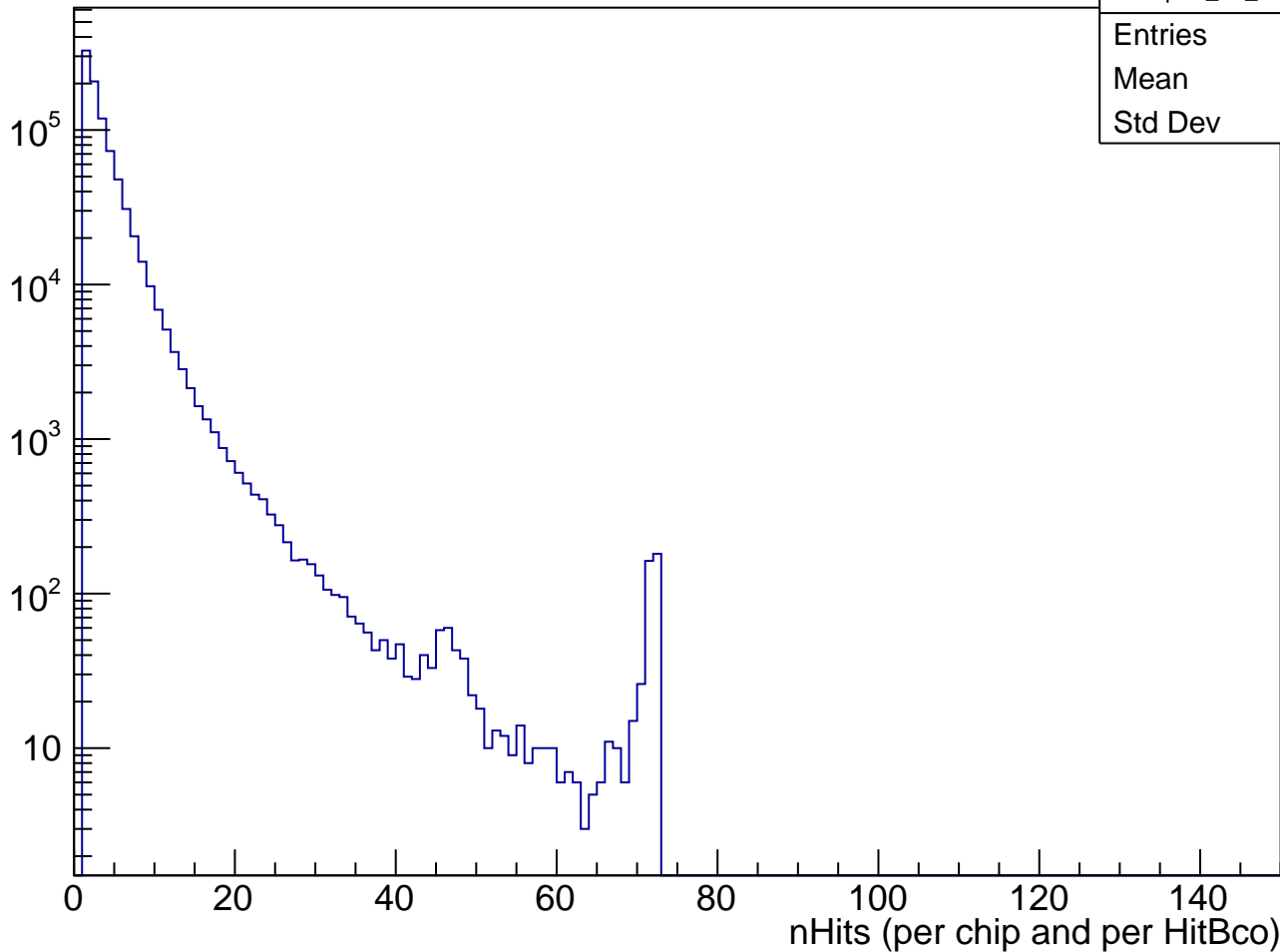
Mean 3.002

Std Dev 3.455

nHits (per chip and per HitBco)

nChipHit_F2_Fch4_Chip6

Entries



nChipHit_F2_Fch4_Chip6

Entries 879223

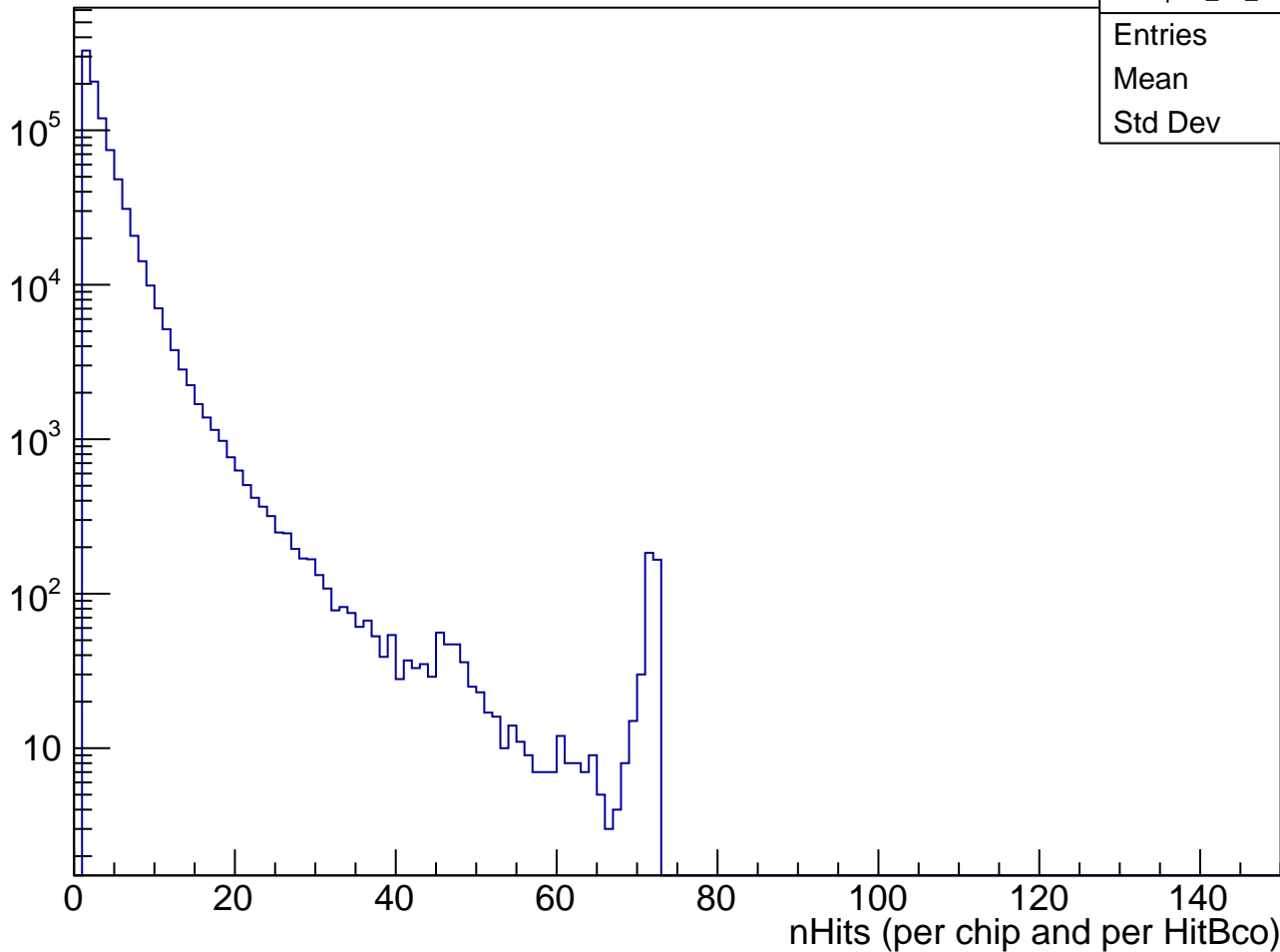
Mean 3.005

Std Dev 3.491

nHits (per chip and per HitBco)

nChipHit_F2_Fch4_Chip7

Entries

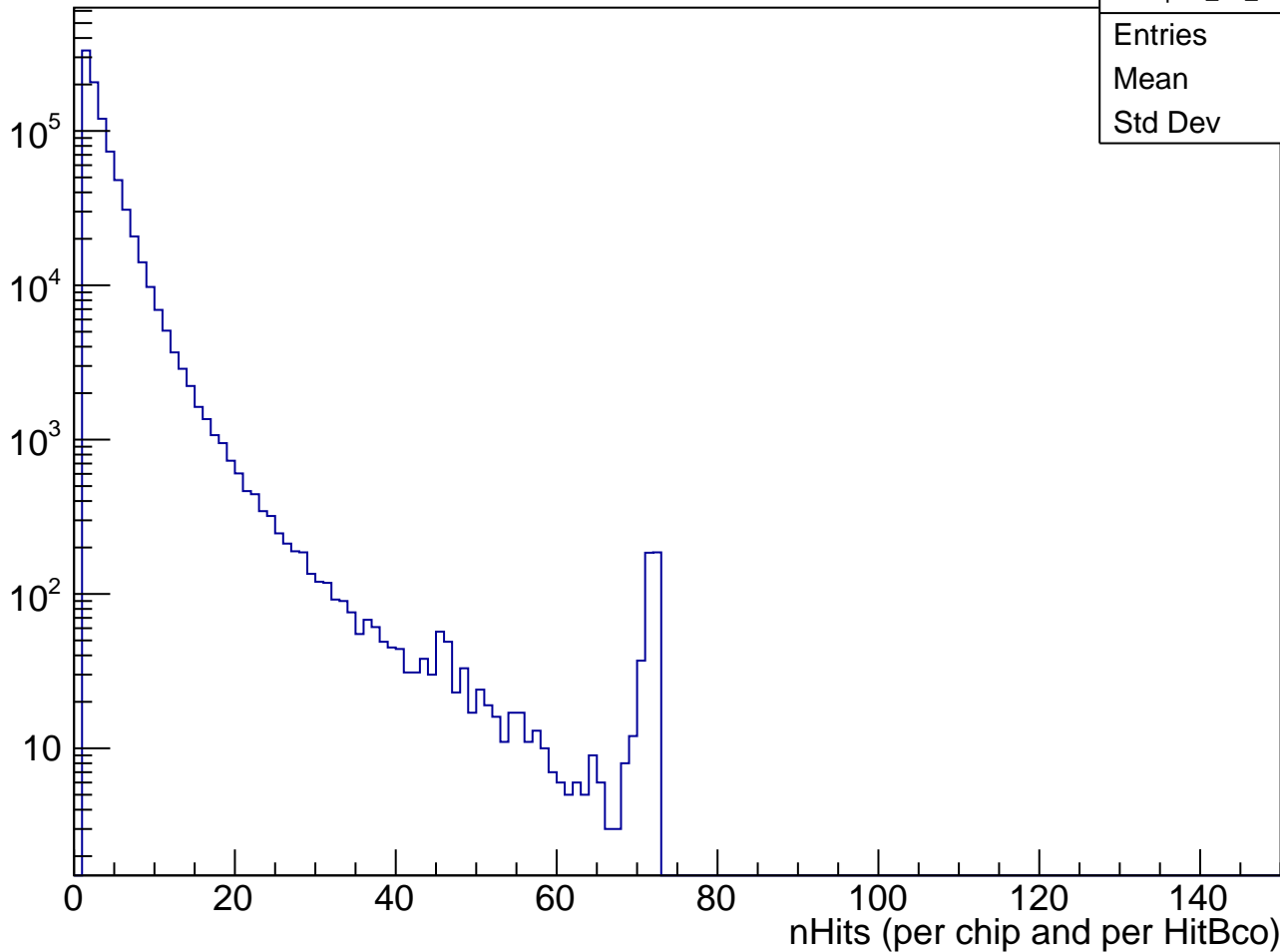


nChipHit_F2_Fch4_Chip7	
Entries	884749
Mean	3.015
Std Dev	3.504

nHits (per chip and per HitBco)

nChipHit_F2_Fch4_Chip8

Entries



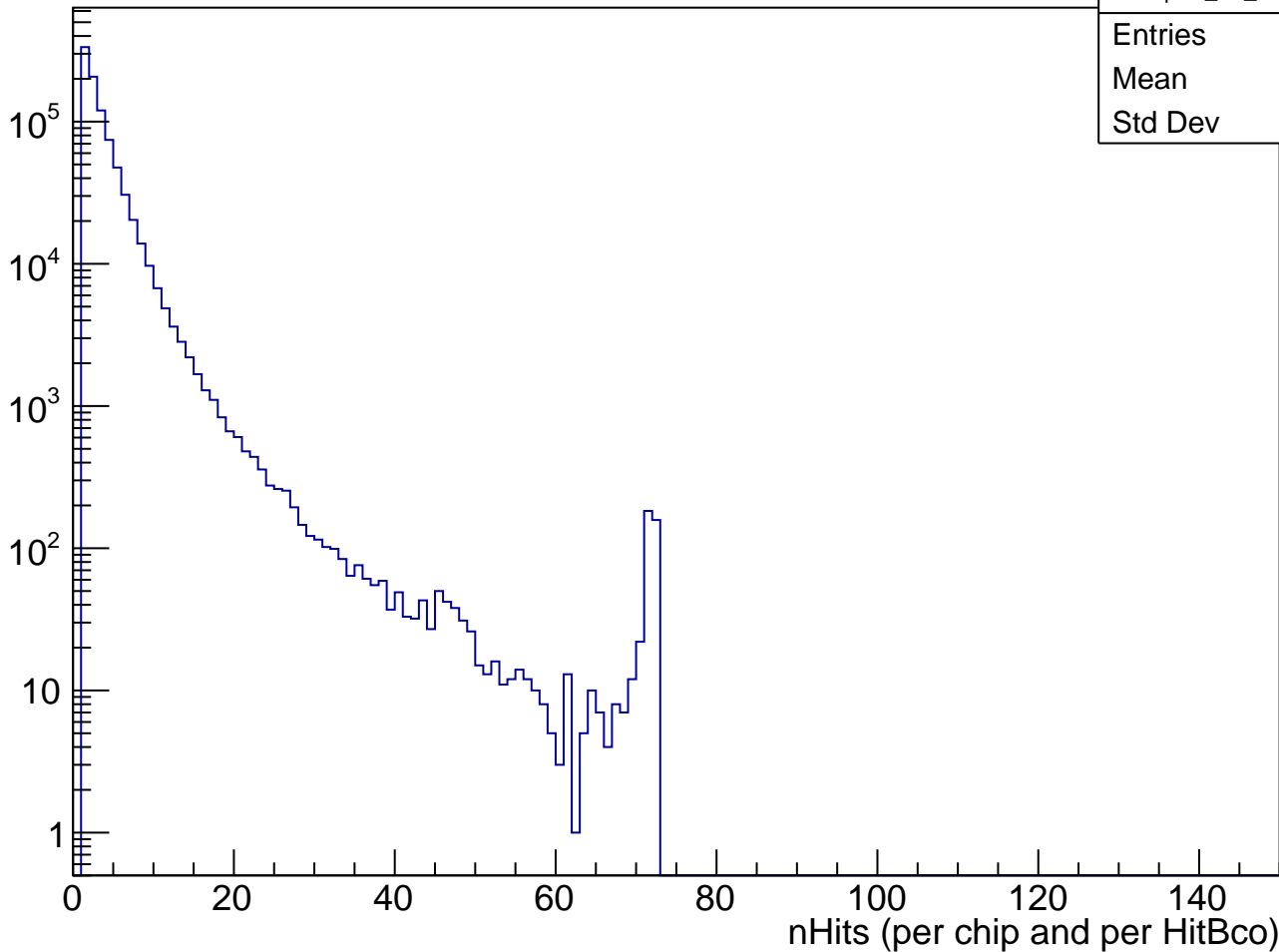
nChipHit_F2_Fch4_Chip8

Entries	887236
Mean	2.997
Std Dev	3.501

nHits (per chip and per HitBco)

nChipHit_F2_Fch4_Chip9

Entries



nChipHit_F2_Fch4_Chip9

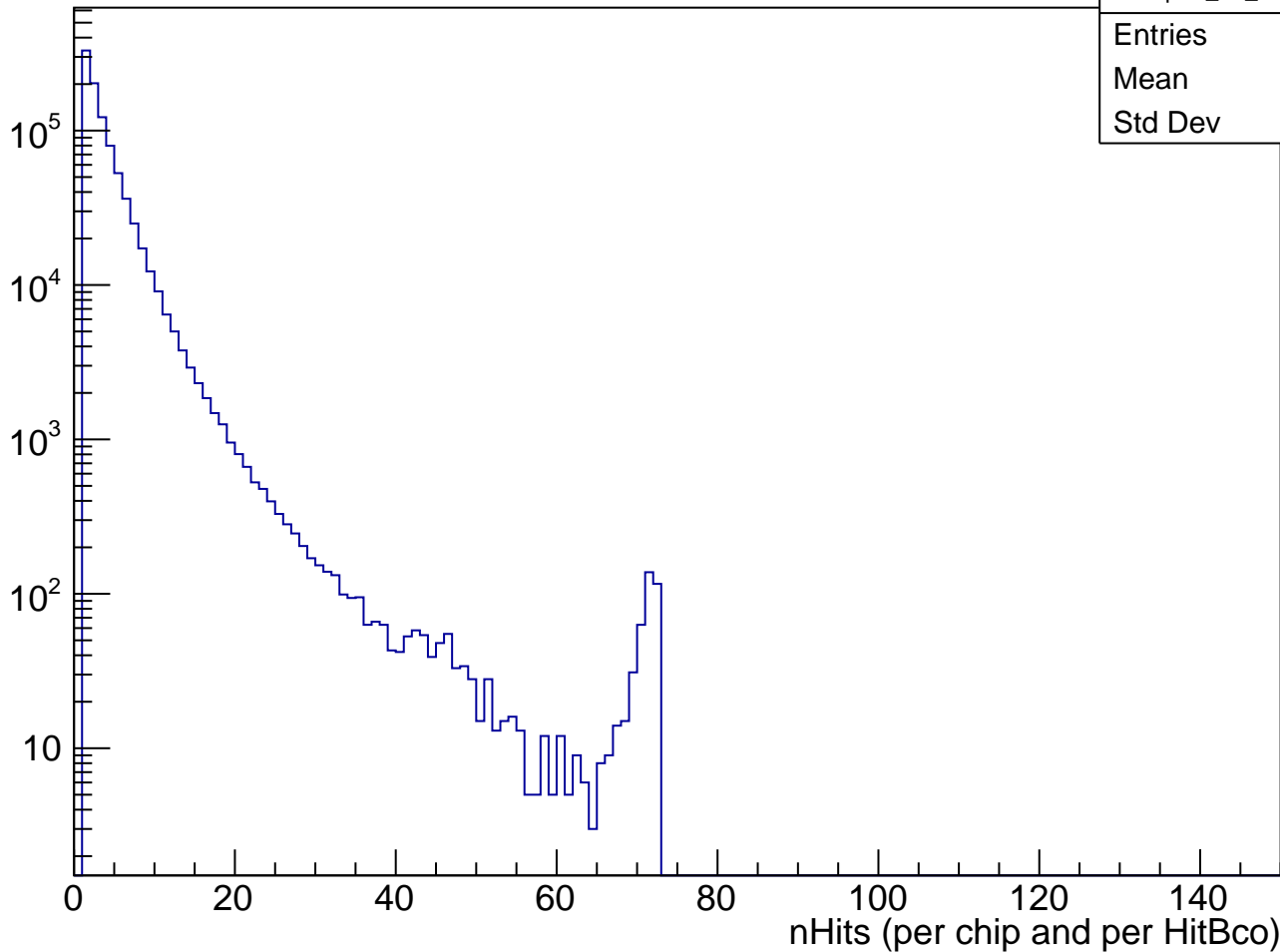
Entries 888009

Mean 2.976

Std Dev 3.445

nChipHit_F2_Fch5_Chip0

Entries



nChipHit_F2_Fch5_Chip0

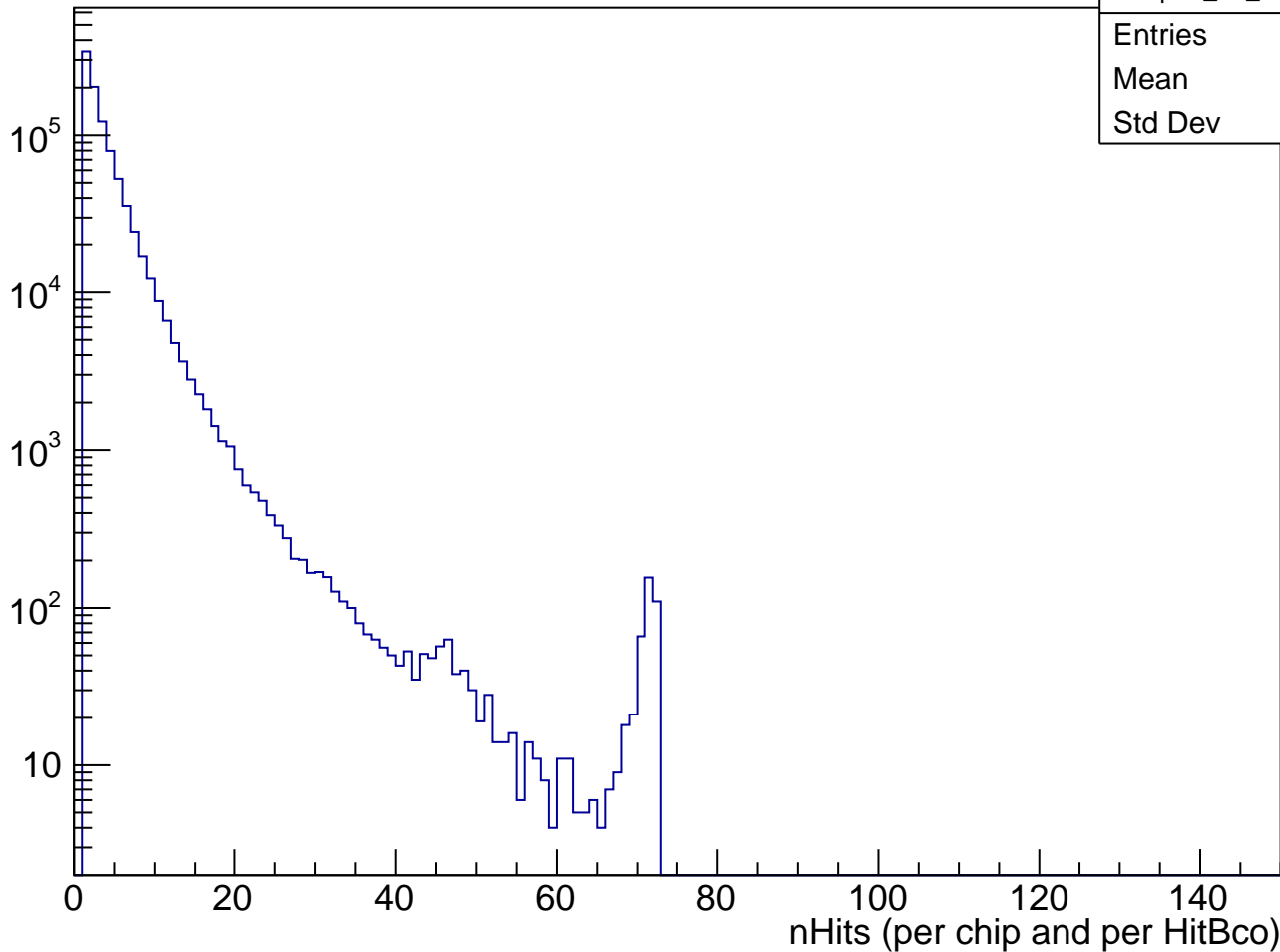
Entries 919416

Mean 3.21

Std Dev 3.673

nChipHit_F2_Fch5_Chip1

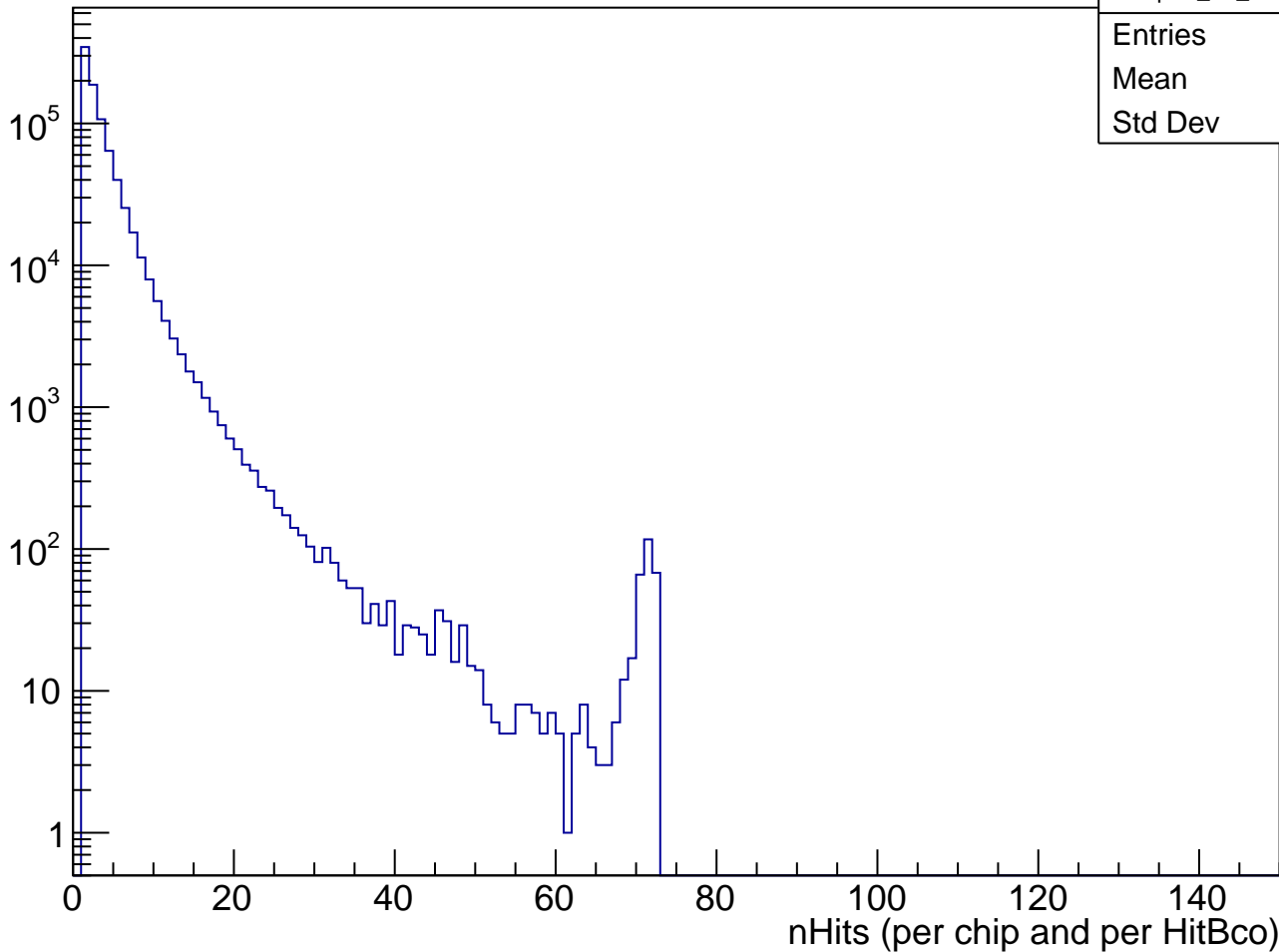
Entries



nChipHit_F2_Fch5_Chip1	
Entries	925953
Mean	3.172
Std Dev	3.654

nChipHit_F2_Fch5_Chip10

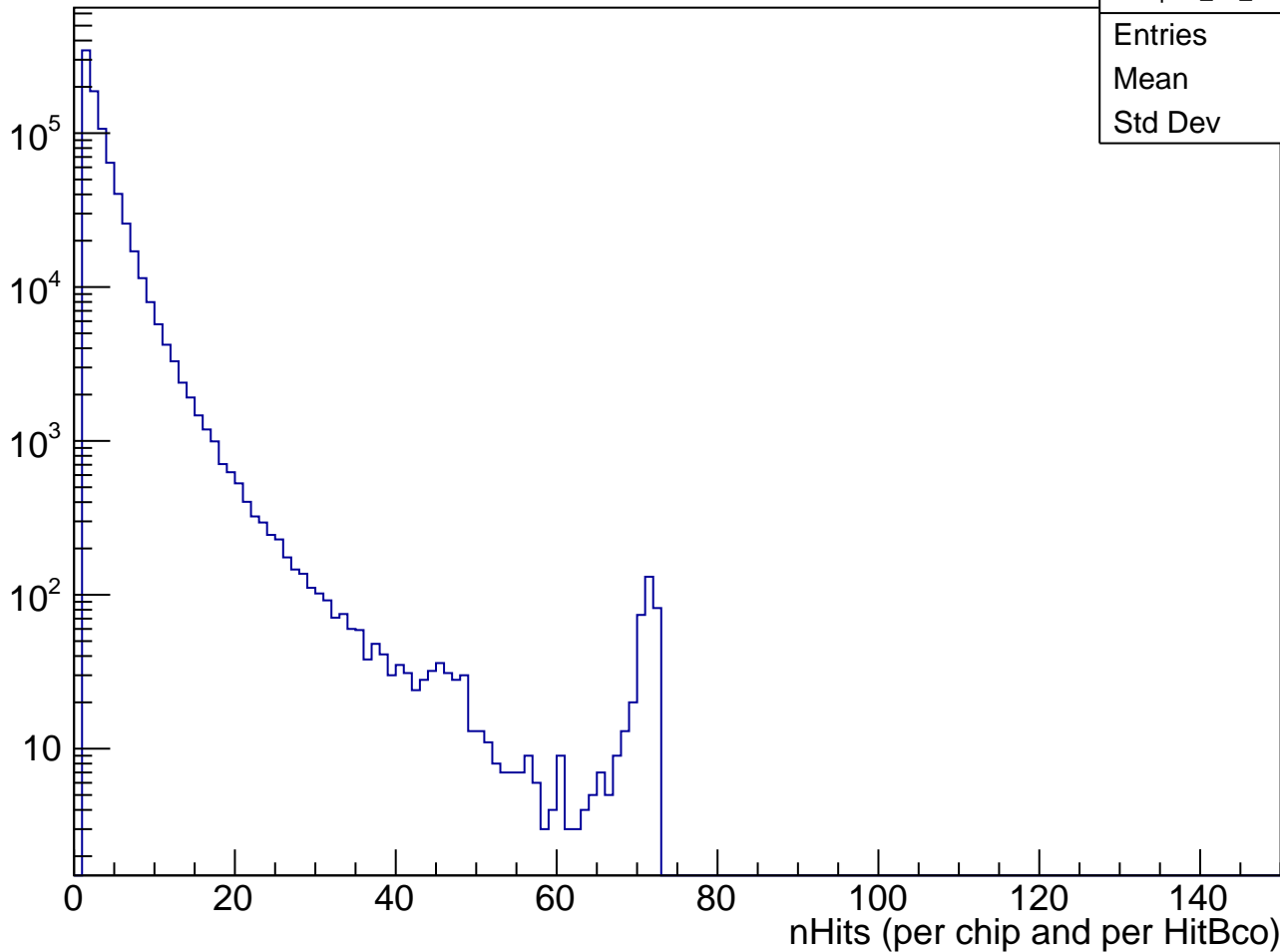
Entries



nChipHit_F2_Fch5_Chip10	
Entries	831494
Mean	2.791
Std Dev	3.213

nChipHit_F2_Fch5_Chip11

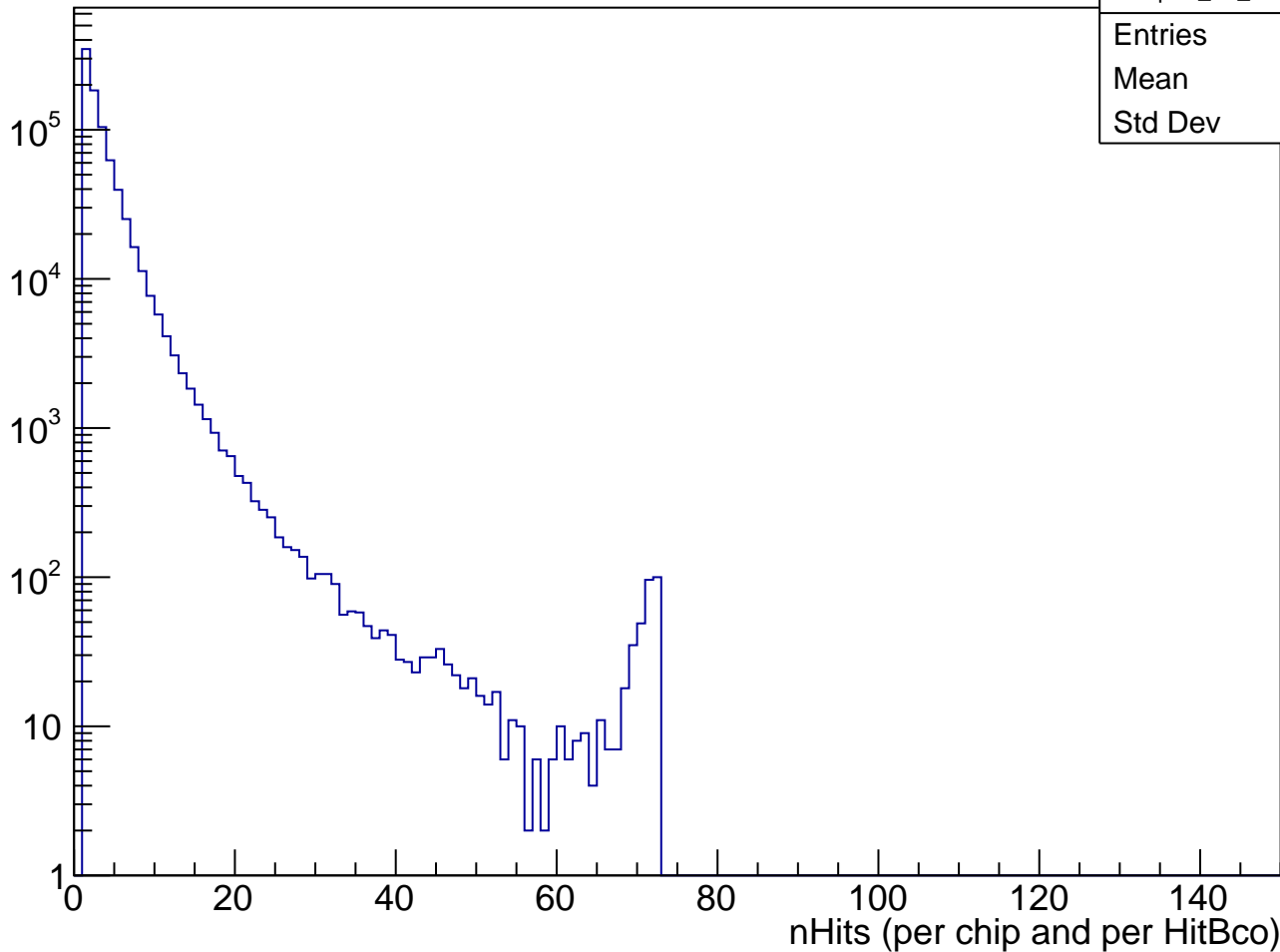
Entries



nChipHit_F2_Fch5_Chip11	
Entries	832538
Mean	2.814
Std Dev	3.293

nChipHit_F2_Fch5_Chip12

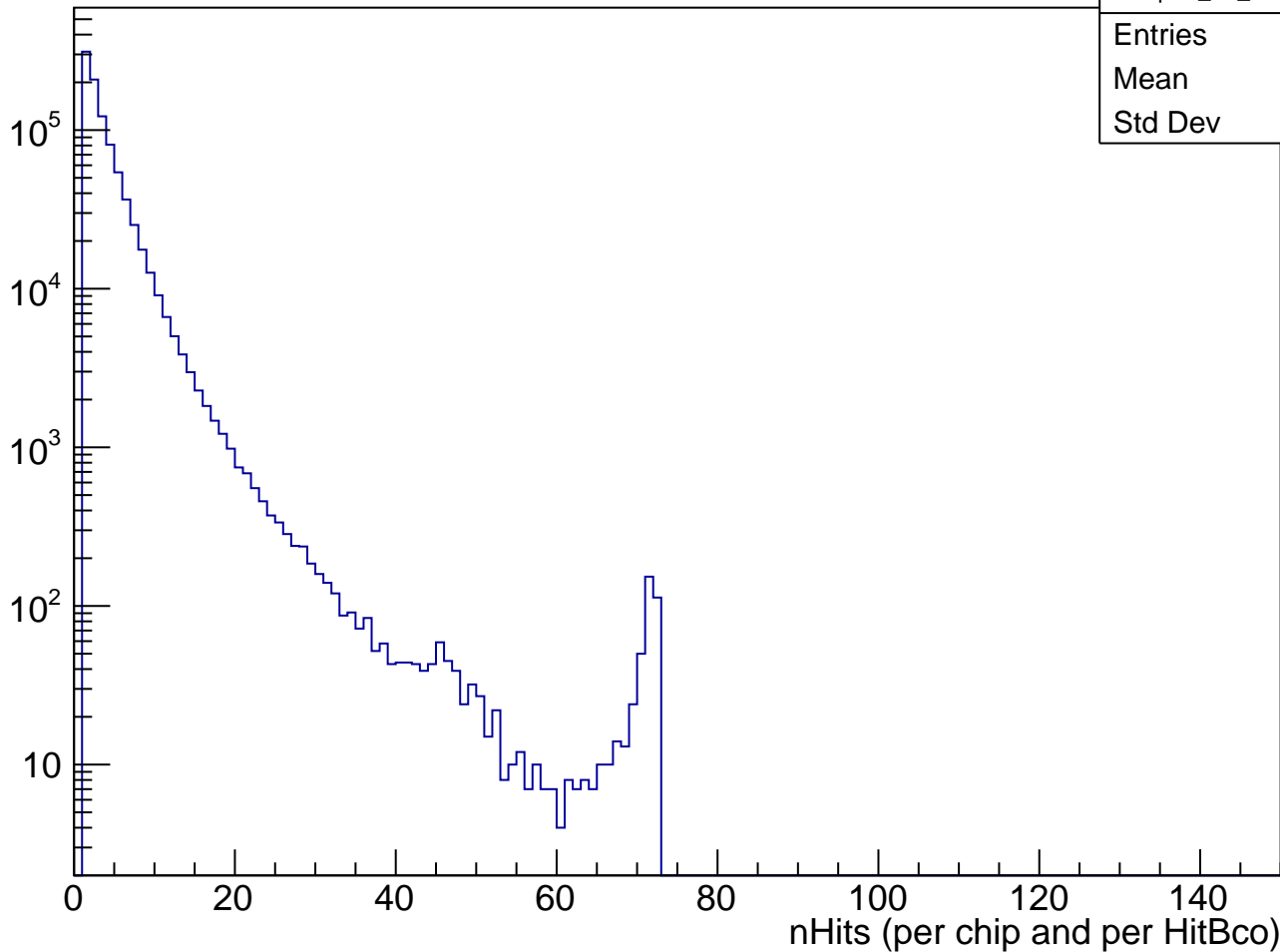
Entries



nChipHit_F2_Fch5_Chip12	
Entries	823959
Mean	2.788
Std Dev	3.279

nChipHit_F2_Fch5_Chip13

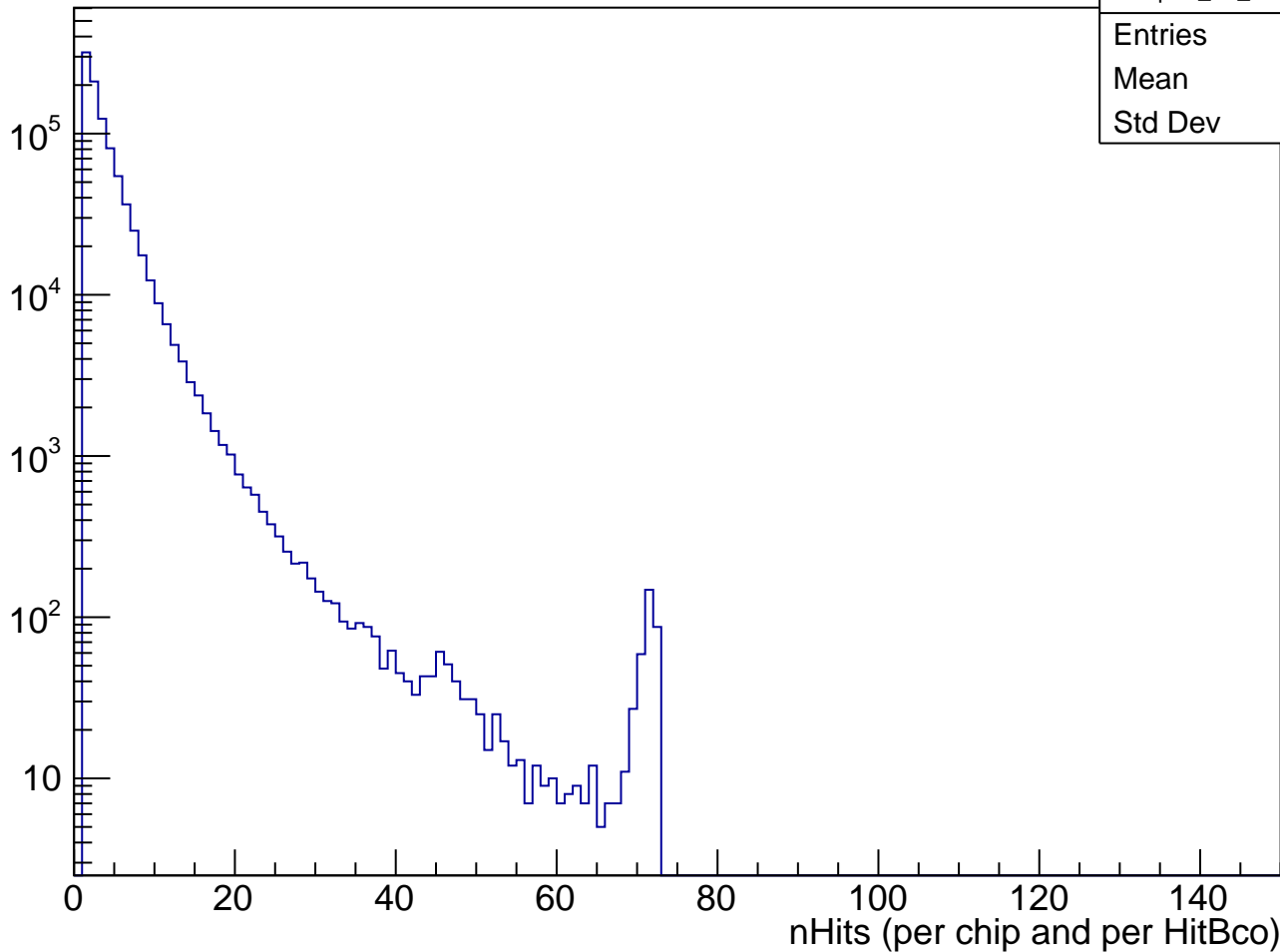
Entries



nChipHit_F2_Fch5_Chip13	
Entries	911023
Mean	3.254
Std Dev	3.667

nChipHit_F2_Fch5_Chip14

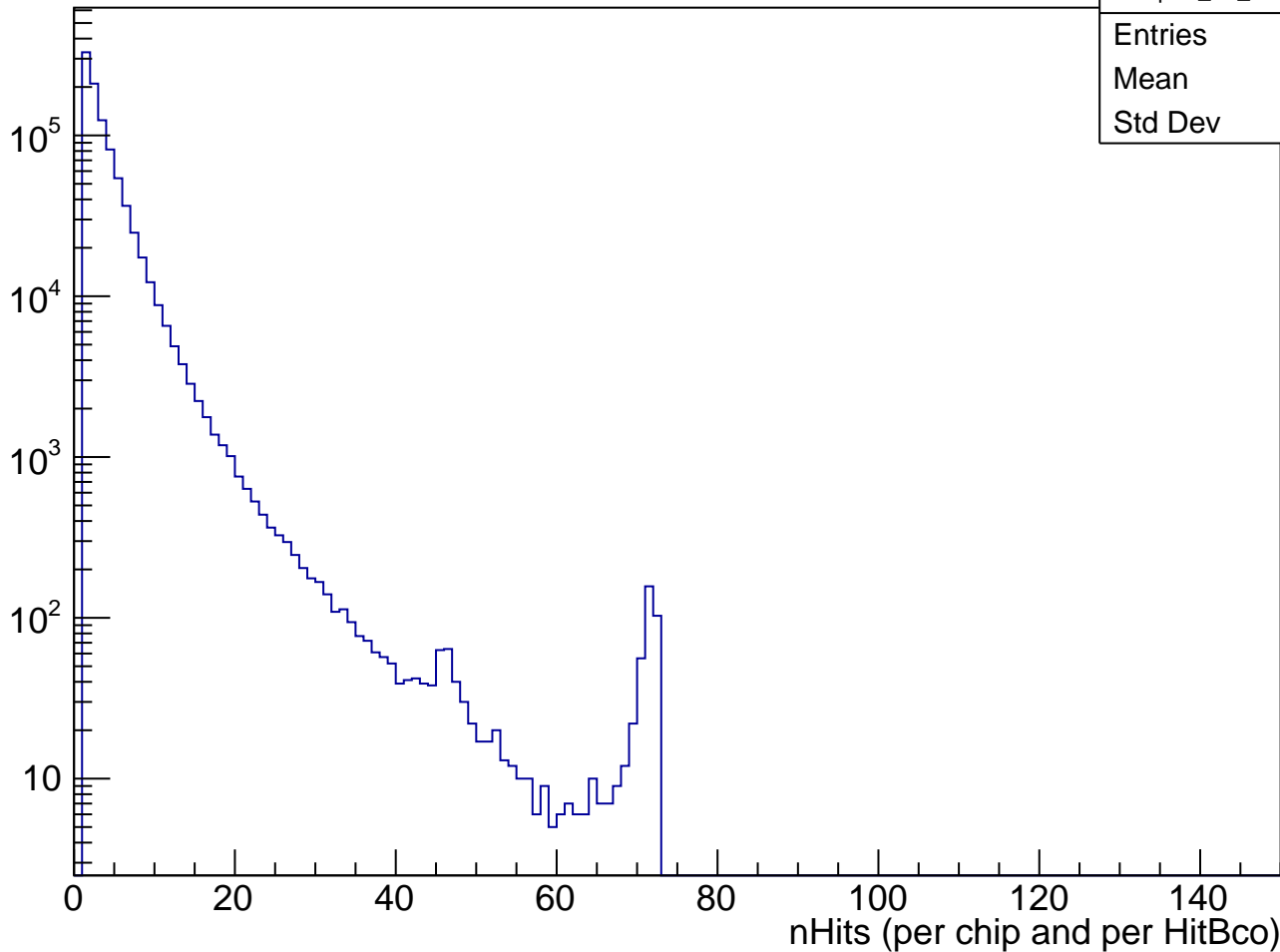
Entries



nChipHit_F2_Fch5_Chip14	
Entries	920816
Mean	3.225
Std Dev	3.636

nChipHit_F2_Fch5_Chip15

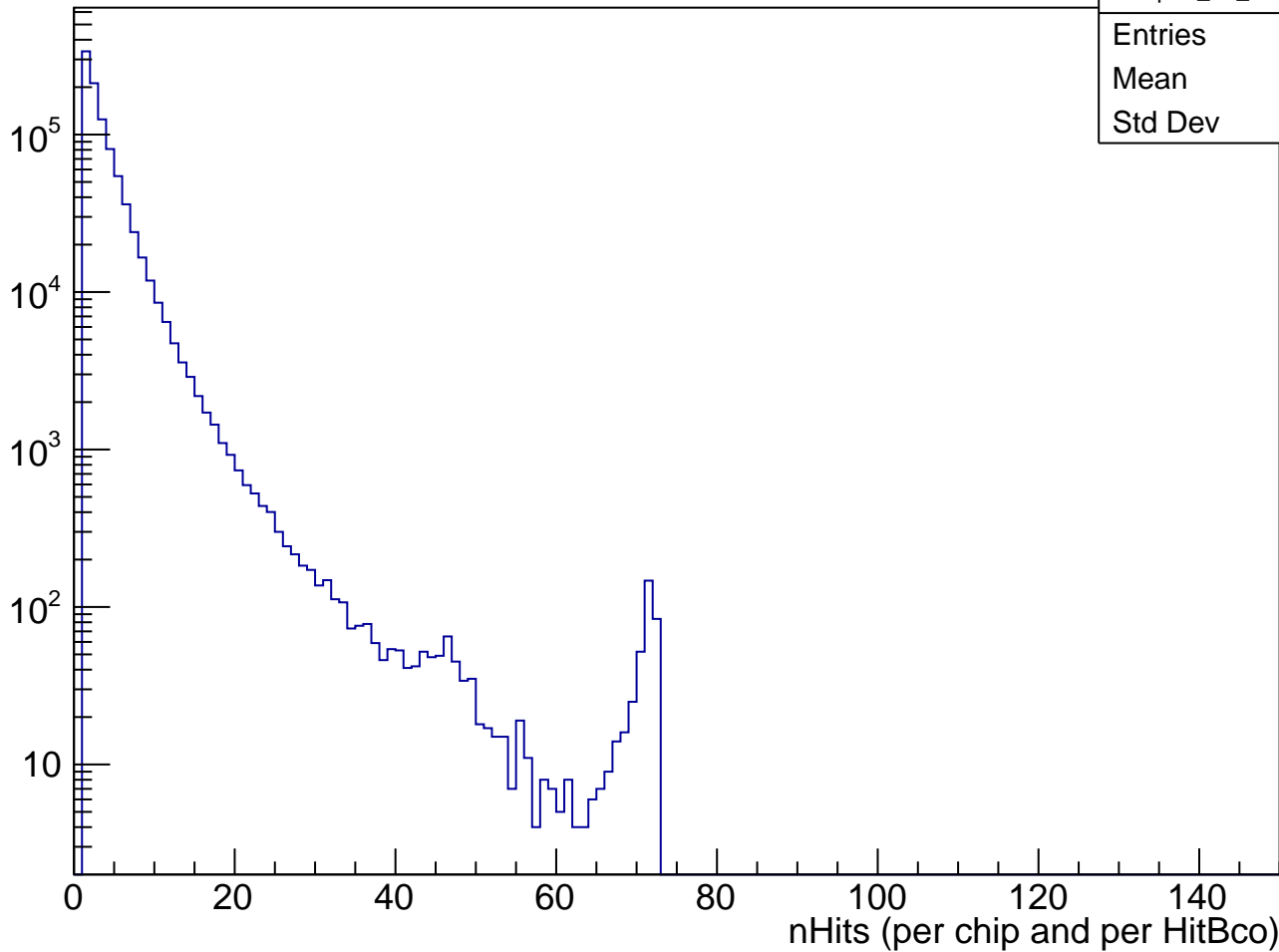
Entries



nChipHit_F2_Fch5_Chip15	
Entries	930165
Mean	3.195
Std Dev	3.615

nChipHit_F2_Fch5_Chip16

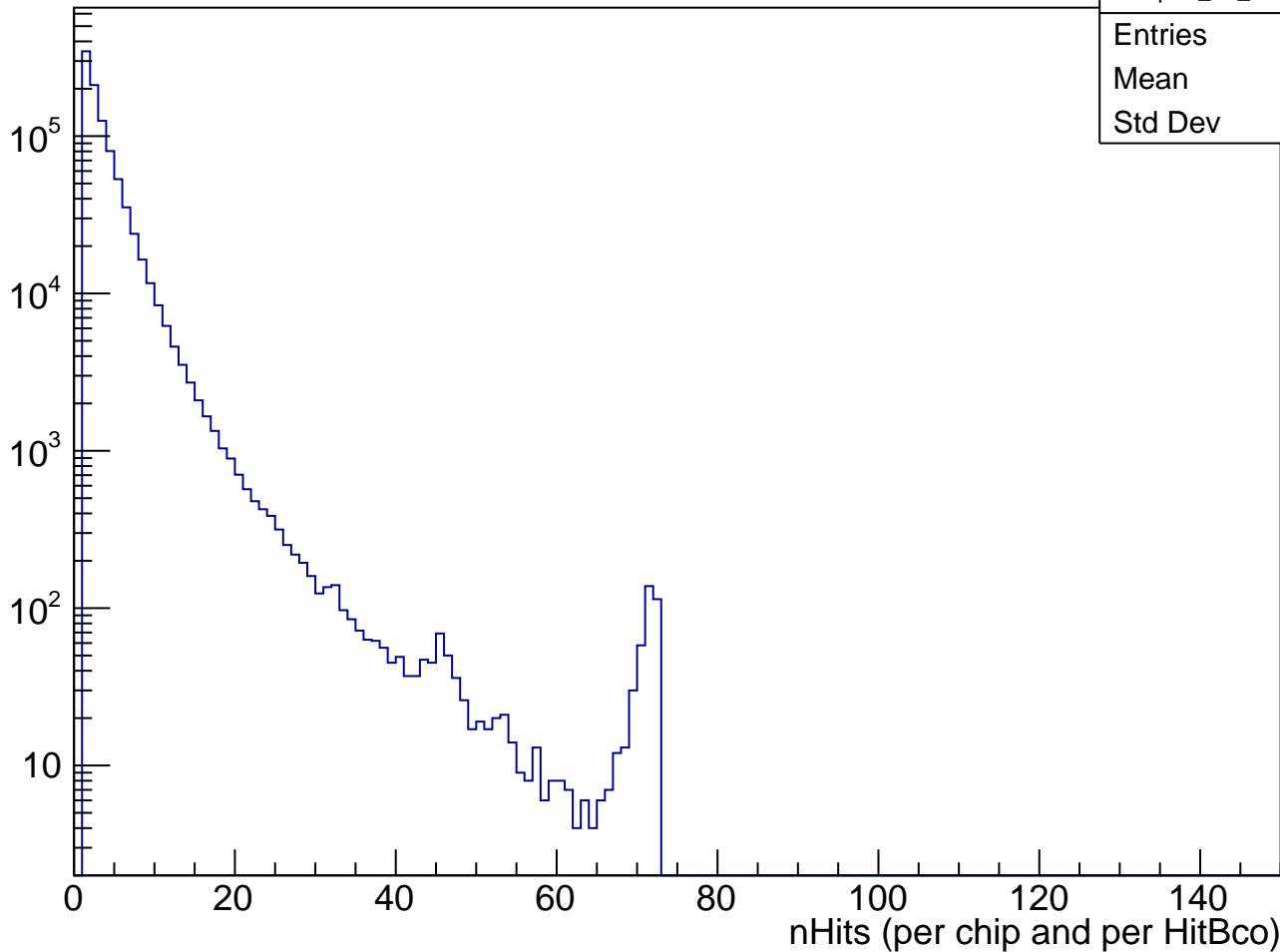
Entries



nChipHit_F2_Fch5_Chip16	
Entries	937478
Mean	3.146
Std Dev	3.563

nChipHit_F2_Fch5_Chip17

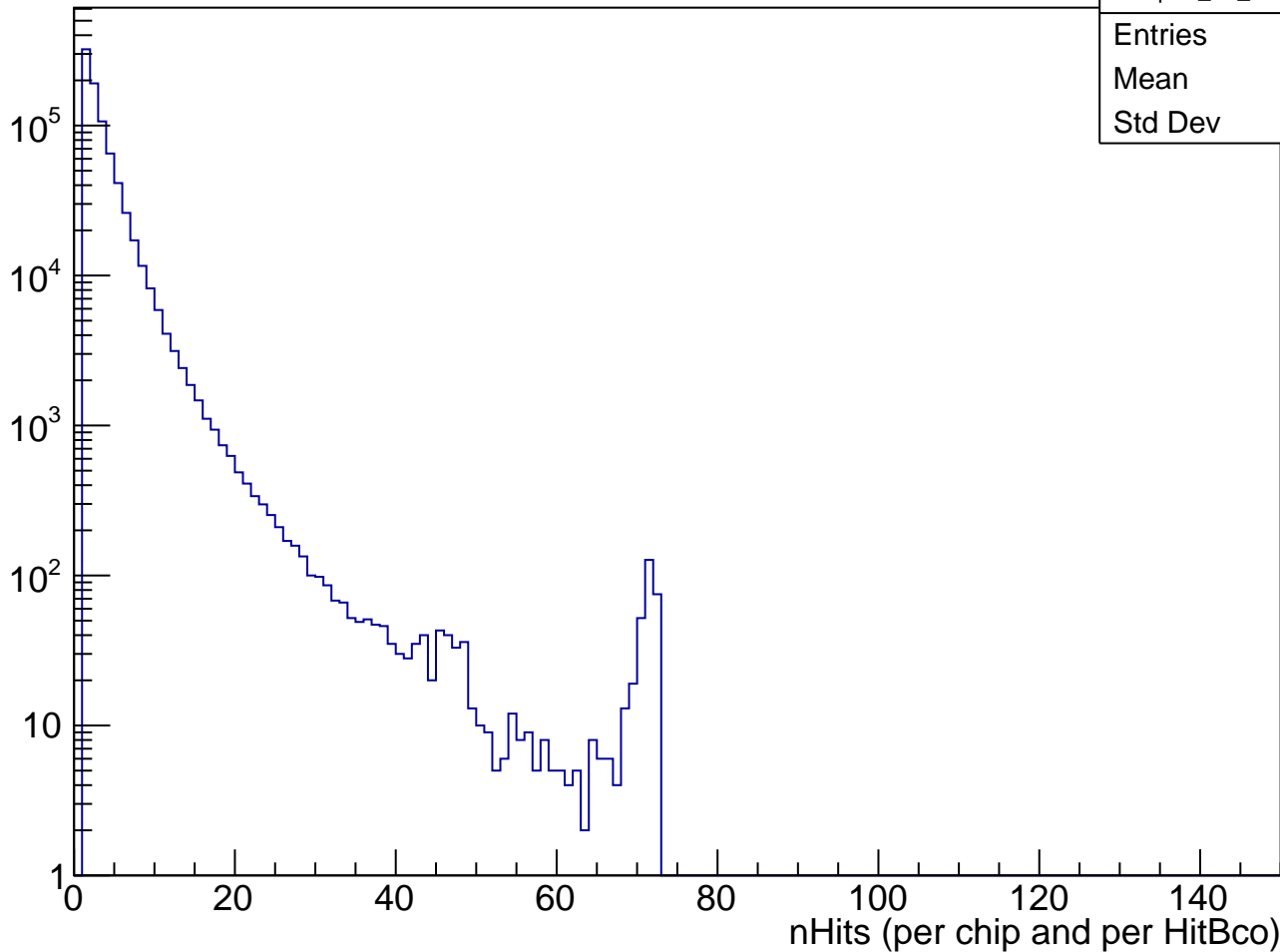
Entries



nChipHit_F2_Fch5_Chip17	
Entries	941146
Mean	3.107
Std Dev	3.543

nChipHit_F2_Fch5_Chip18

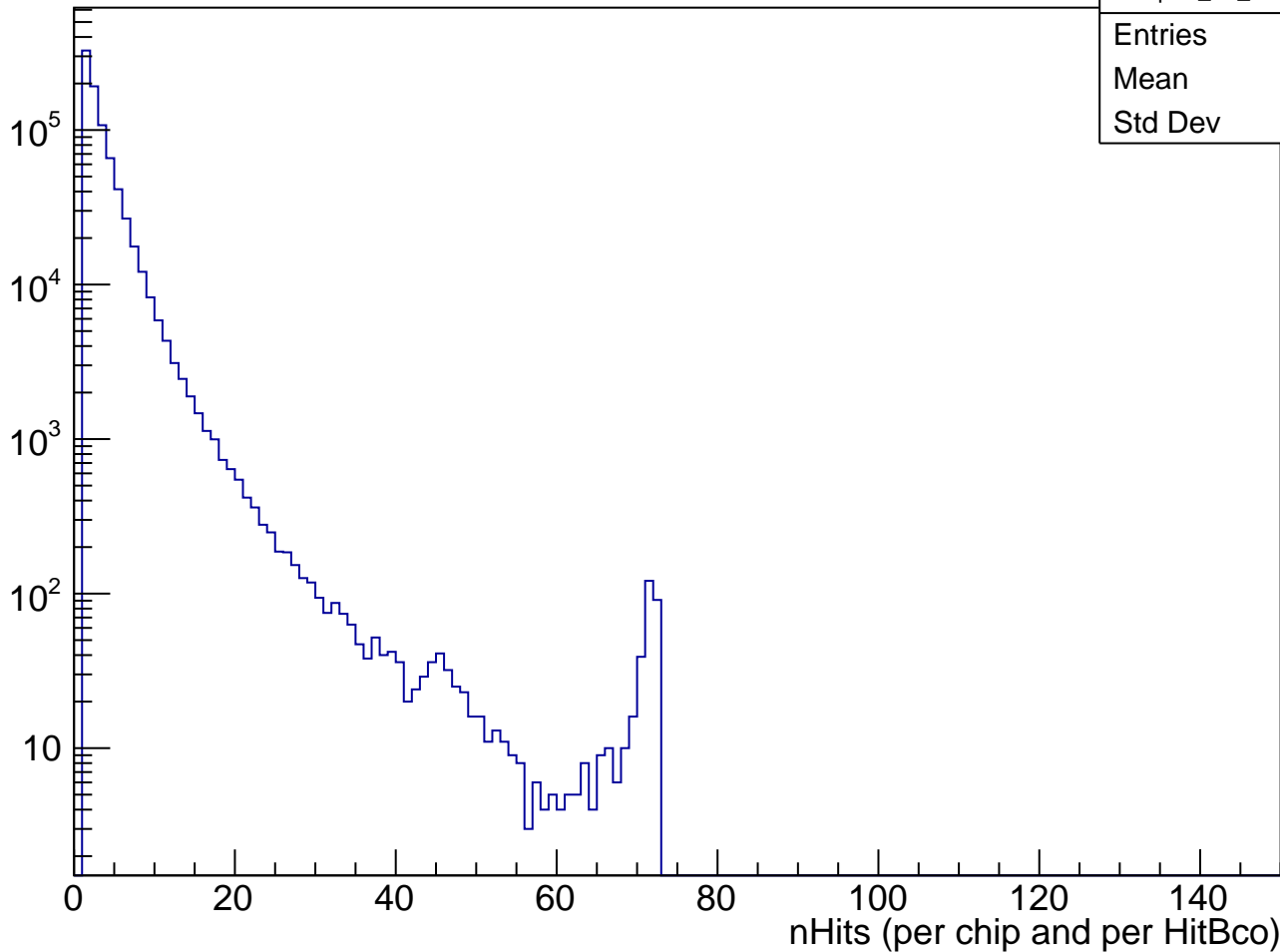
Entries



nChipHit_F2_Fch5_Chip18	
Entries	815666
Mean	2.863
Std Dev	3.287

nChipHit_F2_Fch5_Chip19

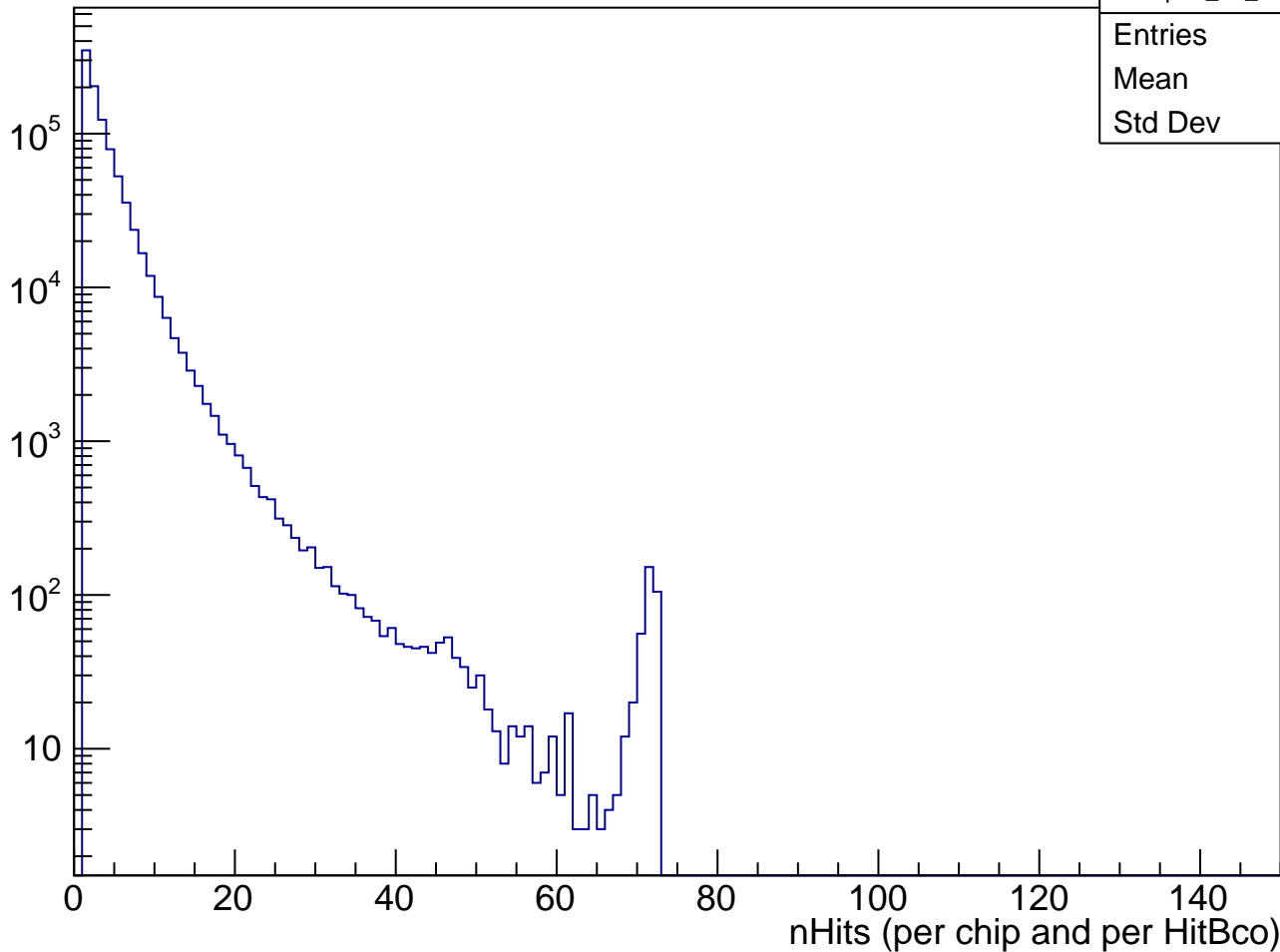
Entries



nChipHit_F2_Fch5_Chip19	
Entries	824558
Mean	2.866
Std Dev	3.282

nChipHit_F2_Fch5_Chip2

Entries



nChipHit_F2_Fch5_Chip2

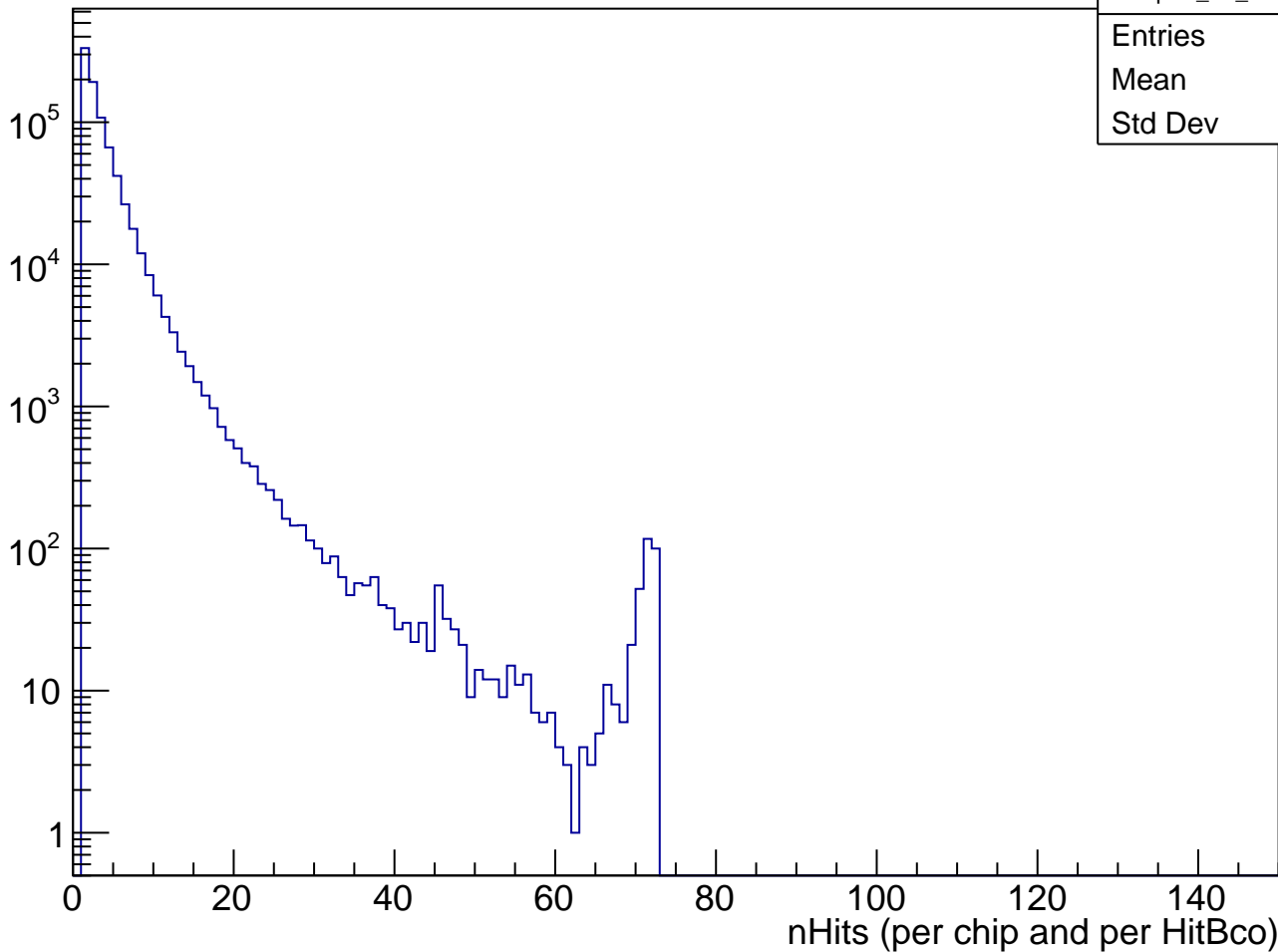
Entries 934429

Mean 3.137

Std Dev 3.608

nChipHit_F2_Fch5_Chip20

Entries

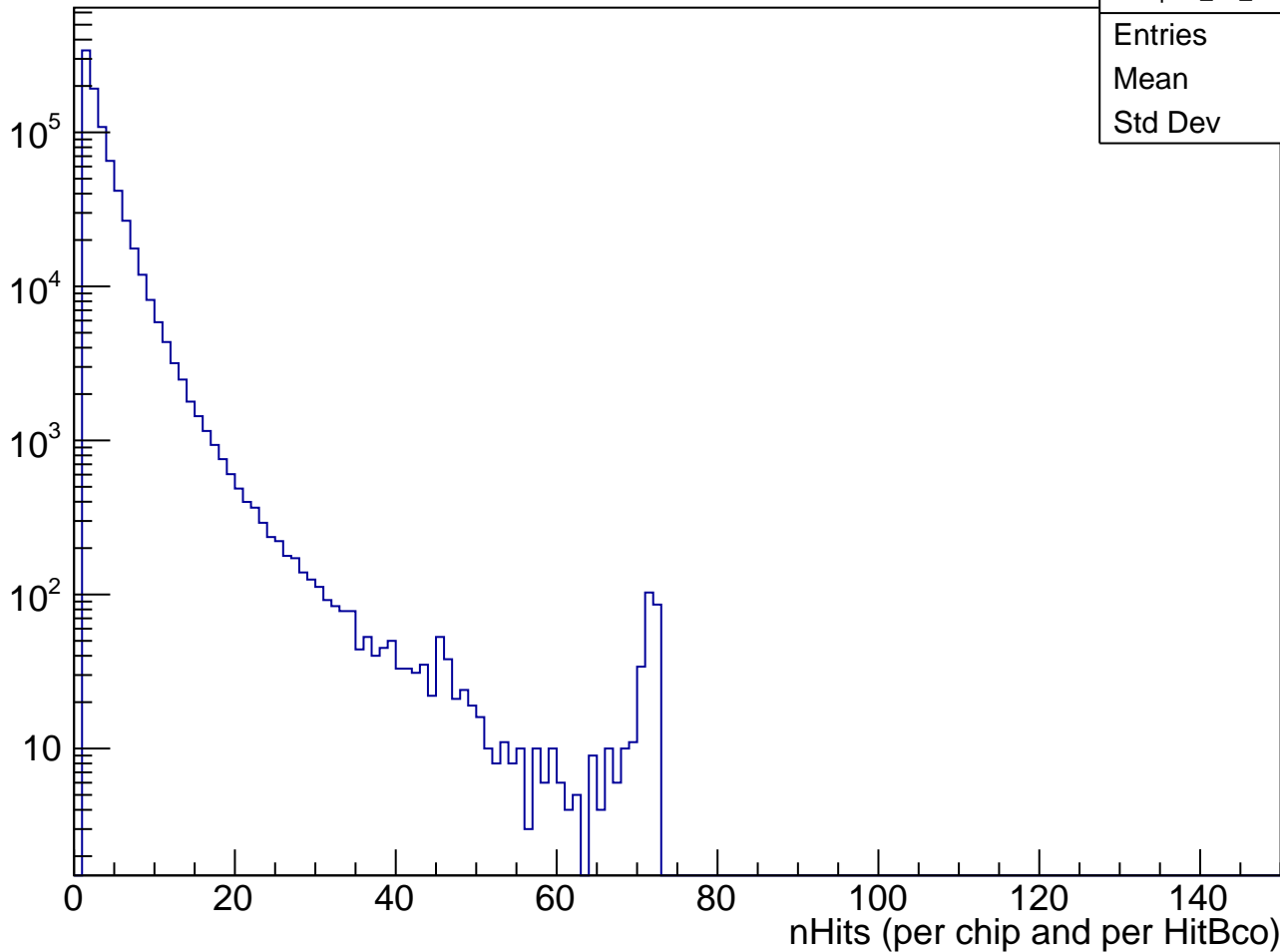


nChipHit_F2_Fch5_Chip20	
Entries	832164
Mean	2.86
Std Dev	3.295

nHits (per chip and per HitBco)

nChipHit_F2_Fch5_Chip21

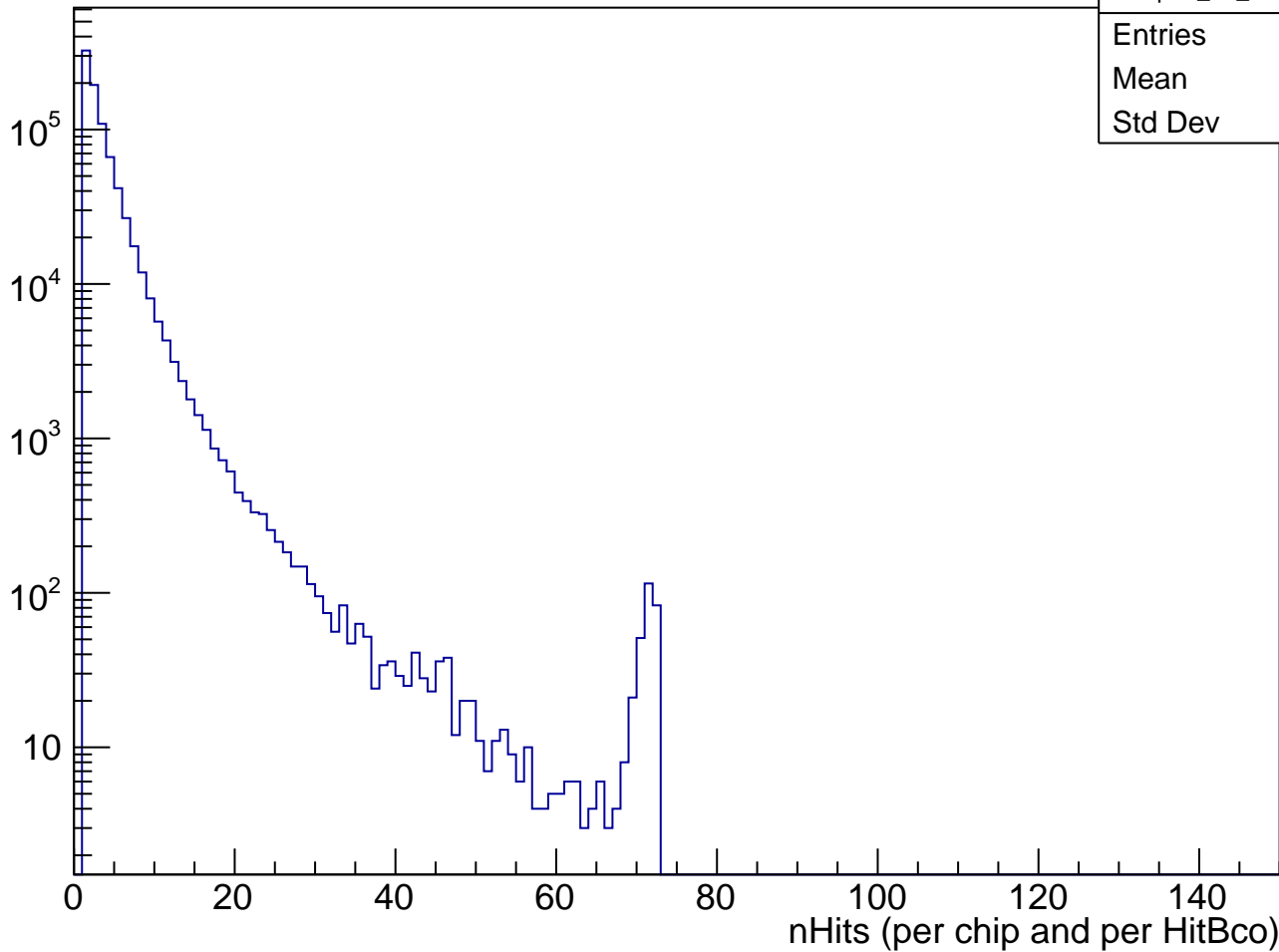
Entries



nChipHit_F2_Fch5_Chip21	
Entries	838832
Mean	2.834
Std Dev	3.251

nChipHit_F2_Fch5_Chip22

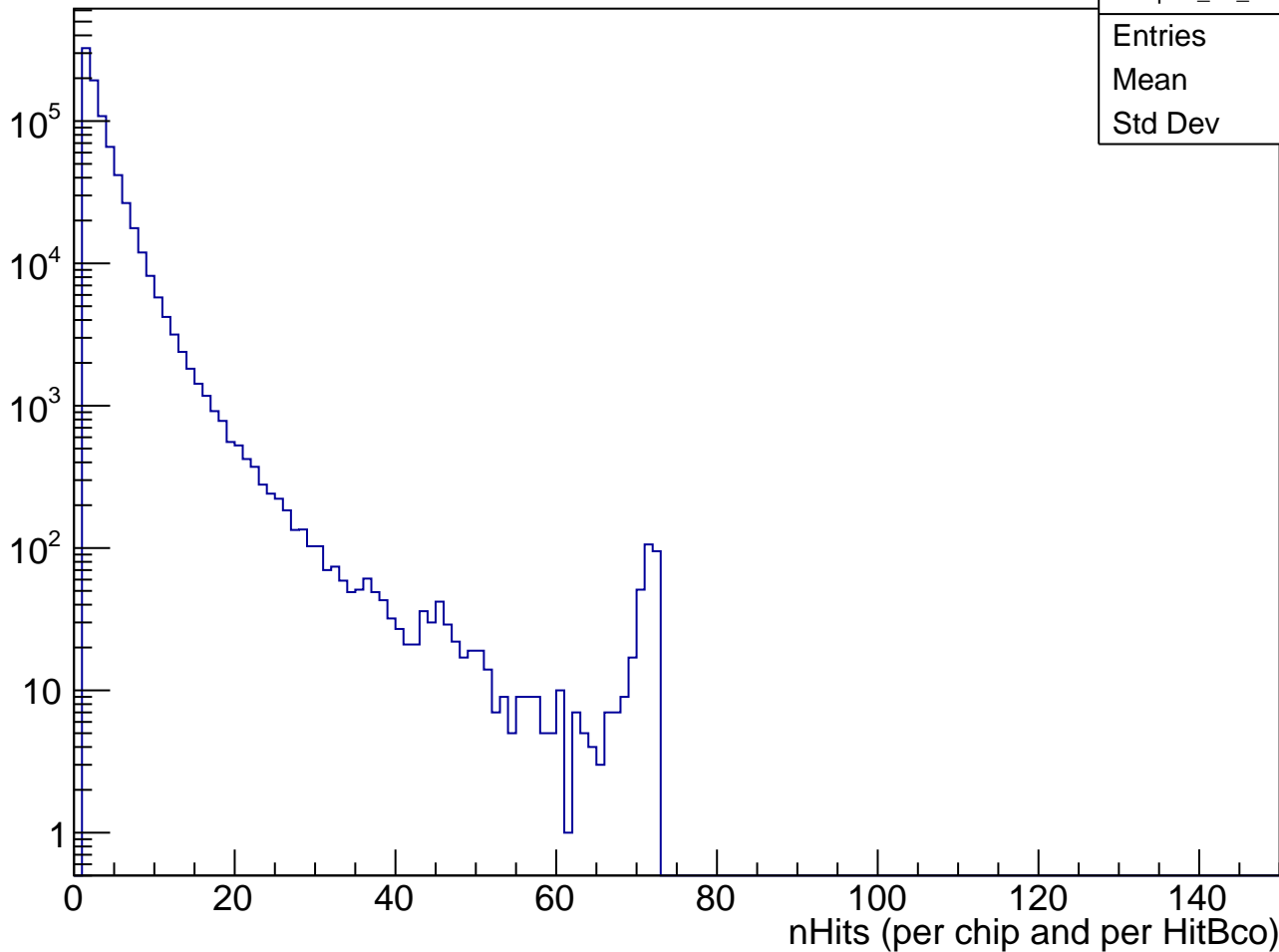
Entries



nChipHit_F2_Fch5_Chip22	
Entries	826232
Mean	2.854
Std Dev	3.236

nChipHit_F2_Fch5_Chip23

Entries



nChipHit_F2_Fch5_Chip23

Entries 824845

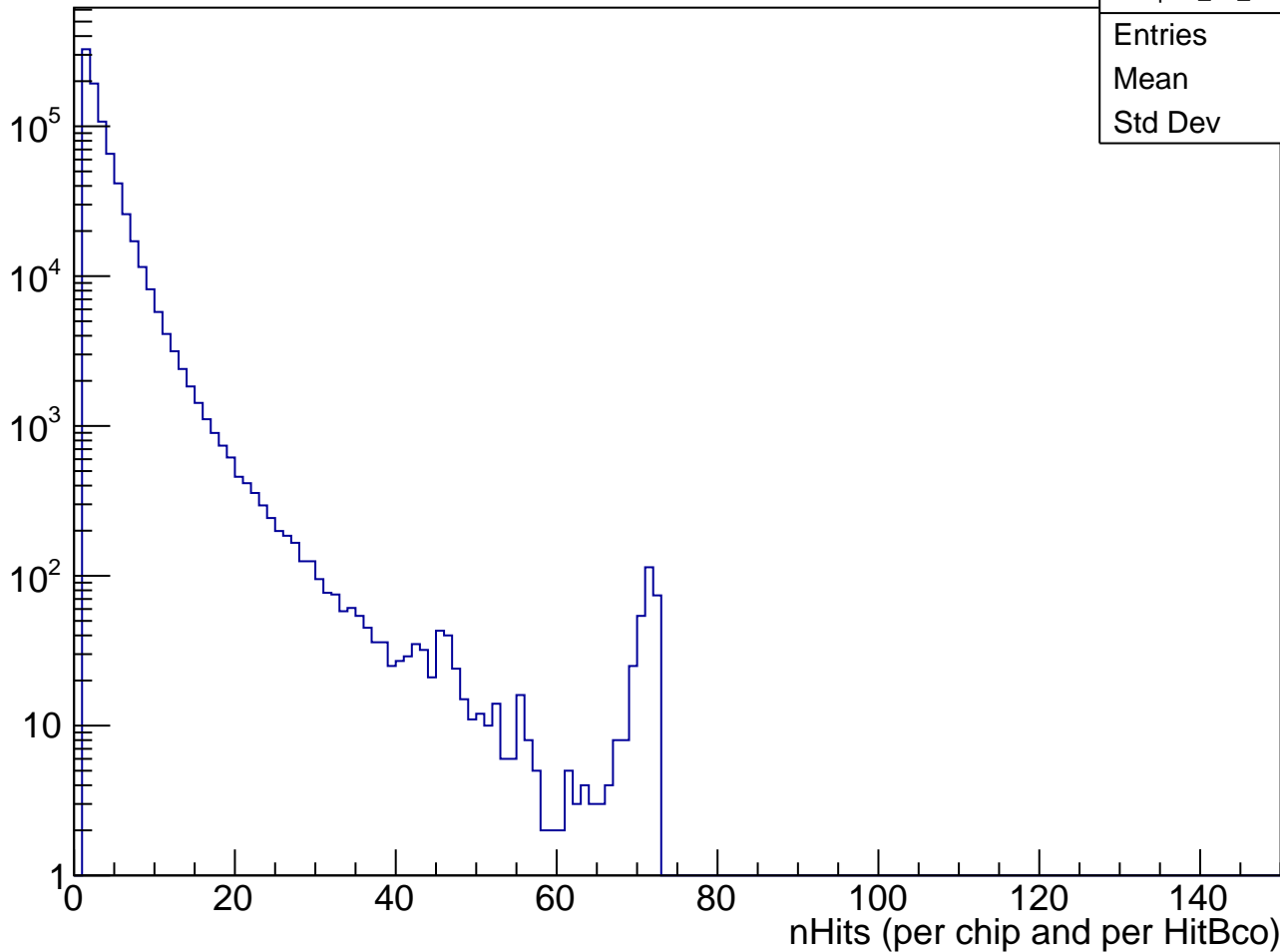
Mean 2.86

Std Dev 3.259

nHits (per chip and per HitBco)

nChipHit_F2_Fch5_Chip24

Entries

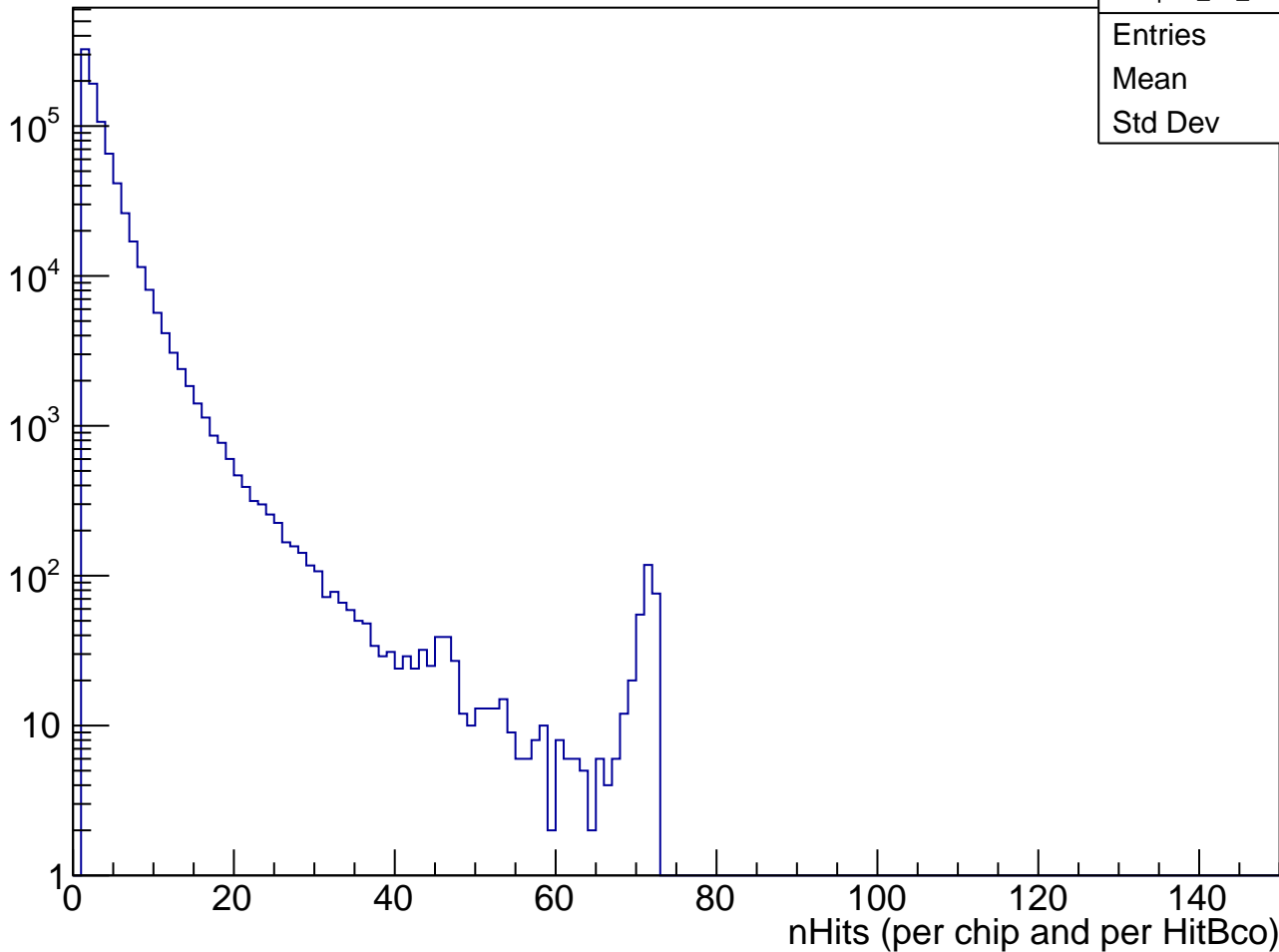


nChipHit_F2_Fch5_Chip24	
Entries	822878
Mean	2.843
Std Dev	3.235

nHits (per chip and per HitBco)

nChipHit_F2_Fch5_Chip25

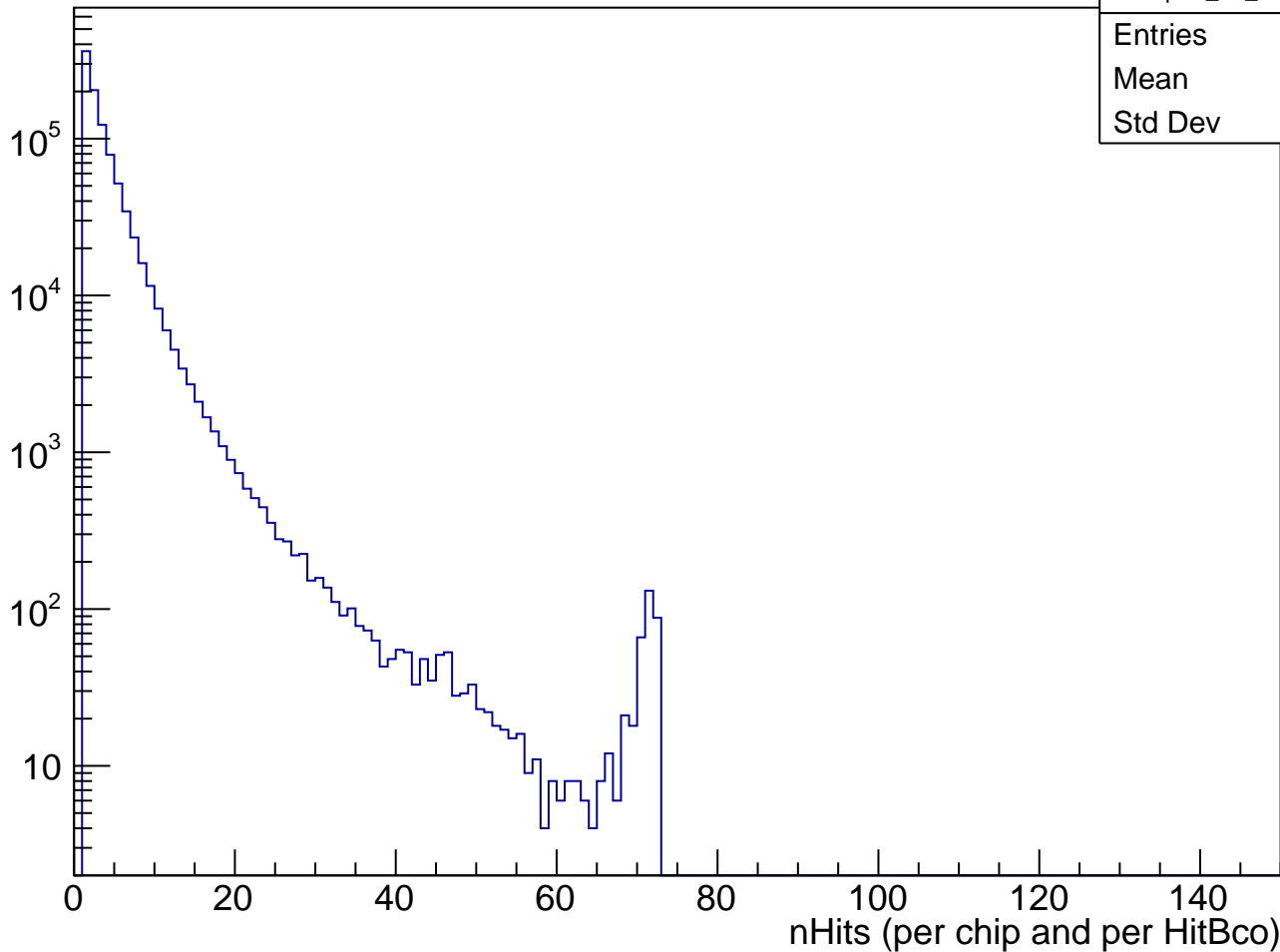
Entries



nChipHit_F2_Fch5_Chip25	
Entries	819066
Mean	2.846
Std Dev	3.253

nChipHit_F2_Fch5_Chip3

Entries



nChipHit_F2_Fch5_Chip3

Entries 941649

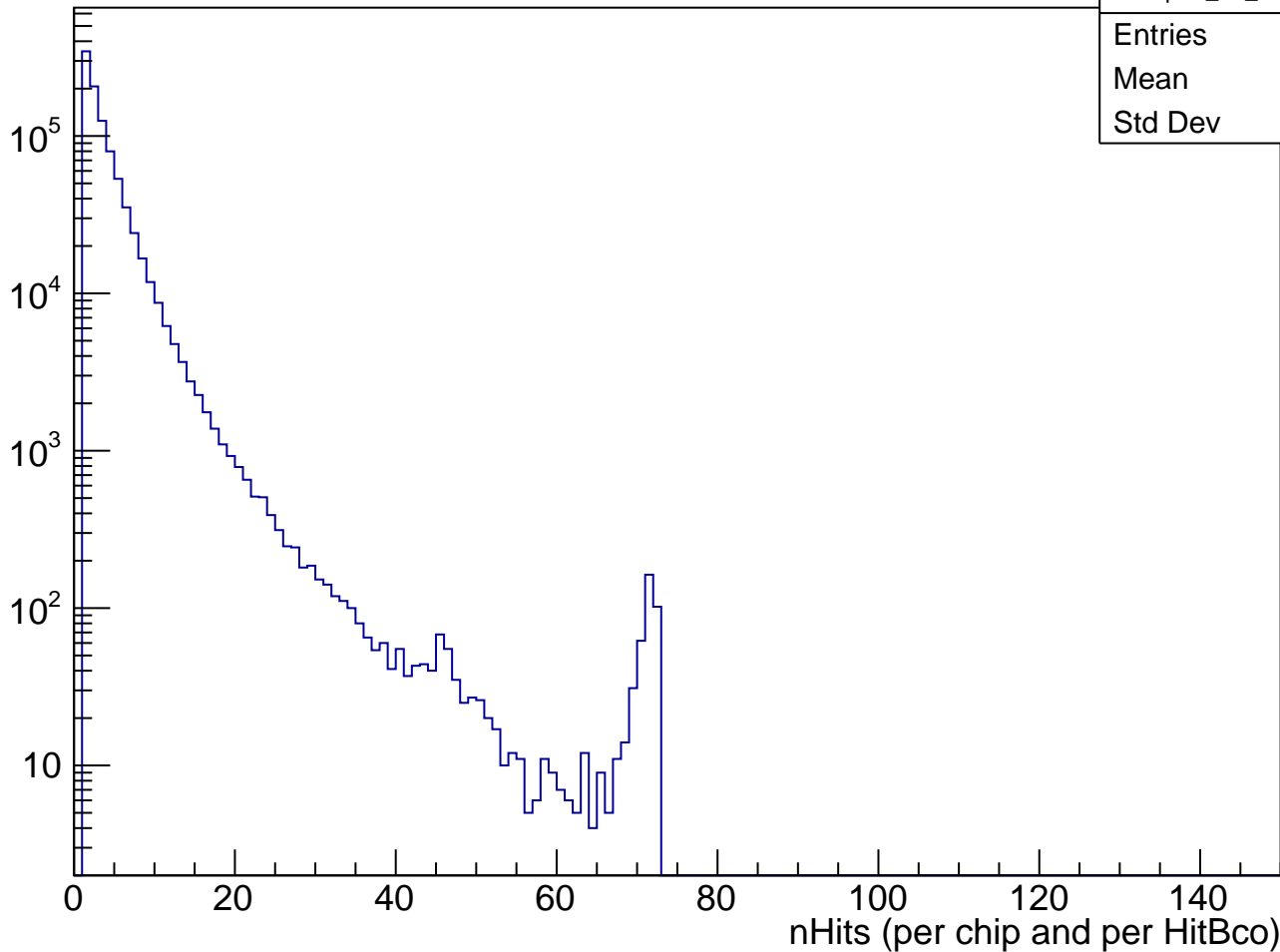
Mean 3.066

Std Dev 3.535

nHits (per chip and per HitBco)

nChipHit_F2_Fch5_Chip4

Entries



nChipHit_F2_Fch5_Chip4

Entries 937159

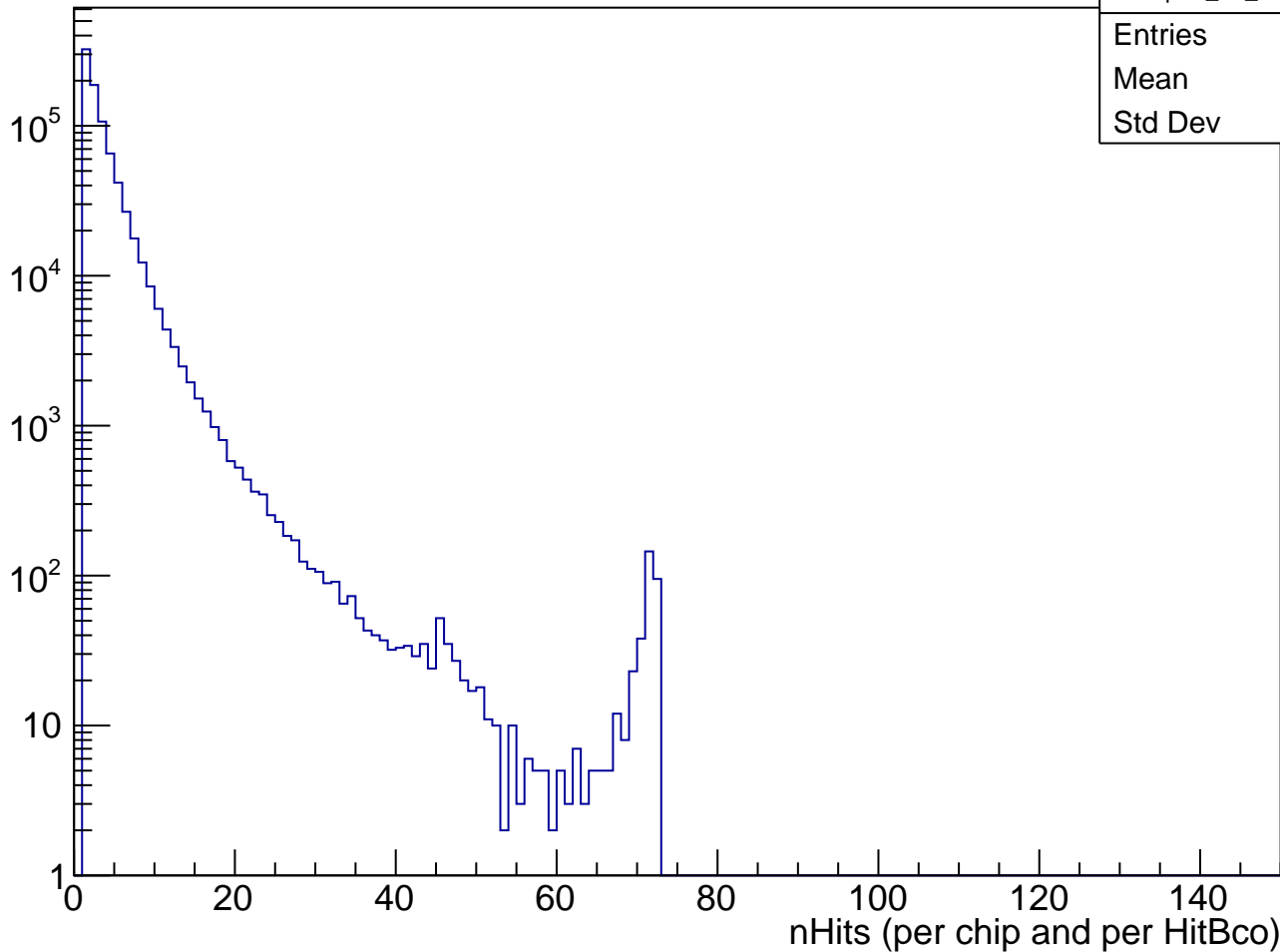
Mean 3.138

Std Dev 3.607

nHits (per chip and per HitBco)

nChipHit_F2_Fch5_Chip5

Entries



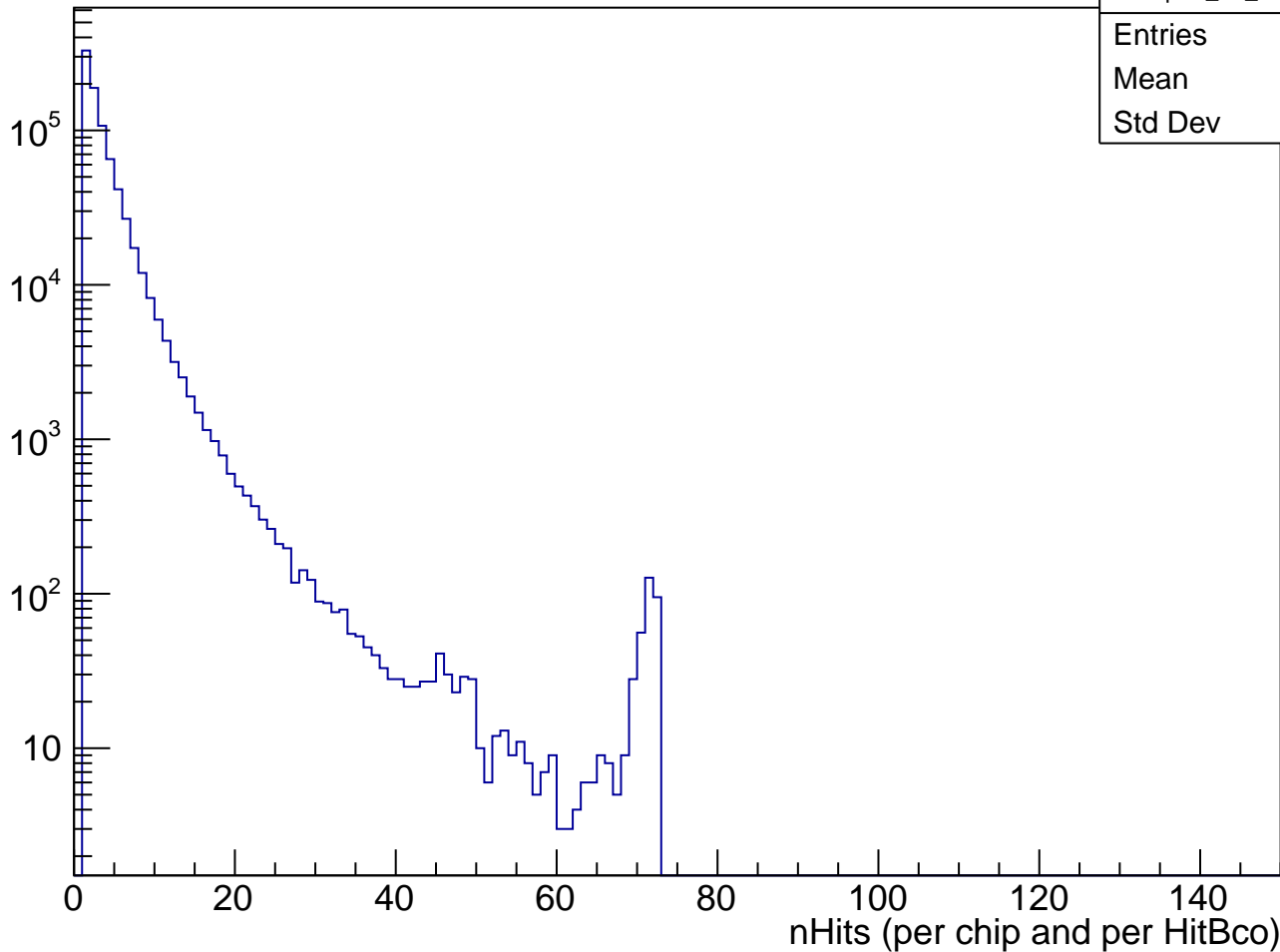
nChipHit_F2_Fch5_Chip5

Entries	817816
Mean	2.895
Std Dev	3.343

nHits (per chip and per HitBco)

nChipHit_F2_Fch5_Chip6

Entries



nChipHit_F2_Fch5_Chip6

Entries 822339

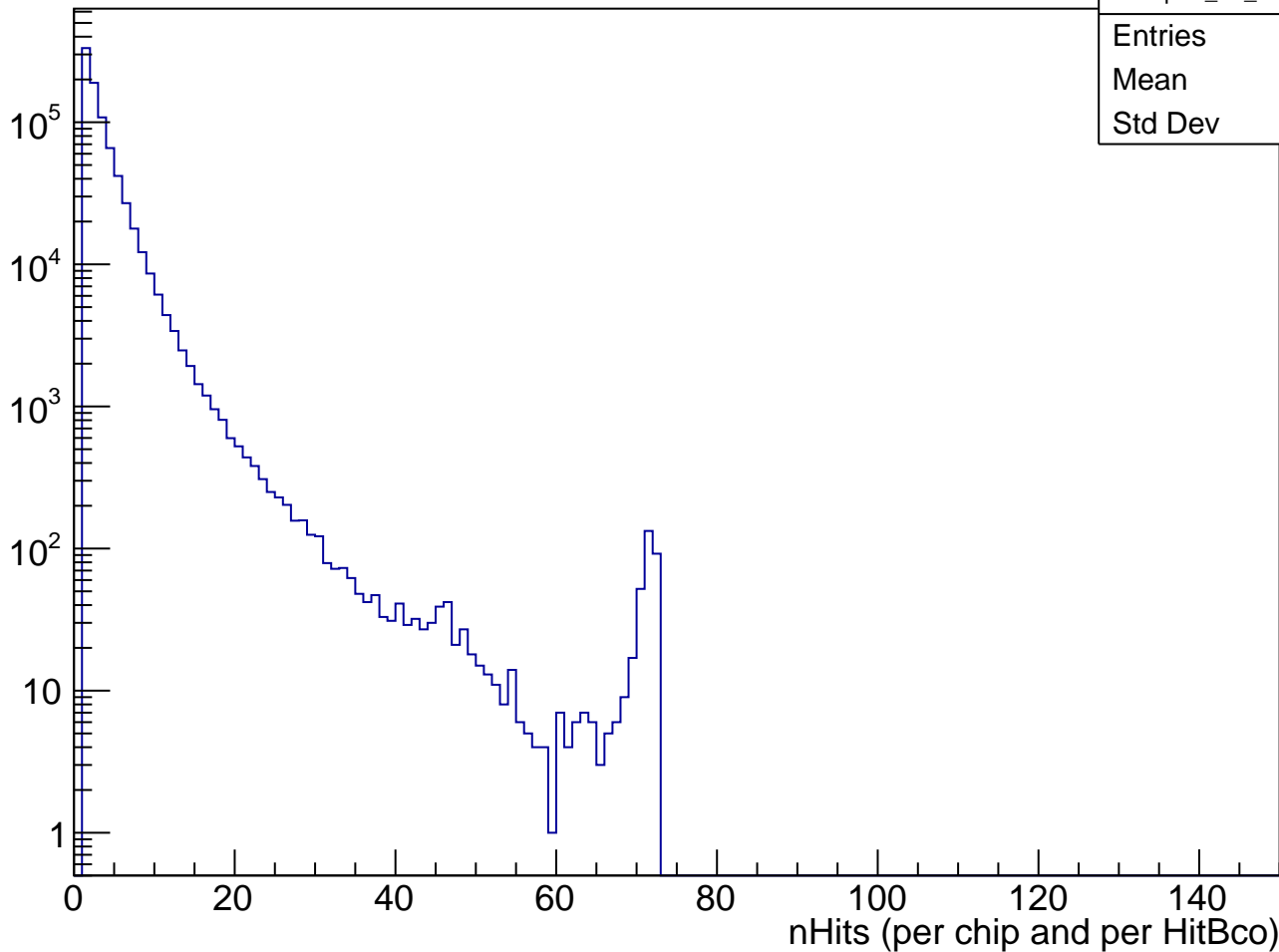
Mean 2.867

Std Dev 3.319

nHits (per chip and per HitBco)

nChipHit_F2_Fch5_Chip7

Entries



nChipHit_F2_Fch5_Chip7

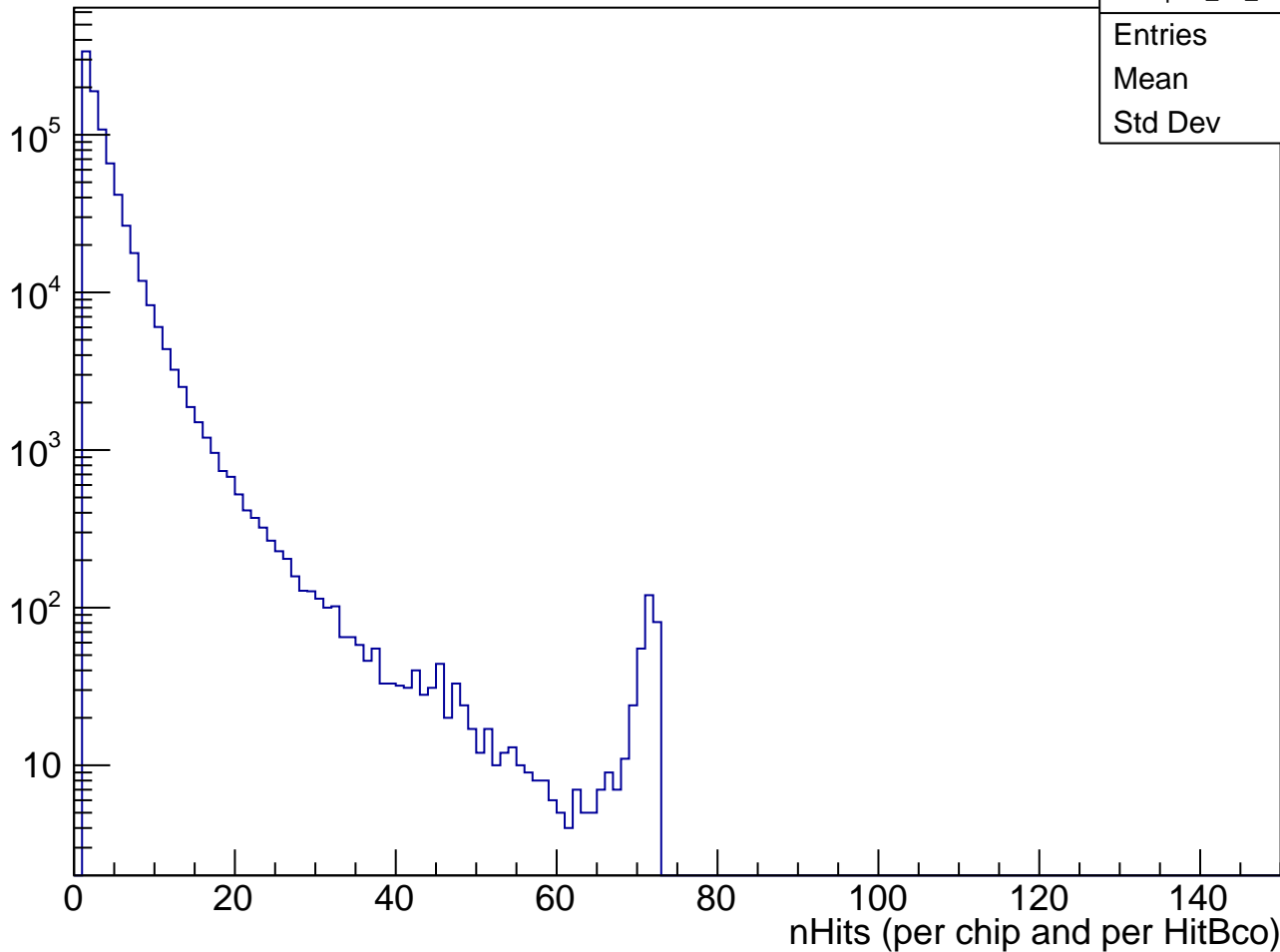
Entries 830767

Mean 2.876

Std Dev 3.32

nChipHit_F2_Fch5_Chip8

Entries



nChipHit_F2_Fch5_Chip8

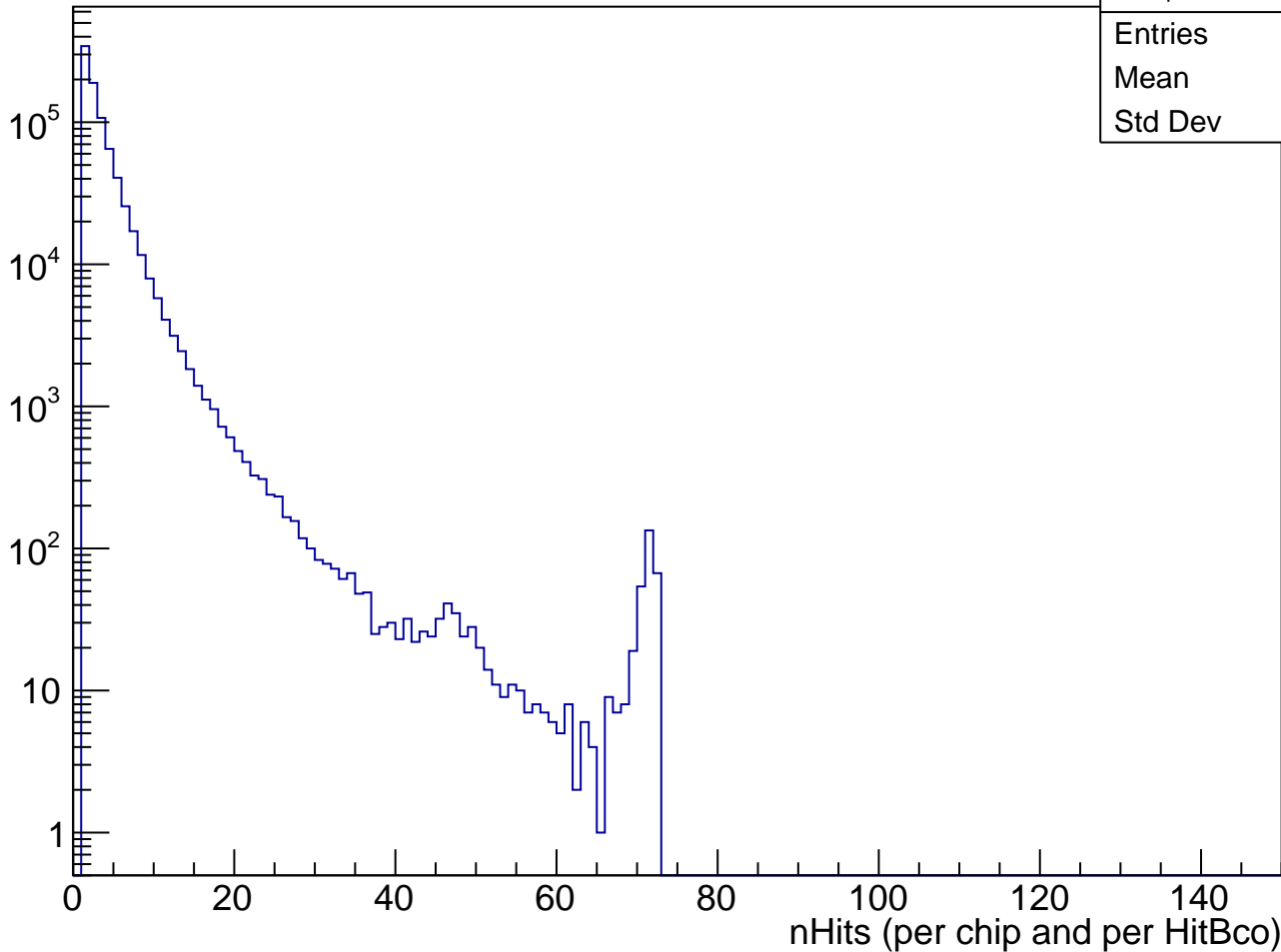
Entries 834197

Mean 2.857

Std Dev 3.321

nChipHit_F2_Fch5_Chip9

Entries



nChipHit_F2_Fch5_Chip9

Entries 833826

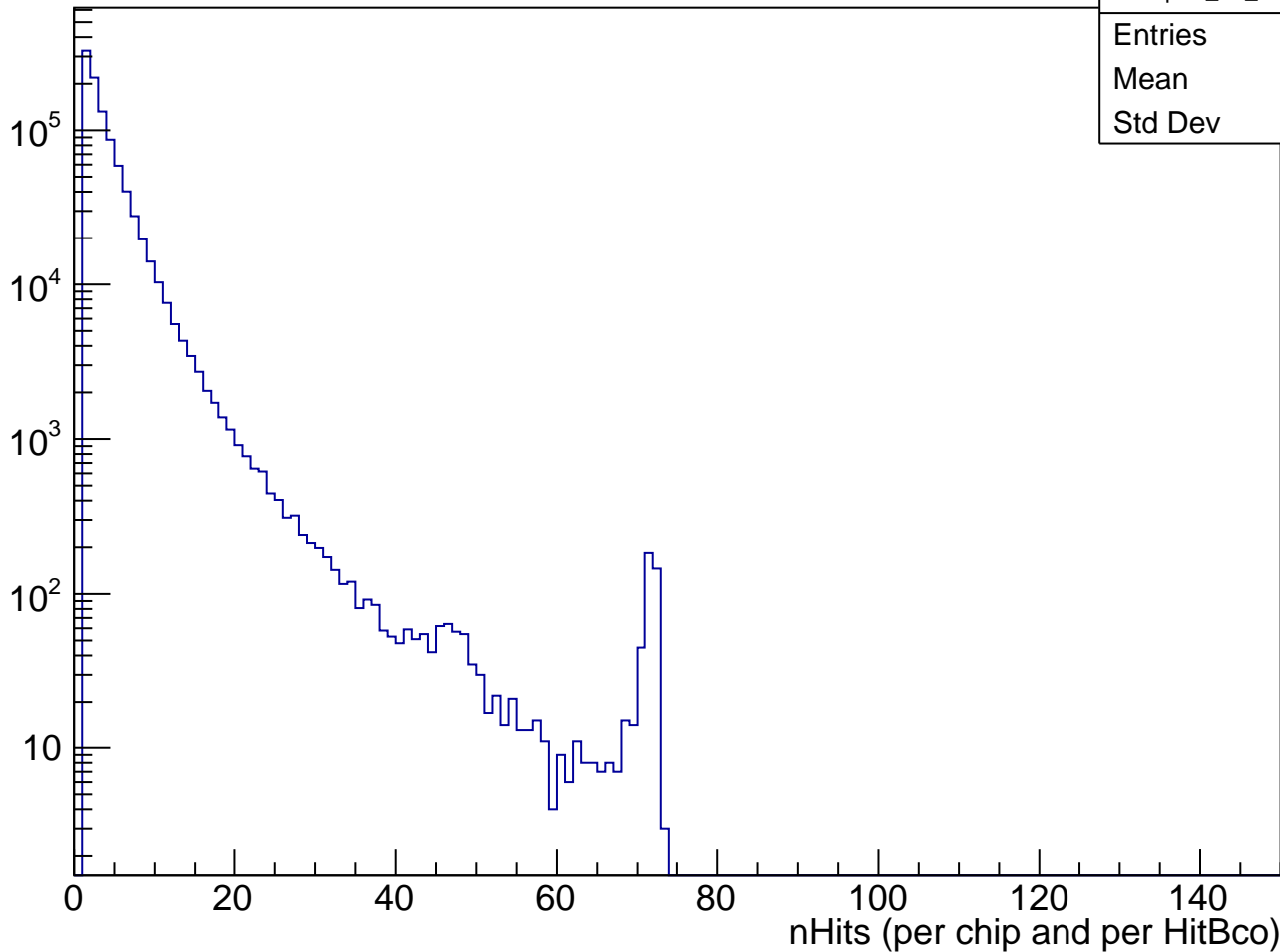
Mean 2.806

Std Dev 3.243

nHits (per chip and per HitBco)

nChipHit_F2_Fch6_Chip0

Entries

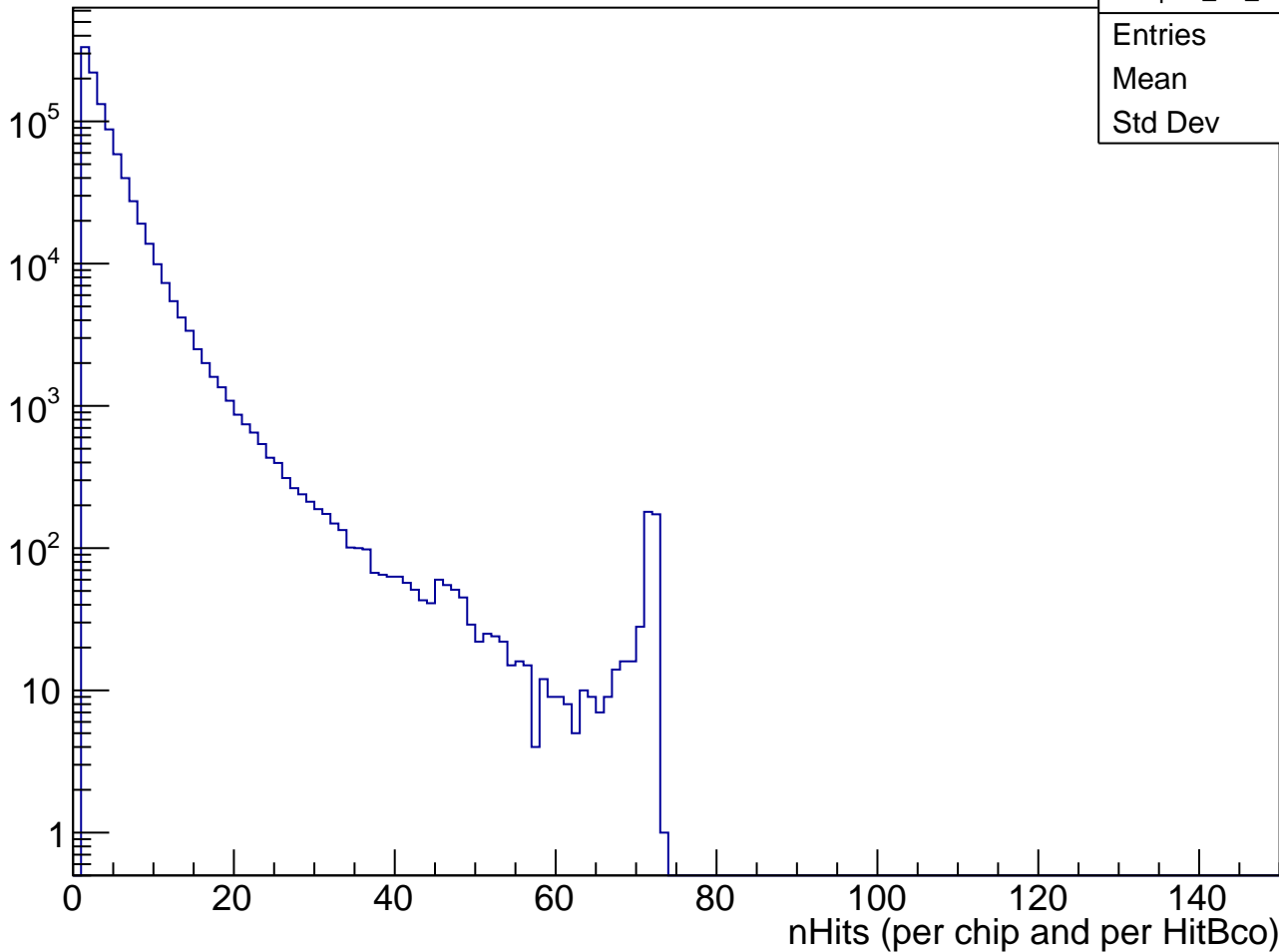


nChipHit_F2_Fch6_Chip0	
Entries	973705
Mean	3.334
Std Dev	3.795

nHits (per chip and per HitBco)

nChipHit_F2_Fch6_Chip1

Entries

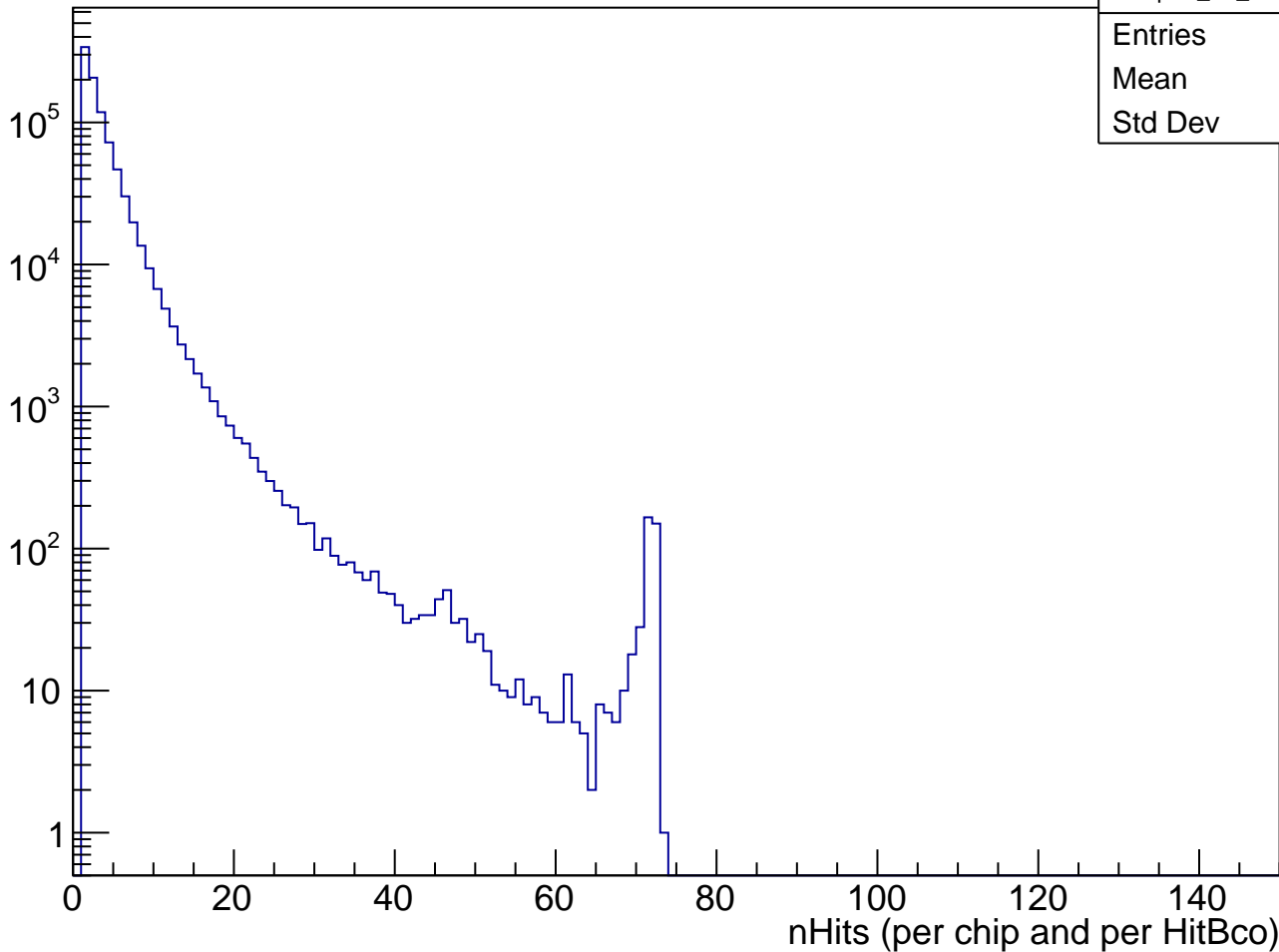


nChipHit_F2_Fch6_Chip1	
Entries	978009
Mean	3.296
Std Dev	3.759

nHits (per chip and per HitBco)

nChipHit_F2_Fch6_Chip10

Entries

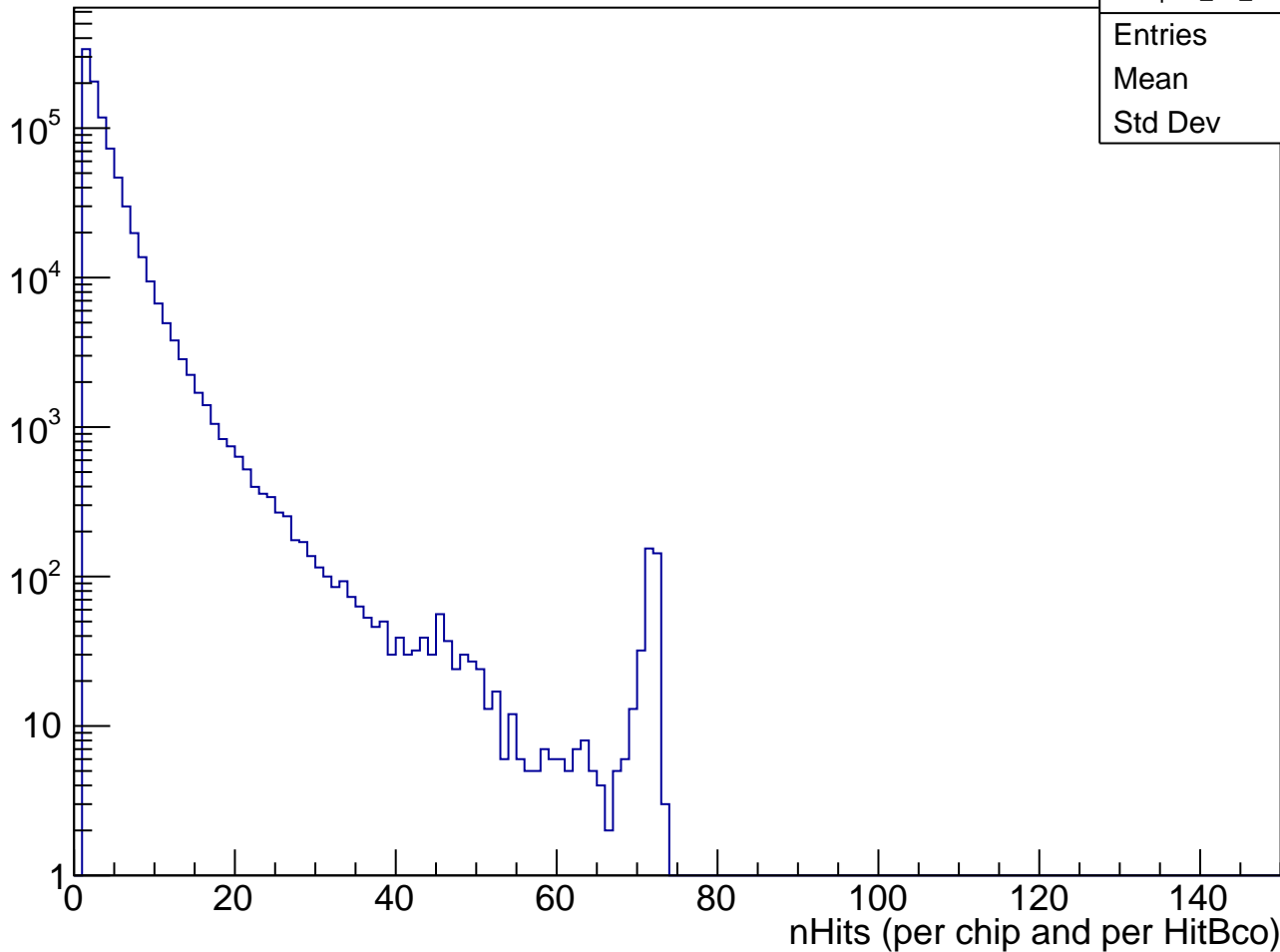


nChipHit_F2_Fch6_Chip10	
Entries	886606
Mean	2.954
Std Dev	3.44

nHits (per chip and per HitBco)

nChipHit_F2_Fch6_Chip11

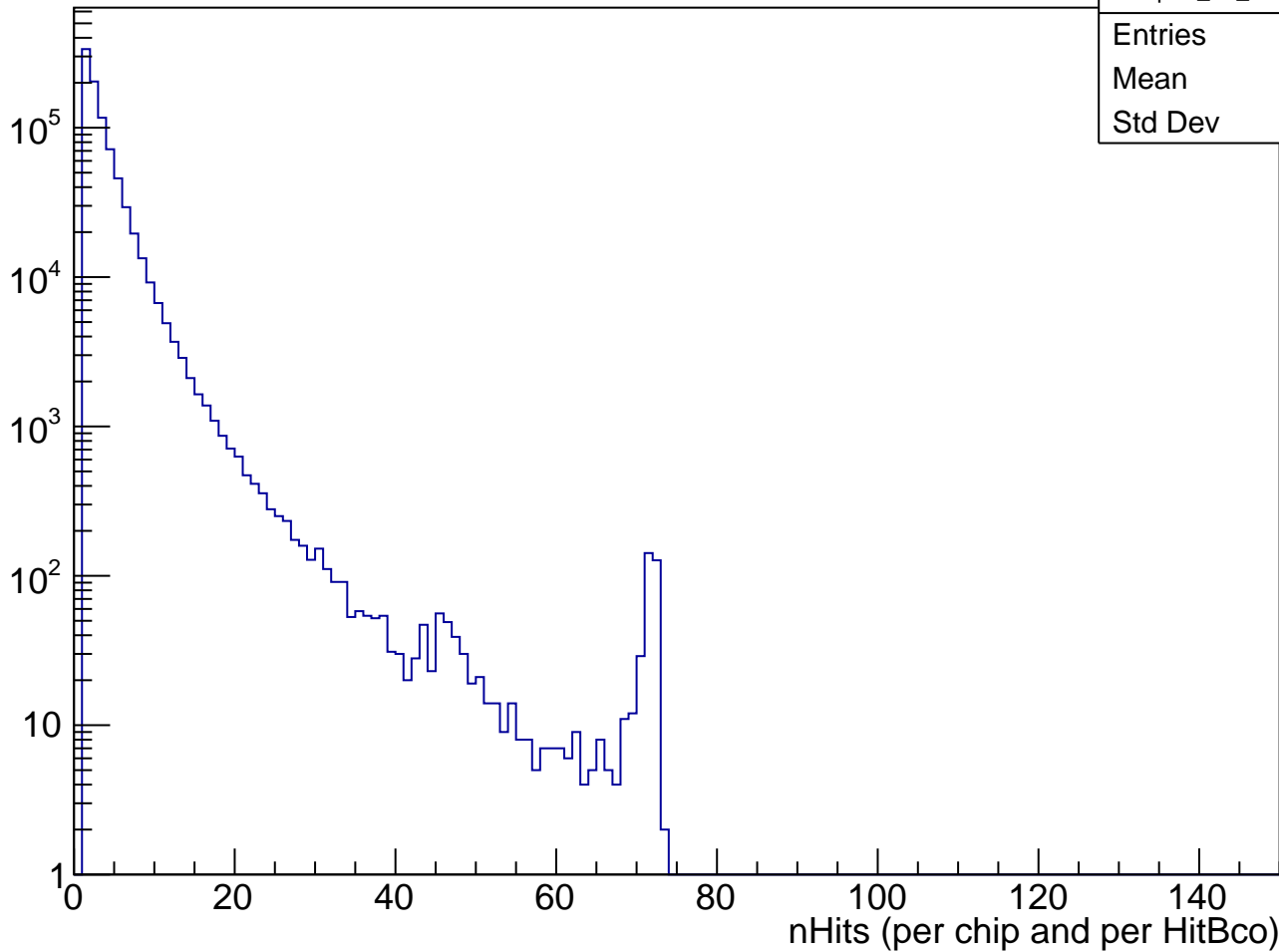
Entries



nChipHit_F2_Fch6_Chip11	
Entries	884166
Mean	2.96
Std Dev	3.413

nChipHit_F2_Fch6_Chip12

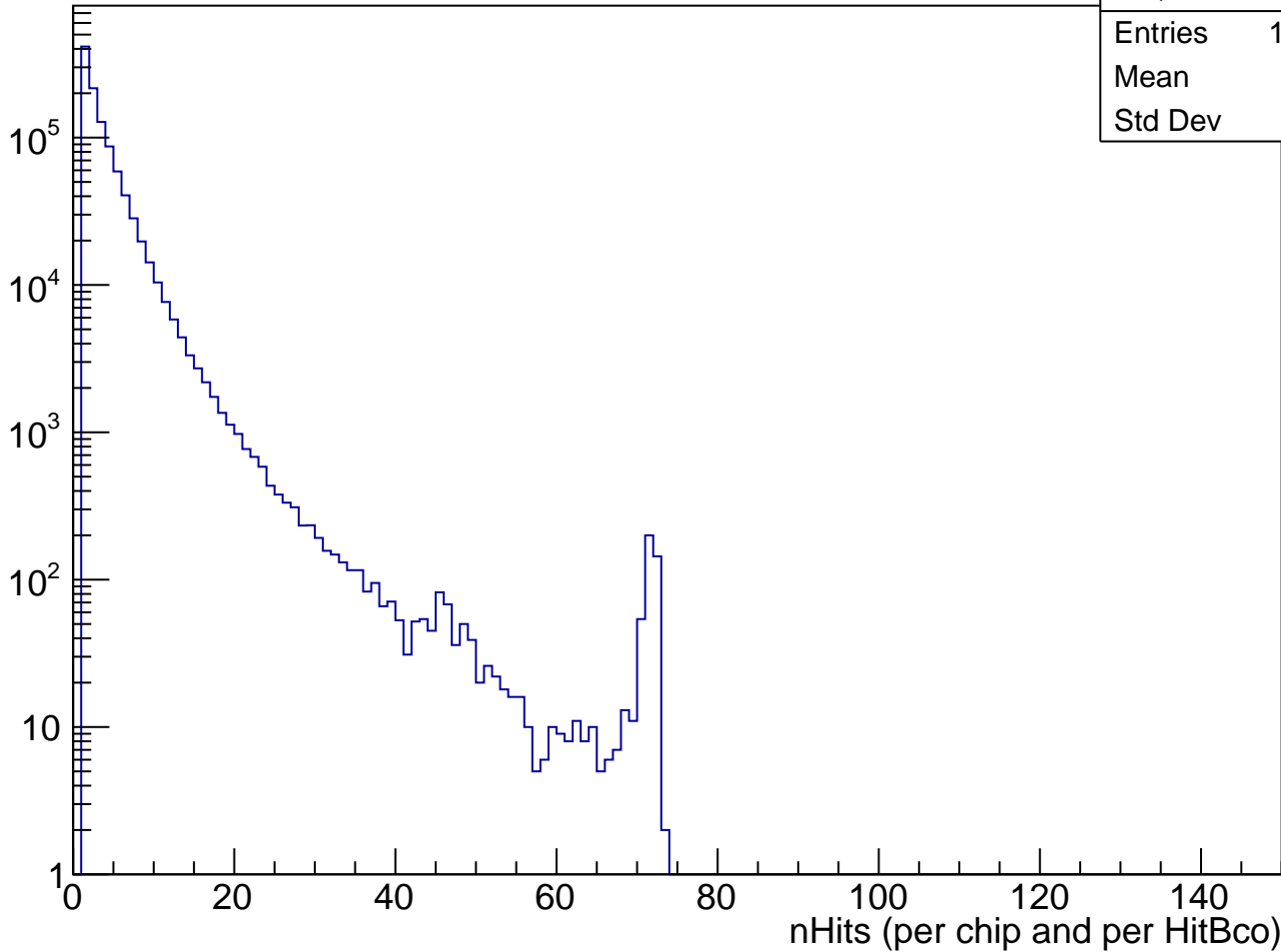
Entries



nChipHit_F2_Fch6_Chip12	
Entries	876404
Mean	2.947
Std Dev	3.395

nChipHit_F2_Fch6_Chip13

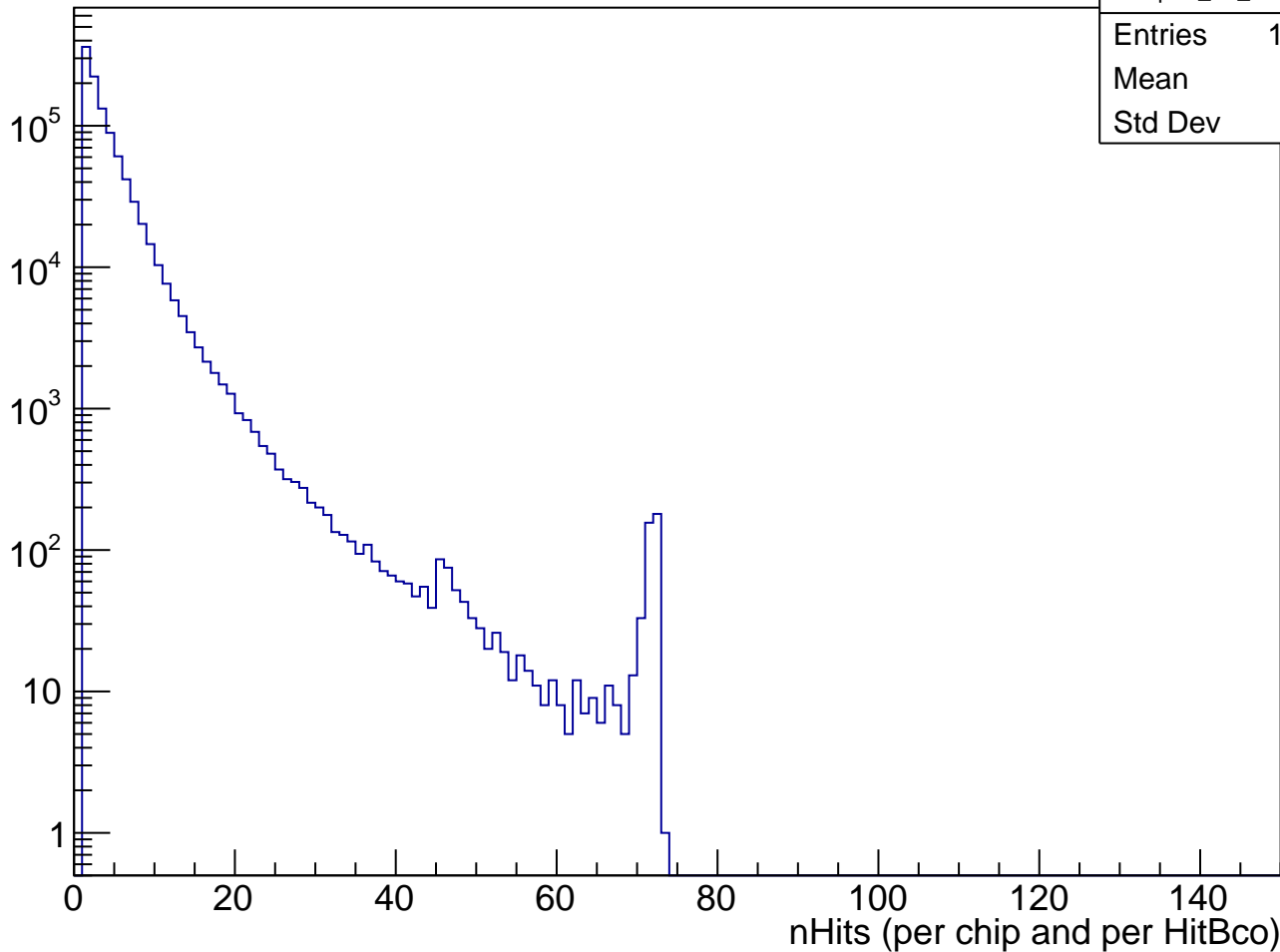
Entries



nChipHit_F2_Fch6_Chip13	
Entries	1055919
Mean	3.157
Std Dev	3.721

nChipHit_F2_Fch6_Chip14

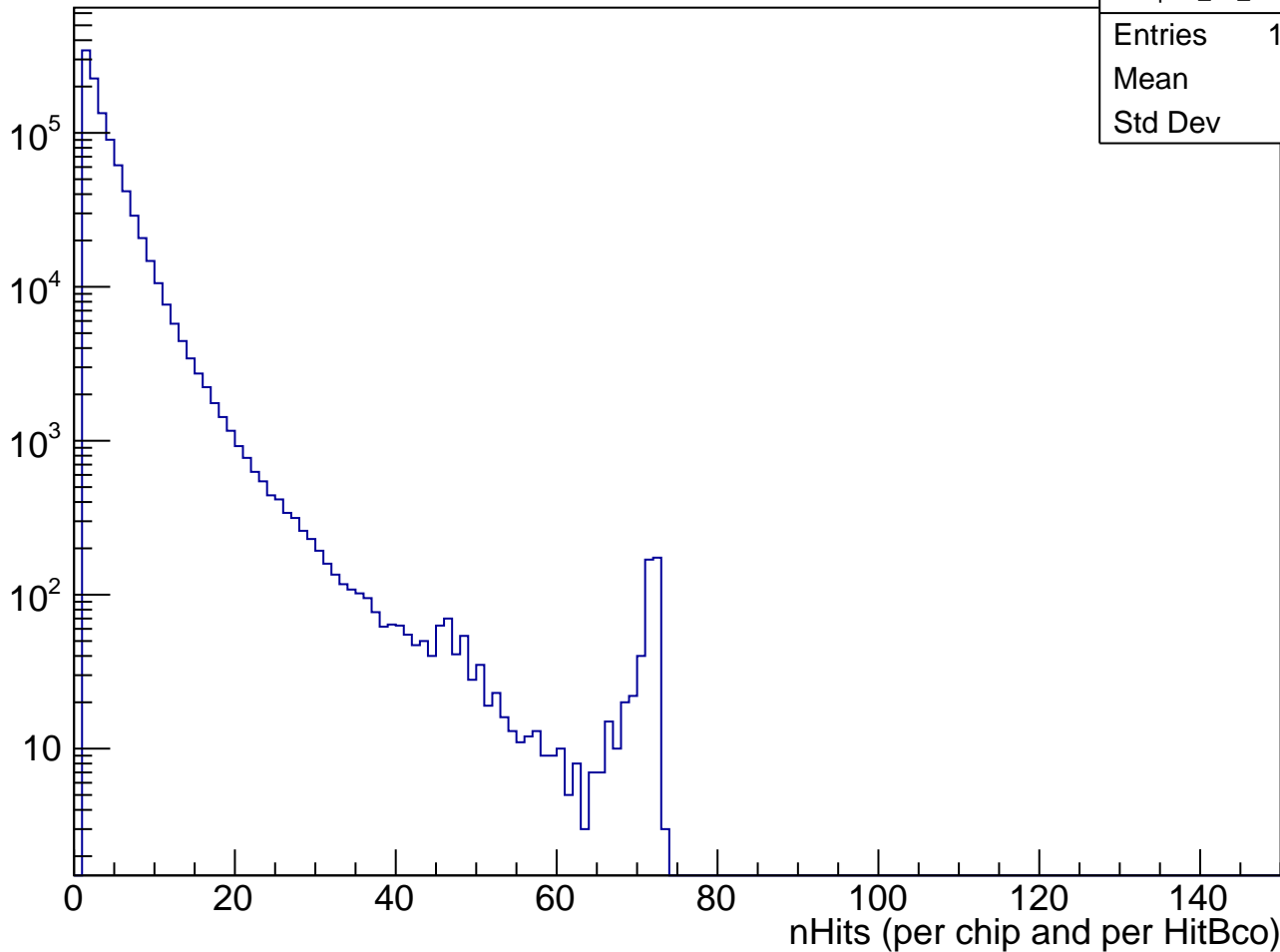
Entries



nChipHit_F2_Fch6_Chip14	
Entries	1020556
Mean	3.284
Std Dev	3.766

nChipHit_F2_Fch6_Chip15

Entries

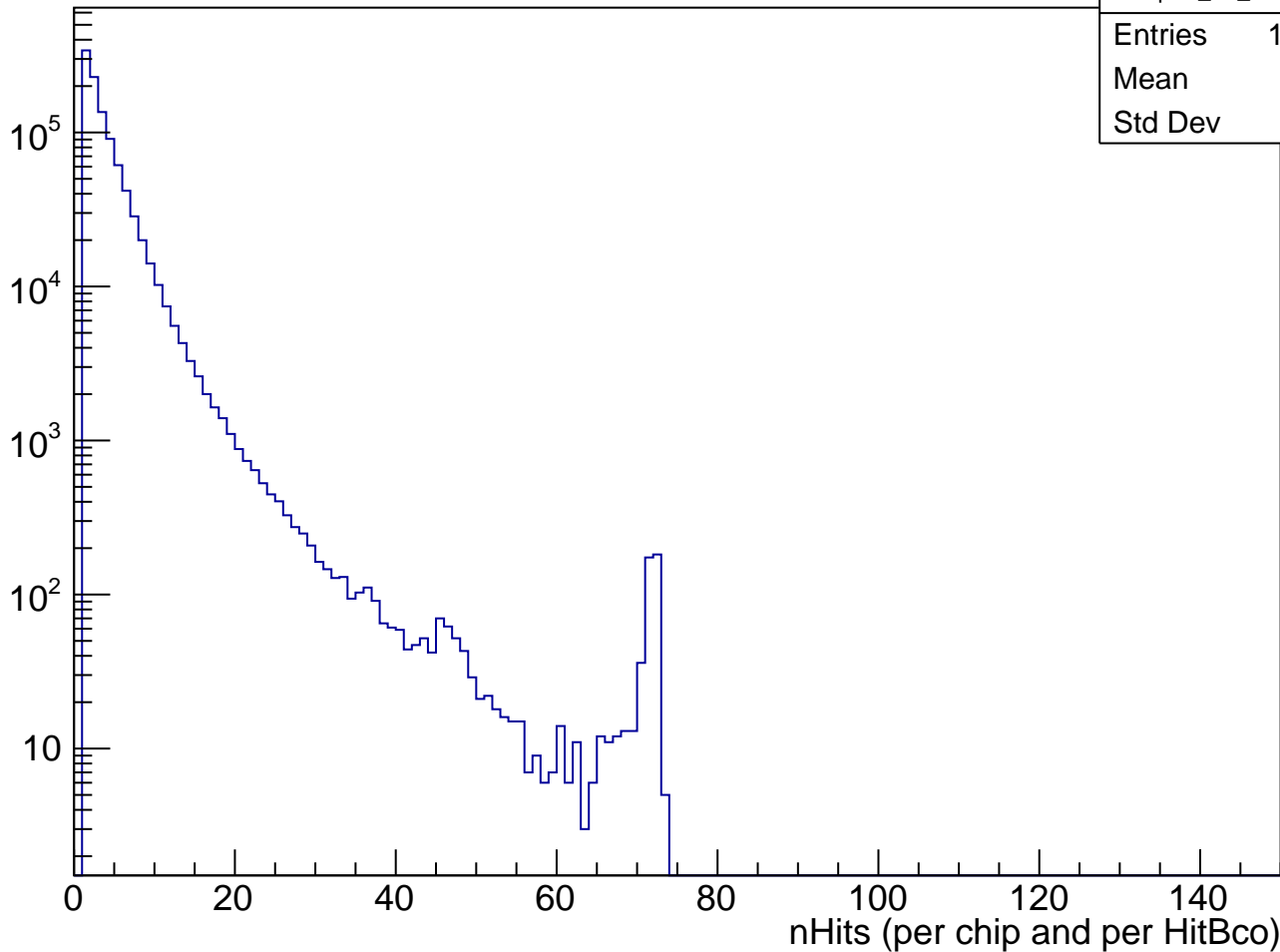


nChipHit_F2_Fch6_Chip15	
Entries	1010098
Mean	3.321
Std Dev	3.772

nHits (per chip and per HitBco)

nChipHit_F2_Fch6_Chip16

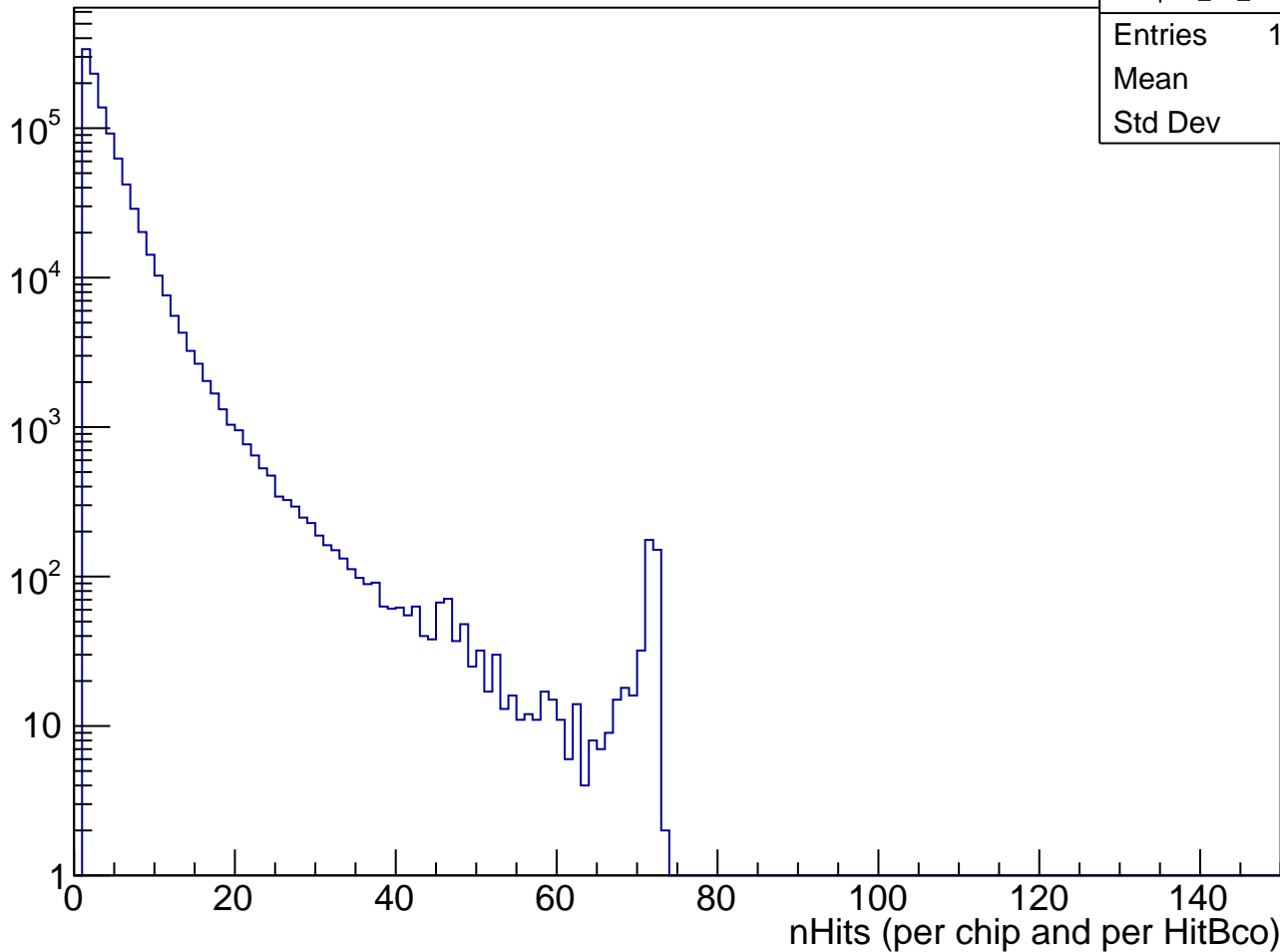
Entries



nChipHit_F2_Fch6_Chip16	
Entries	1008867
Mean	3.292
Std Dev	3.726

nChipHit_F2_Fch6_Chip17

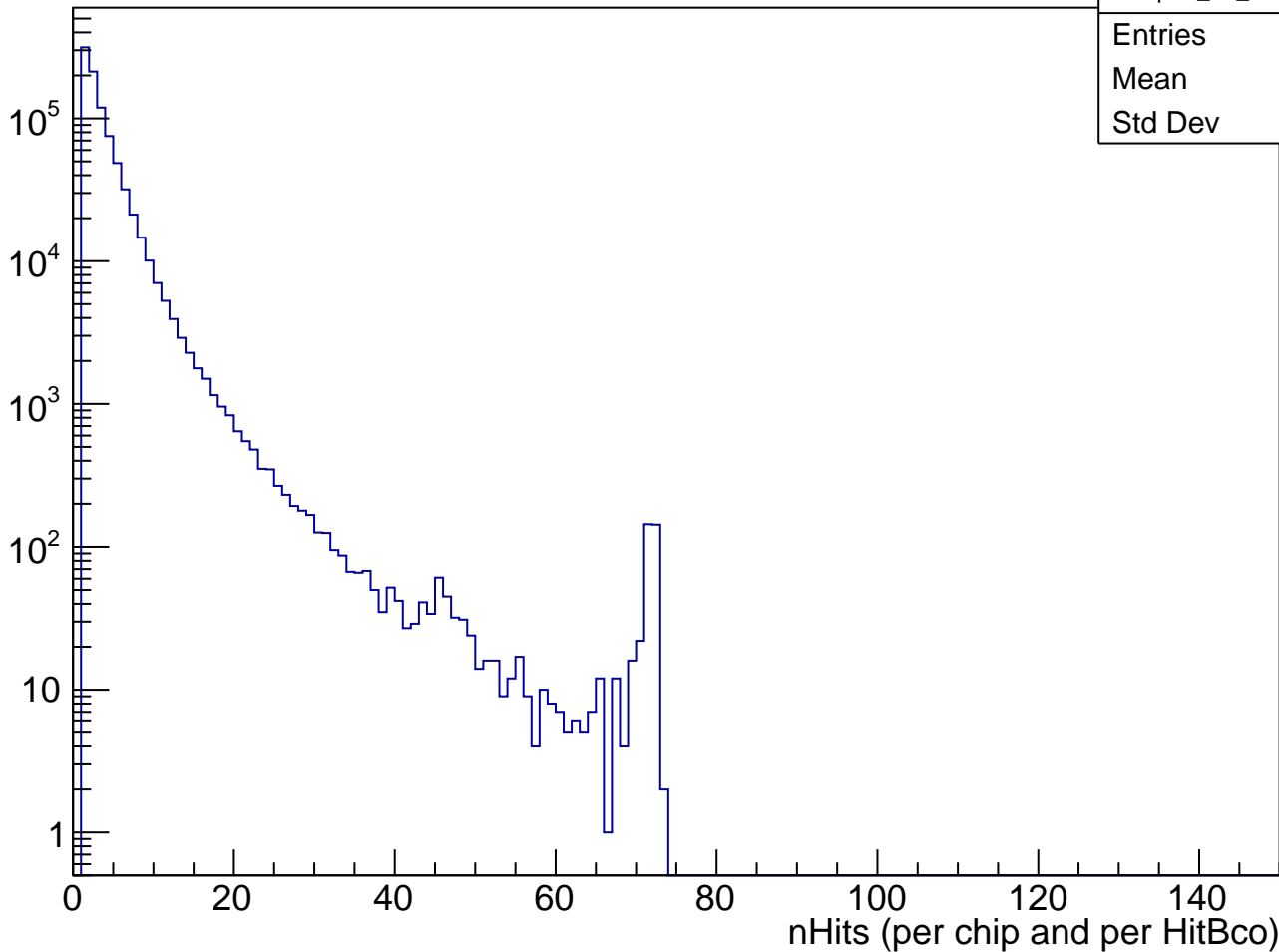
Entries



nChipHit_F2_Fch6_Chip17	
Entries	1014199
Mean	3.306
Std Dev	3.722

nChipHit_F2_Fch6_Chip18

Entries

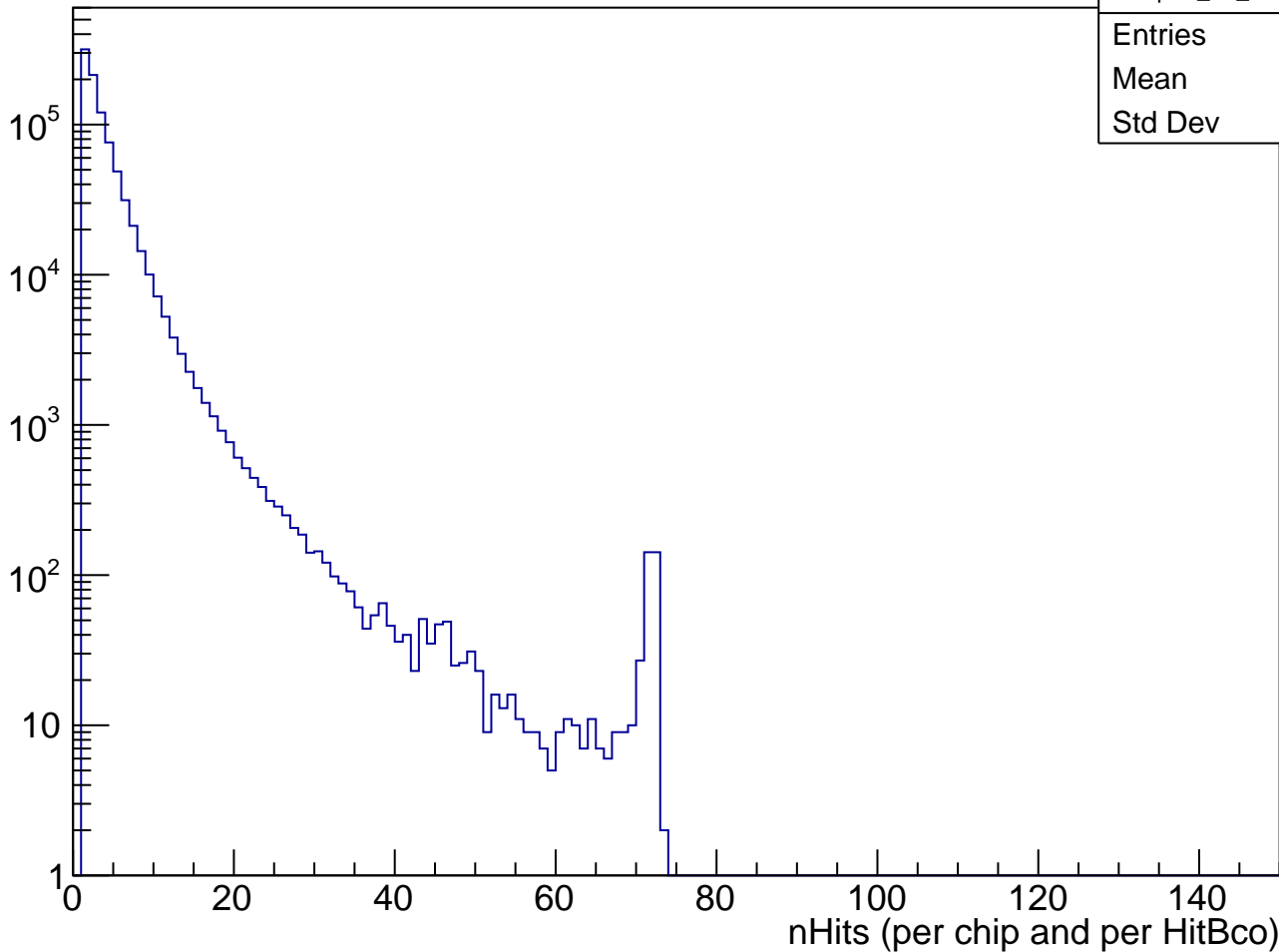


nChipHit_F2_Fch6_Chip18	
Entries	880528
Mean	3.056
Std Dev	3.475

nHits (per chip and per HitBco)

nChipHit_F2_Fch6_Chip19

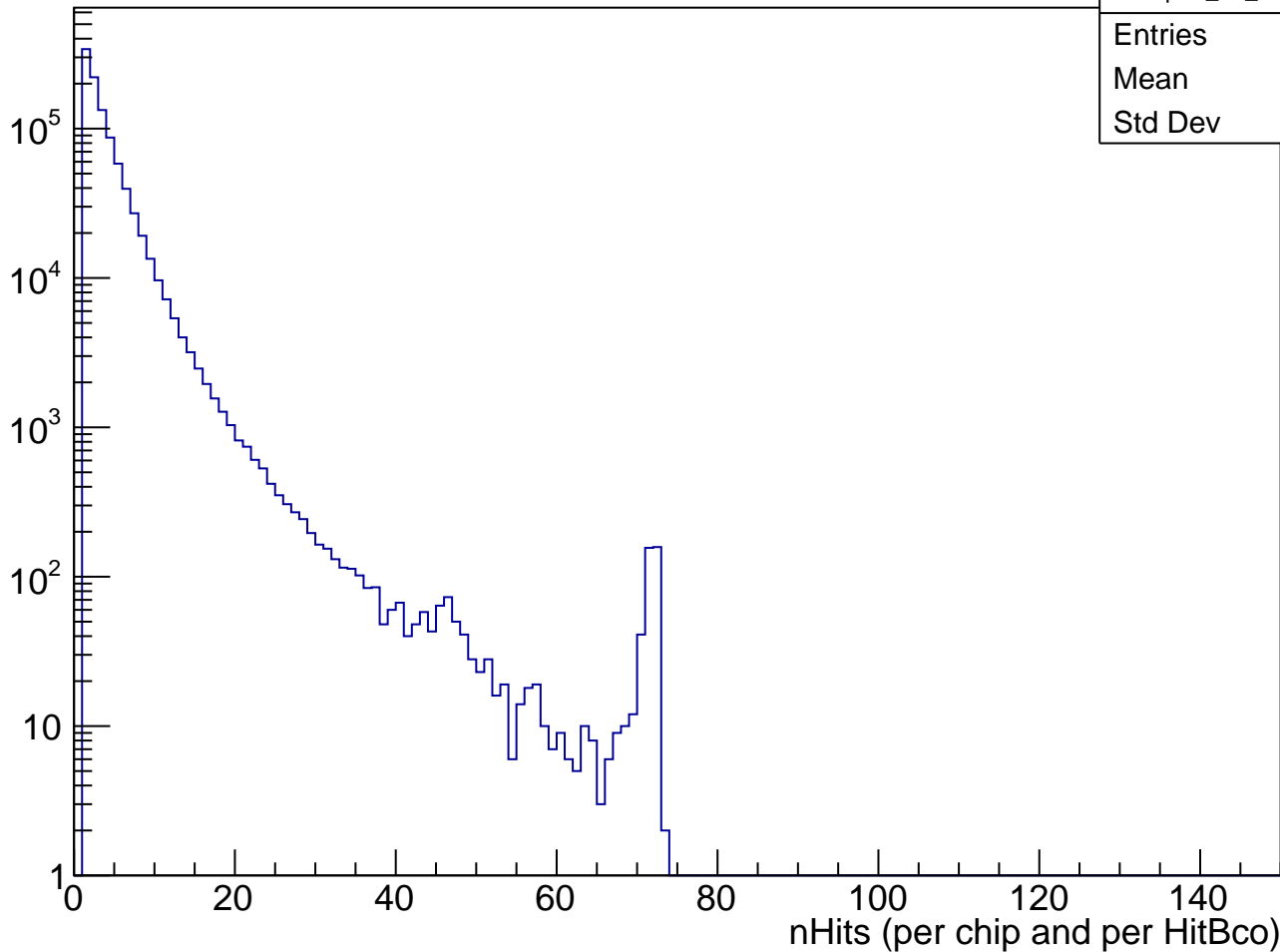
Entries



nChipHit_F2_Fch6_Chip19	
Entries	885455
Mean	3.044
Std Dev	3.467

nChipHit_F2_Fch6_Chip2

Entries



nChipHit_F2_Fch6_Chip2

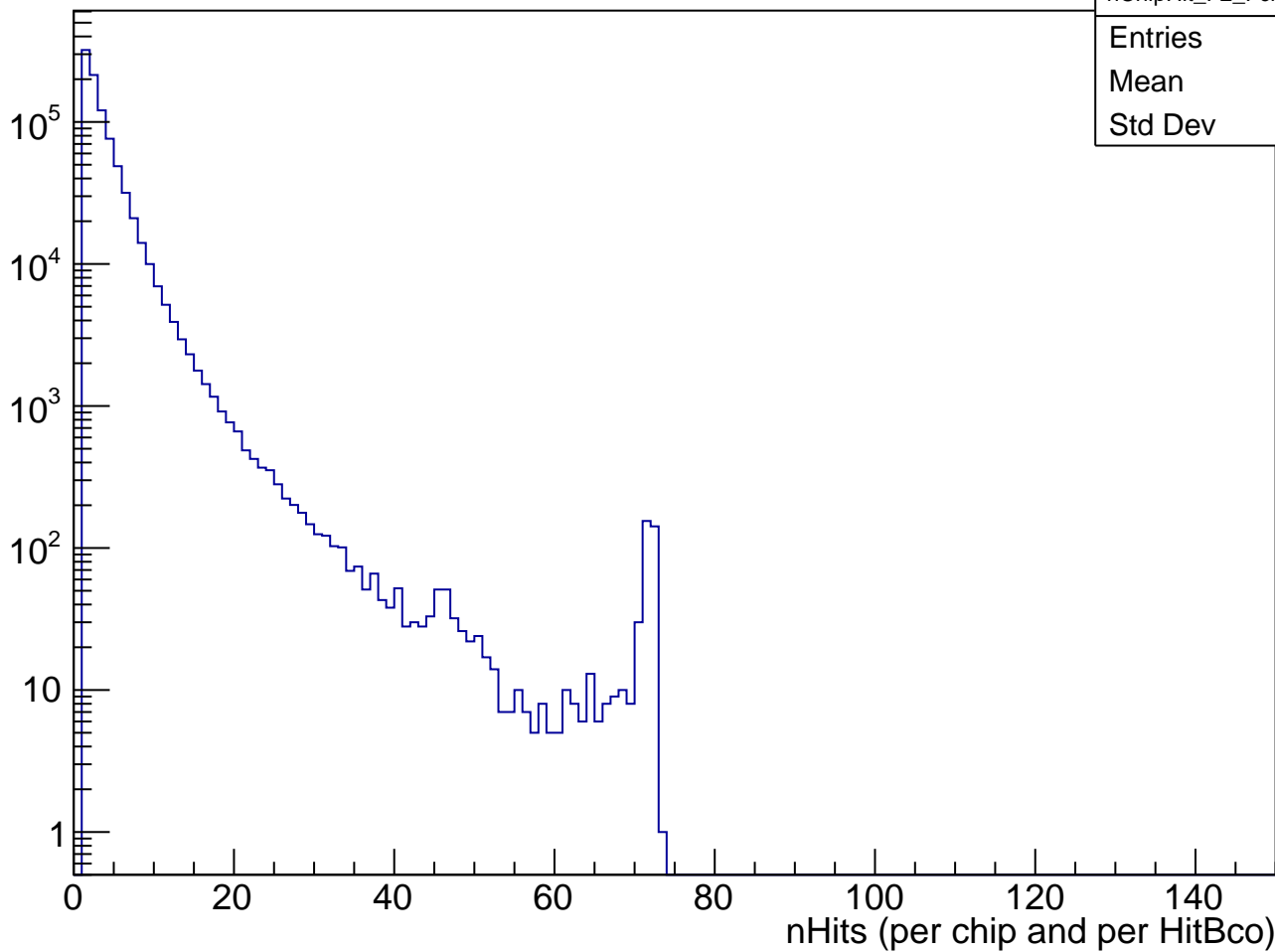
Entries 983413

Mean 3.252

Std Dev 3.692

nChipHit_F2_Fch6_Chip20

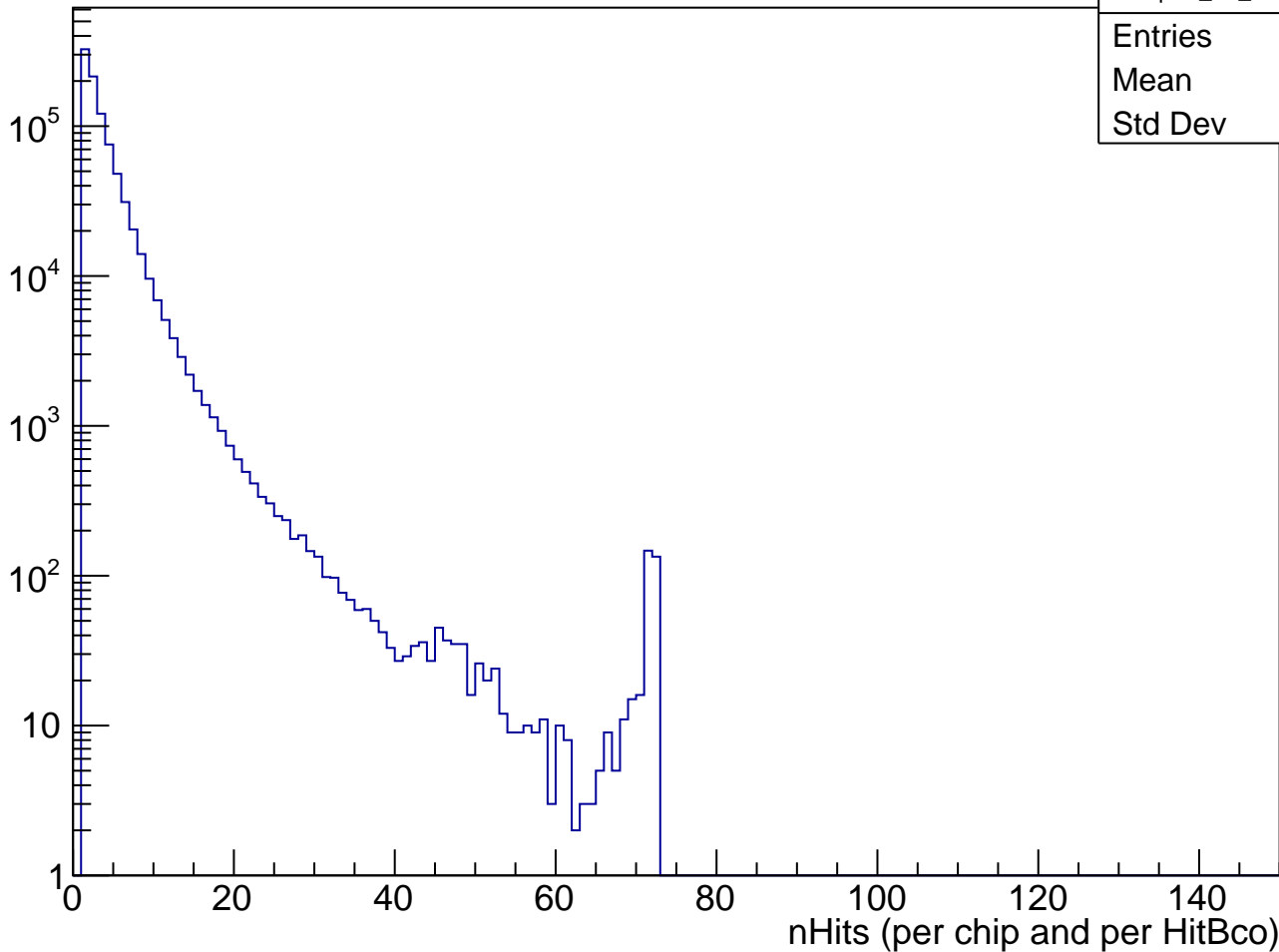
Entries



nChipHit_F2_Fch6_Chip20	
Entries	890050
Mean	3.031
Std Dev	3.456

nChipHit_F2_Fch6_Chip21

Entries

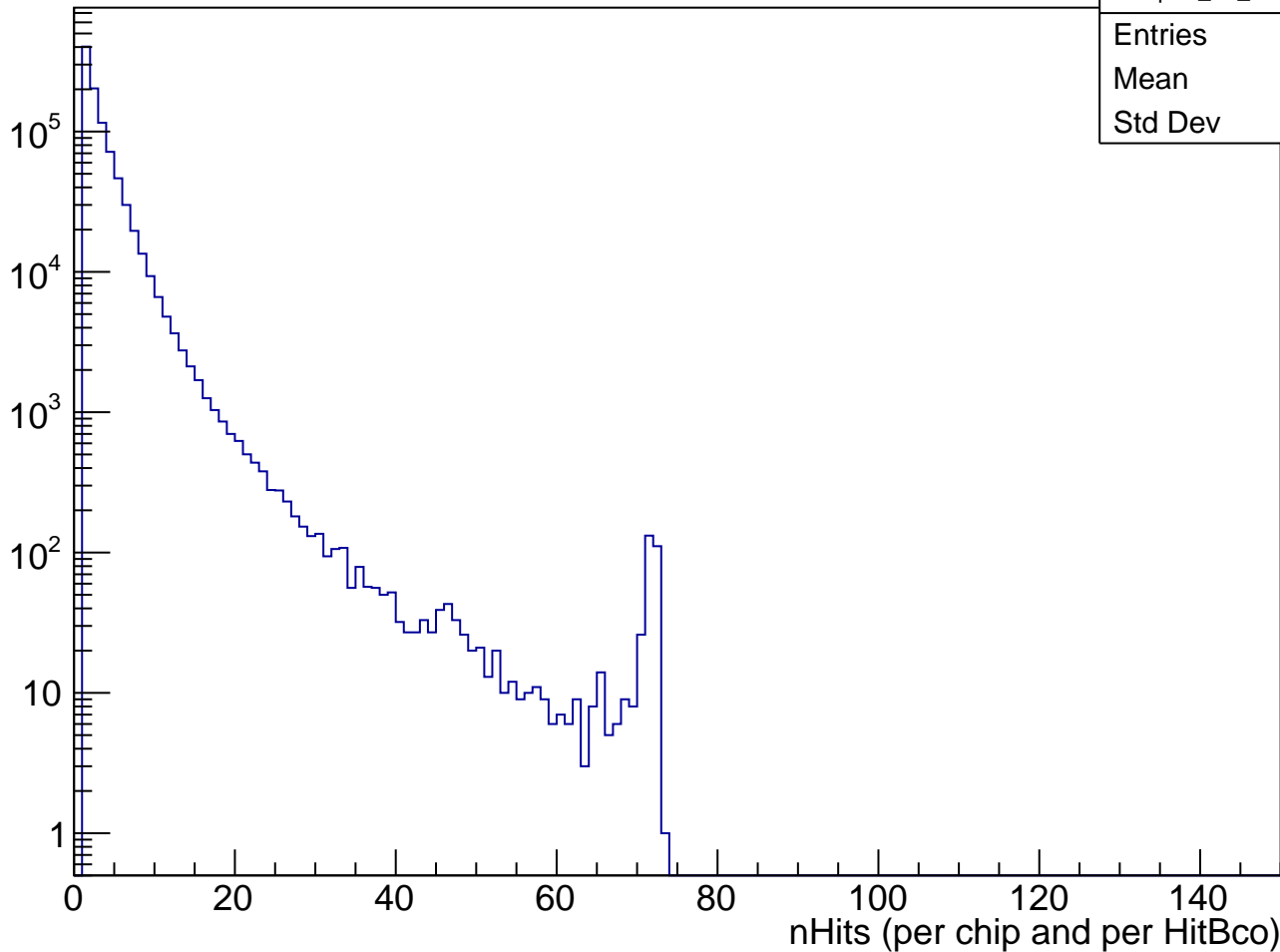


nChipHit_F2_Fch6_Chip21	
Entries	891661
Mean	2.994
Std Dev	3.391

nHits (per chip and per HitBco)

nChipHit_F2_Fch6_Chip22

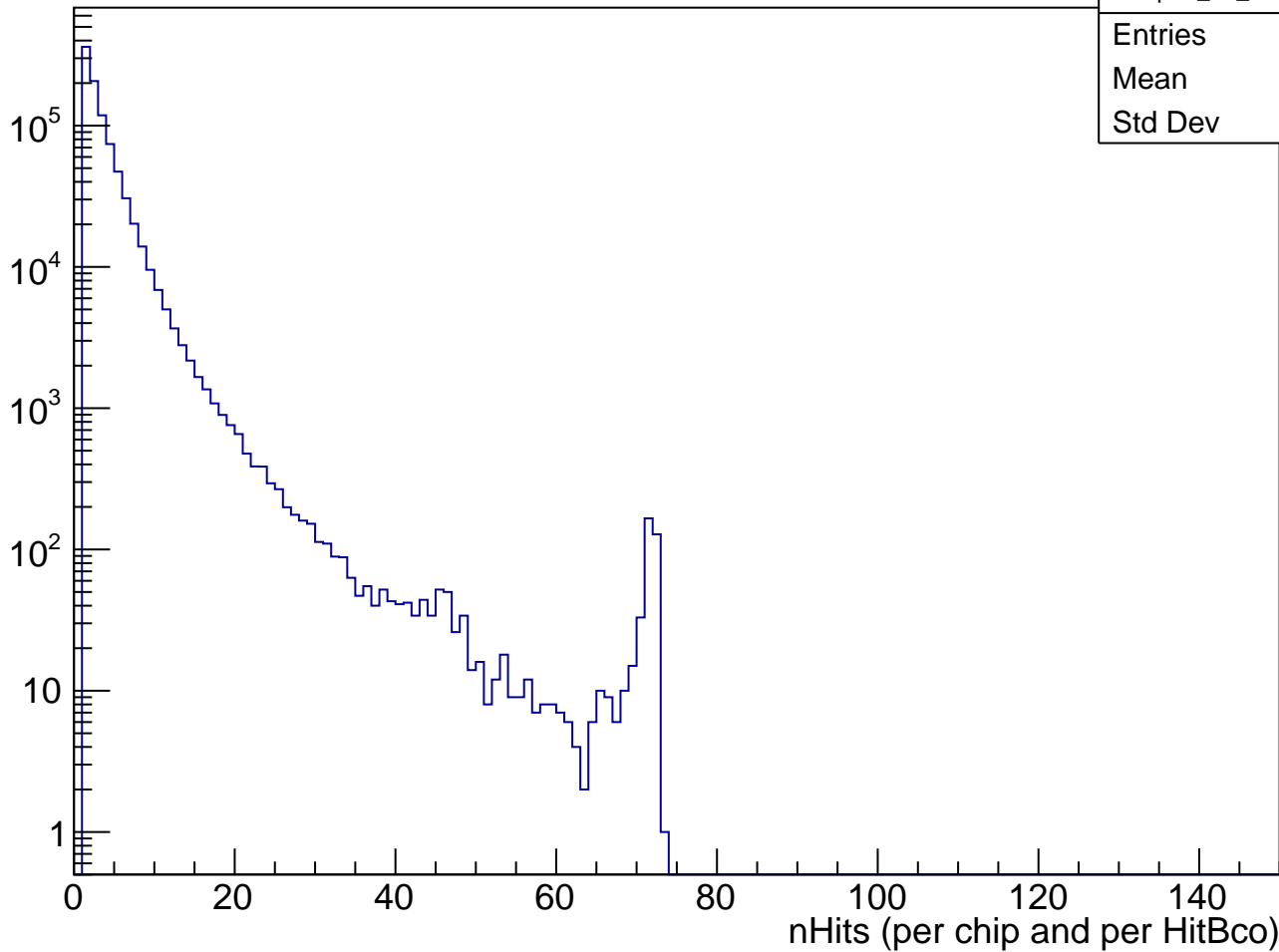
Entries



nChipHit_F2_Fch6_Chip22	
Entries	942320
Mean	2.811
Std Dev	3.297

nChipHit_F2_Fch6_Chip23

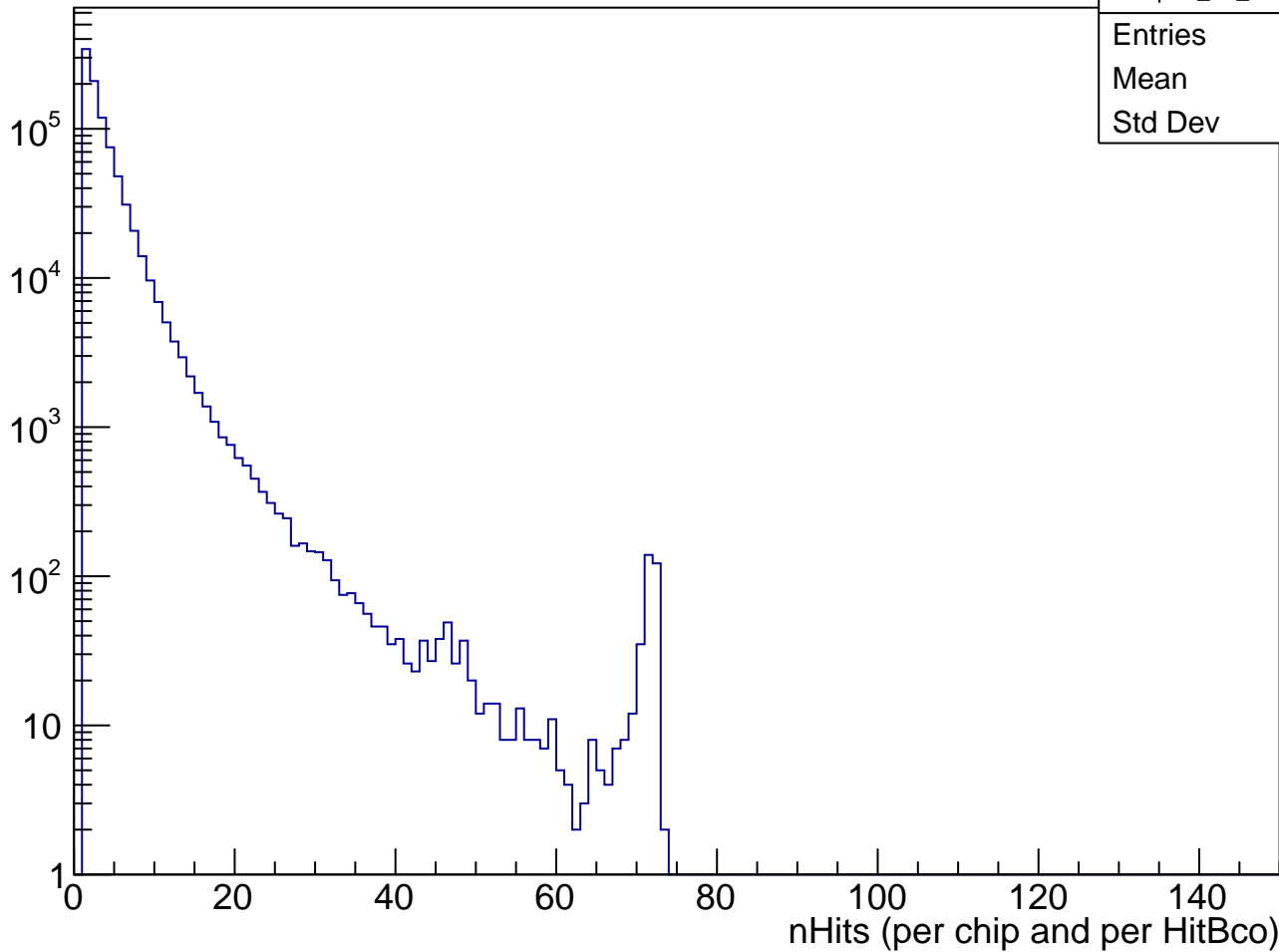
Entries



nChipHit_F2_Fch6_Chip23	
Entries	912829
Mean	2.916
Std Dev	3.386

nChipHit_F2_Fch6_Chip24

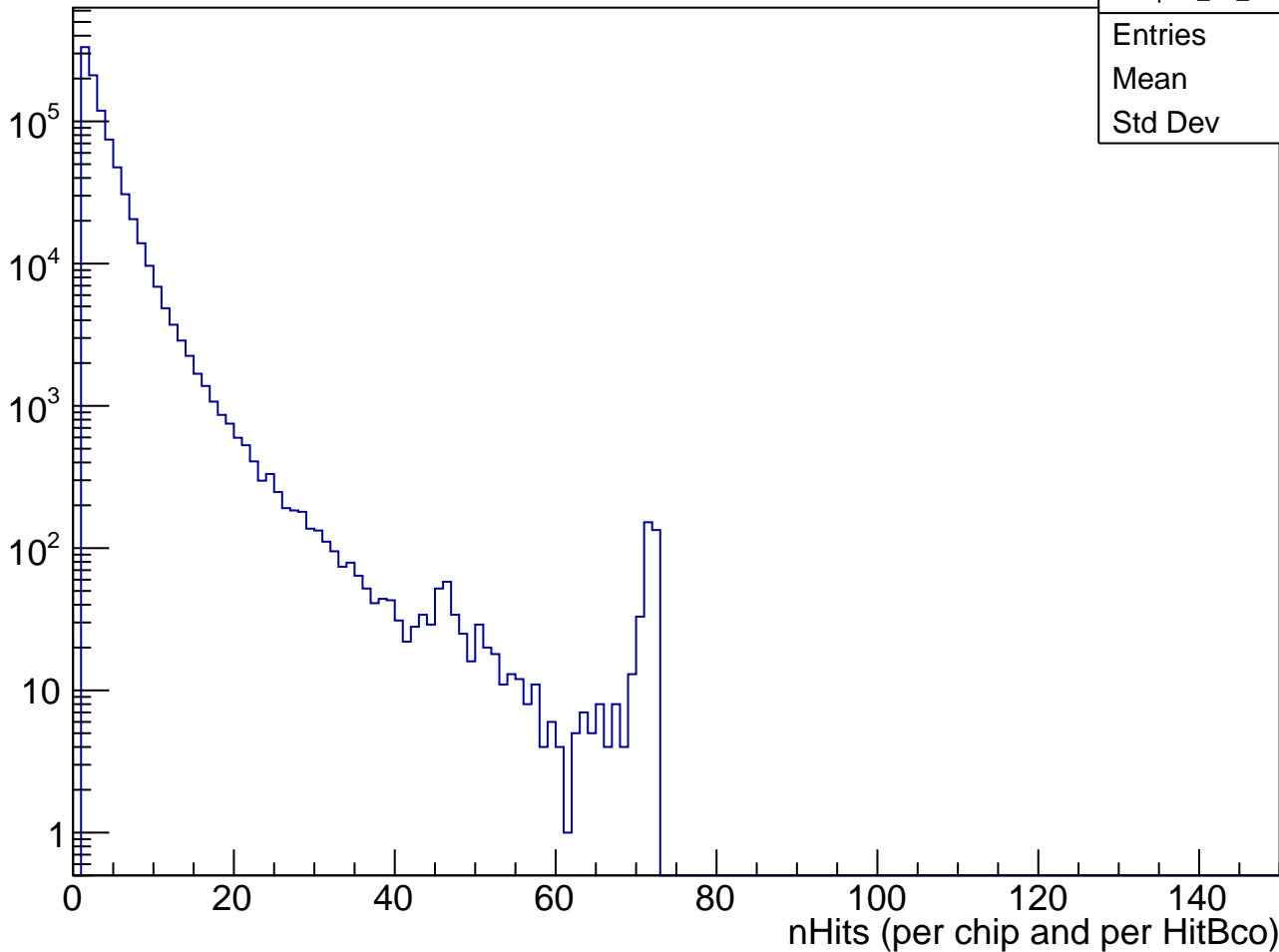
Entries



nChipHit_F2_Fch6_Chip24	
Entries	900796
Mean	2.963
Std Dev	3.375

nChipHit_F2_Fch6_Chip25

Entries

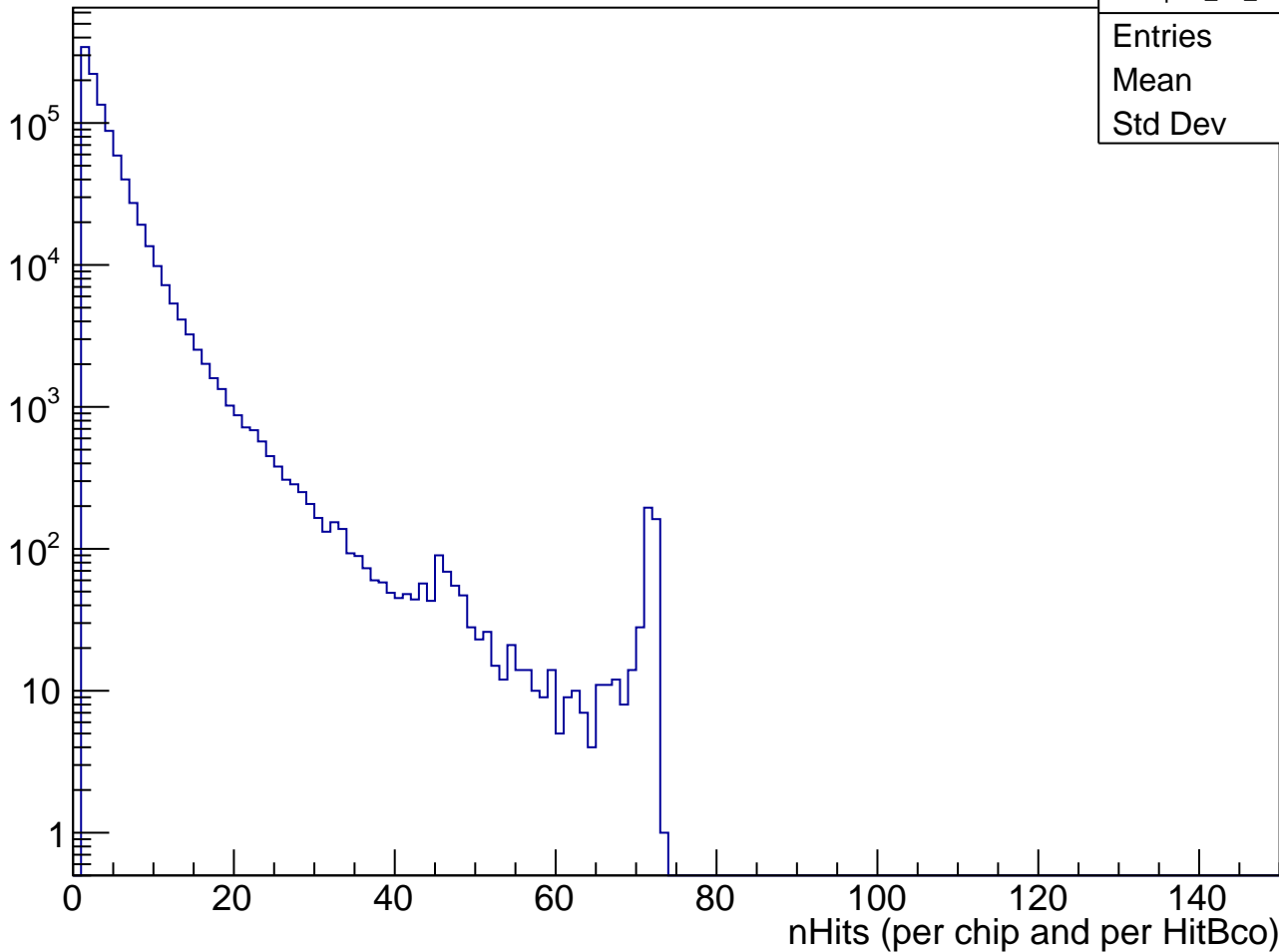


nChipHit_F2_Fch6_Chip25	
Entries	890064
Mean	2.976
Std Dev	3.399

nHits (per chip and per HitBco)

nChipHit_F2_Fch6_Chip3

Entries

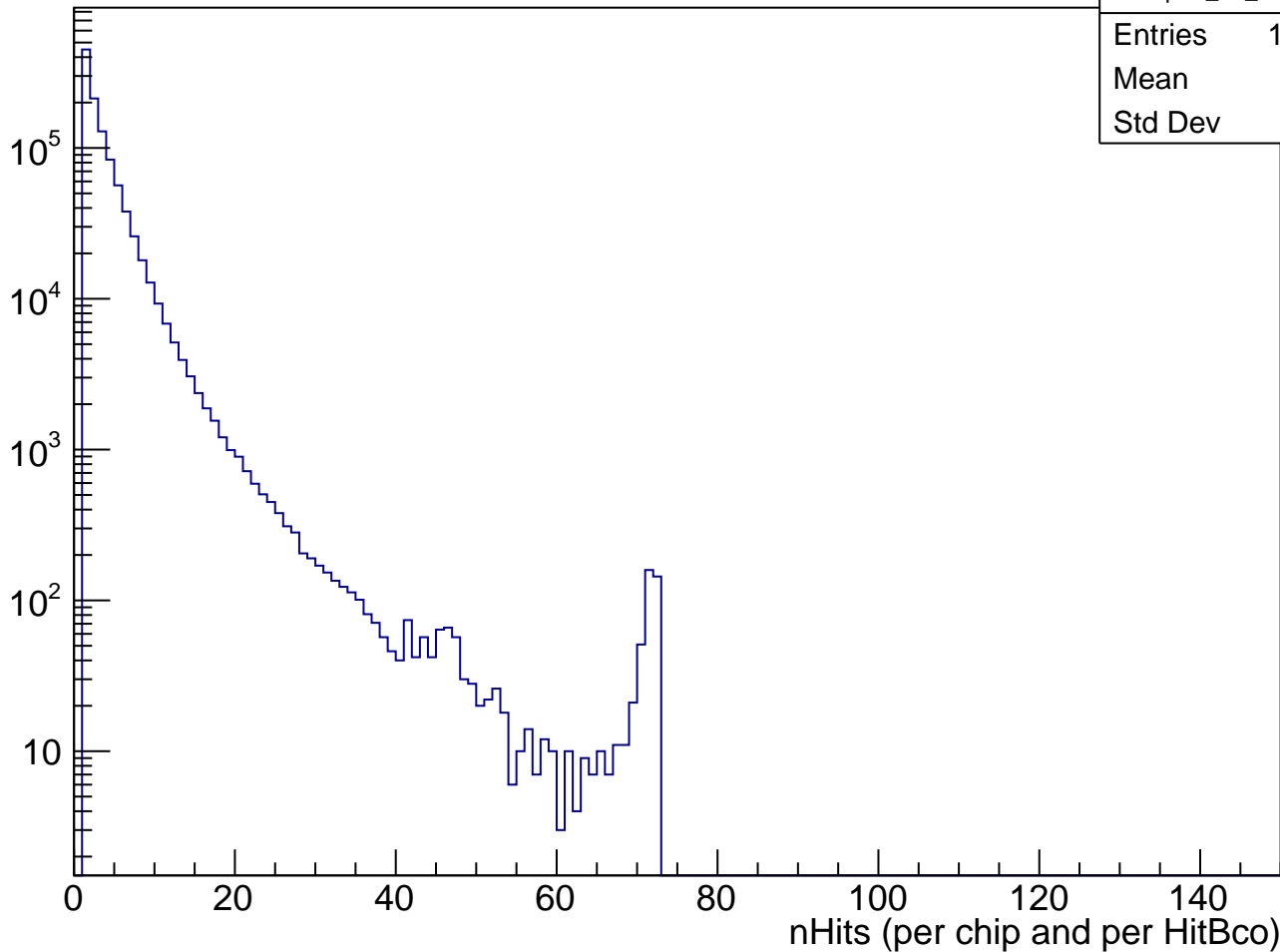


nChipHit_F2_Fch6_Chip3	
Entries	991581
Mean	3.262
Std Dev	3.724

nHits (per chip and per HitBco)

nChipHit_F2_Fch6_Chip4

Entries



nChipHit_F2_Fch6_Chip4

Entries 1069177

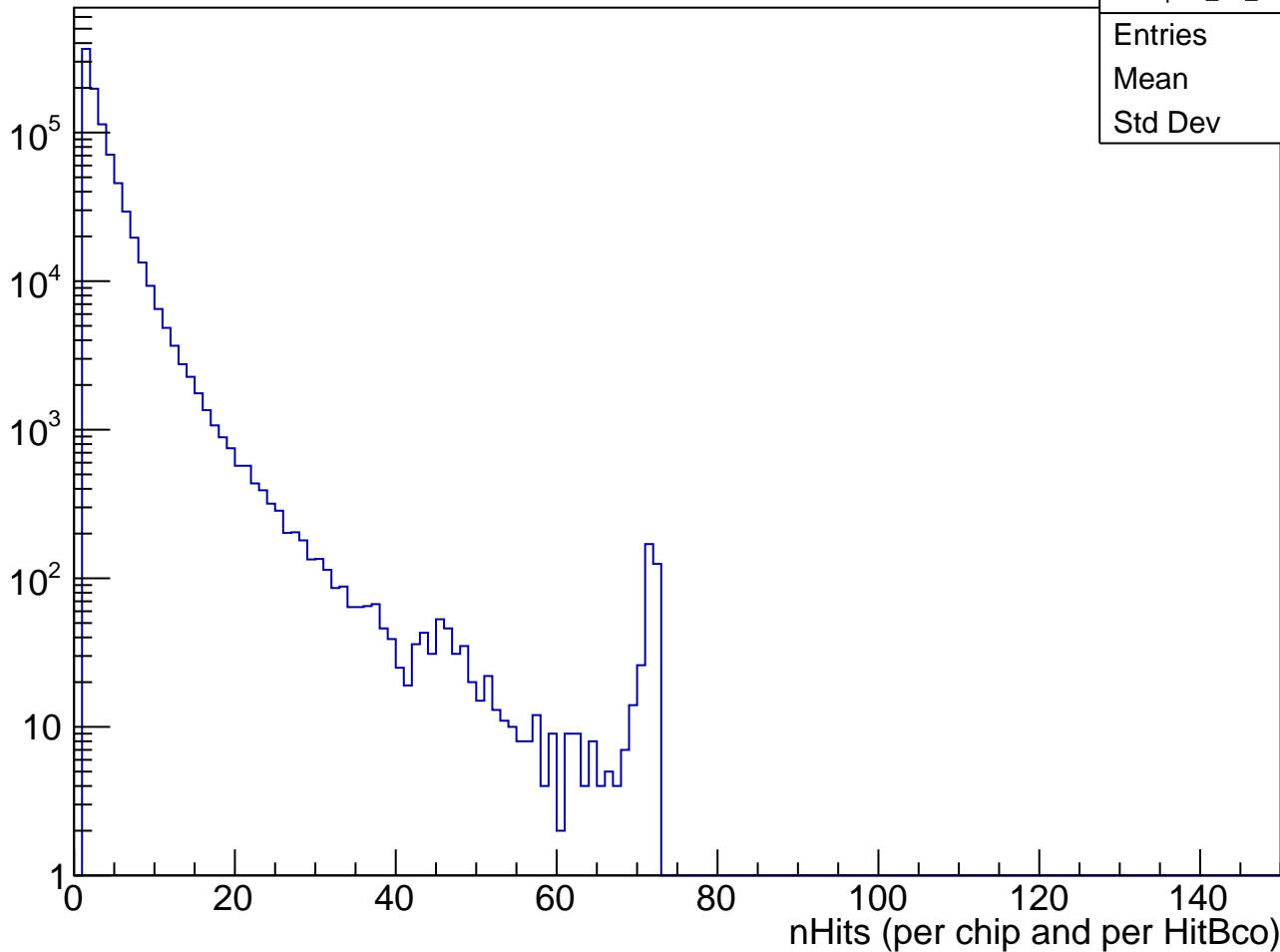
Mean 2.997

Std Dev 3.572

nHits (per chip and per HitBco)

nChipHit_F2_Fch6_Chip5

Entries



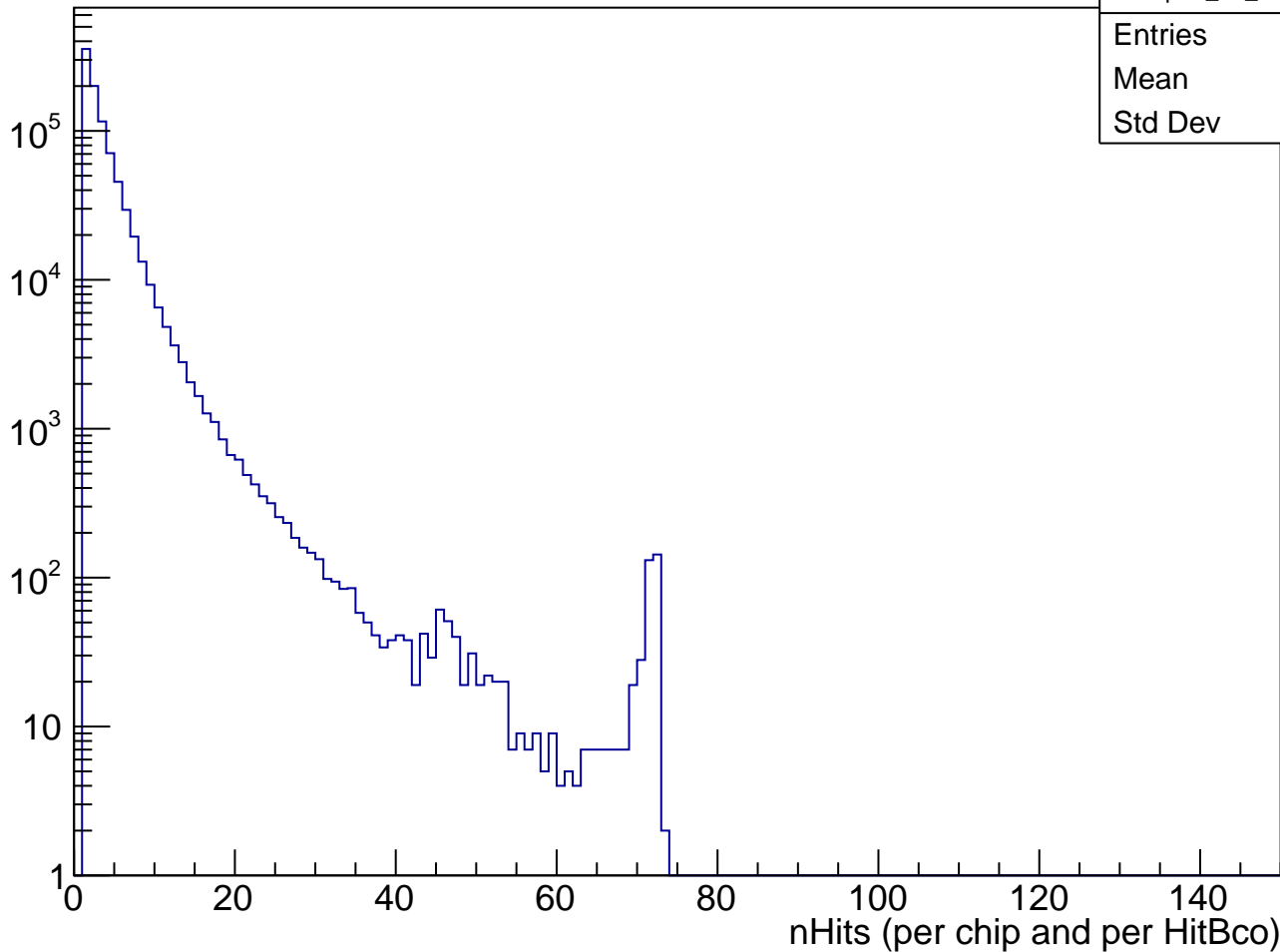
nChipHit_F2_Fch6_Chip5

Entries	895703
Mean	2.896
Std Dev	3.413

nHits (per chip and per HitBco)

nChipHit_F2_Fch6_Chip6

Entries



nChipHit_F2_Fch6_Chip6

Entries 889118

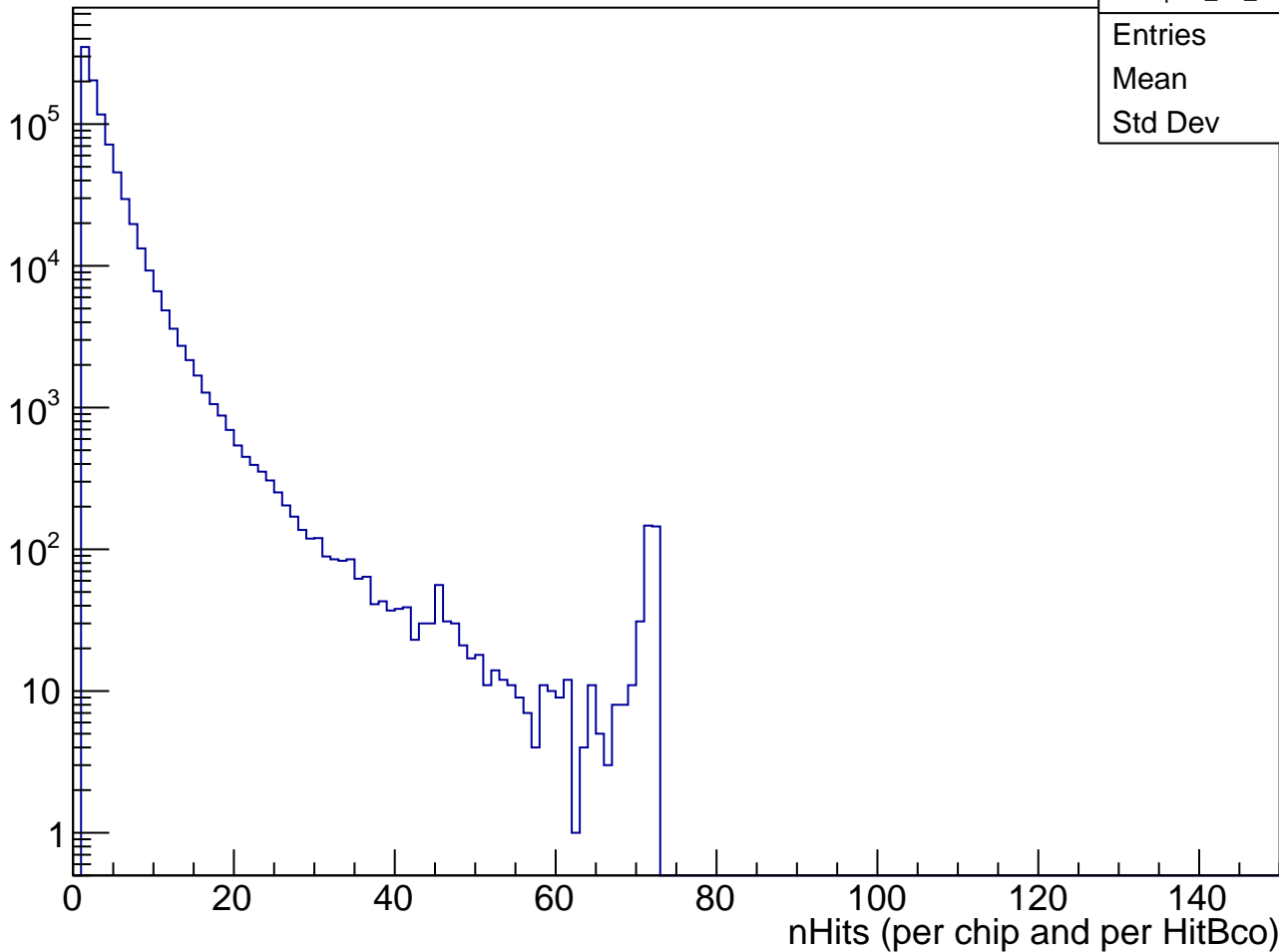
Mean 2.905

Std Dev 3.395

nHits (per chip and per HitBco)

nChipHit_F2_Fch6_Chip7

Entries



nChipHit_F2_Fch6_Chip7

Entries 889564

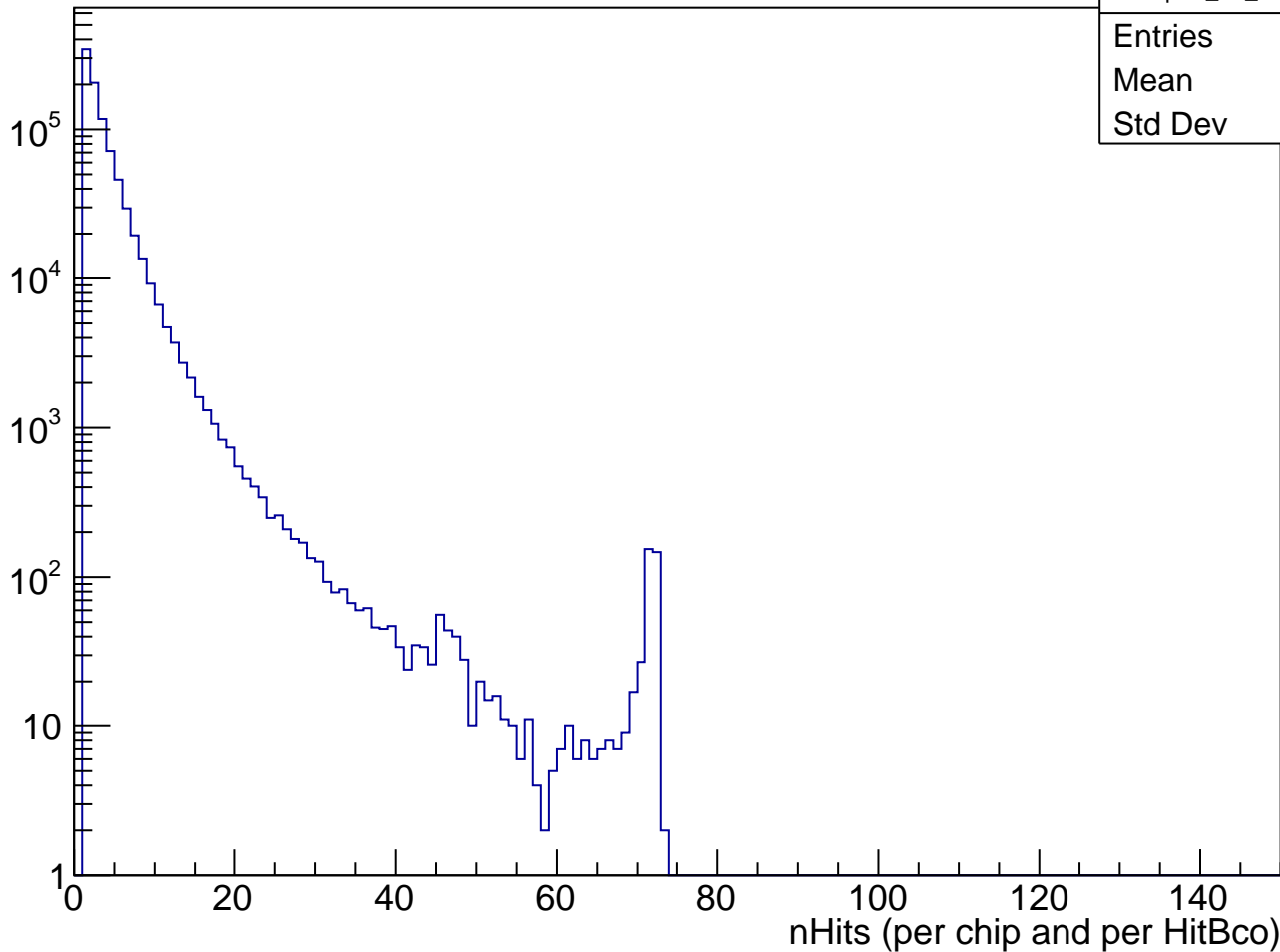
Mean 2.909

Std Dev 3.365

nHits (per chip and per HitBco)

nChipHit_F2_Fch6_Chip8

Entries



nChipHit_F2_Fch6_Chip8

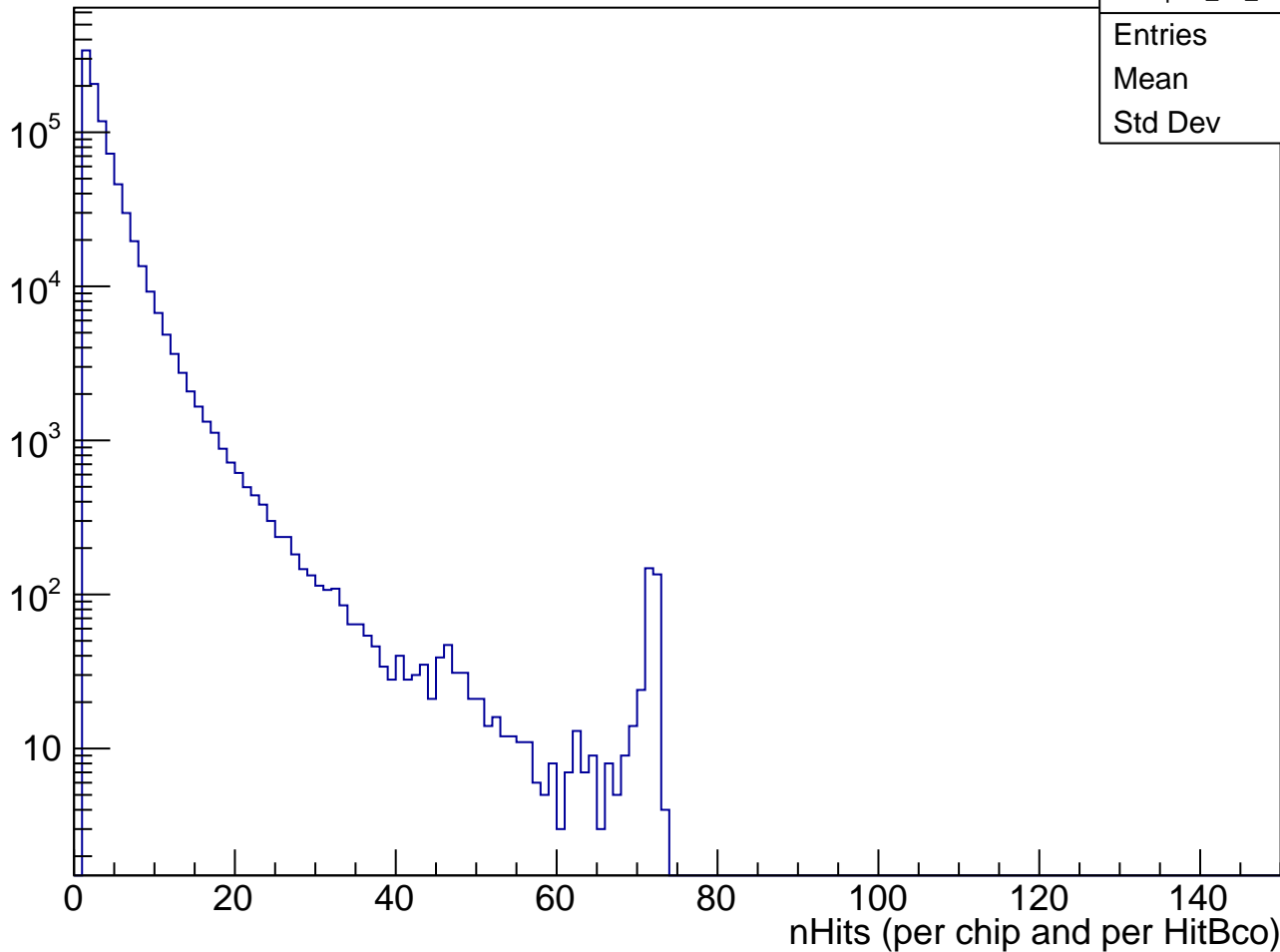
Entries 886521

Mean 2.922

Std Dev 3.387

nChipHit_F2_Fch6_Chip9

Entries



nChipHit_F2_Fch6_Chip9

Entries 885252

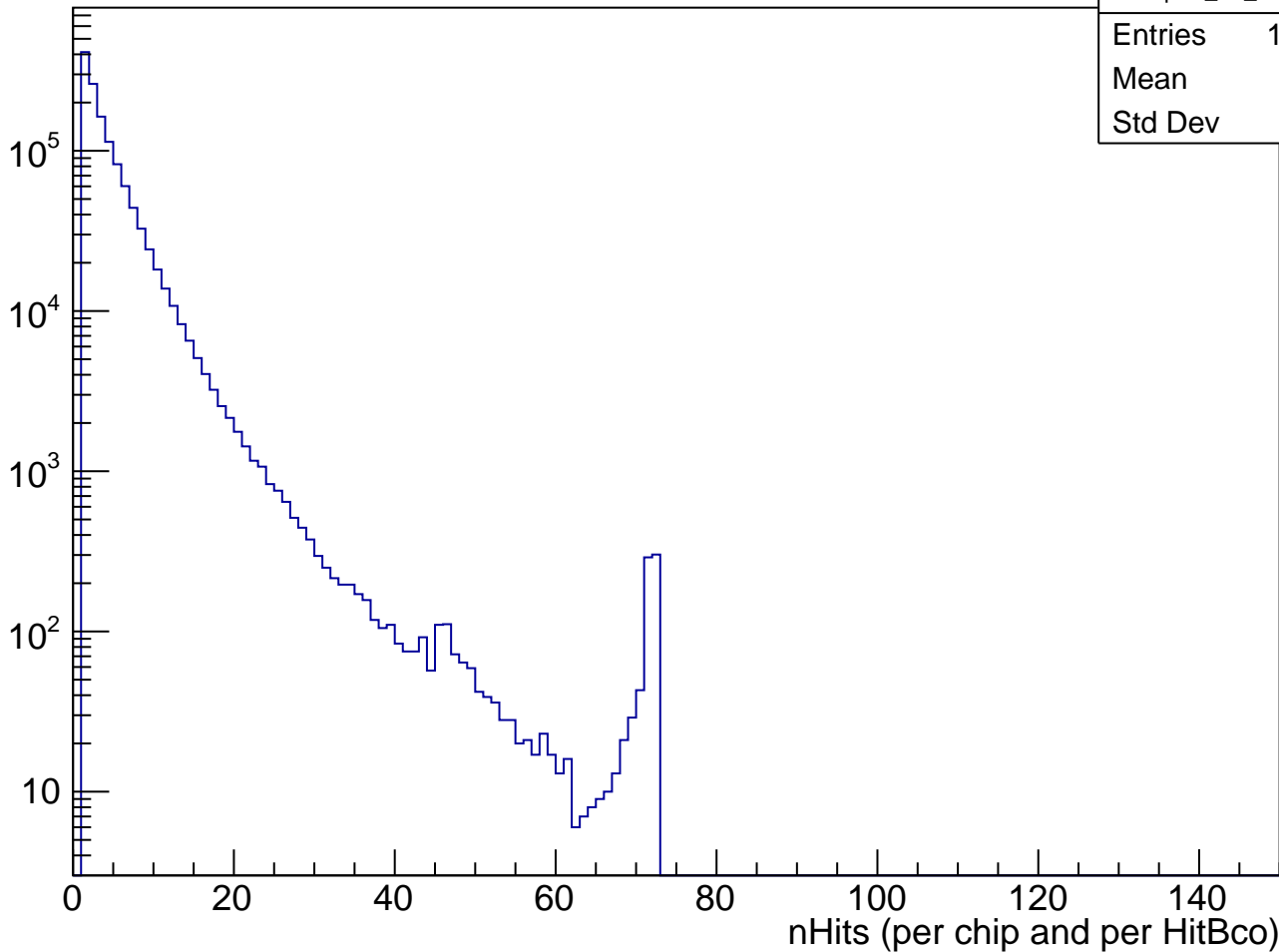
Mean 2.94

Std Dev 3.389

nHits (per chip and per HitBco)

nChipHit_F2_Fch7_Chip0

Entries



nChipHit_F2_Fch7_Chip0

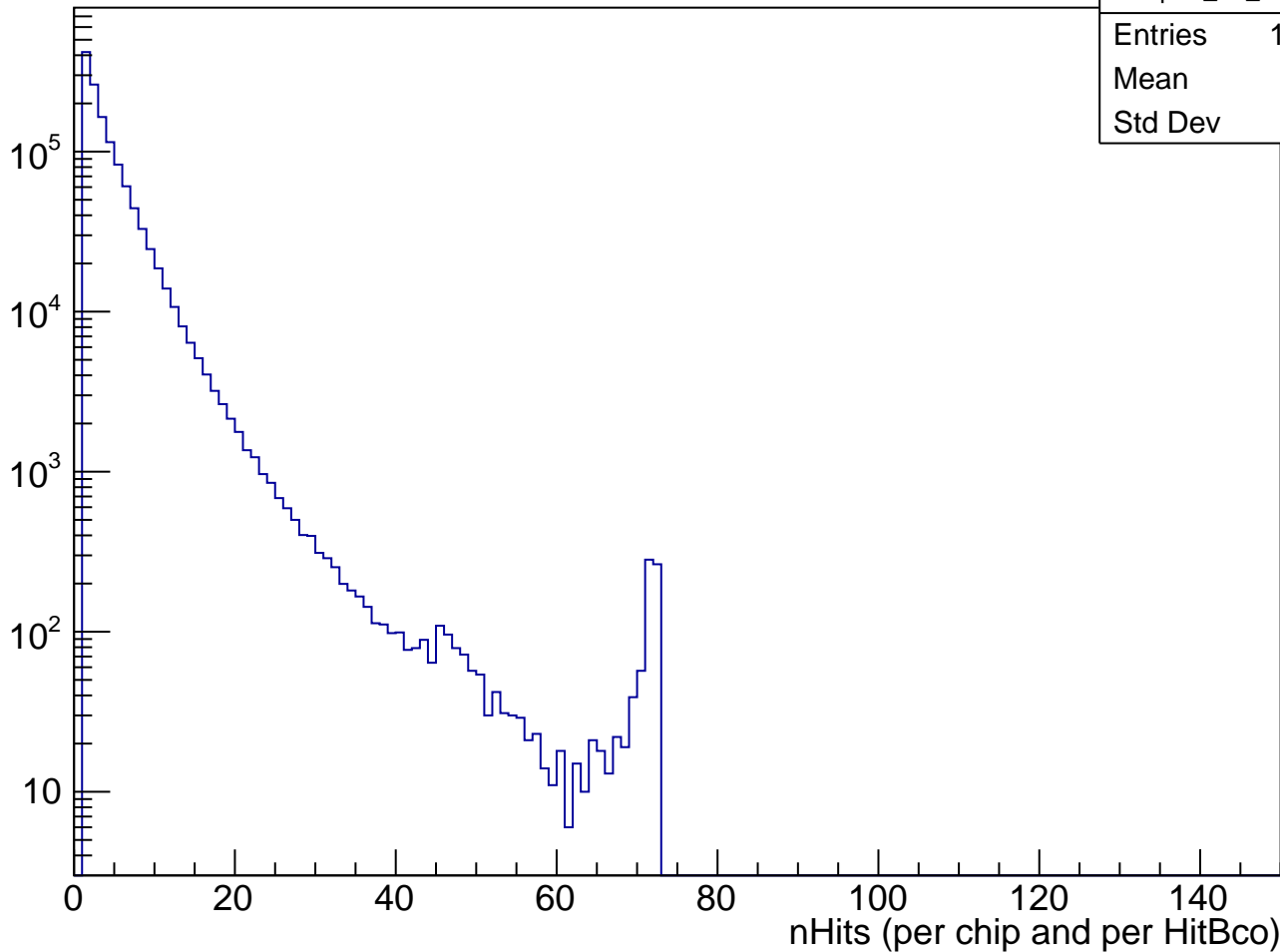
Entries 1282372

Mean 3.712

Std Dev 4.241

nChipHit_F2_Fch7_Chip1

Entries

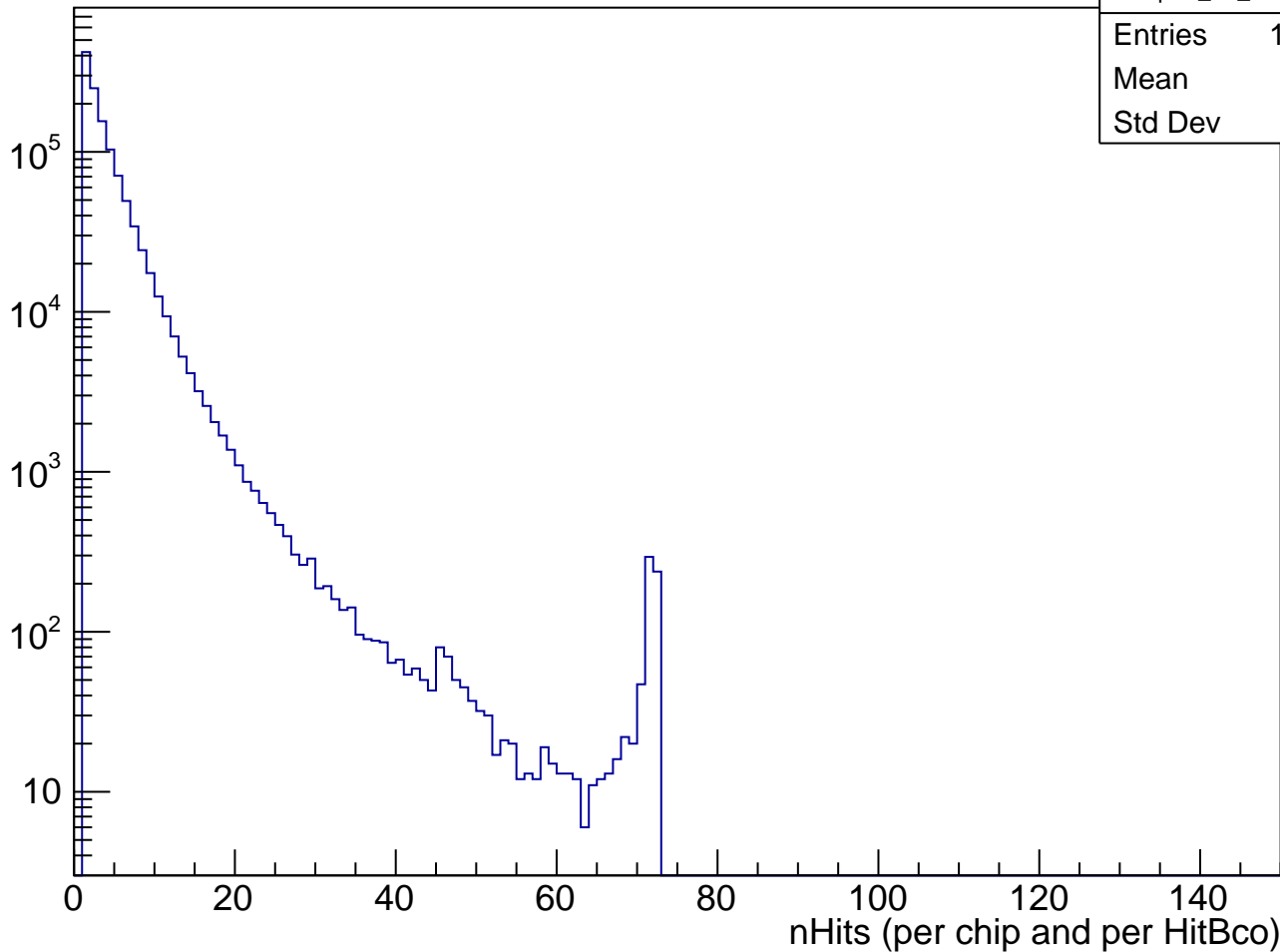


nChipHit_F2_Fch7_Chip1	
Entries	1292963
Mean	3.702
Std Dev	4.232

nHits (per chip and per HitBco)

nChipHit_F2_Fch7_Chip10

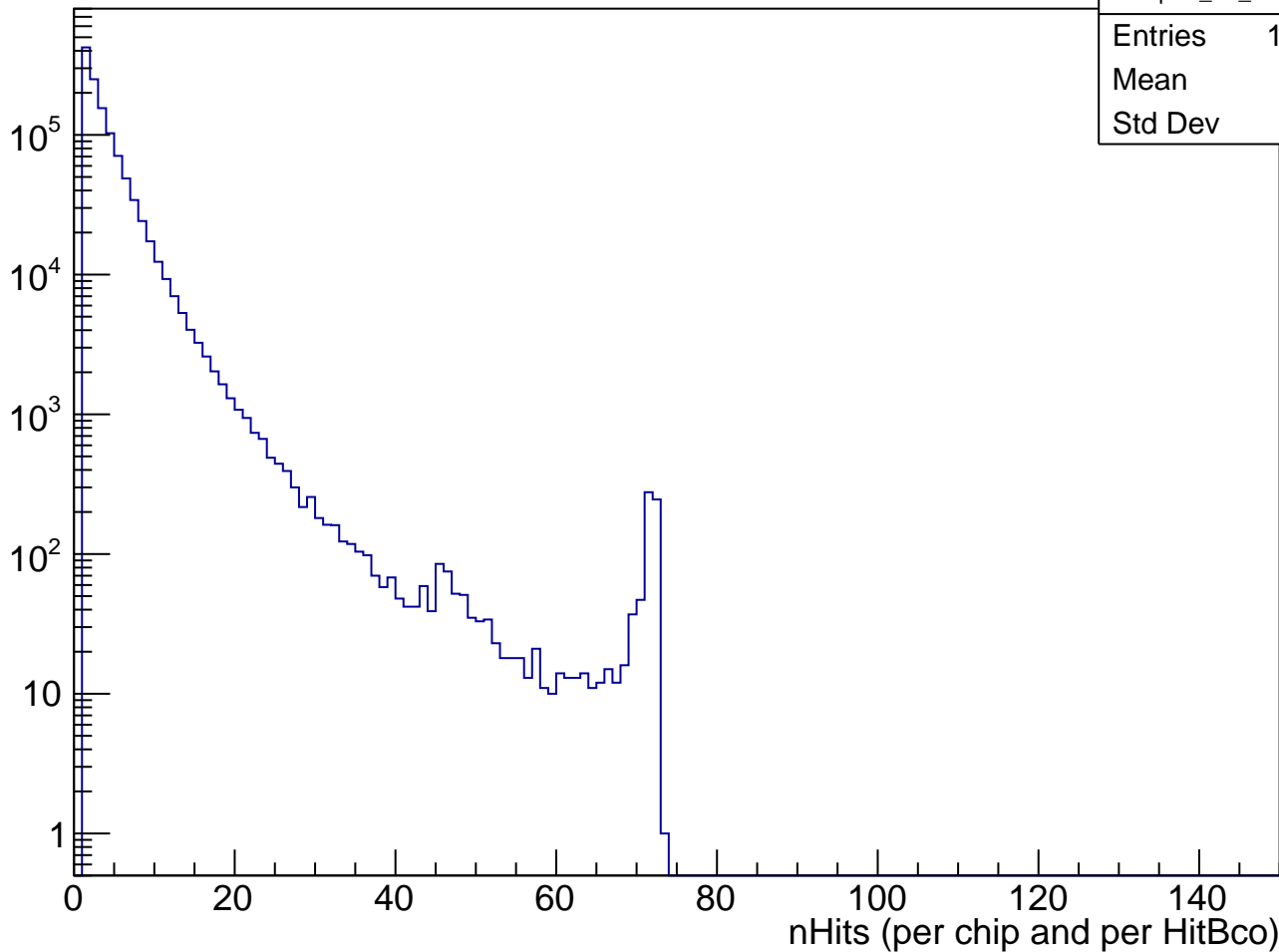
Entries



nChipHit_F2_Fch7_Chip10	
Entries	1183553
Mean	3.312
Std Dev	3.84

nChipHit_F2_Fch7_Chip11

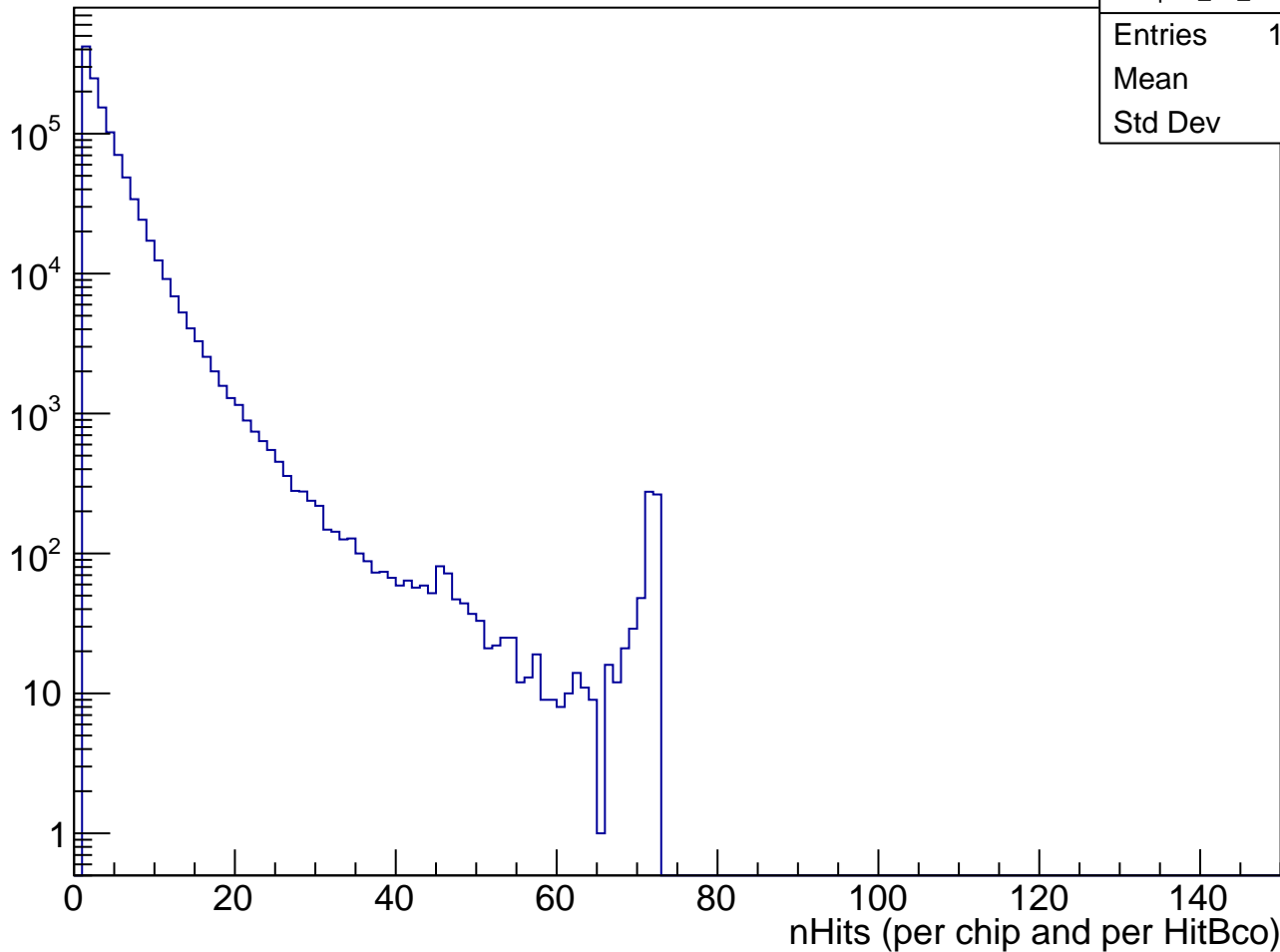
Entries



nChipHit_F2_Fch7_Chip11	
Entries	1183516
Mean	3.296
Std Dev	3.821

nChipHit_F2_Fch7_Chip12

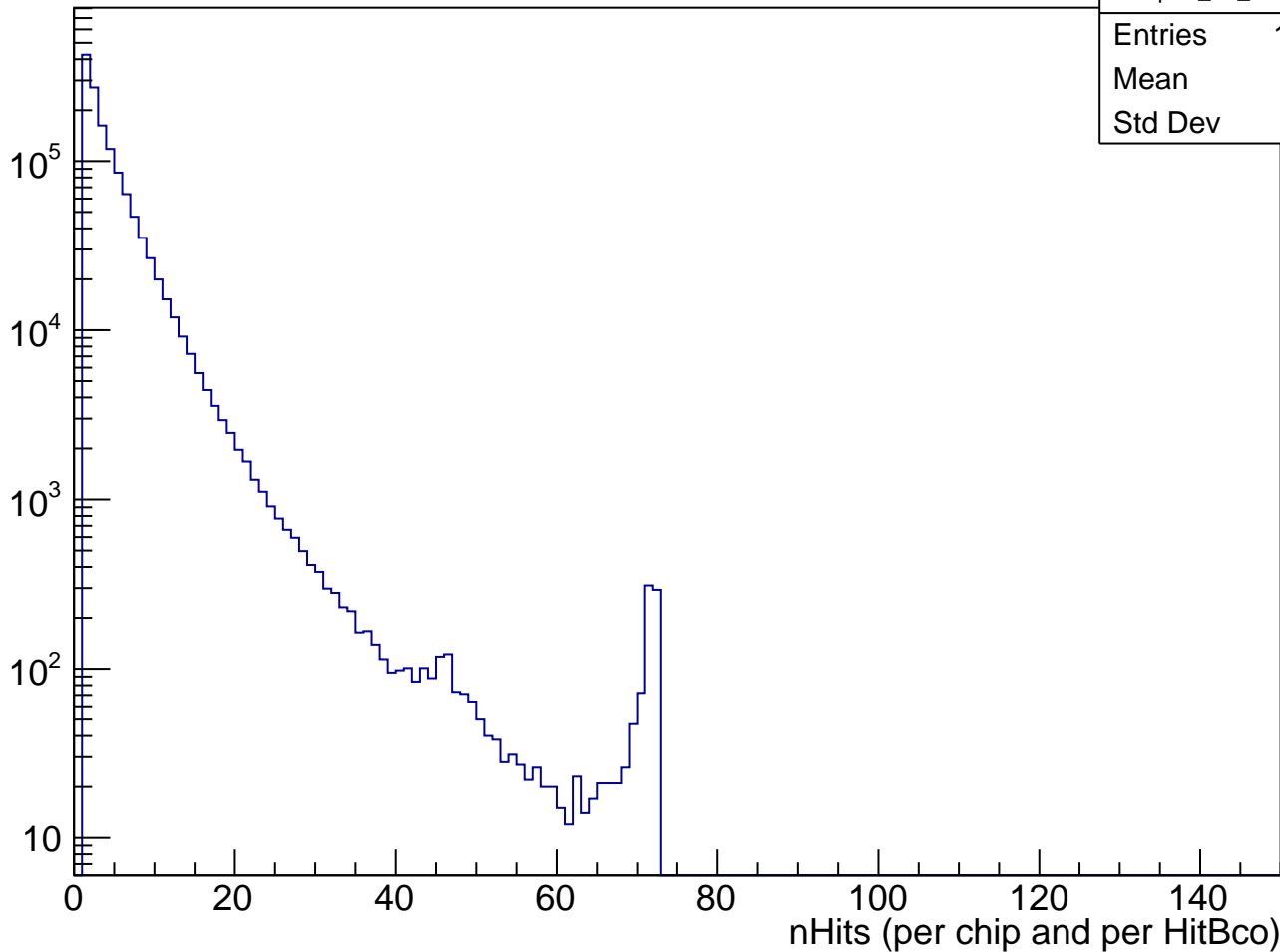
Entries



nChipHit_F2_Fch7_Chip12	
Entries	1175905
Mean	3.302
Std Dev	3.832

nChipHit_F2_Fch7_Chip13

Entries



nChipHit_F2_Fch7_Chip13

Entries 1332861

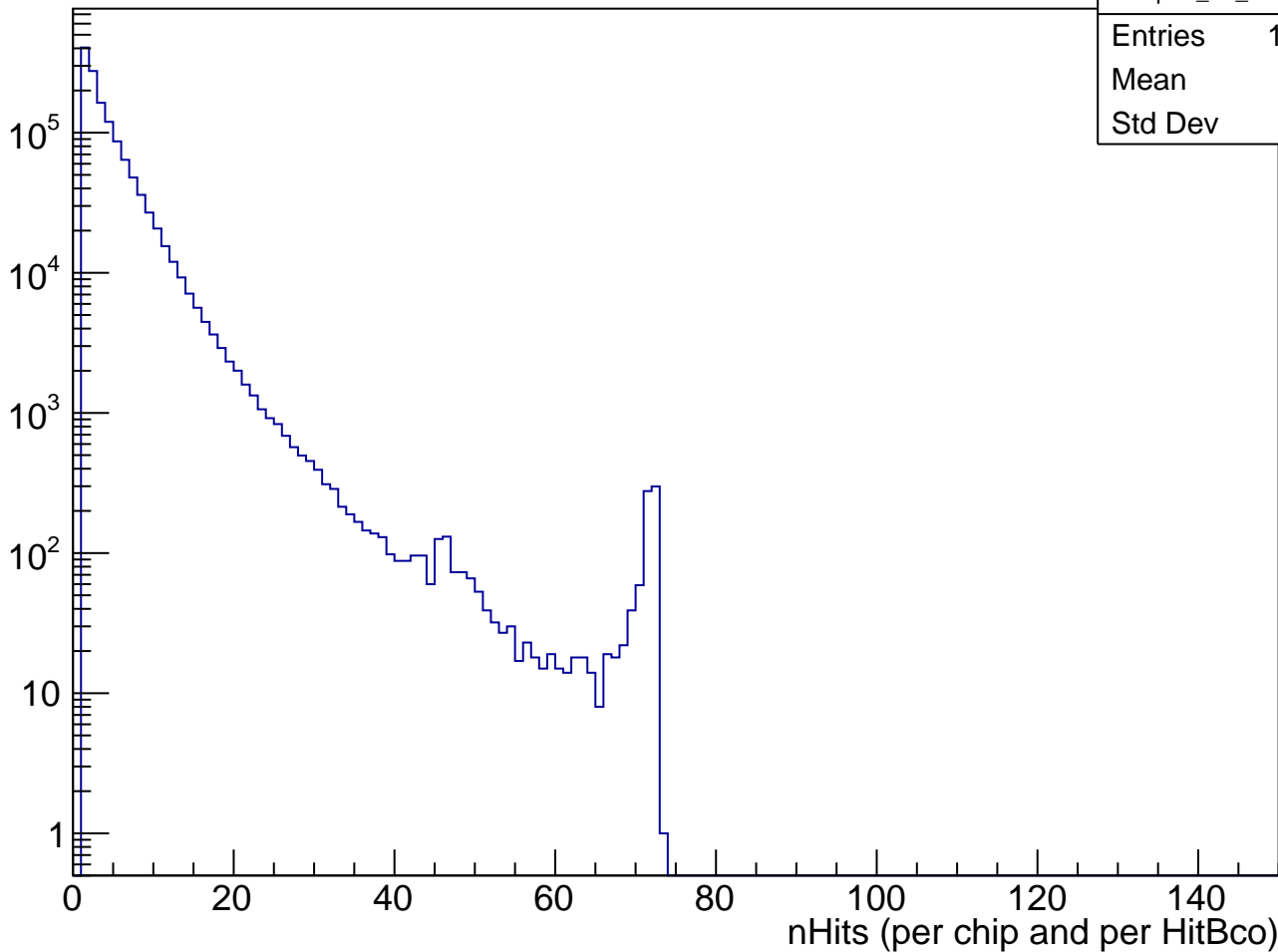
Mean 3.79

Std Dev 4.364

nHits (per chip and per HitBco)

nChipHit_F2_Fch7_Chip14

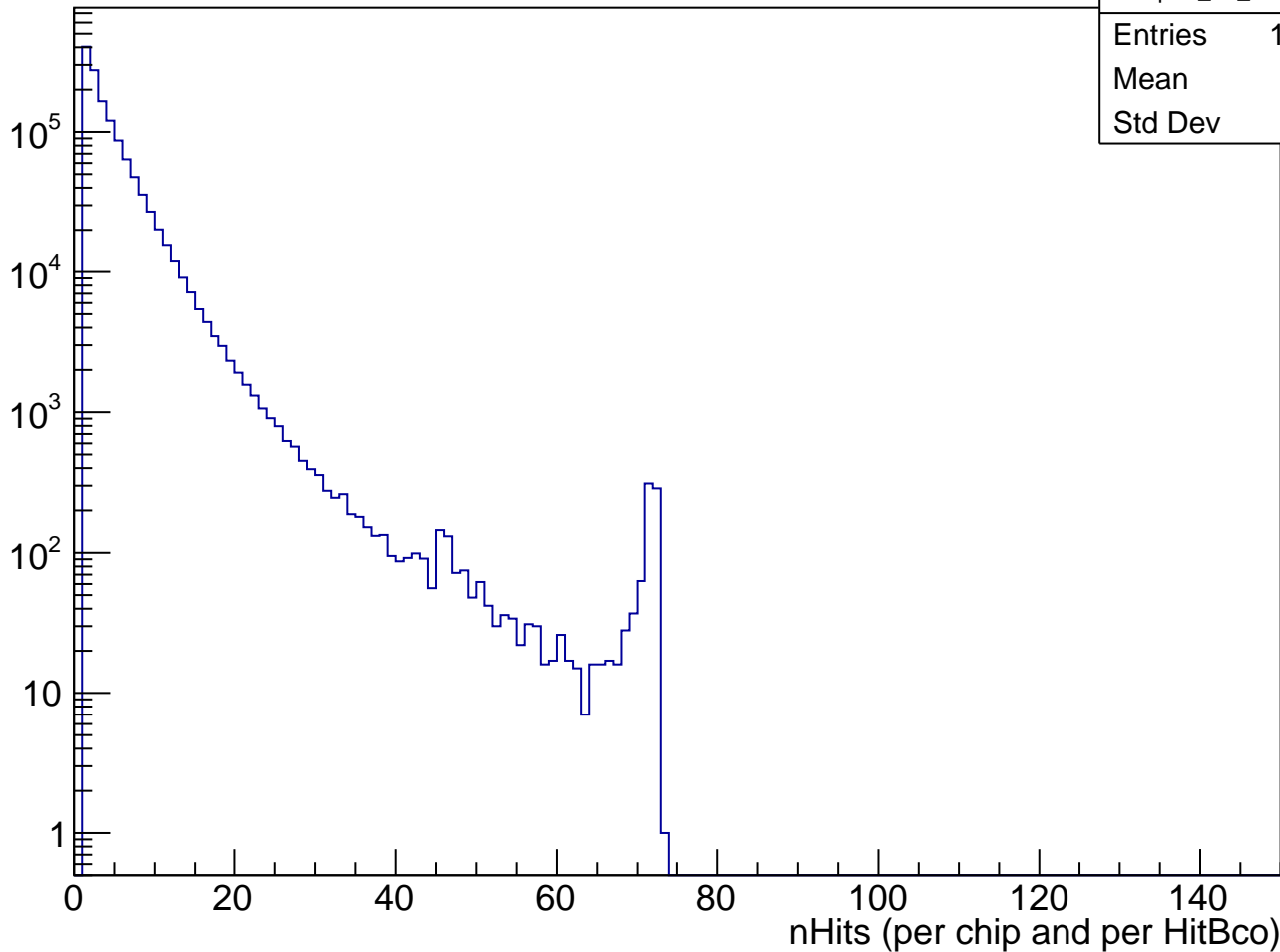
Entries



nChipHit_F2_Fch7_Chip14	
Entries	1322833
Mean	3.836
Std Dev	4.339

nChipHit_F2_Fch7_Chip15

Entries

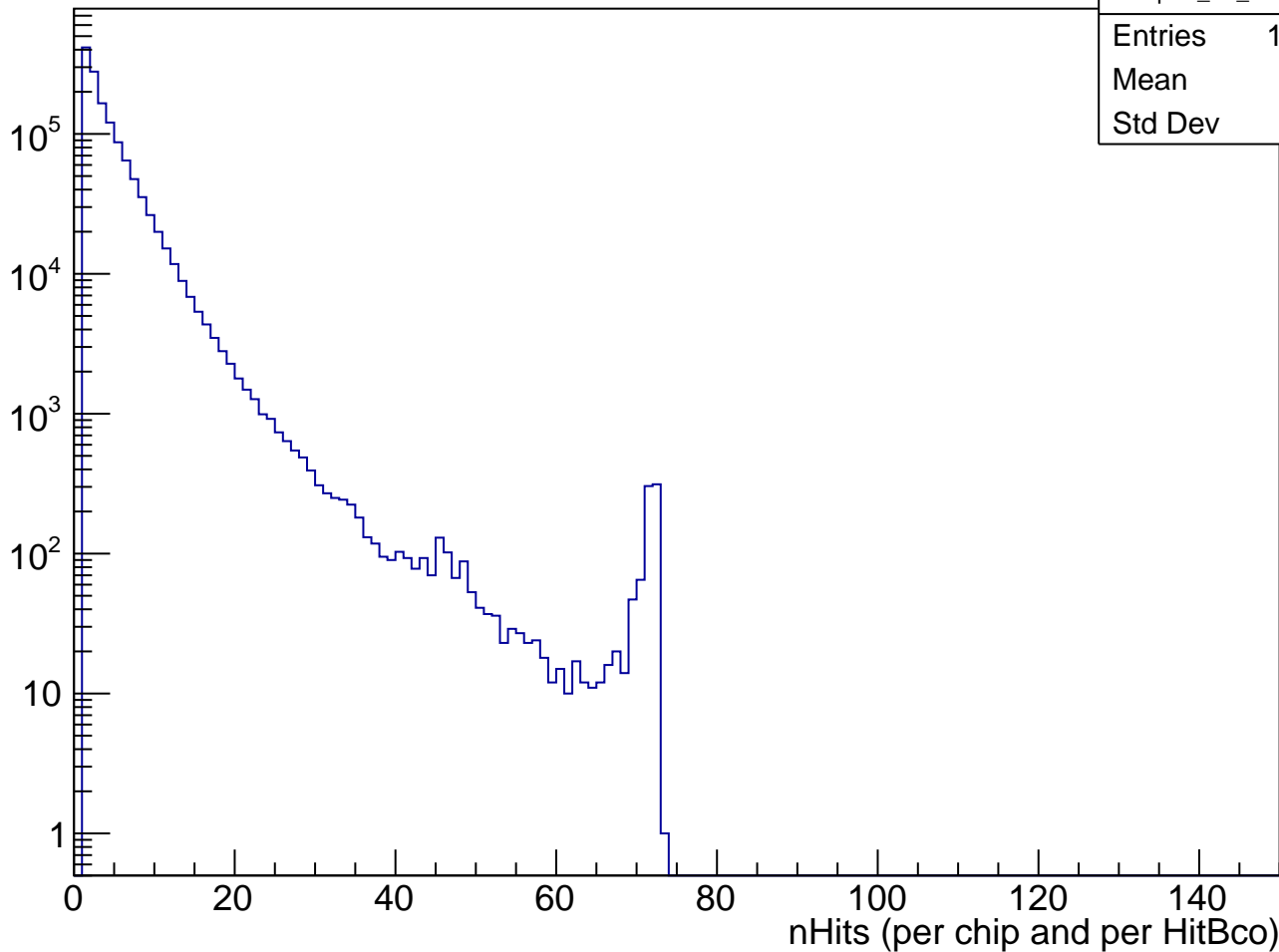


nChipHit_F2_Fch7_Chip15	
Entries	1322093
Mean	3.825
Std Dev	4.337

nHits (per chip and per HitBco)

nChipHit_F2_Fch7_Chip16

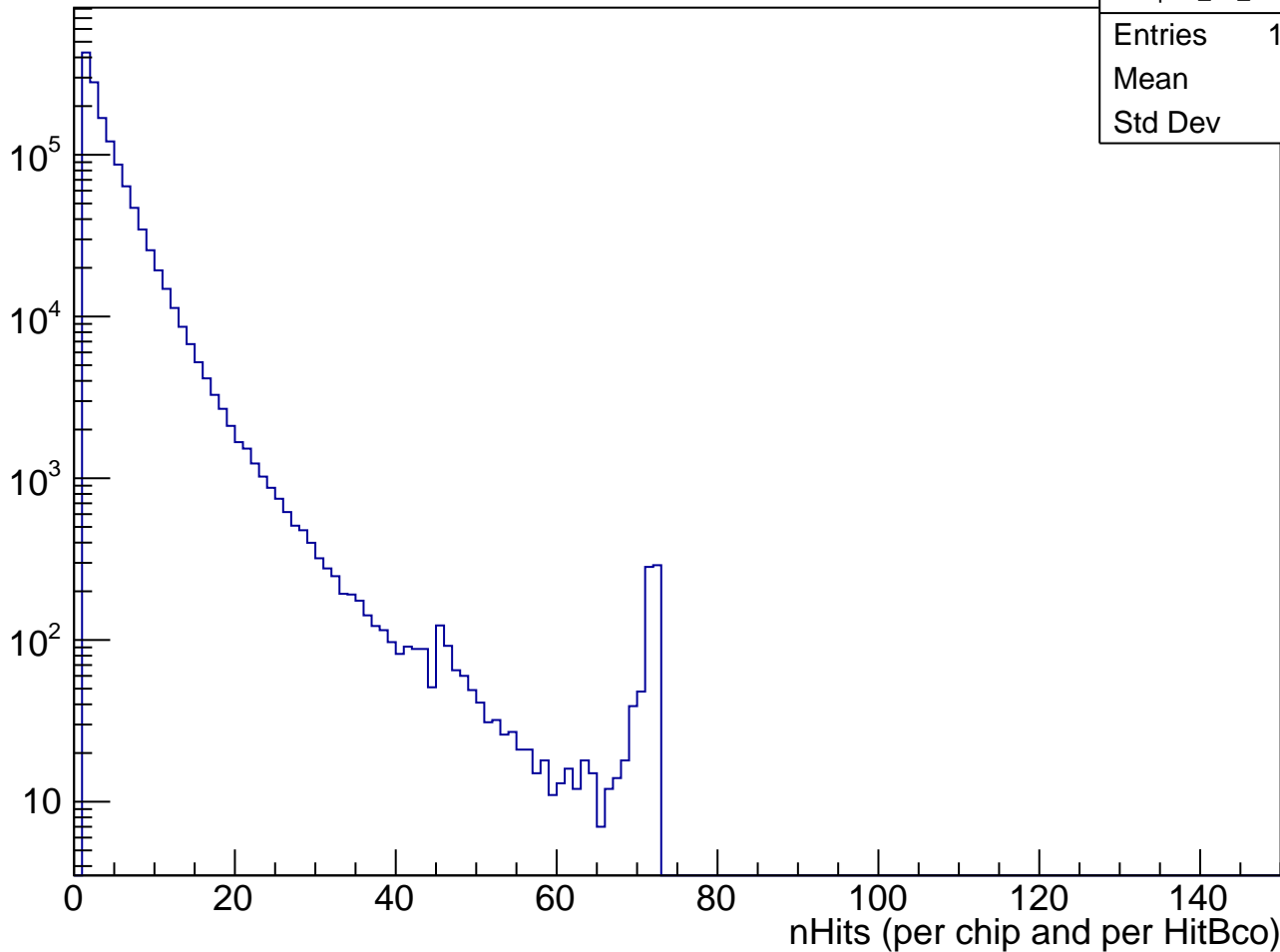
Entries



nChipHit_F2_Fch7_Chip16	
Entries	1335054
Mean	3.776
Std Dev	4.282

nChipHit_F2_Fch7_Chip17

Entries

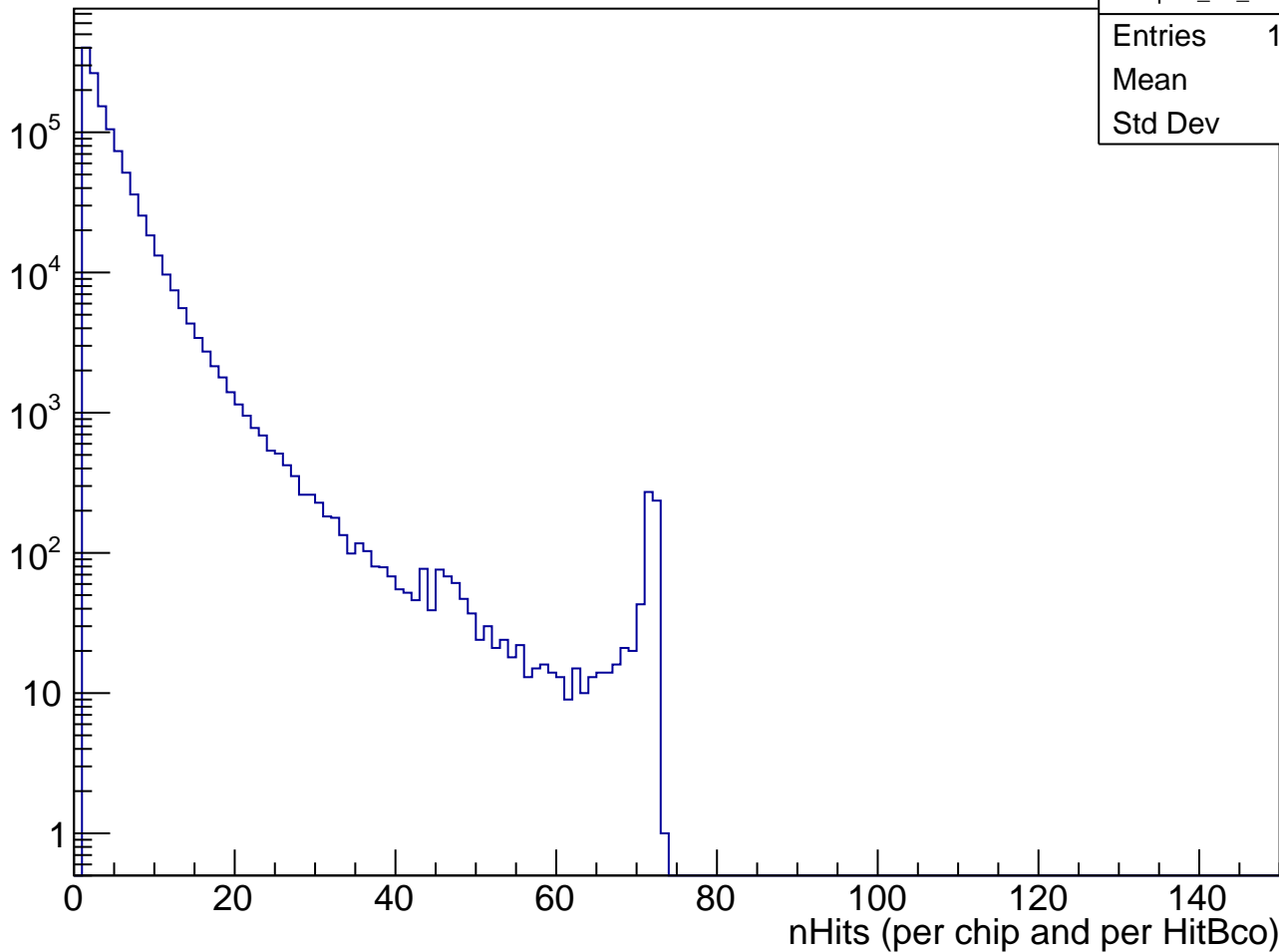


nChipHit_F2_Fch7_Chip17	
Entries	1348425
Mean	3.707
Std Dev	4.189

nHits (per chip and per HitBco)

nChipHit_F2_Fch7_Chip18

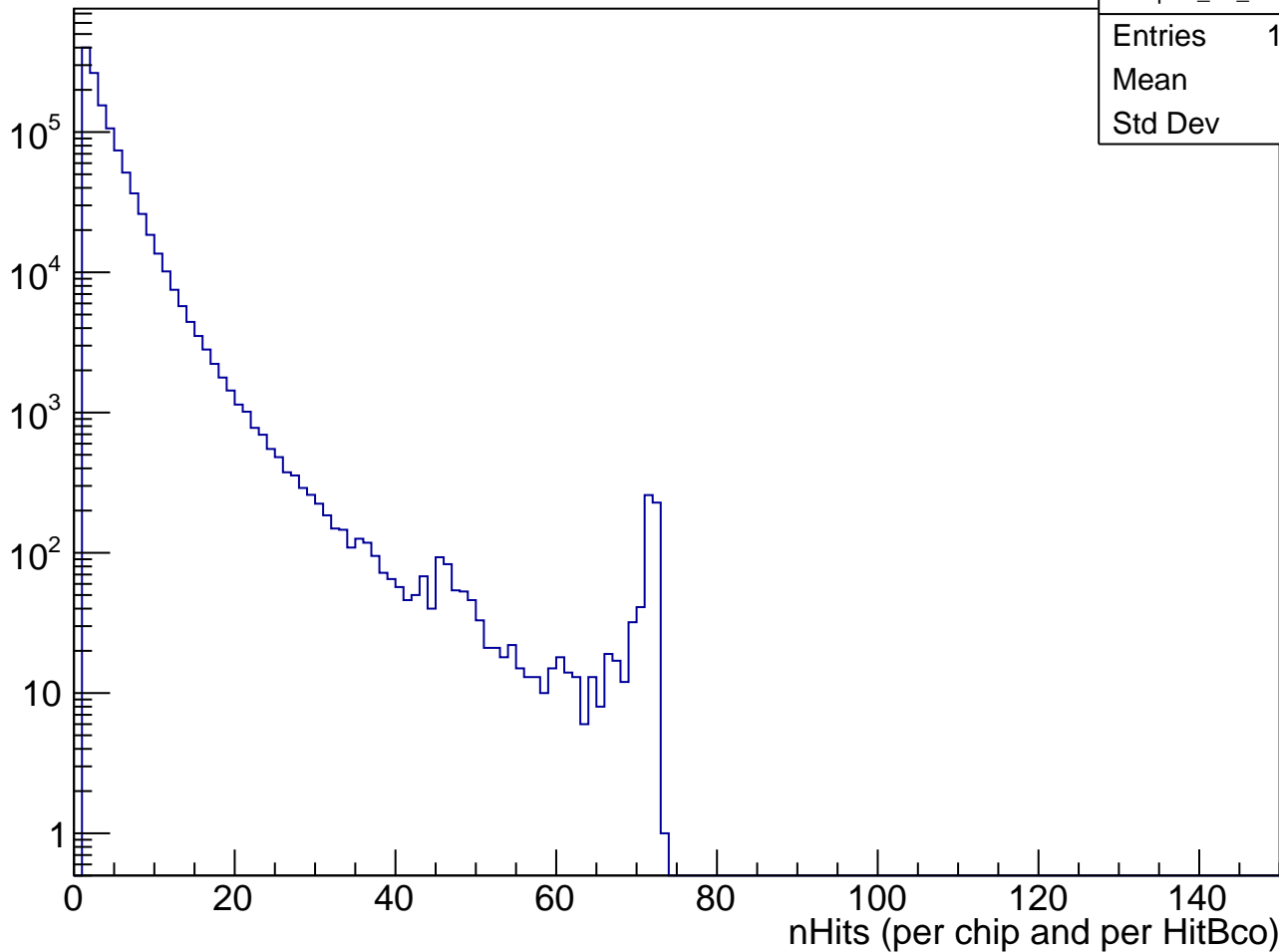
Entries



nChipHit_F2_Fch7_Chip18	
Entries	1189678
Mean	3.381
Std Dev	3.864

nChipHit_F2_Fch7_Chip19

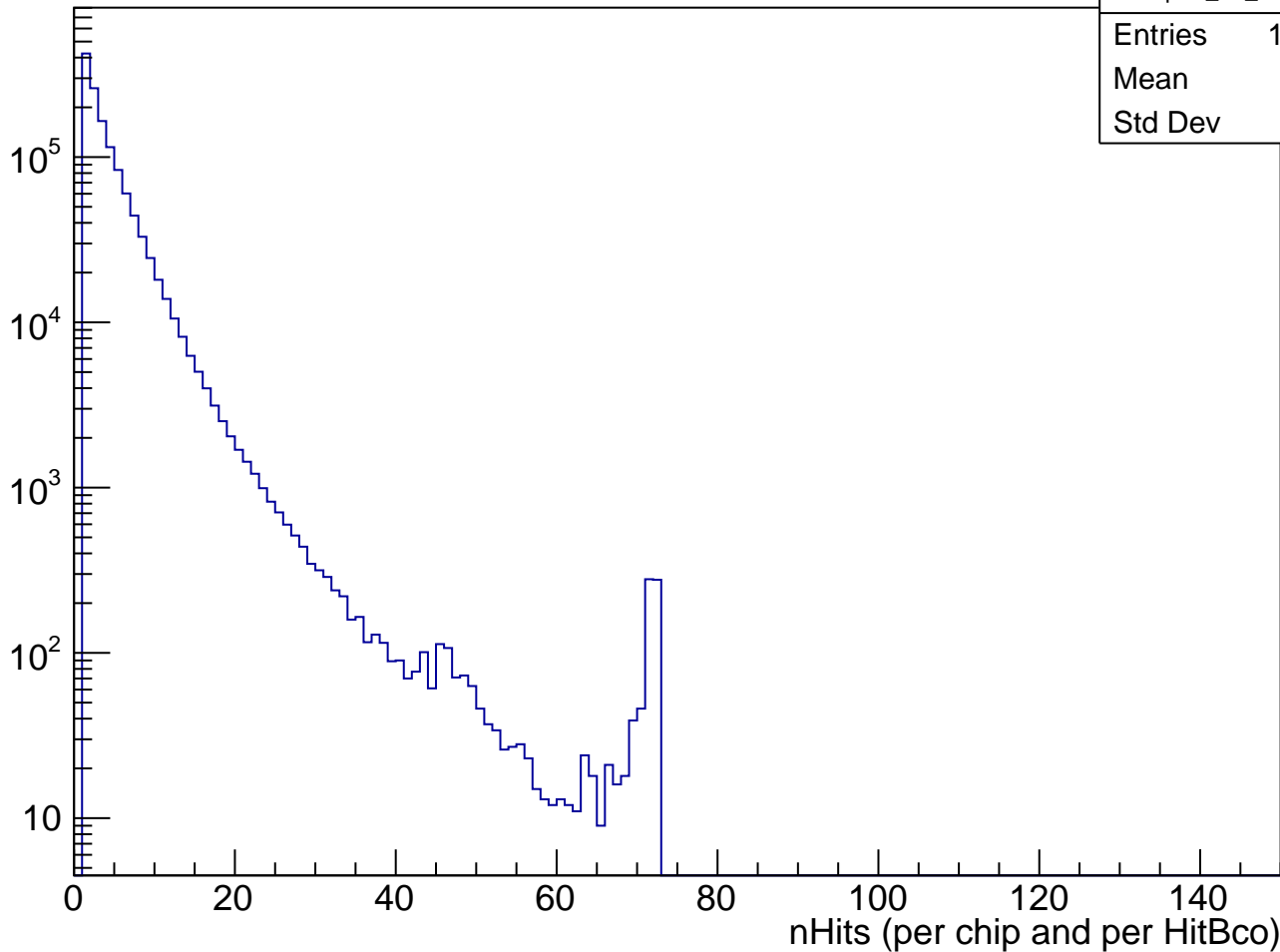
Entries



nChipHit_F2_Fch7_Chip19	
Entries	1193774
Mean	3.401
Std Dev	3.868

nChipHit_F2_Fch7_Chip2

Entries

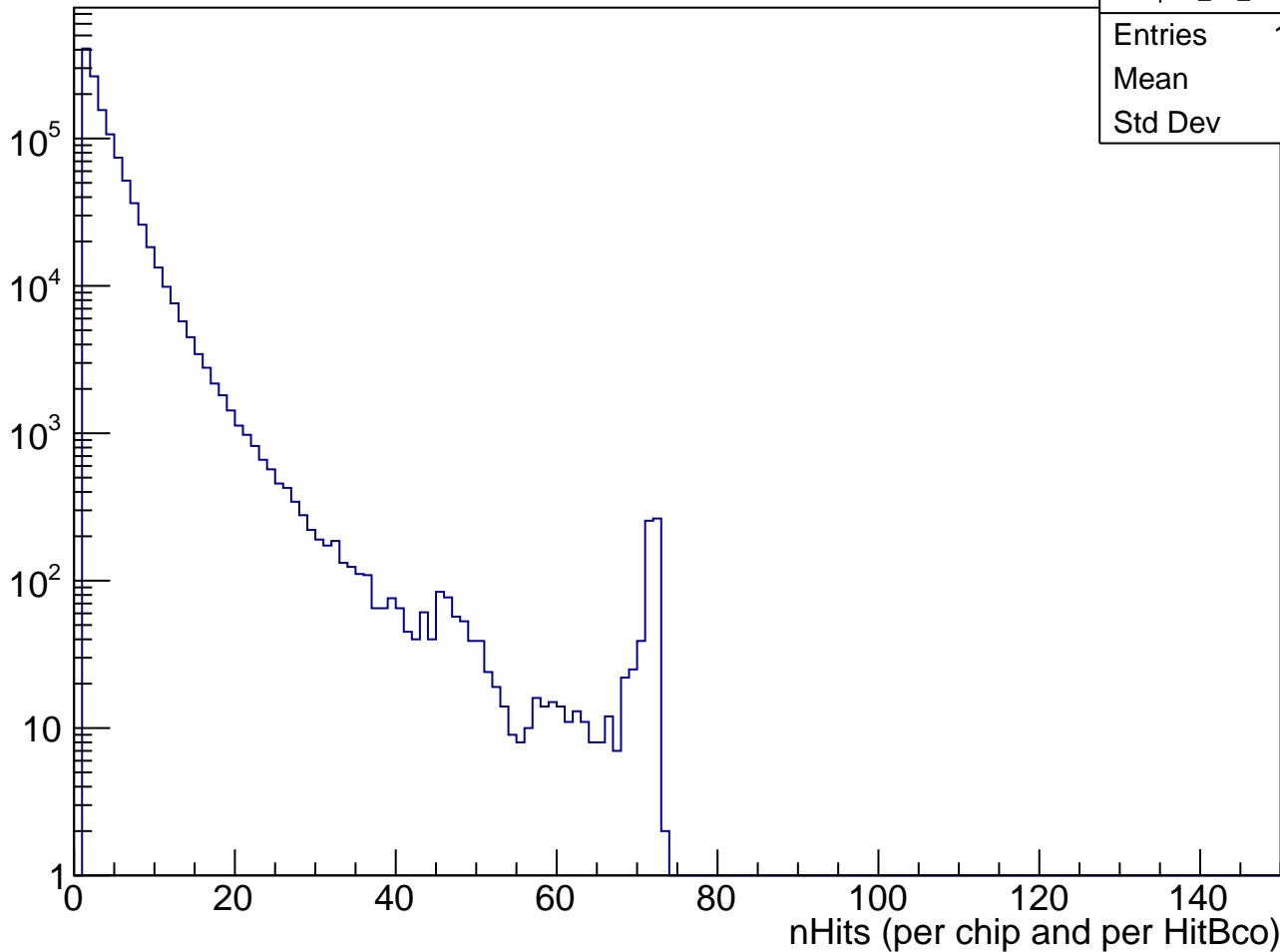


nChipHit_F2_Fch7_Chip2	
Entries	1296135
Mean	3.683
Std Dev	4.211

nHits (per chip and per HitBco)

nChipHit_F2_Fch7_Chip20

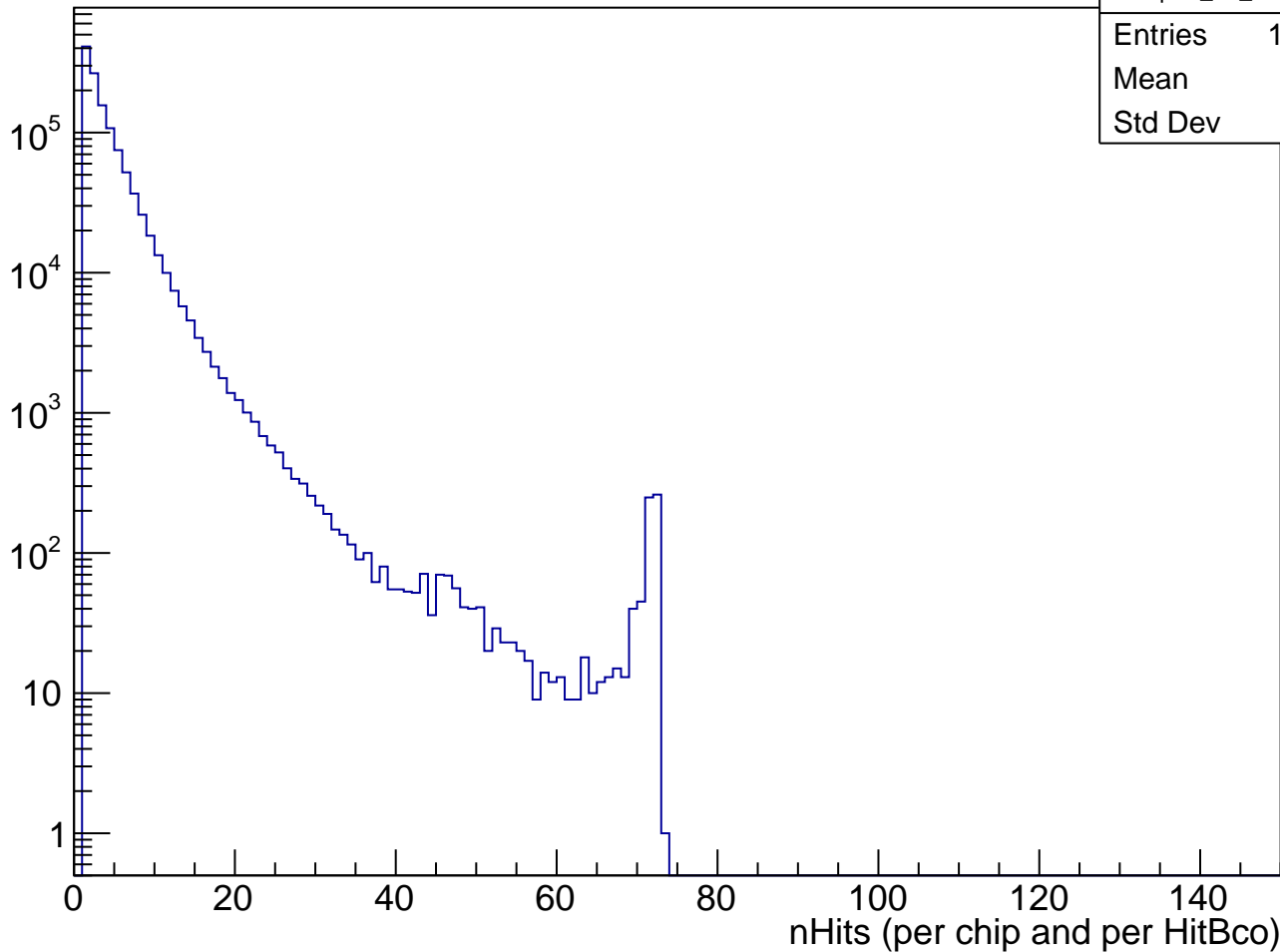
Entries



nChipHit_F2_Fch7_Chip20	
Entries	1201721
Mean	3.379
Std Dev	3.845

nChipHit_F2_Fch7_Chip21

Entries

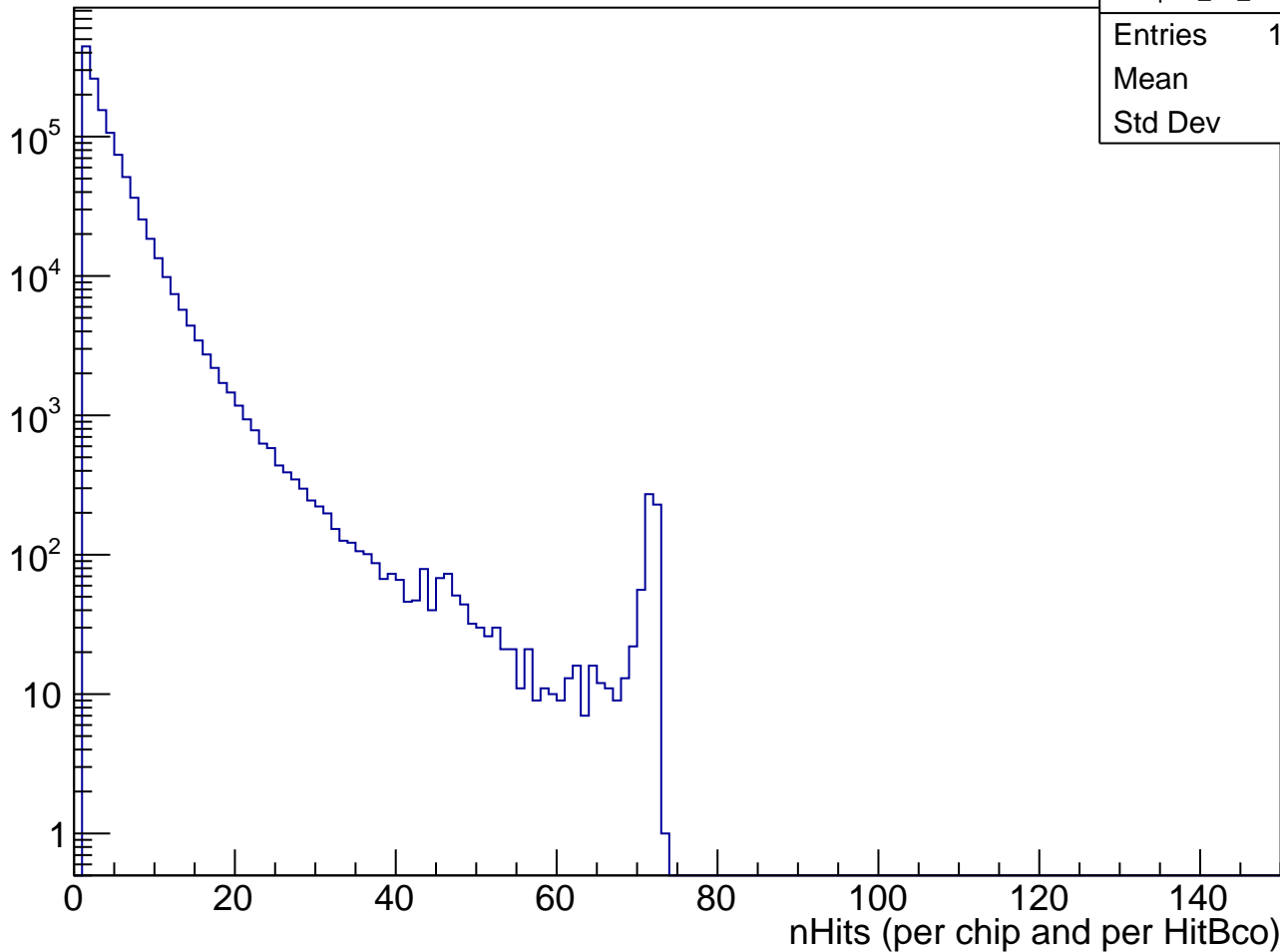


nChipHit_F2_Fch7_Chip21	
Entries	1209378
Mean	3.378
Std Dev	3.855

nHits (per chip and per HitBco)

nChipHit_F2_Fch7_Chip22

Entries

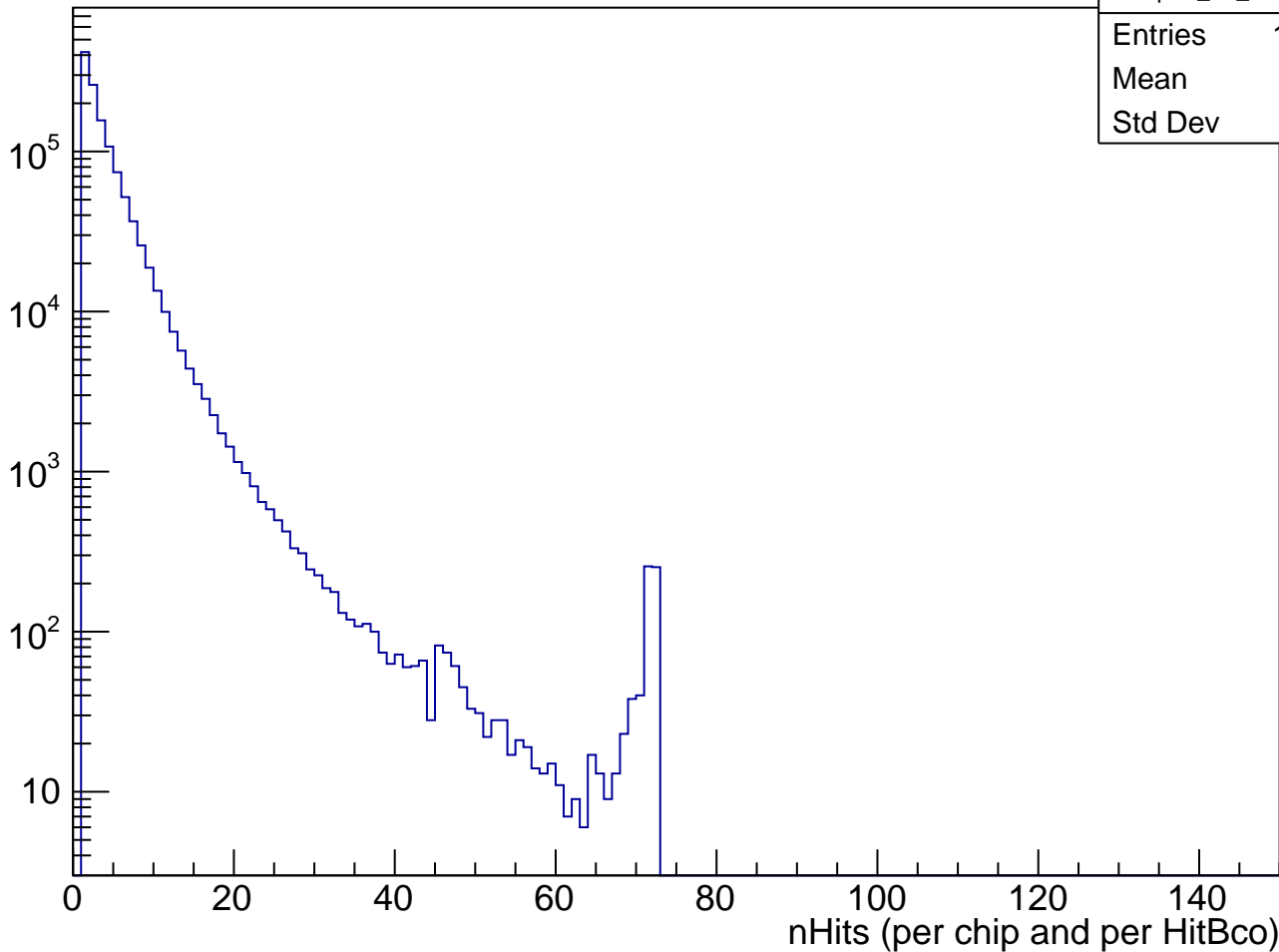


nChipHit_F2_Fch7_Chip22	
Entries	1232492
Mean	3.306
Std Dev	3.81

nHits (per chip and per HitBco)

nChipHit_F2_Fch7_Chip23

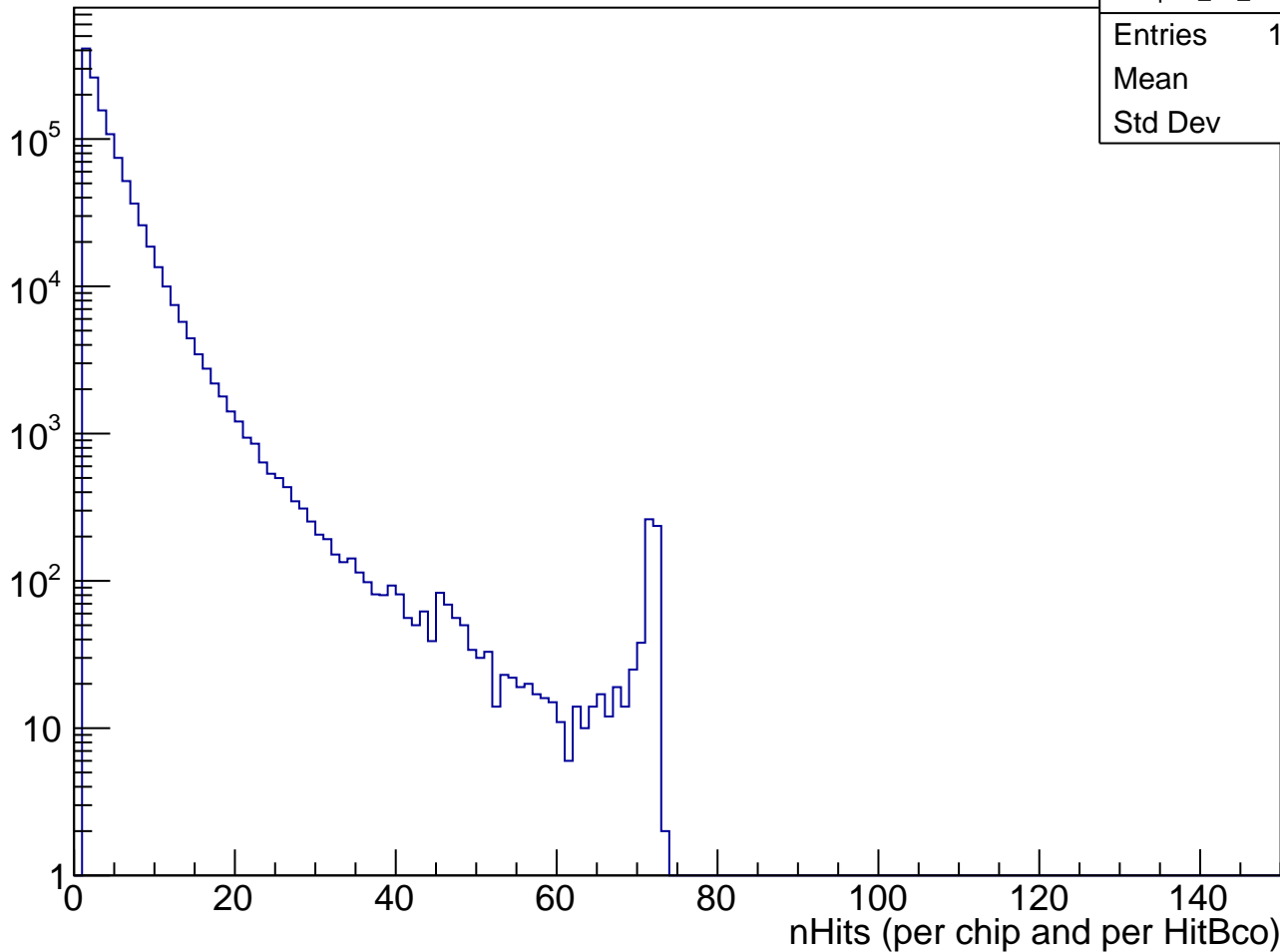
Entries



nChipHit_F2_Fch7_Chip23	
Entries	1211391
Mean	3.373
Std Dev	3.865

nChipHit_F2_Fch7_Chip24

Entries



nChipHit_F2_Fch7_Chip24

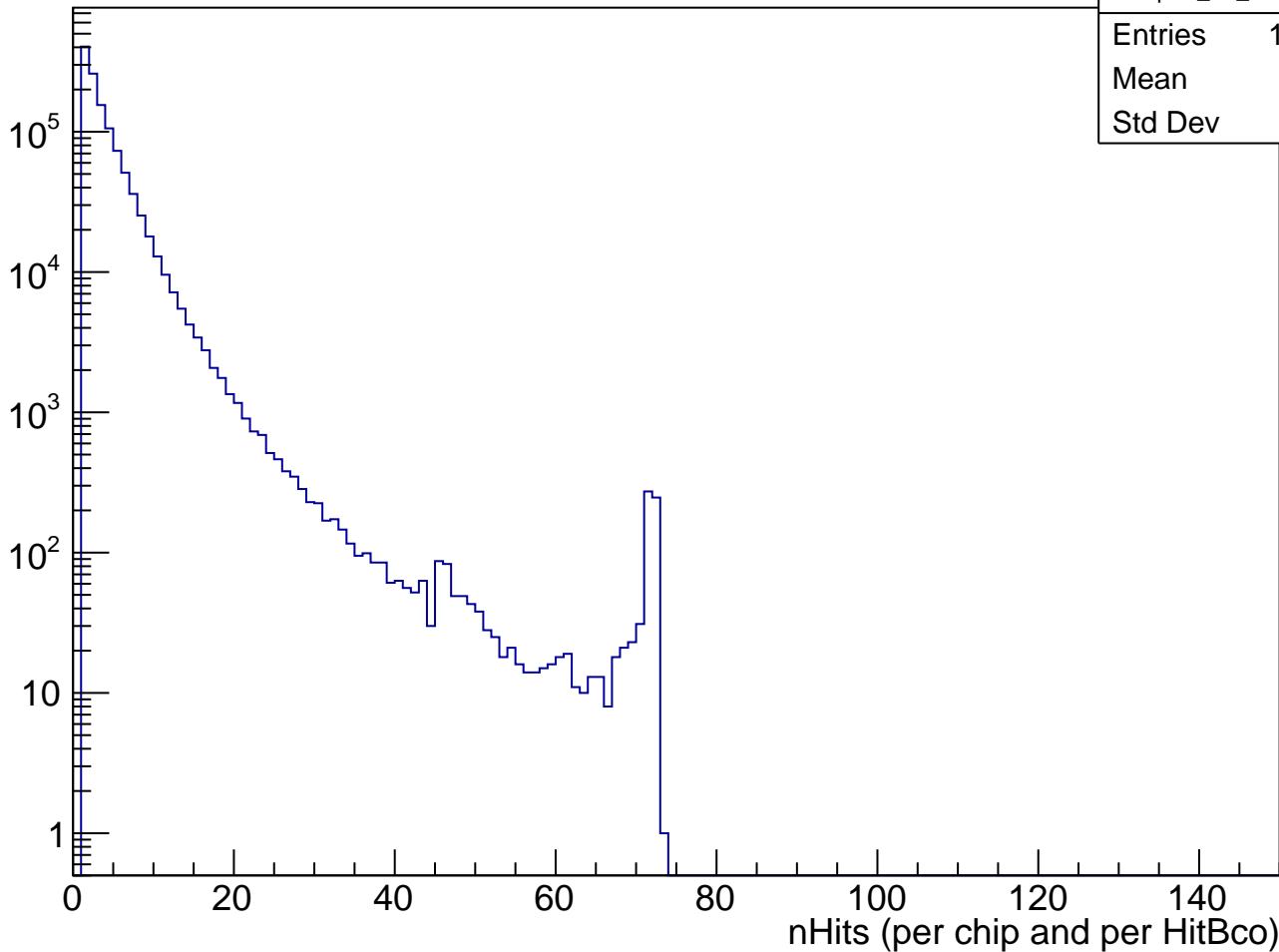
Entries 1206268

Mean 3.382

Std Dev 3.861

nChipHit_F2_Fch7_Chip25

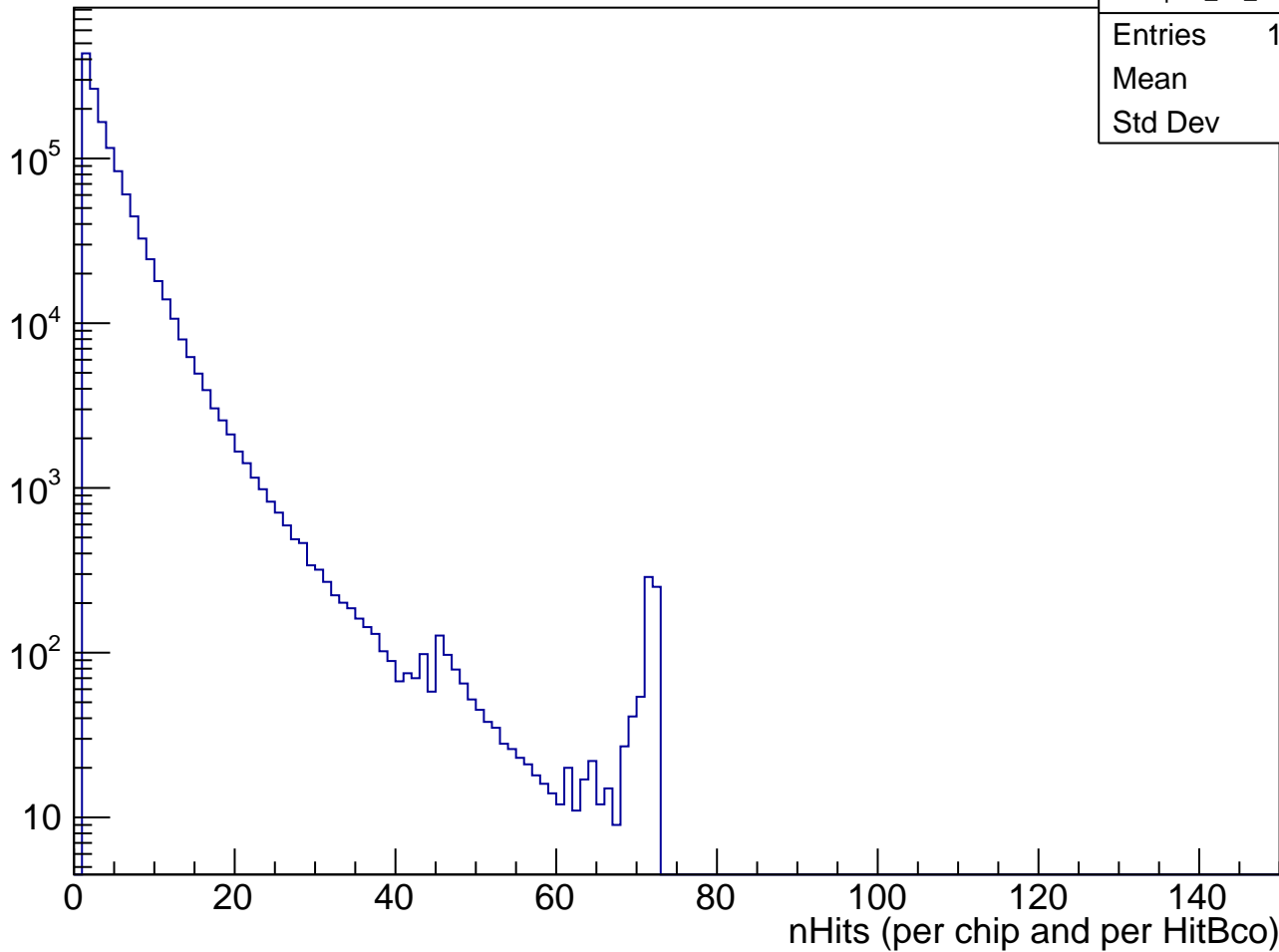
Entries



nChipHit_F2_Fch7_Chip25	
Entries	1187460
Mean	3.367
Std Dev	3.854

nChipHit_F2_Fch7_Chip3

Entries



nChipHit_F2_Fch7_Chip3

Entries 1312864

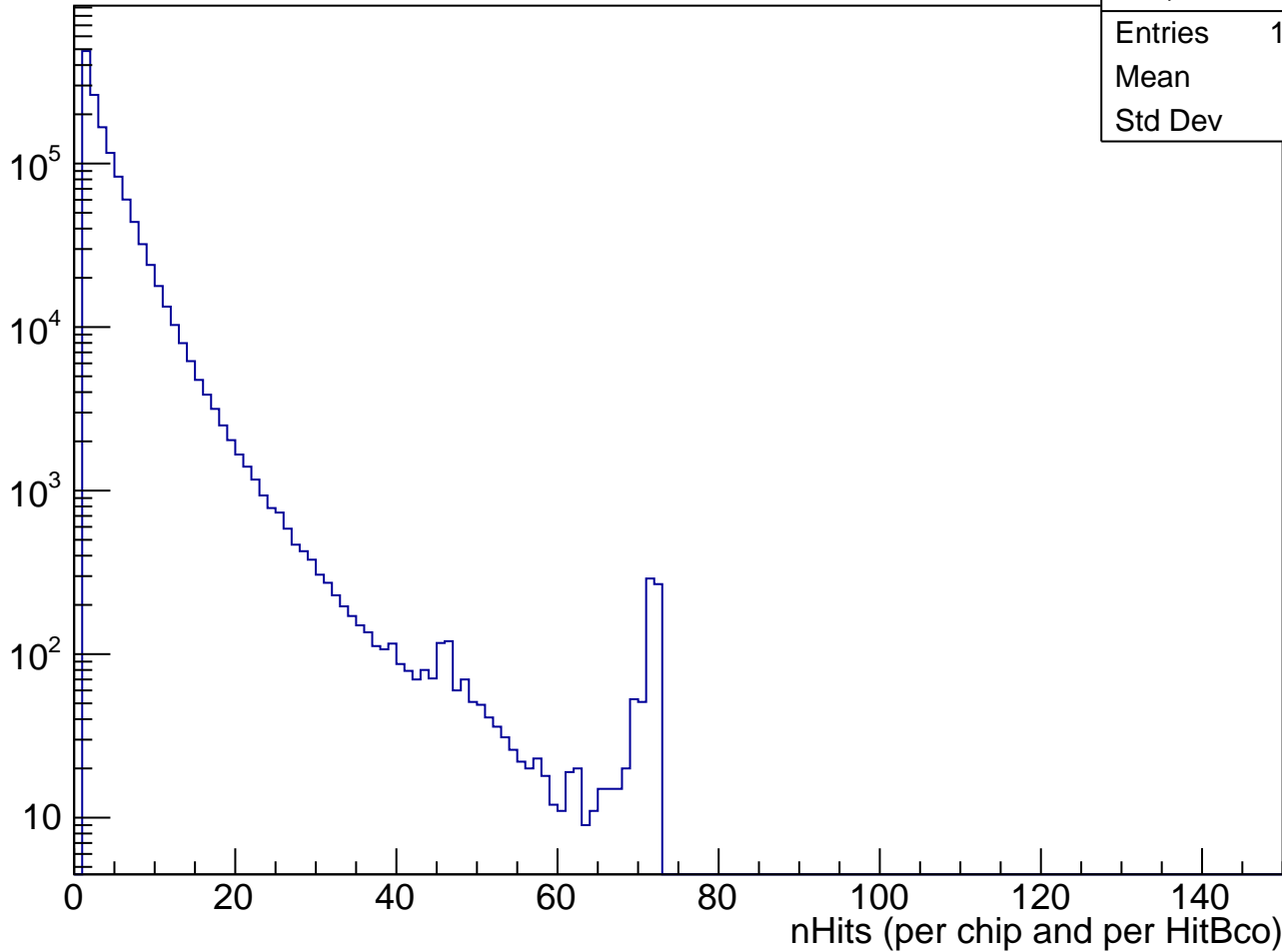
Mean 3.65

Std Dev 4.179

nHits (per chip and per HitBco)

nChipHit_F2_Fch7_Chip4

Entries



nChipHit_F2_Fch7_Chip4

Entries 1360658

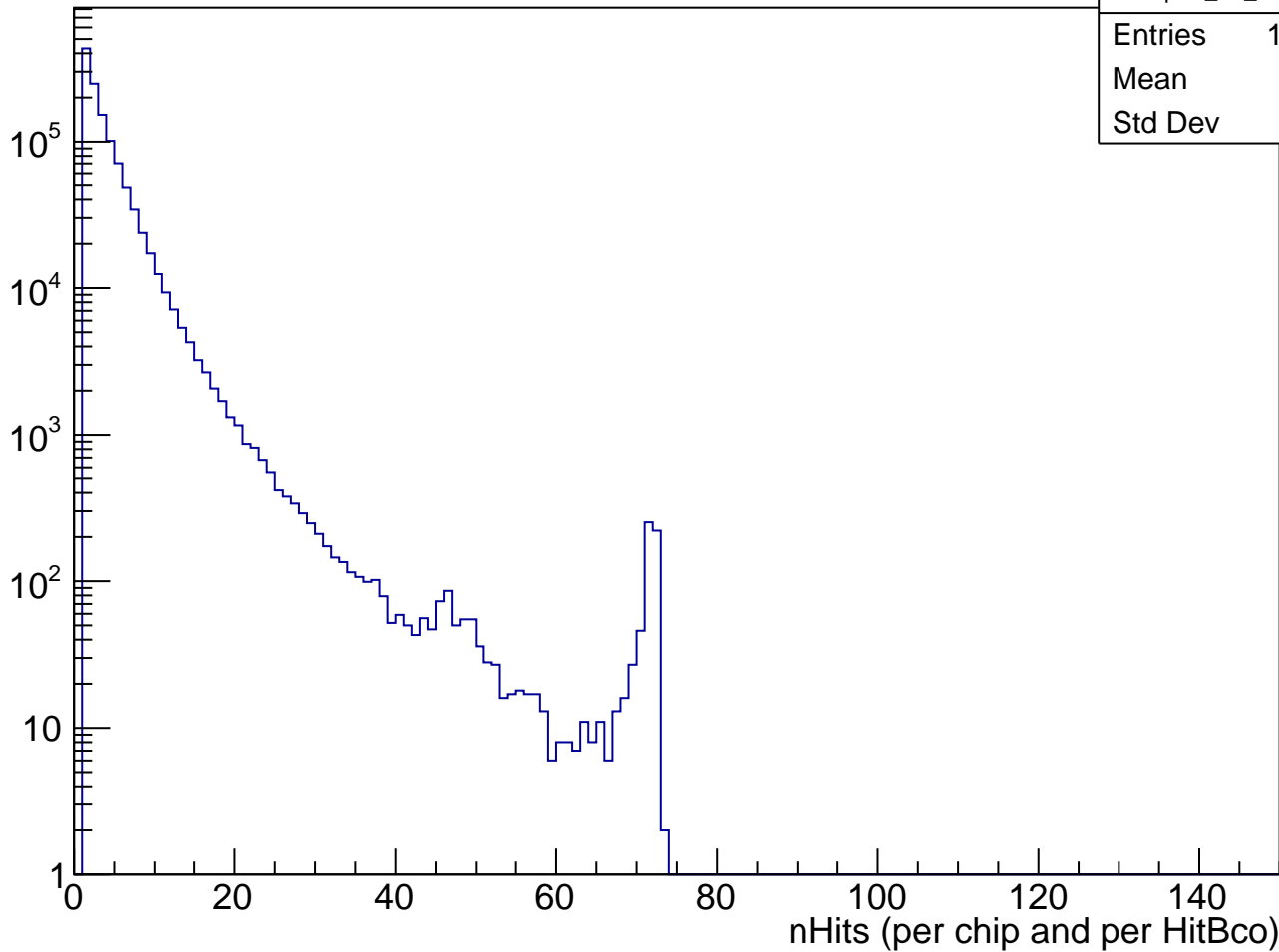
Mean 3.534

Std Dev 4.131

nHits (per chip and per HitBco)

nChipHit_F2_Fch7_Chip5

Entries



nChipHit_F2_Fch7_Chip5

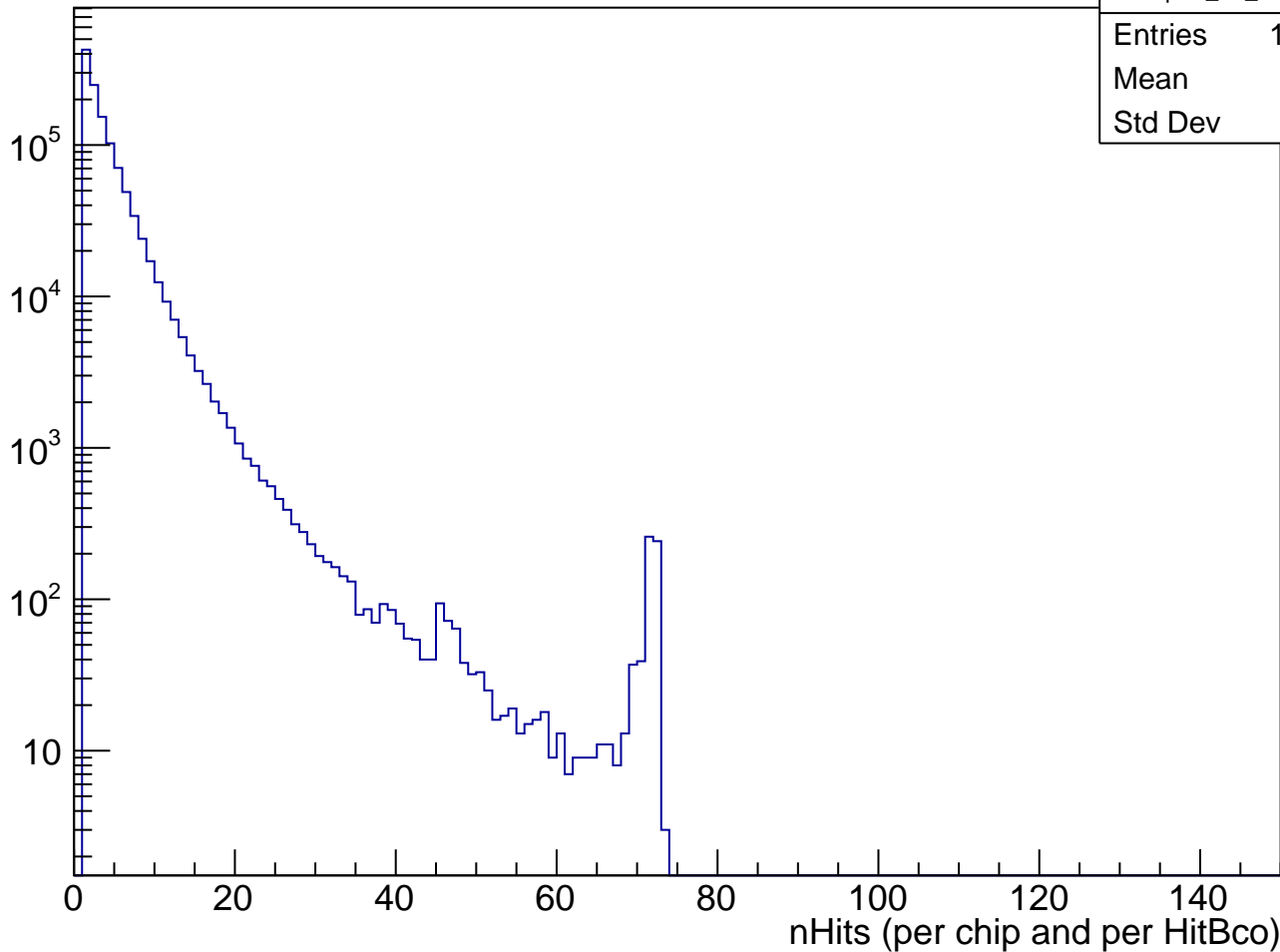
Entries 1185664

Mean 3.285

Std Dev 3.812

nChipHit_F2_Fch7_Chip6

Entries



nChipHit_F2_Fch7_Chip6

Entries 1183423

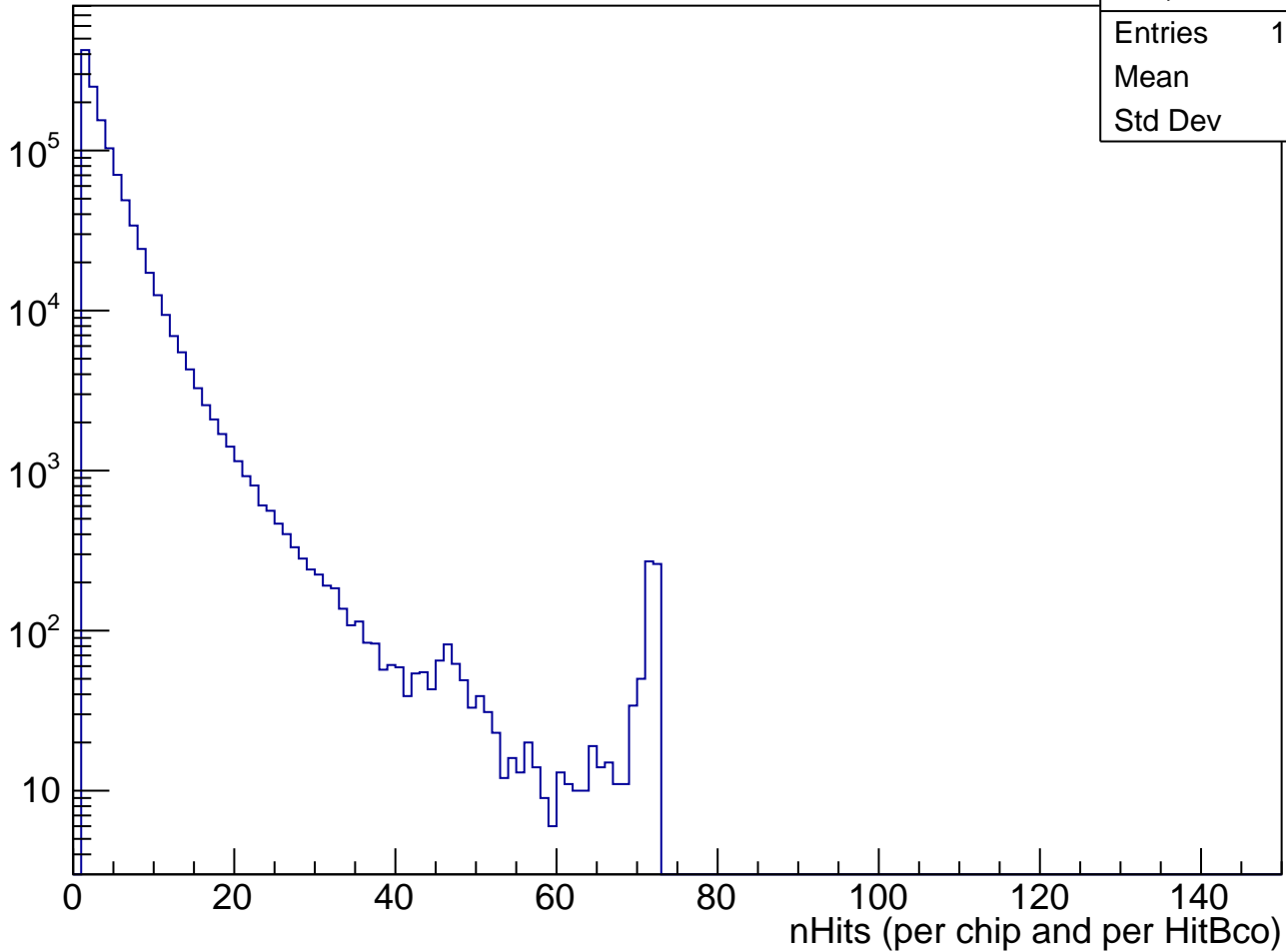
Mean 3.291

Std Dev 3.809

nHits (per chip and per HitBco)

nChipHit_F2_Fch7_Chip7

Entries



nChipHit_F2_Fch7_Chip7

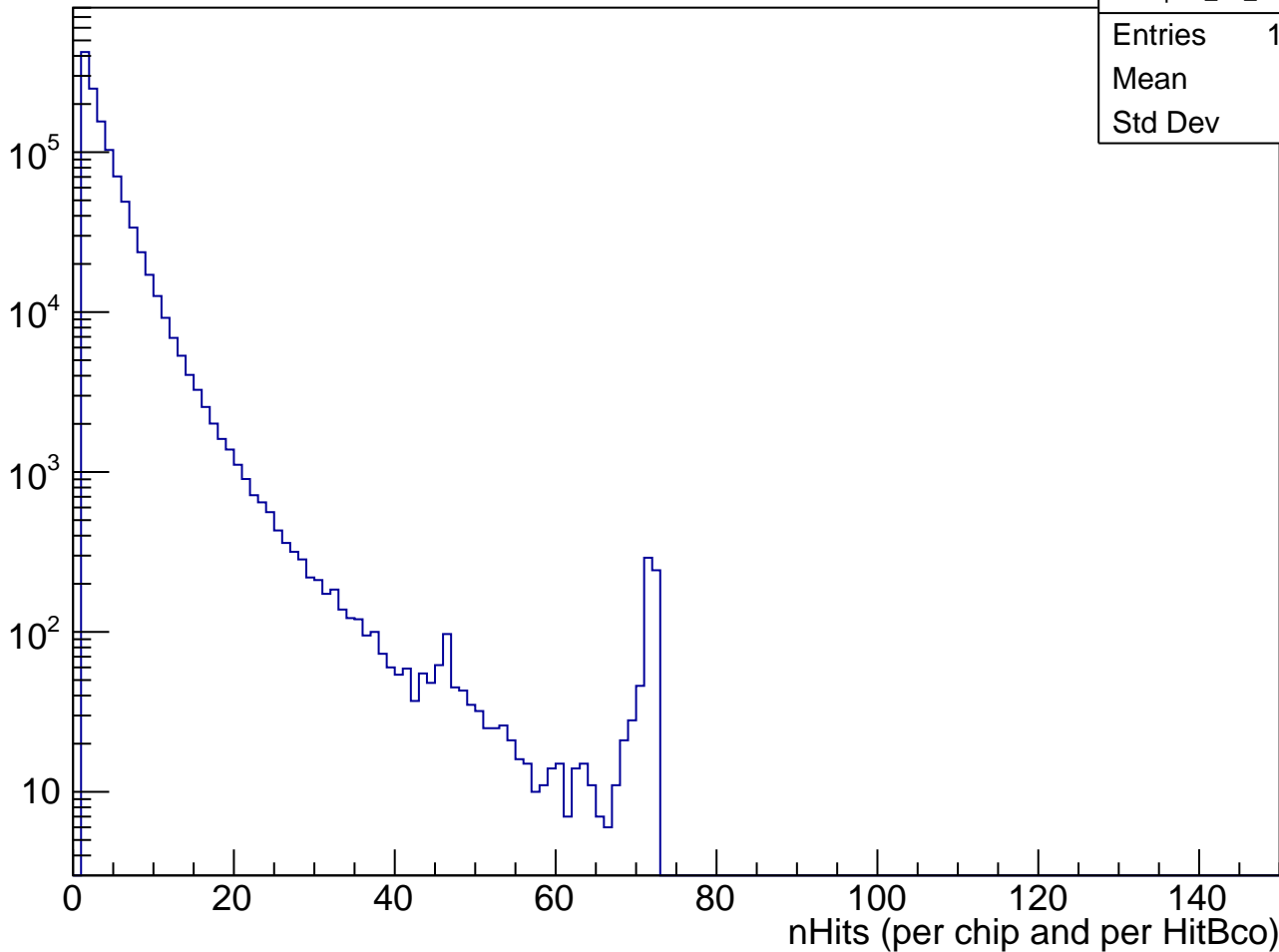
Entries 1185070

Mean 3.307

Std Dev 3.848

nChipHit_F2_Fch7_Chip8

Entries



nChipHit_F2_Fch7_Chip8

Entries 1182634

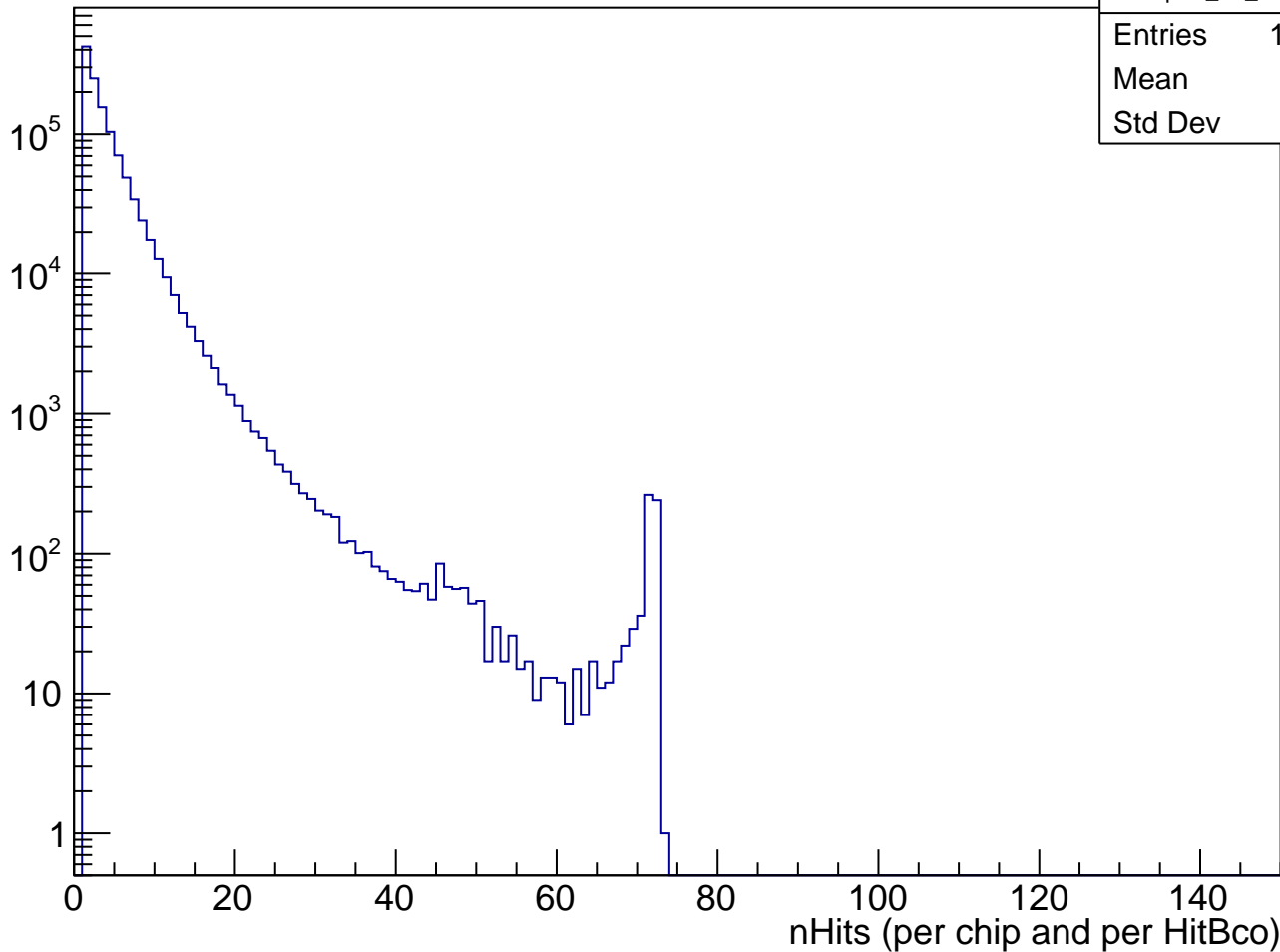
Mean 3.297

Std Dev 3.829

nHits (per chip and per HitBco)

nChipHit_F2_Fch7_Chip9

Entries



nChipHit_F2_Fch7_Chip9

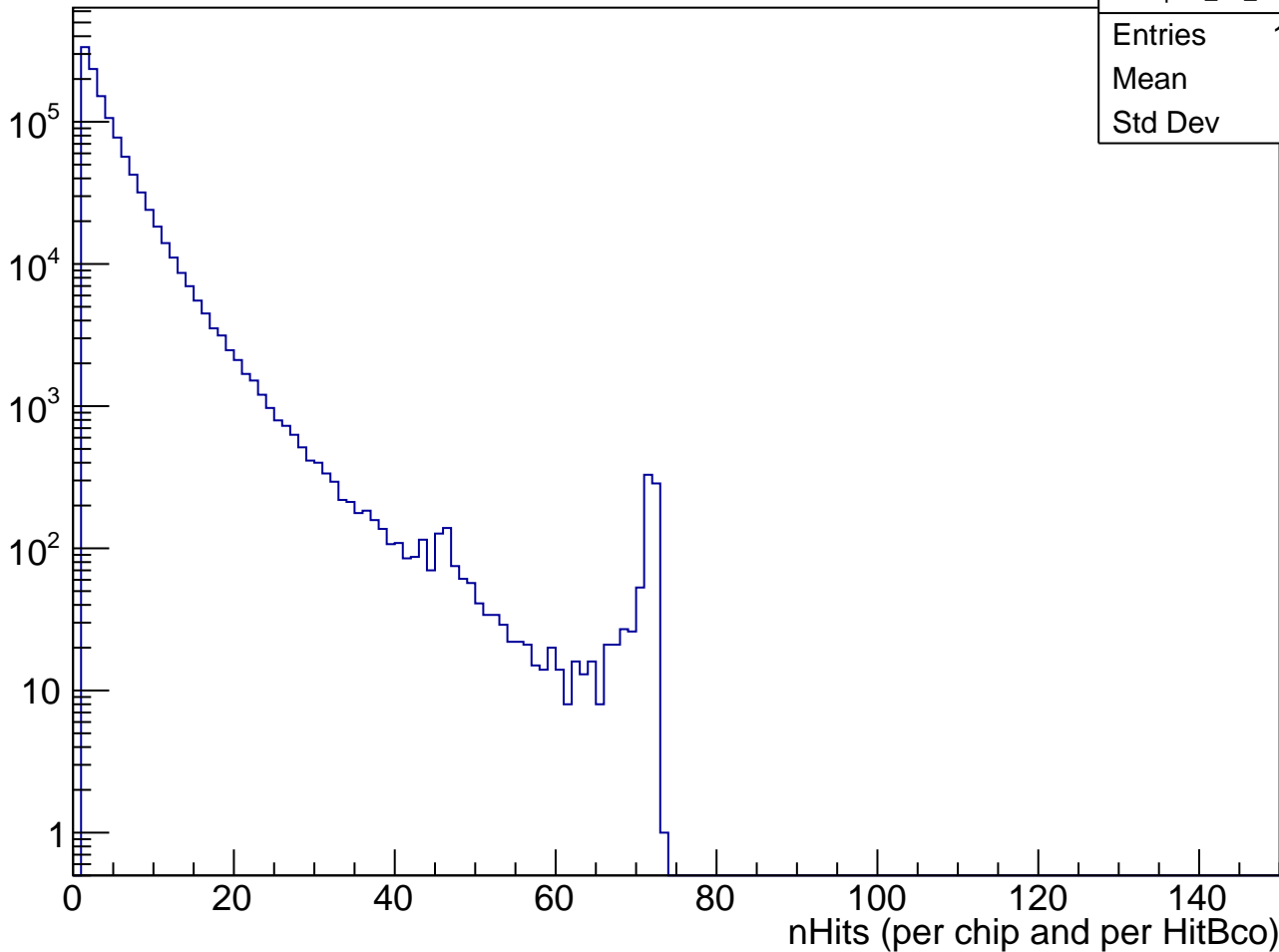
Entries 1185413

Mean 3.309

Std Dev 3.831

nChipHit_F2_Fch8_Chip0

Entries

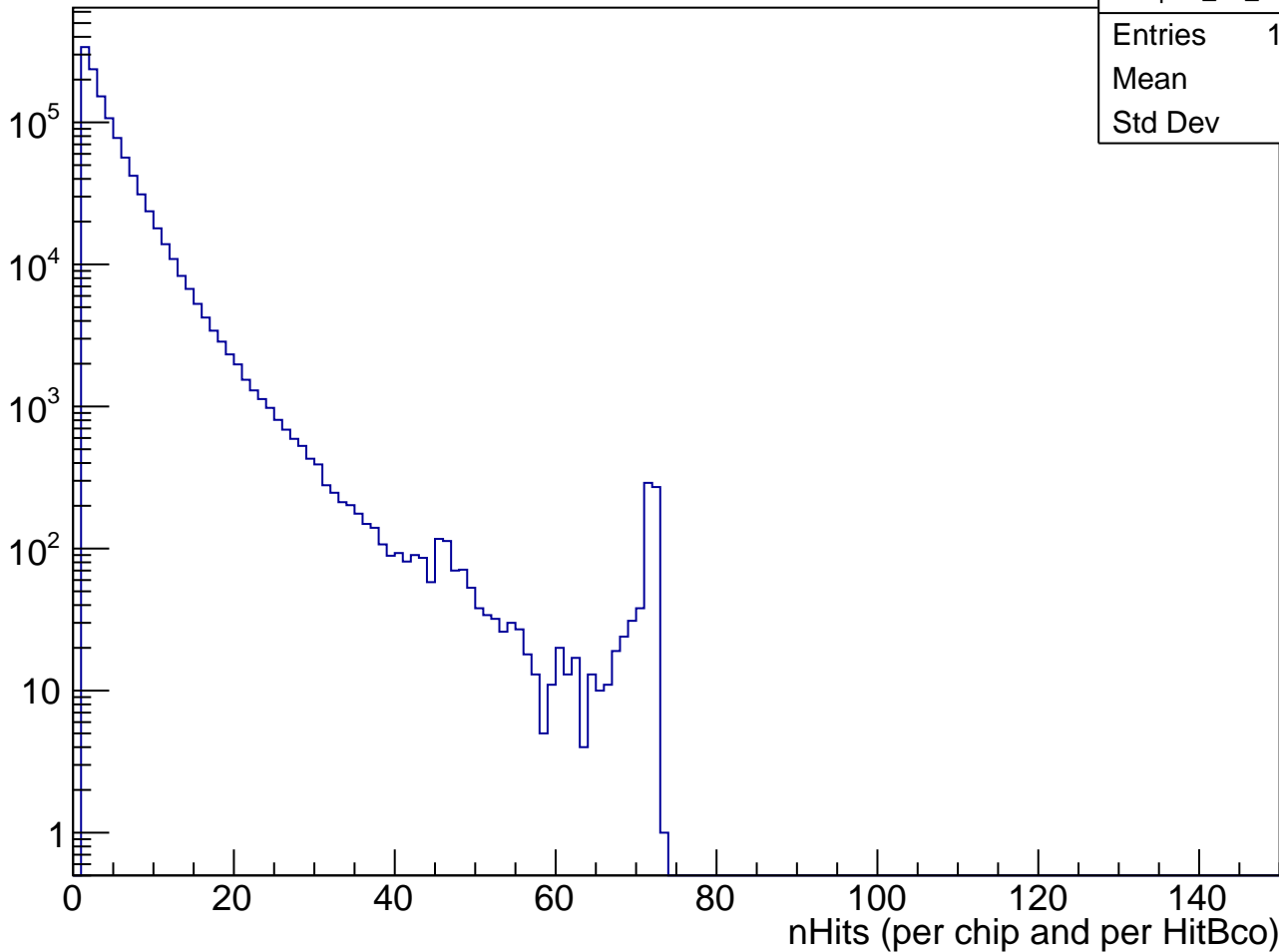


nChipHit_F2_Fch8_Chip0

Entries	1154251
Mean	3.997
Std Dev	4.571

nChipHit_F2_Fch8_Chip1

Entries

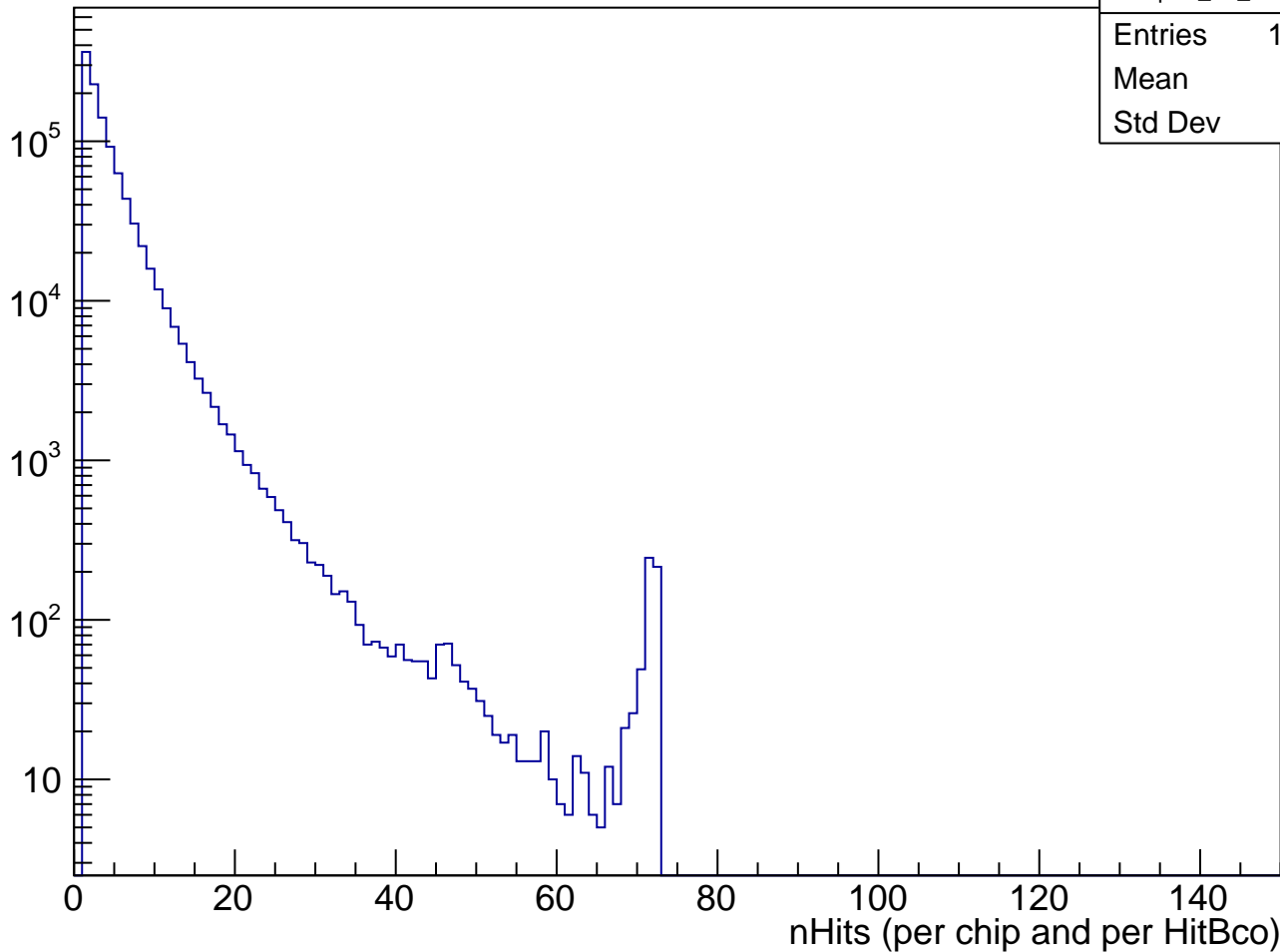


nChipHit_F2_Fch8_Chip1	
Entries	1155279
Mean	3.935
Std Dev	4.454

nHits (per chip and per HitBco)

nChipHit_F2_Fch8_Chip10

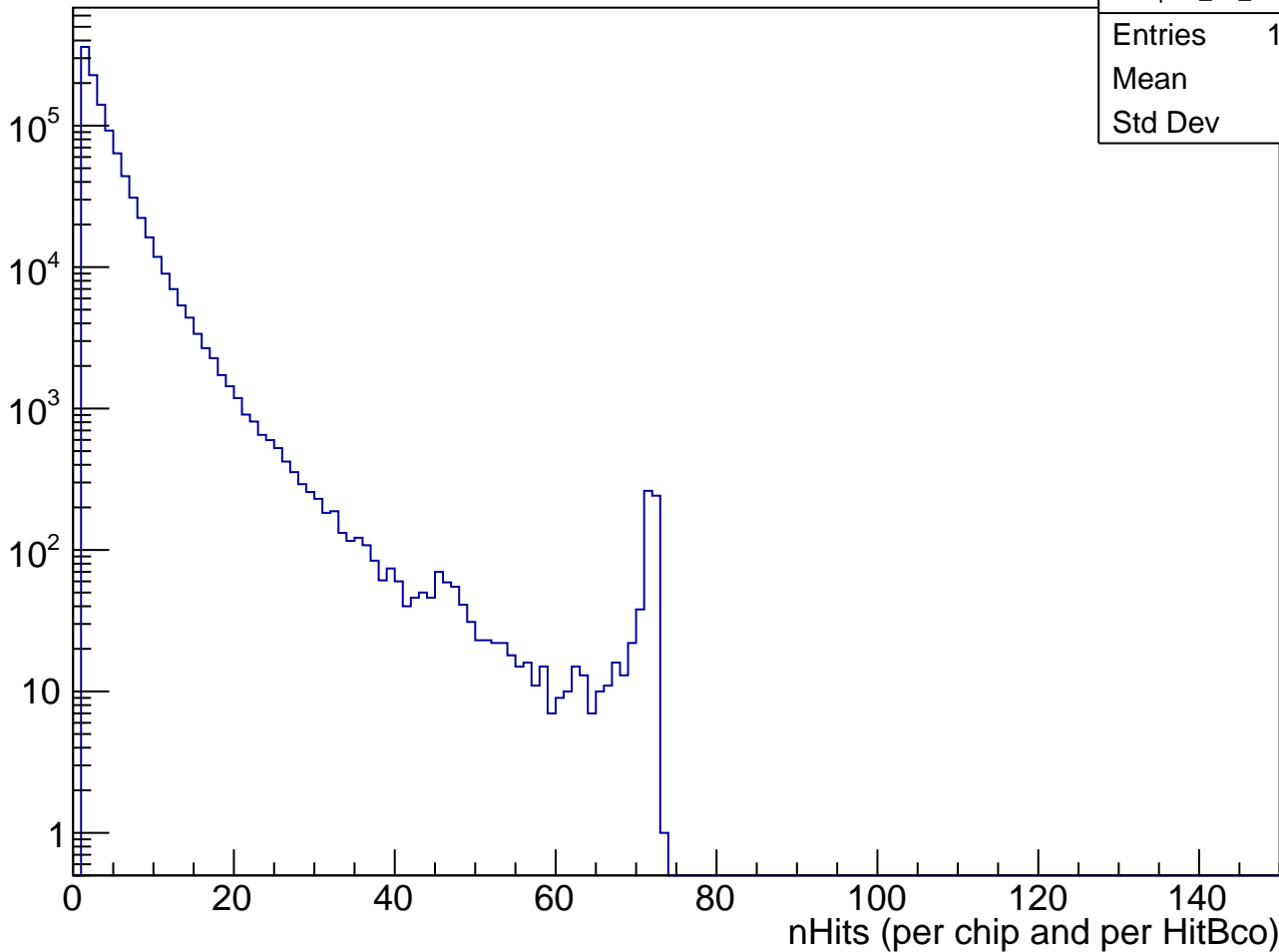
Entries



nChipHit_F2_Fch8_Chip10	
Entries	1055732
Mean	3.397
Std Dev	3.95

nChipHit_F2_Fch8_Chip11

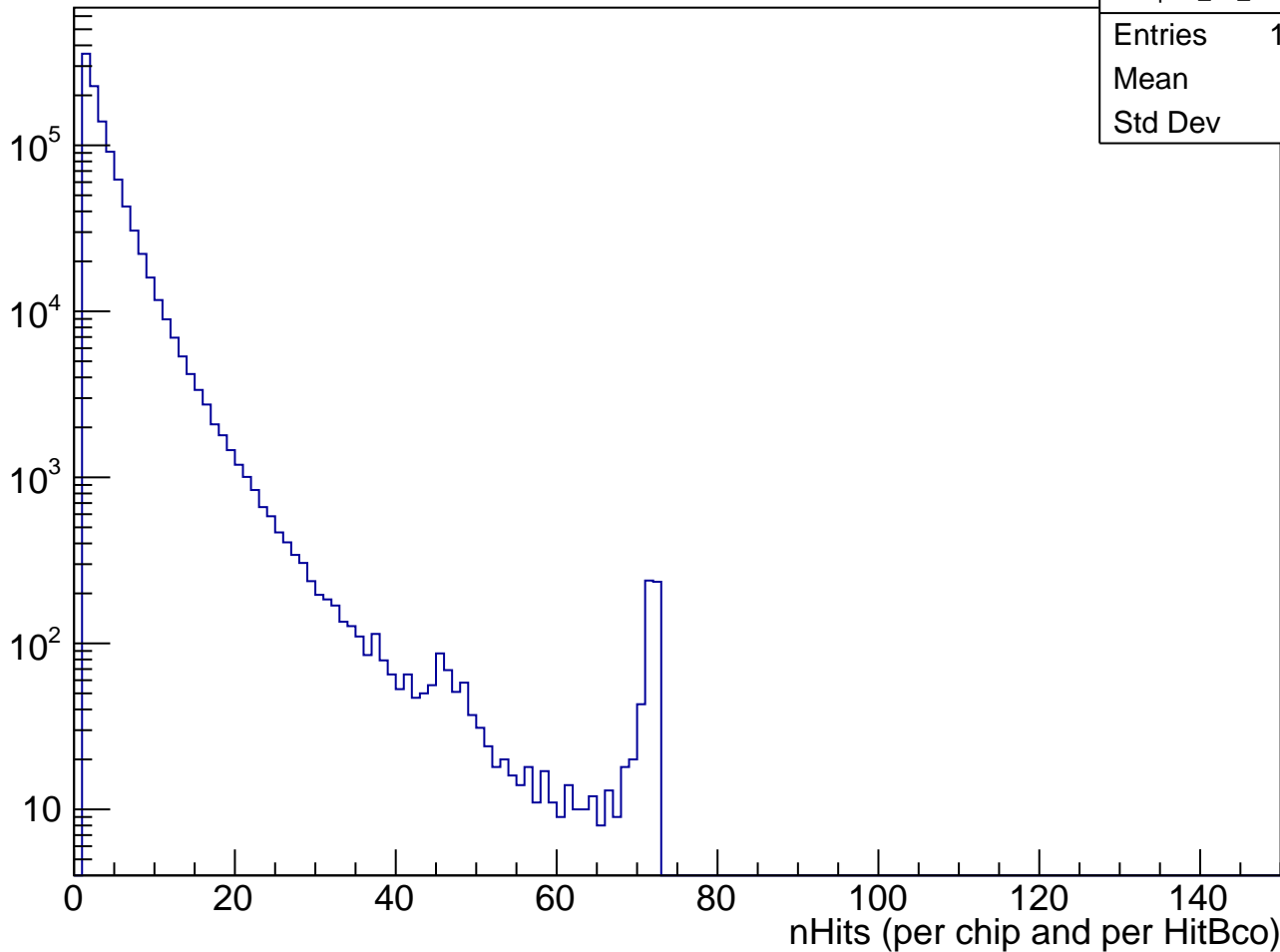
Entries



nChipHit_F2_Fch8_Chip11	
Entries	1055655
Mean	3.423
Std Dev	3.99

nChipHit_F2_Fch8_Chip12

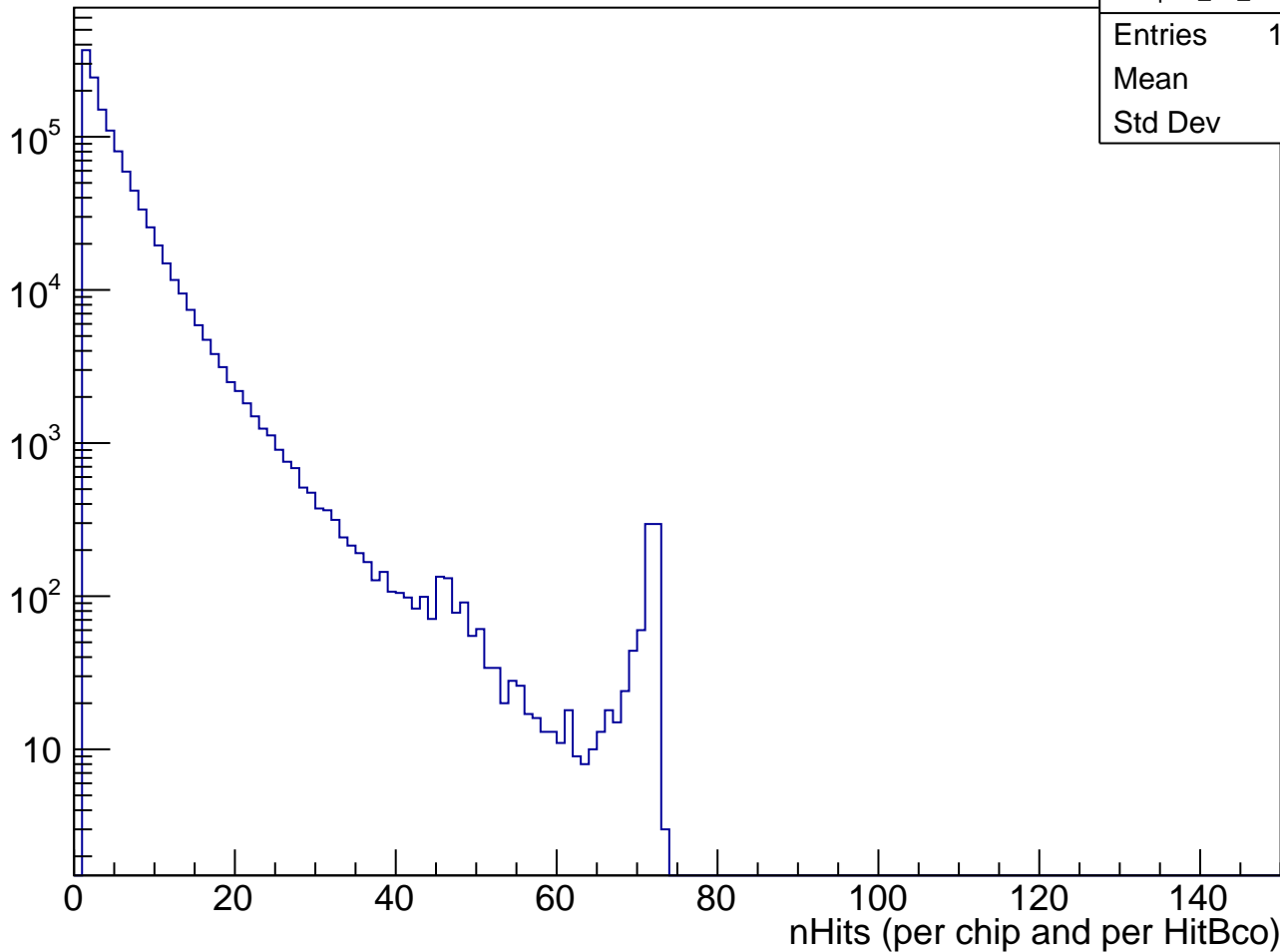
Entries



nChipHit_F2_Fch8_Chip12	
Entries	1045153
Mean	3.422
Std Dev	3.999

nChipHit_F2_Fch8_Chip13

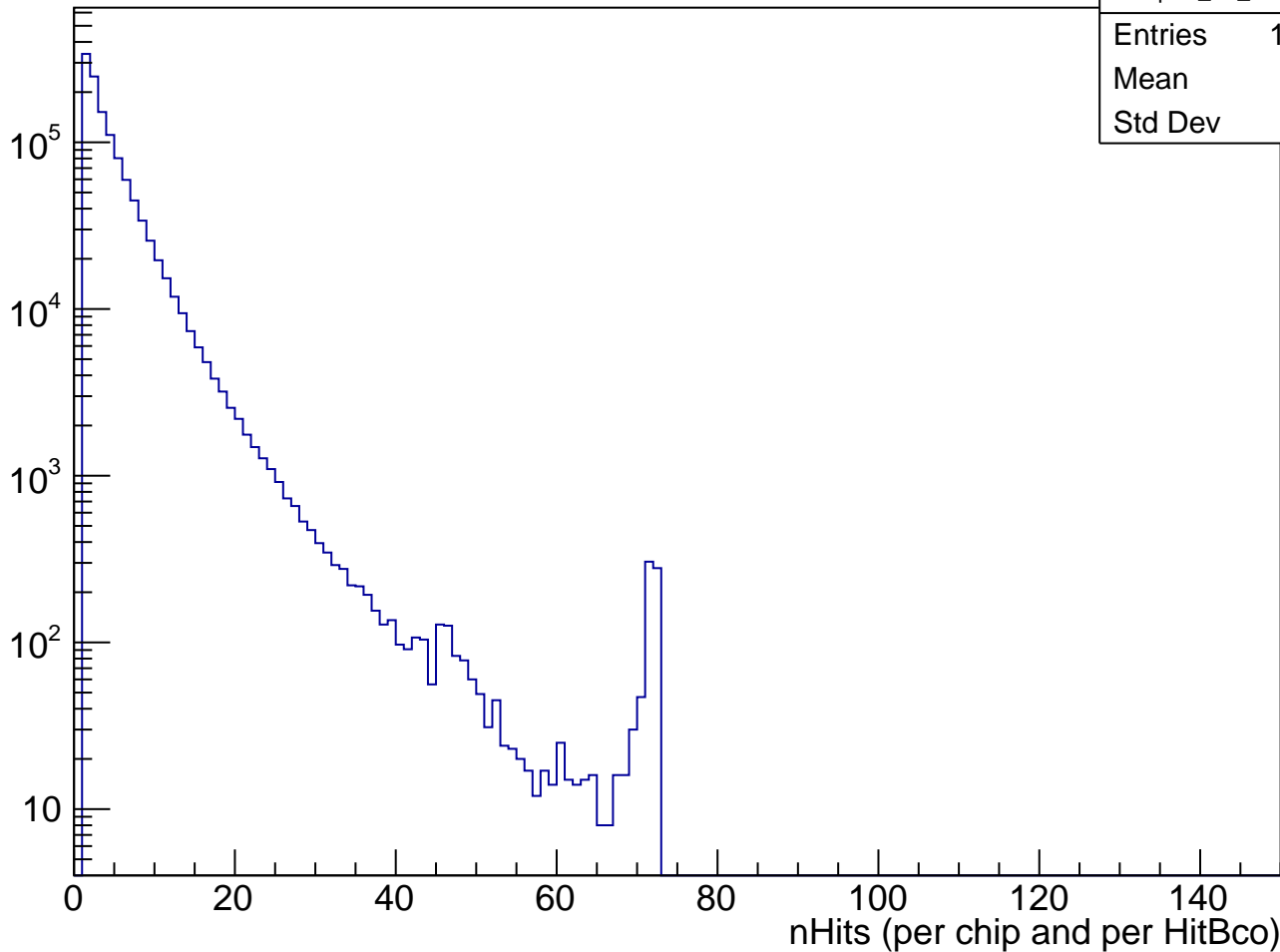
Entries



nChipHit_F2_Fch8_Chip13	
Entries	1214265
Mean	3.97
Std Dev	4.553

nChipHit_F2_Fch8_Chip14

Entries

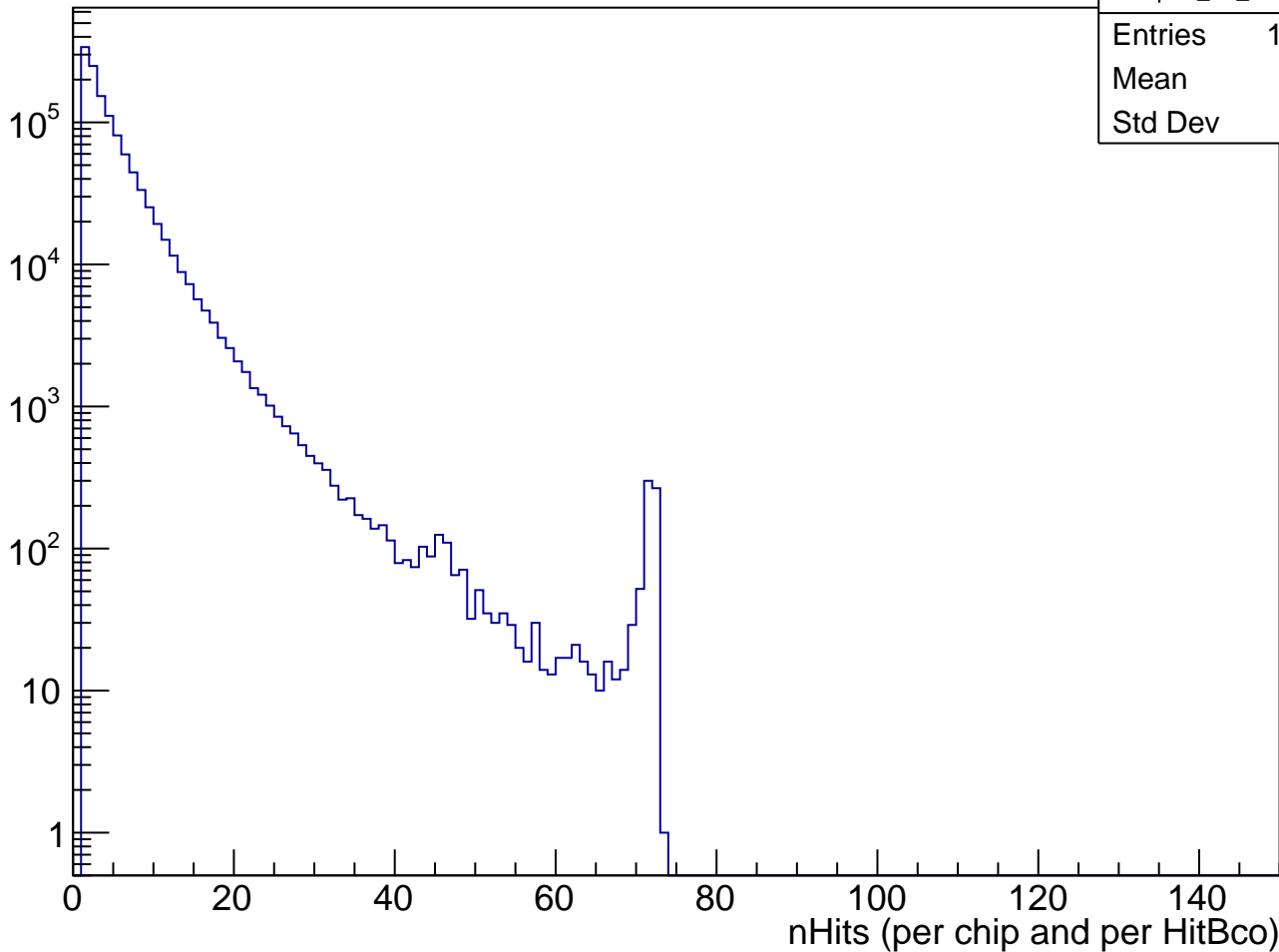


nChipHit_F2_Fch8_Chip14	
Entries	1193223
Mean	4.042
Std Dev	4.567

nHits (per chip and per HitBco)

nChipHit_F2_Fch8_Chip15

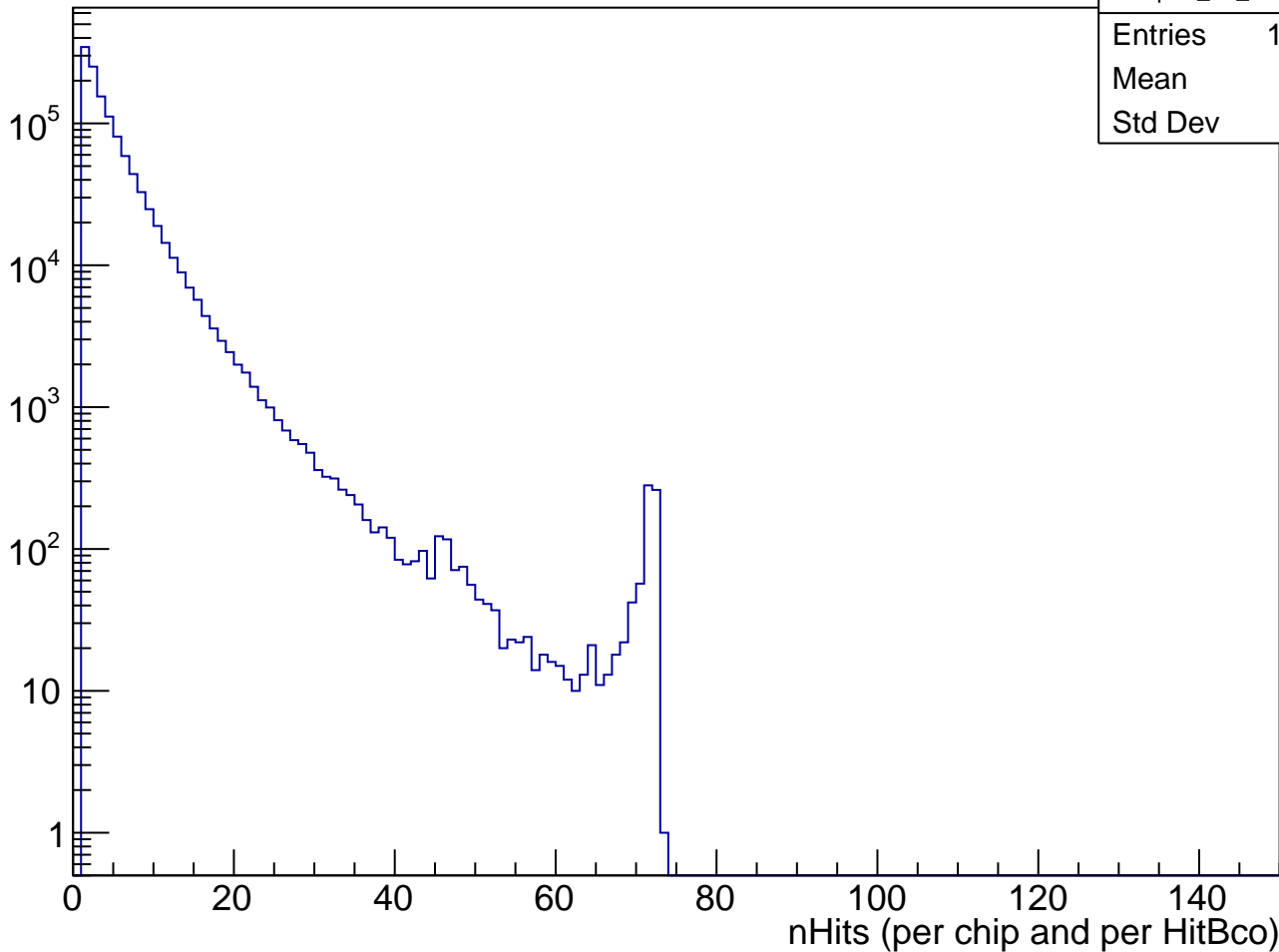
Entries



nChipHit_F2_Fch8_Chip15	
Entries	1192855
Mean	4.006
Std Dev	4.507

nChipHit_F2_Fch8_Chip16

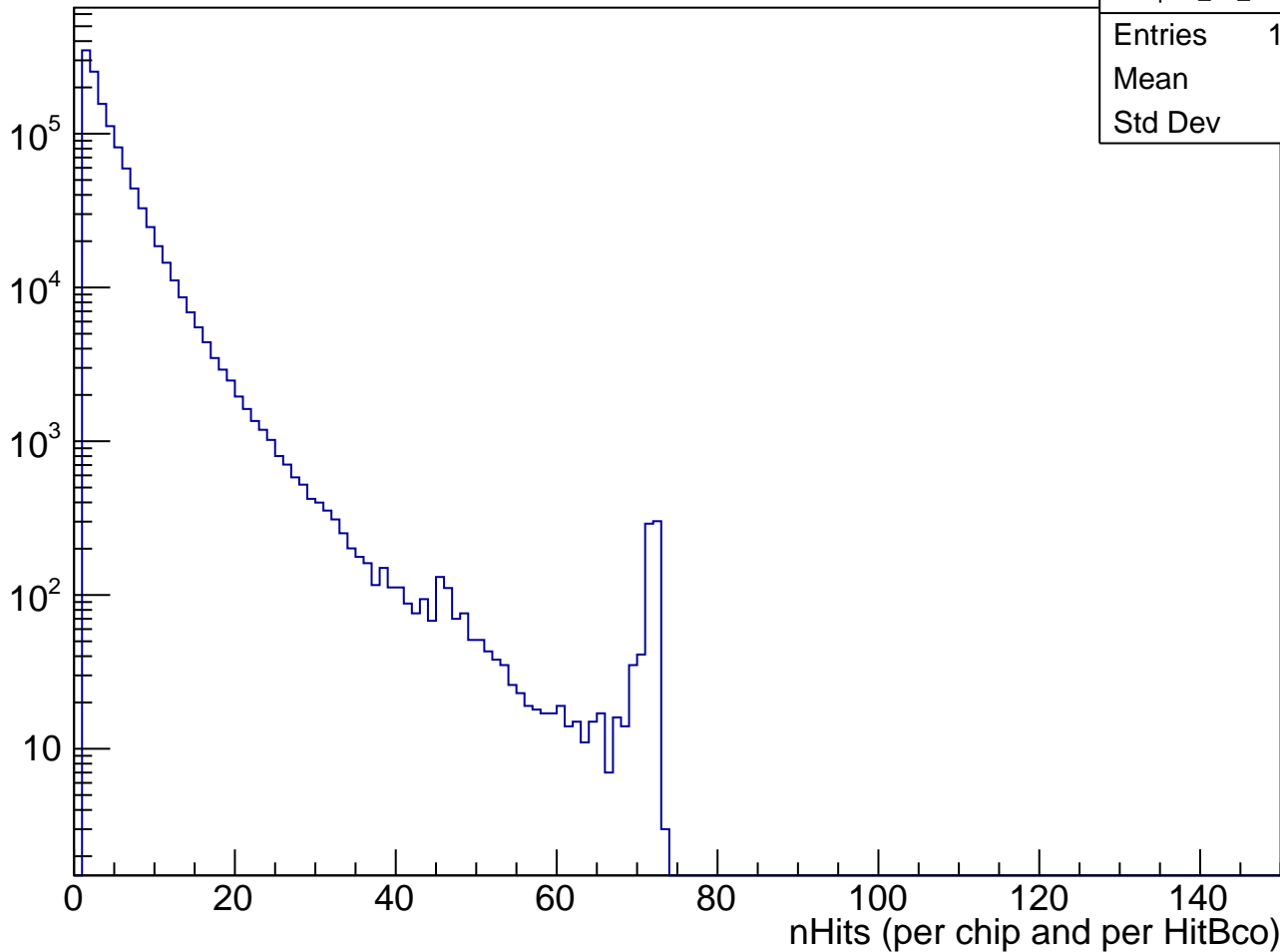
Entries



nChipHit_F2_Fch8_Chip16	
Entries	1198319
Mean	3.958
Std Dev	4.472

nChipHit_F2_Fch8_Chip17

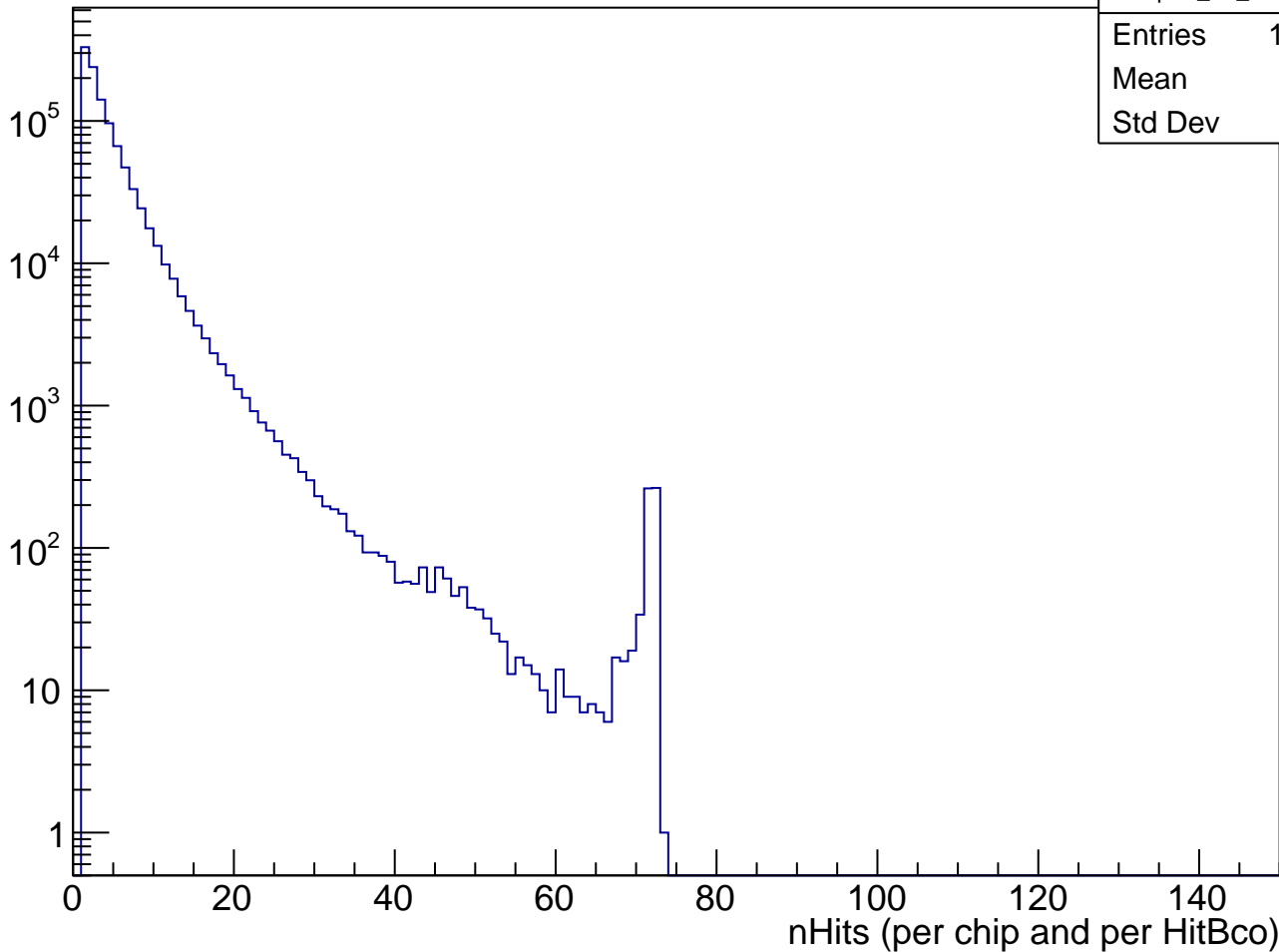
Entries



nChipHit_F2_Fch8_Chip17	
Entries	1205347
Mean	3.939
Std Dev	4.465

nChipHit_F2_Fch8_Chip18

Entries

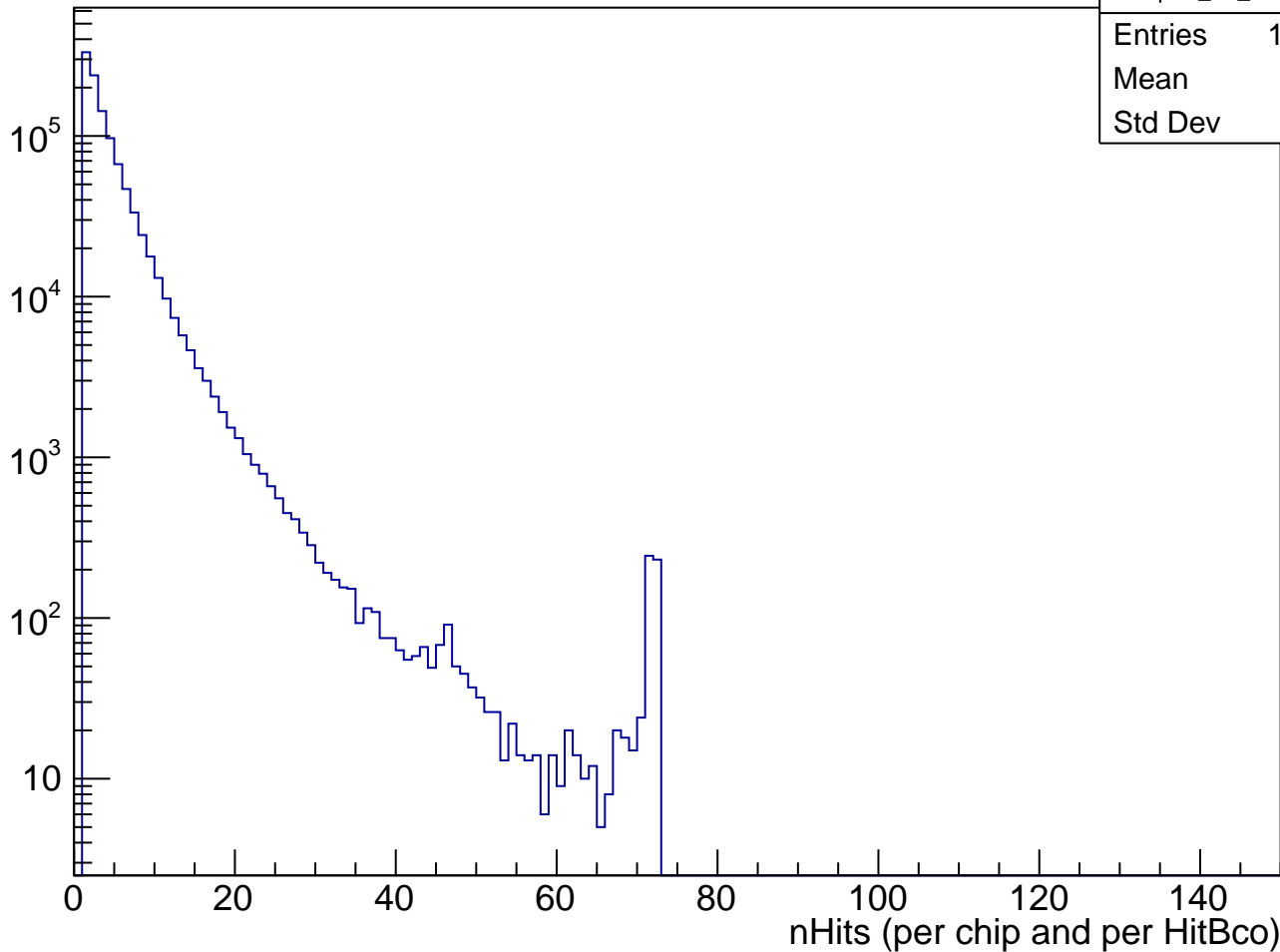


nChipHit_F2_Fch8_Chip18	
Entries	1058970
Mean	3.578
Std Dev	4.102

nHits (per chip and per HitBco)

nChipHit_F2_Fch8_Chip19

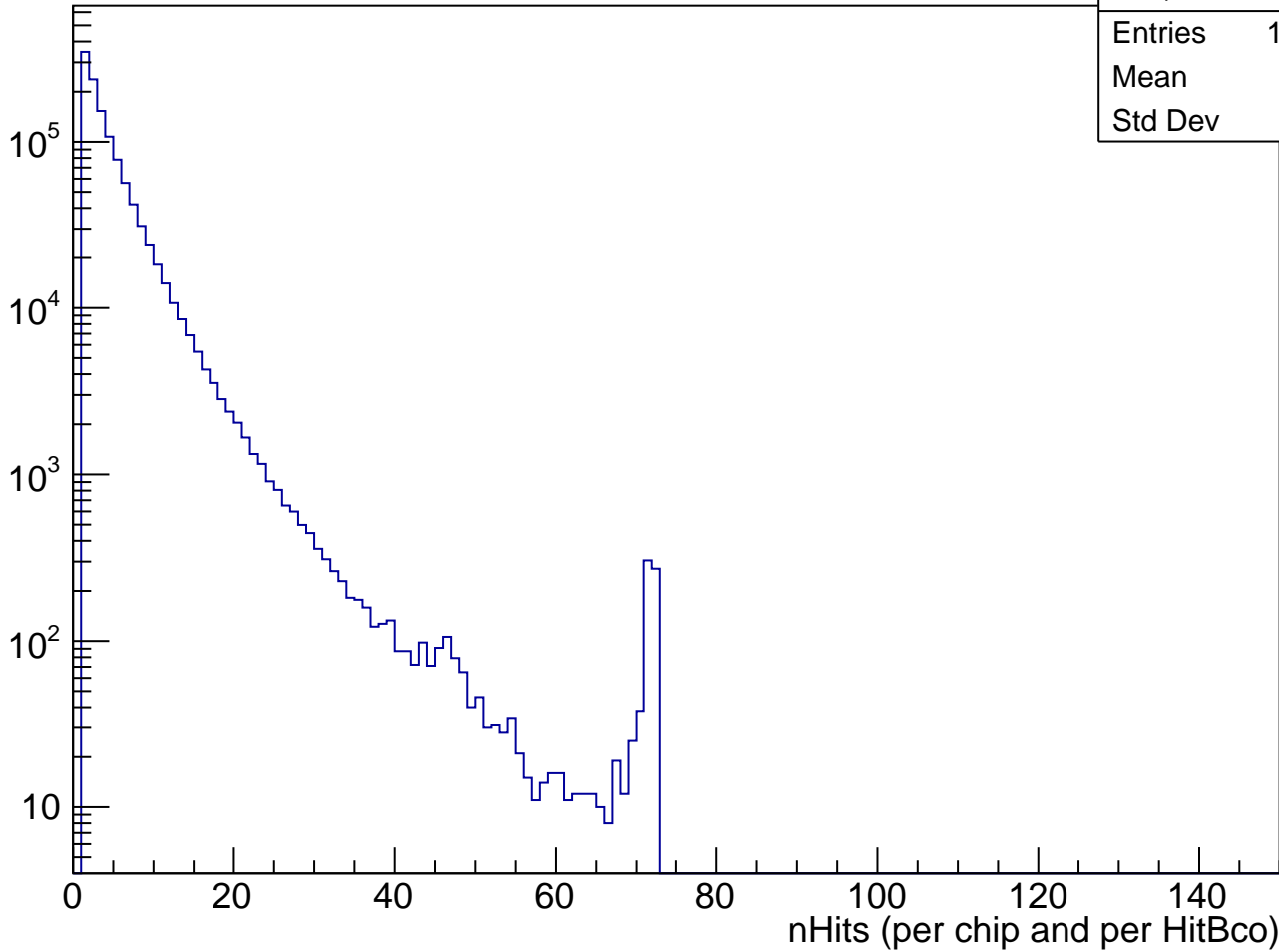
Entries



nChipHit_F2_Fch8_Chip19	
Entries	1061456
Mean	3.561
Std Dev	4.057

nChipHit_F2_Fch8_Chip2

Entries



nChipHit_F2_Fch8_Chip2

Entries 1167224

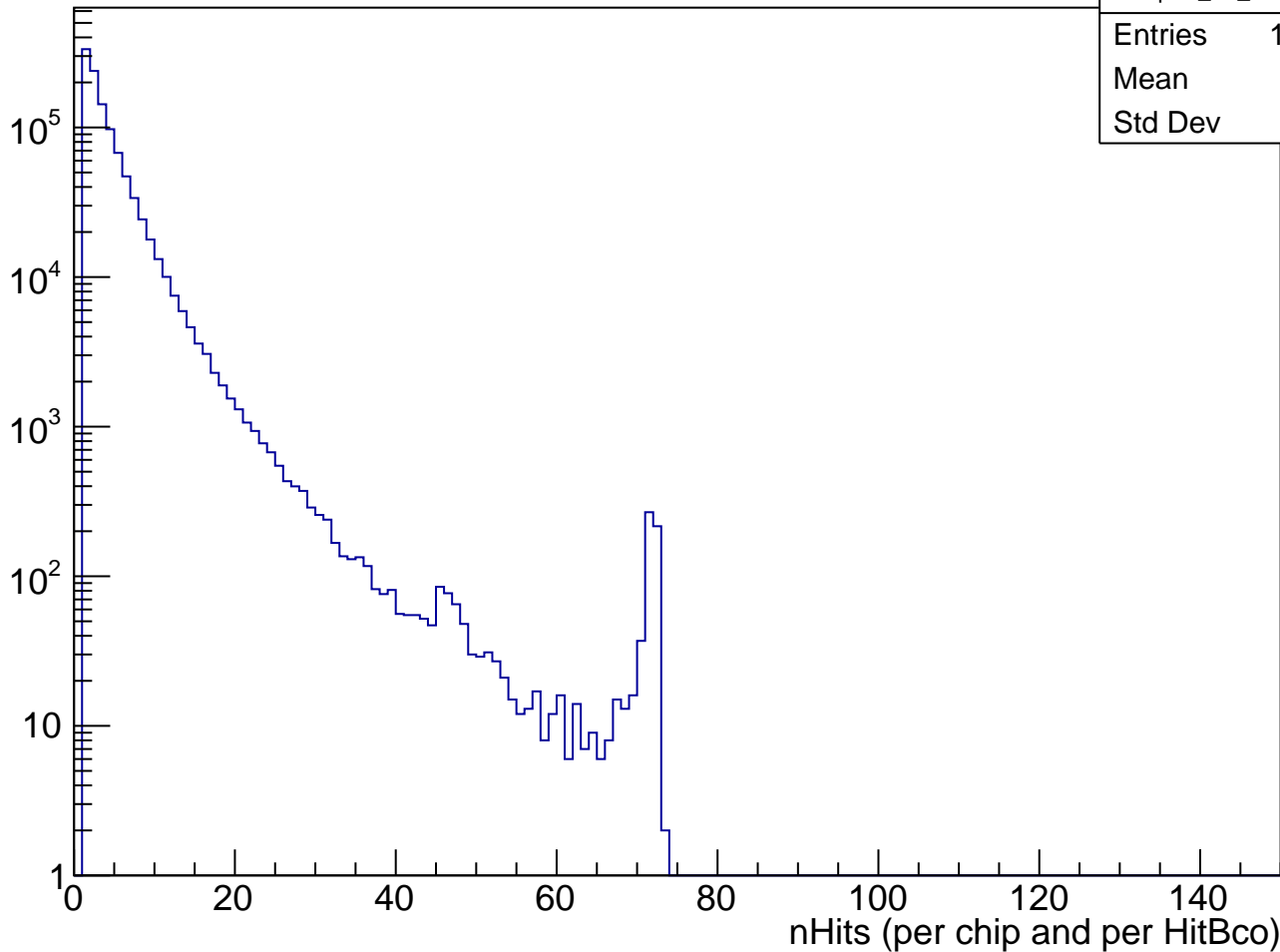
Mean 3.925

Std Dev 4.452

nHits (per chip and per HitBco)

nChipHit_F2_Fch8_Chip20

Entries

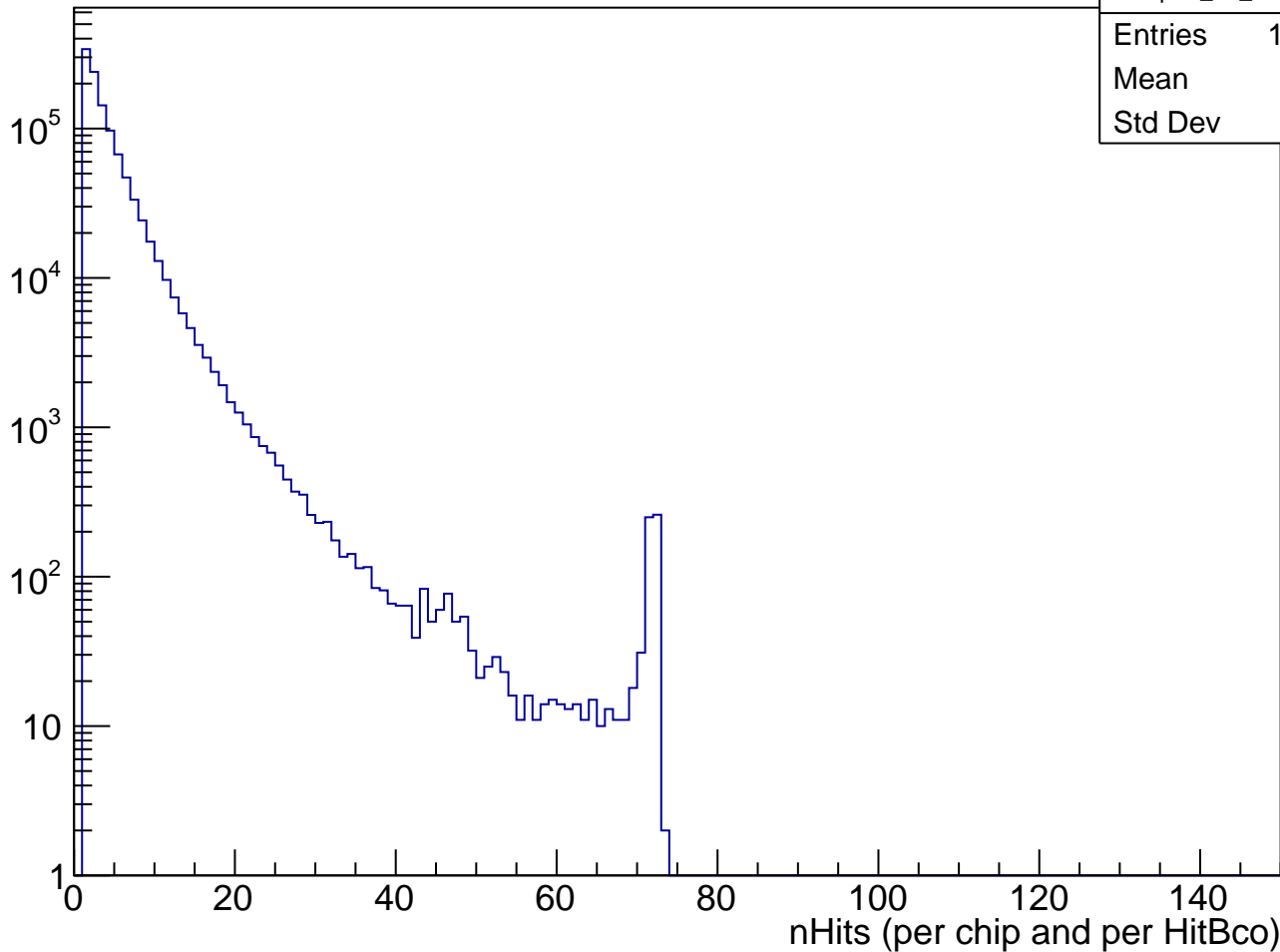


nChipHit_F2_Fch8_Chip20	
Entries	1066640
Mean	3.567
Std Dev	4.064

nHits (per chip and per HitBco)

nChipHit_F2_Fch8_Chip21

Entries

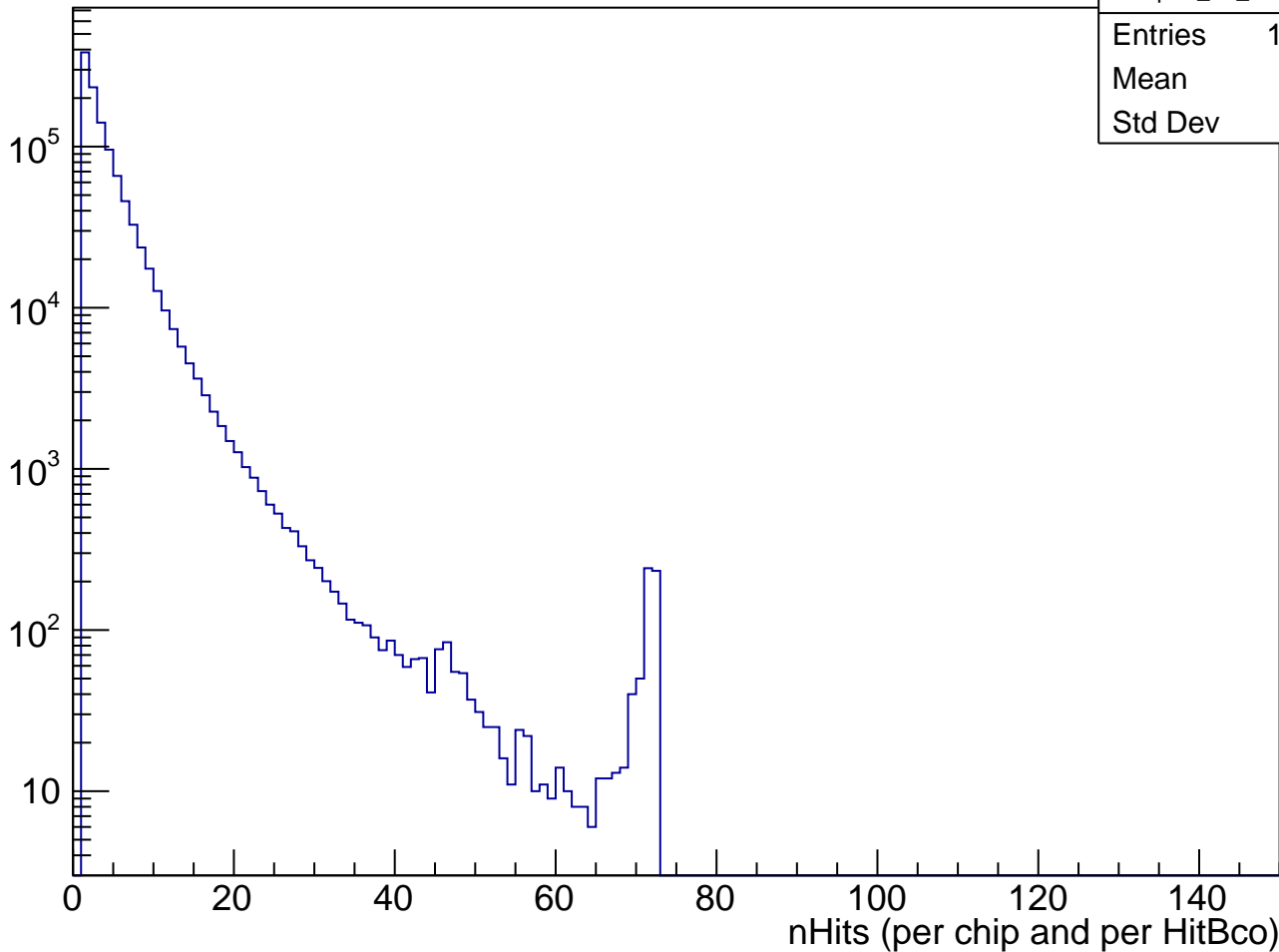


nChipHit_F2_Fch8_Chip21	
Entries	1071349
Mean	3.536
Std Dev	4.055

nHits (per chip and per HitBco)

nChipHit_F2_Fch8_Chip22

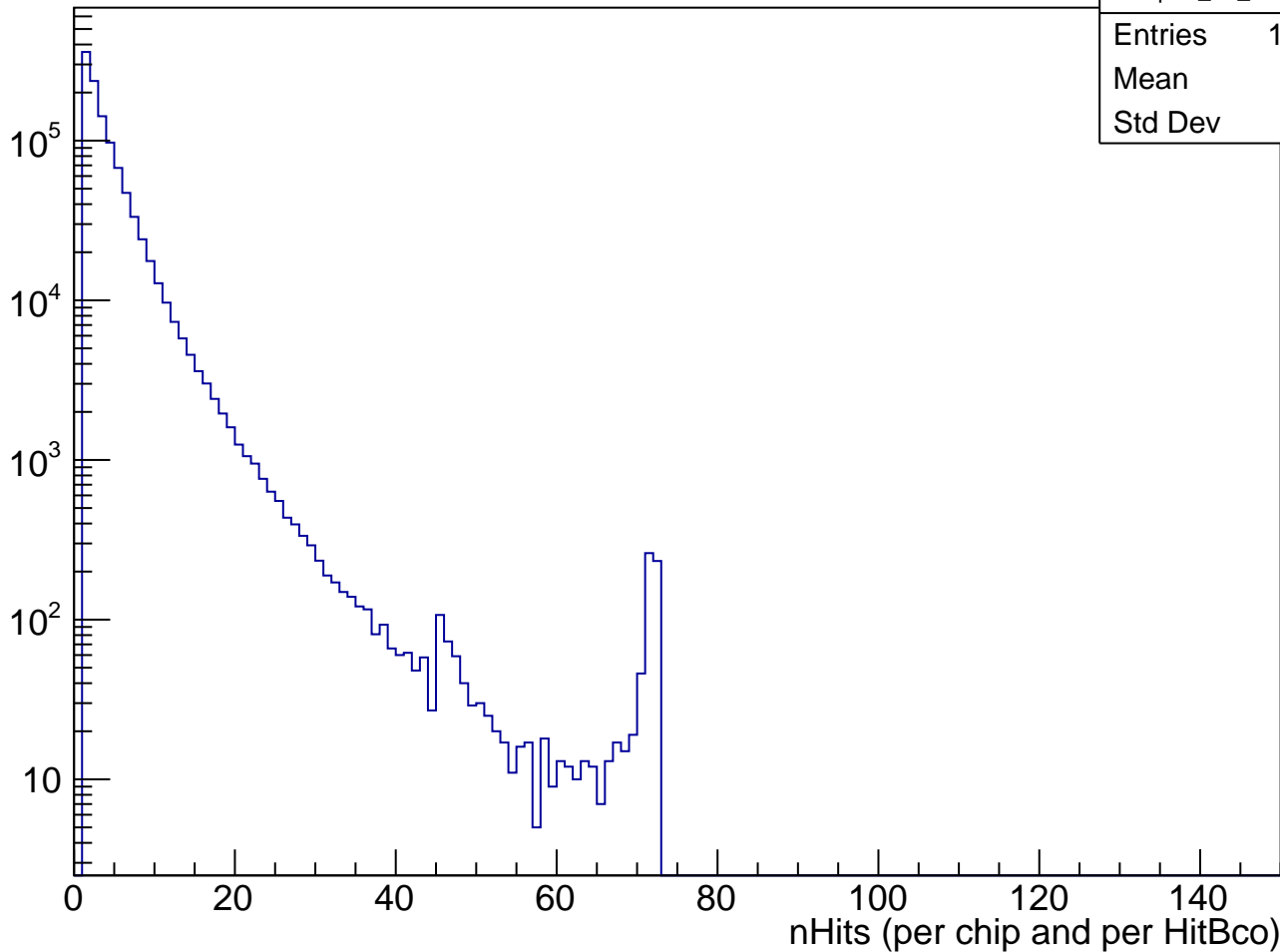
Entries



nChipHit_F2_Fch8_Chip22	
Entries	1101176
Mean	3.428
Std Dev	4.013

nChipHit_F2_Fch8_Chip23

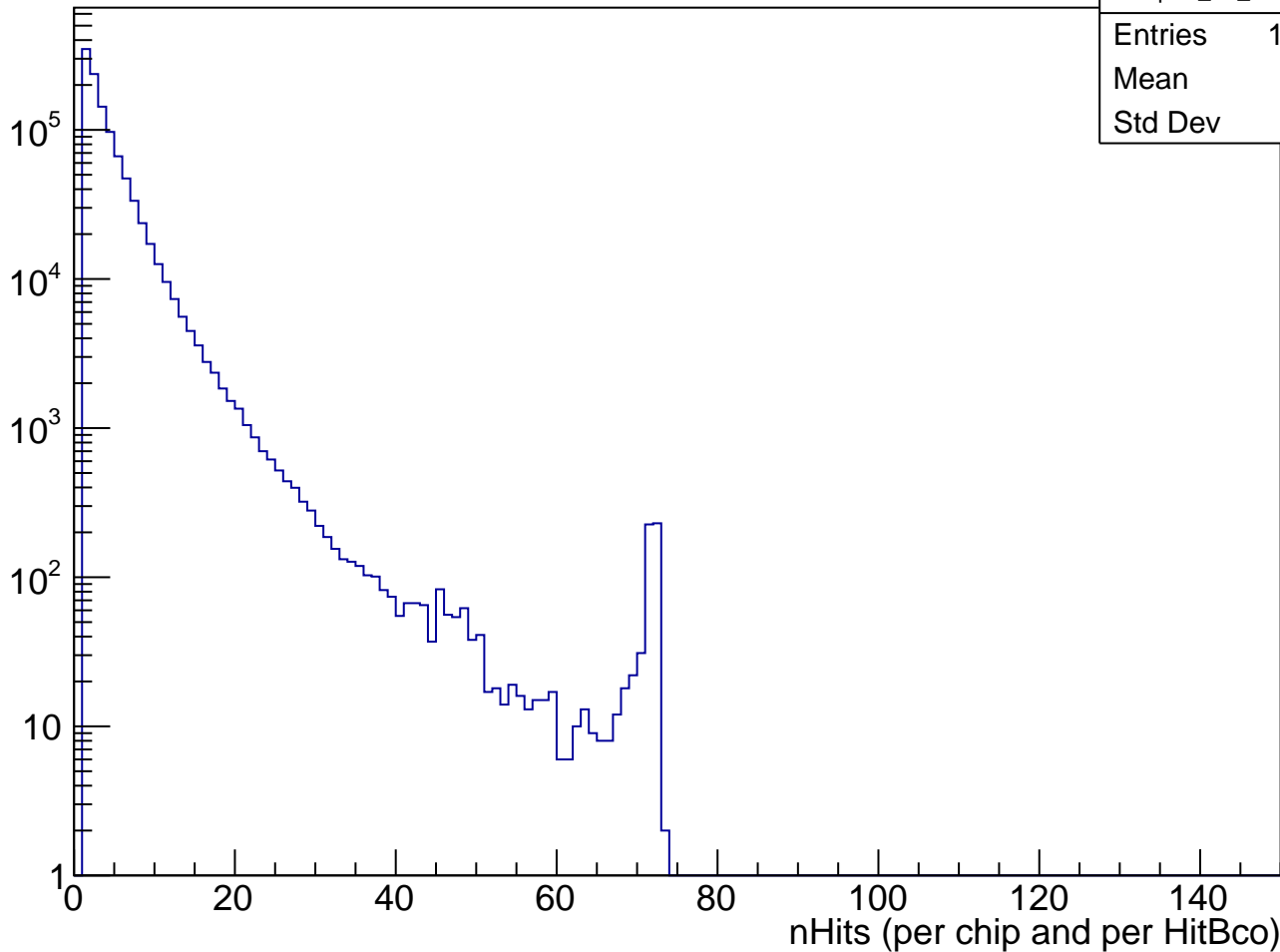
Entries



nChipHit_F2_Fch8_Chip23	
Entries	1086579
Mean	3.499
Std Dev	4.04

nChipHit_F2_Fch8_Chip24

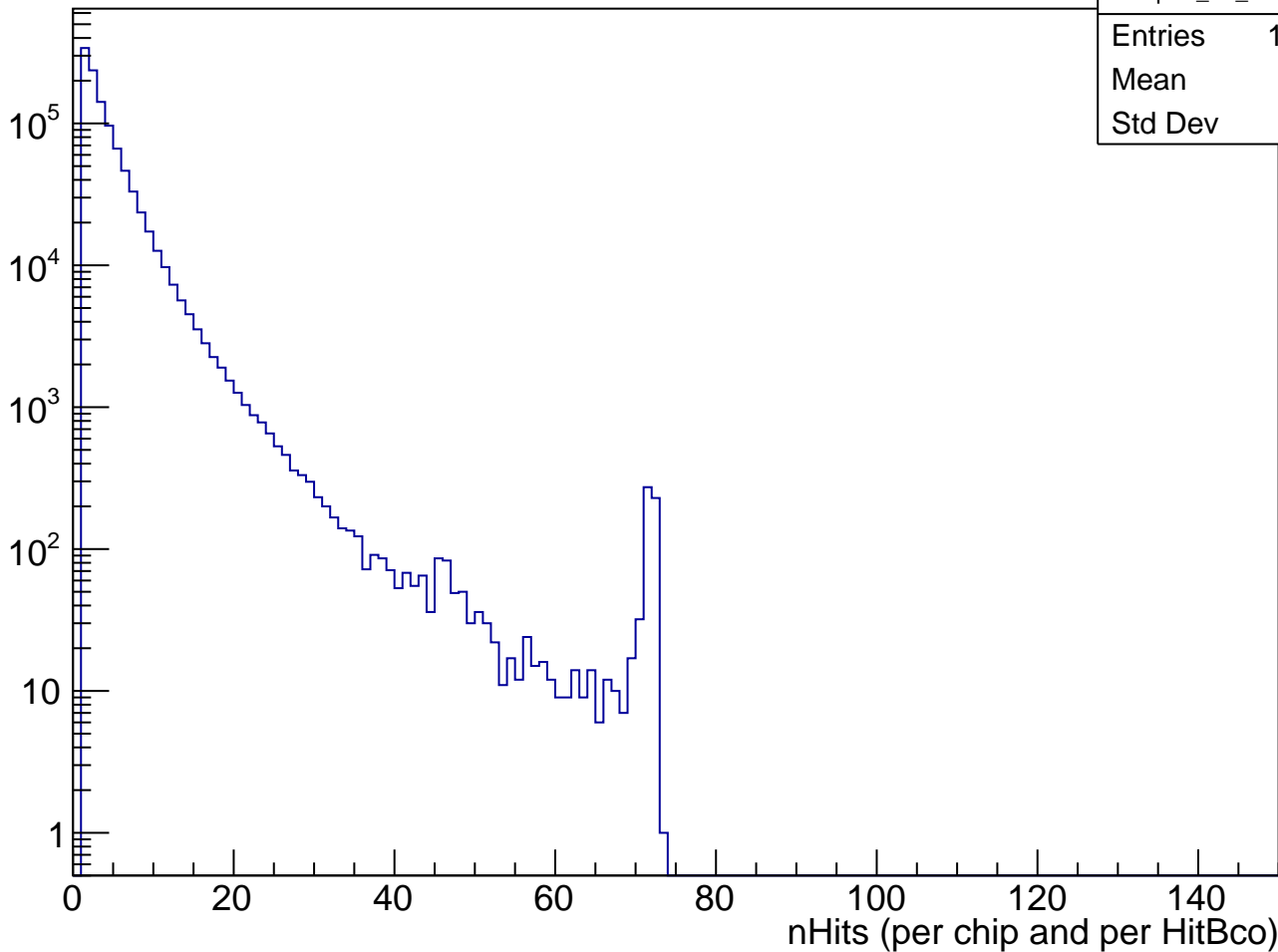
Entries



nChipHit_F2_Fch8_Chip24	
Entries	1074165
Mean	3.501
Std Dev	4.001

nChipHit_F2_Fch8_Chip25

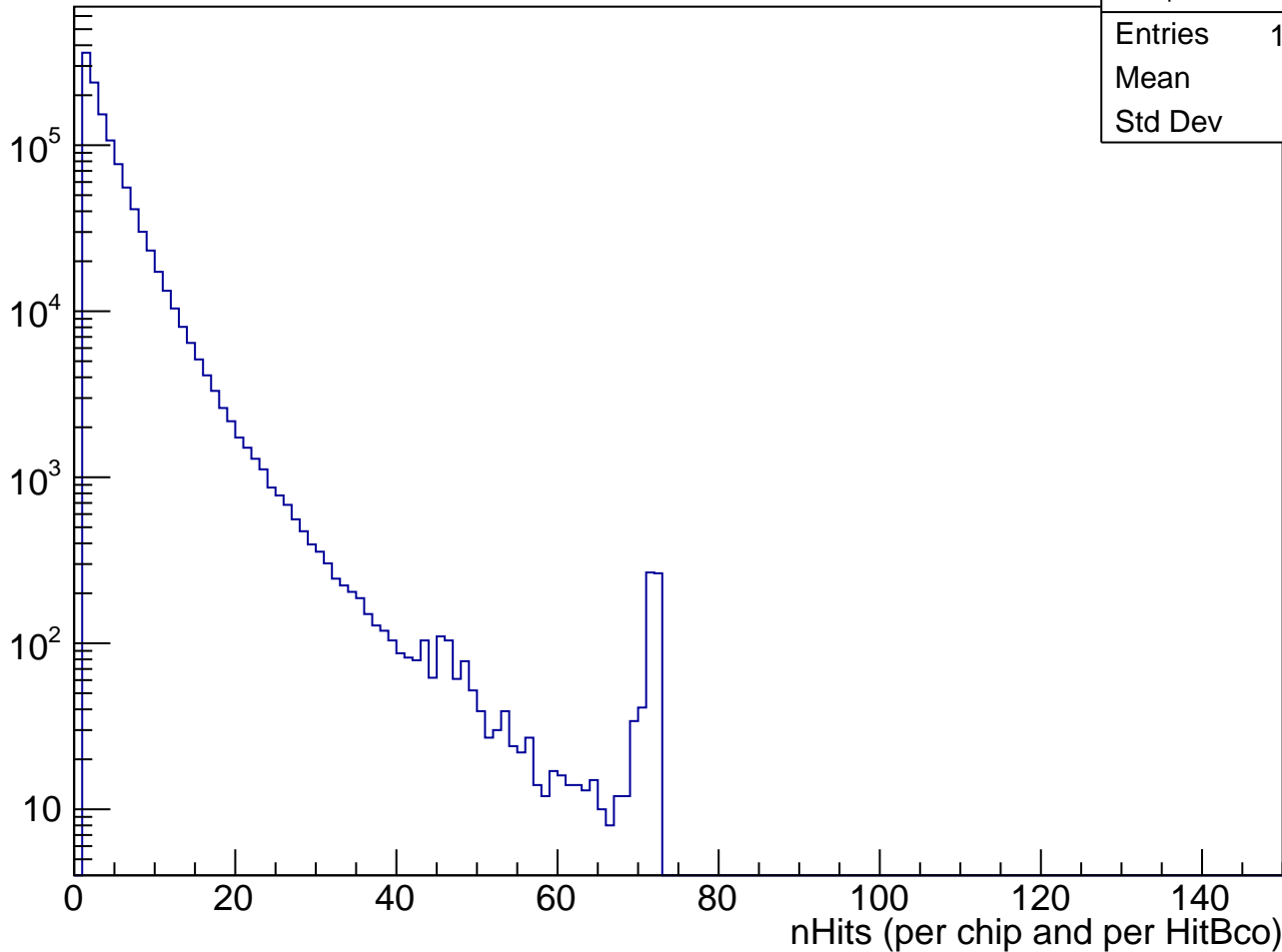
Entries



nChipHit_F2_Fch8_Chip25	
Entries	1062730
Mean	3.525
Std Dev	4.044

nChipHit_F2_Fch8_Chip3

Entries



nChipHit_F2_Fch8_Chip3

Entries 1172272

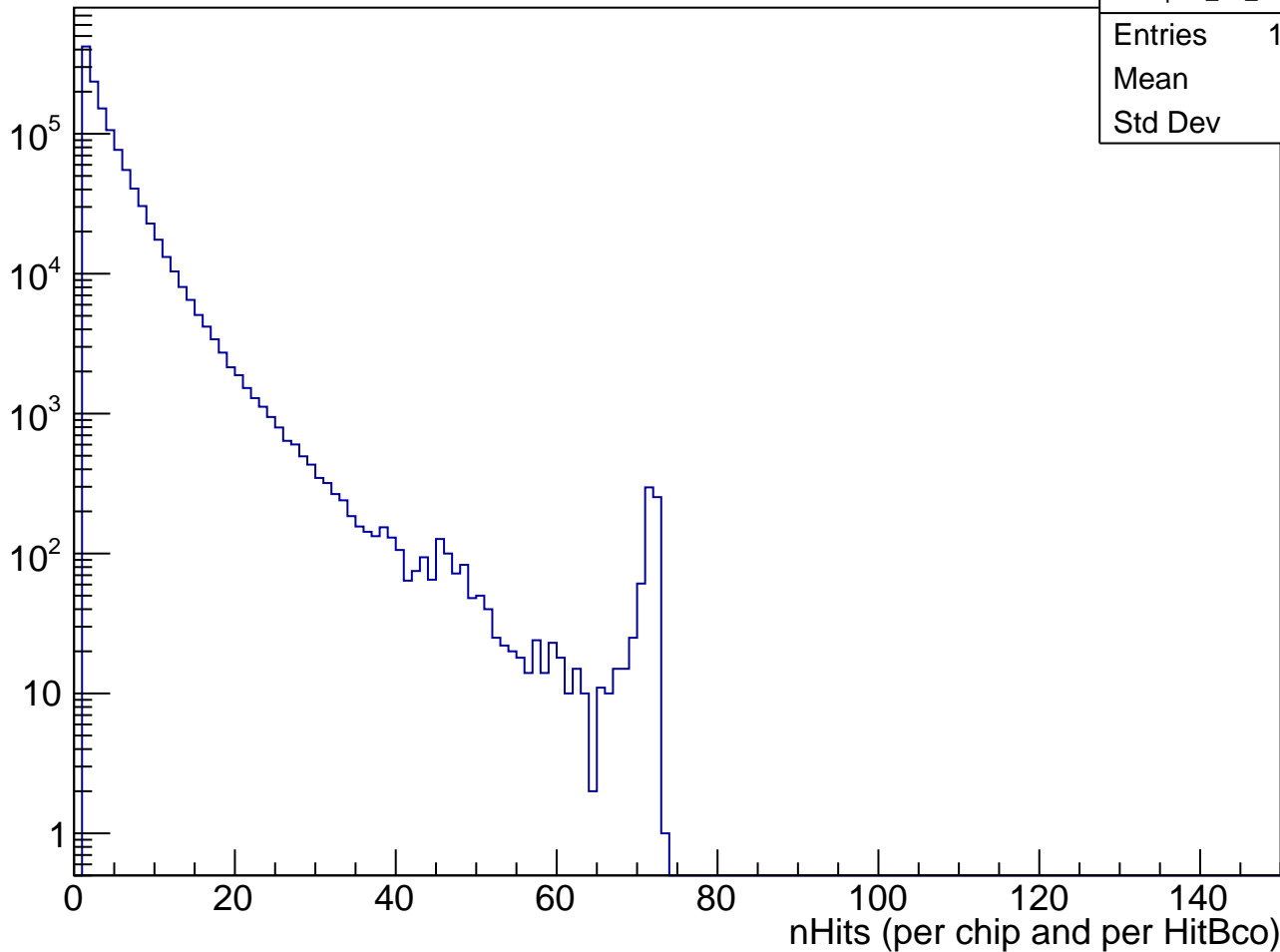
Mean 3.832

Std Dev 4.377

nHits (per chip and per HitBco)

nChipHit_F2_Fch8_Chip4

Entries



nChipHit_F2_Fch8_Chip4

Entries 1227589

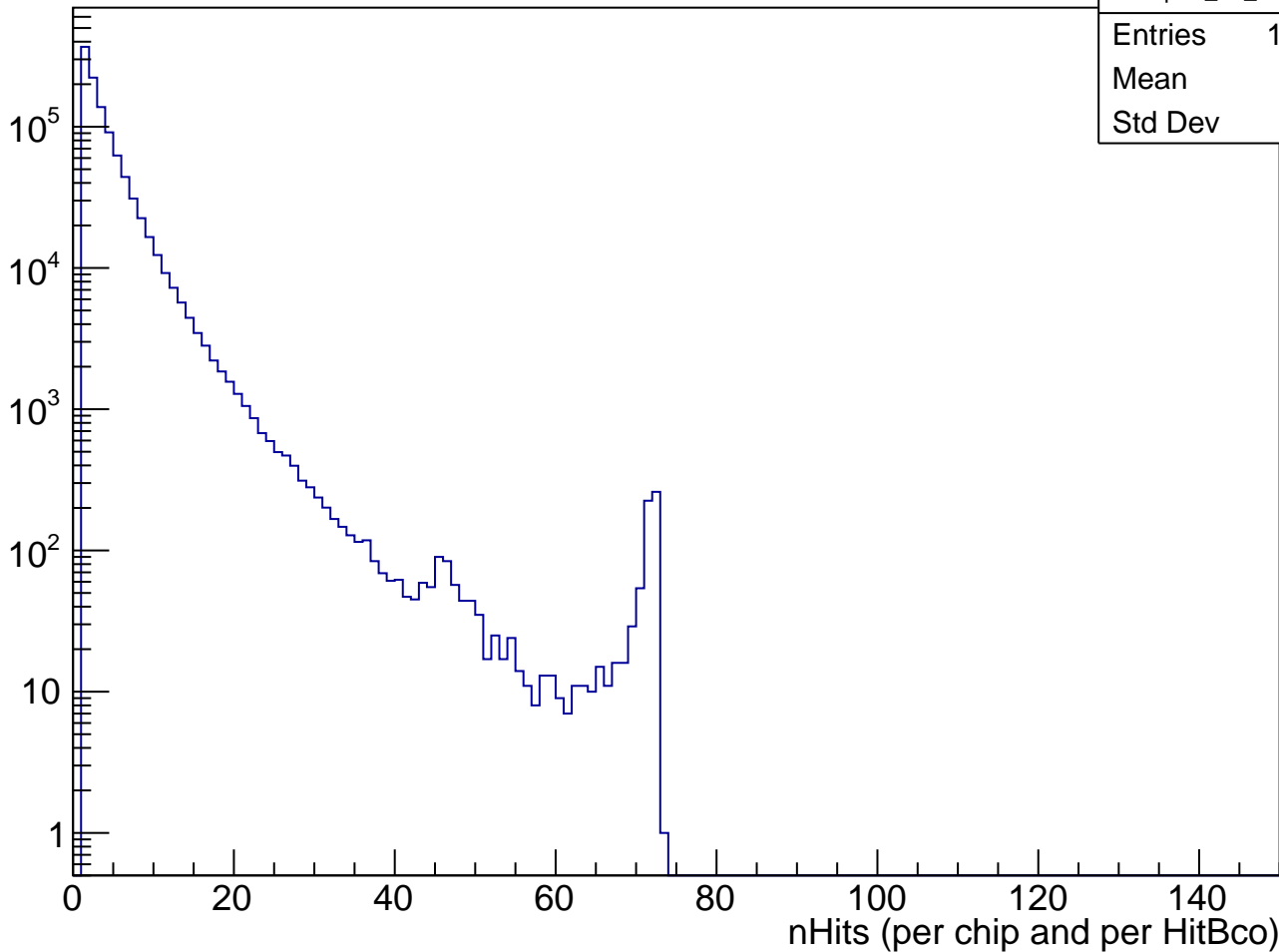
Mean 3.706

Std Dev 4.353

nHits (per chip and per HitBco)

nChipHit_F2_Fch8_Chip5

Entries



nChipHit_F2_Fch8_Chip5

Entries 1056117

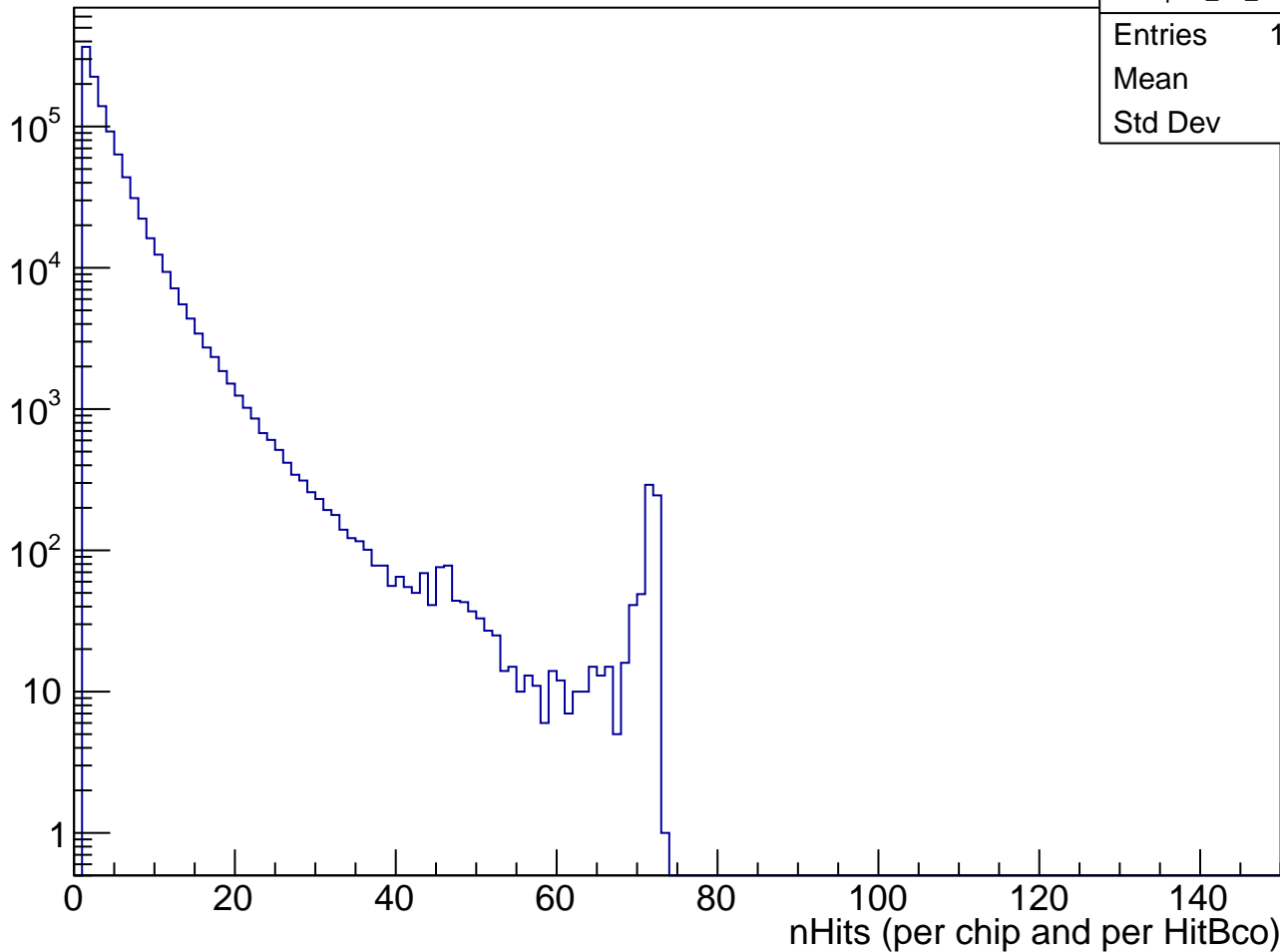
Mean 3.443

Std Dev 4.06

nHits (per chip and per HitBco)

nChipHit_F2_Fch8_Chip6

Entries



nChipHit_F2_Fch8_Chip6

Entries 1059243

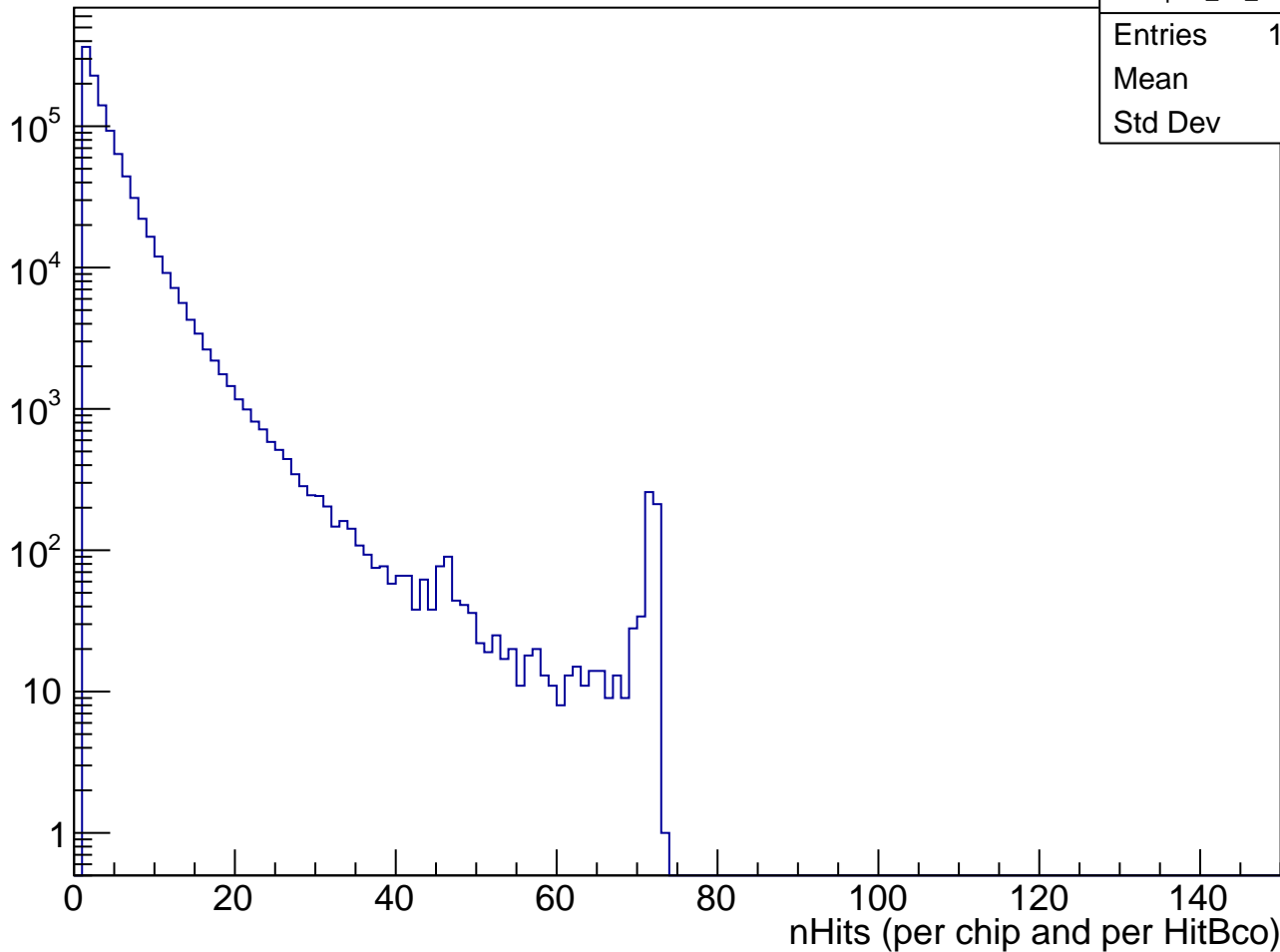
Mean 3.436

Std Dev 4.053

nHits (per chip and per HitBco)

nChipHit_F2_Fch8_Chip7

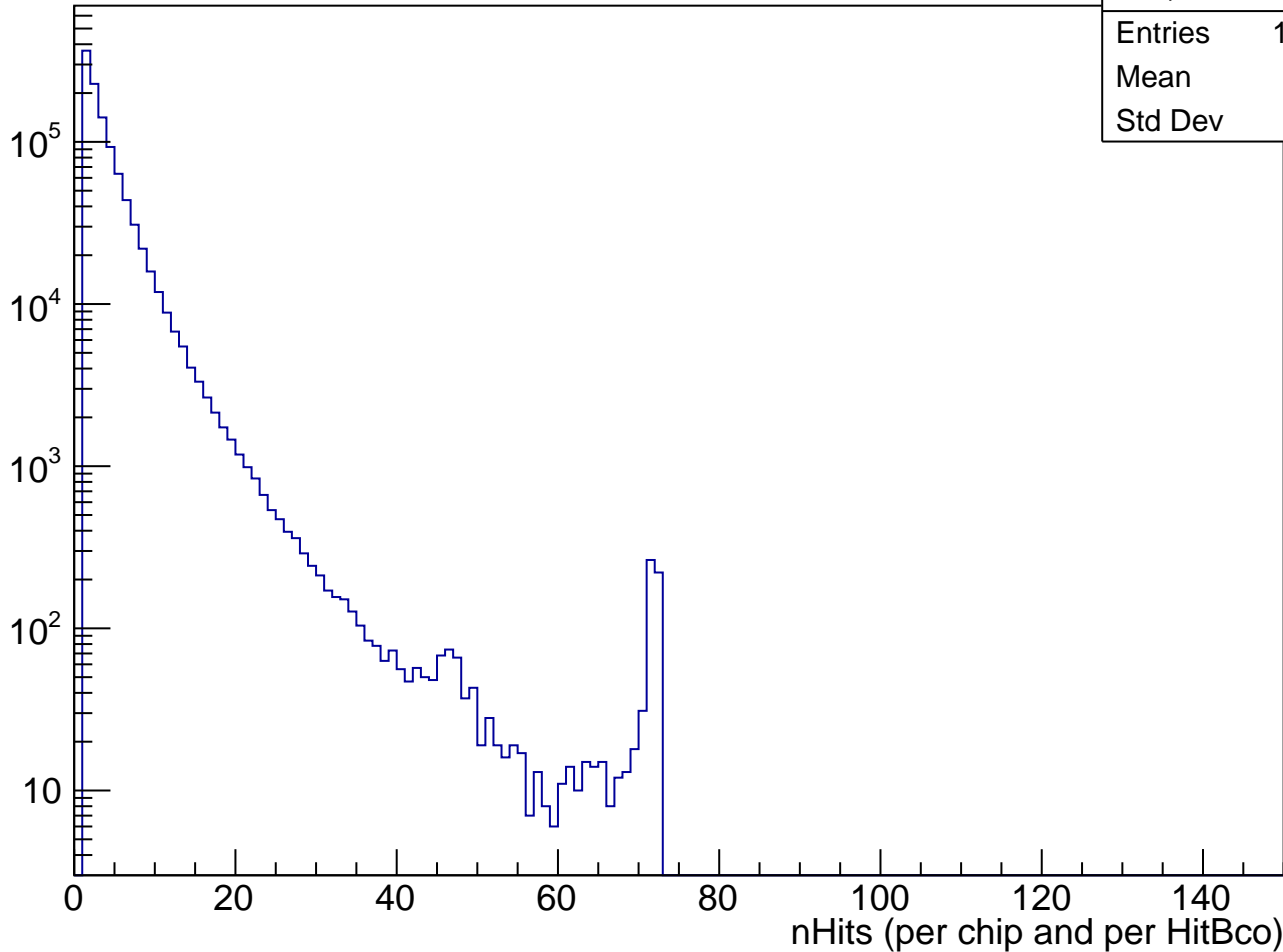
Entries



nChipHit_F2_Fch8_Chip7	
Entries	1061857
Mean	3.421
Std Dev	3.986

nChipHit_F2_Fch8_Chip8

Entries



nChipHit_F2_Fch8_Chip8

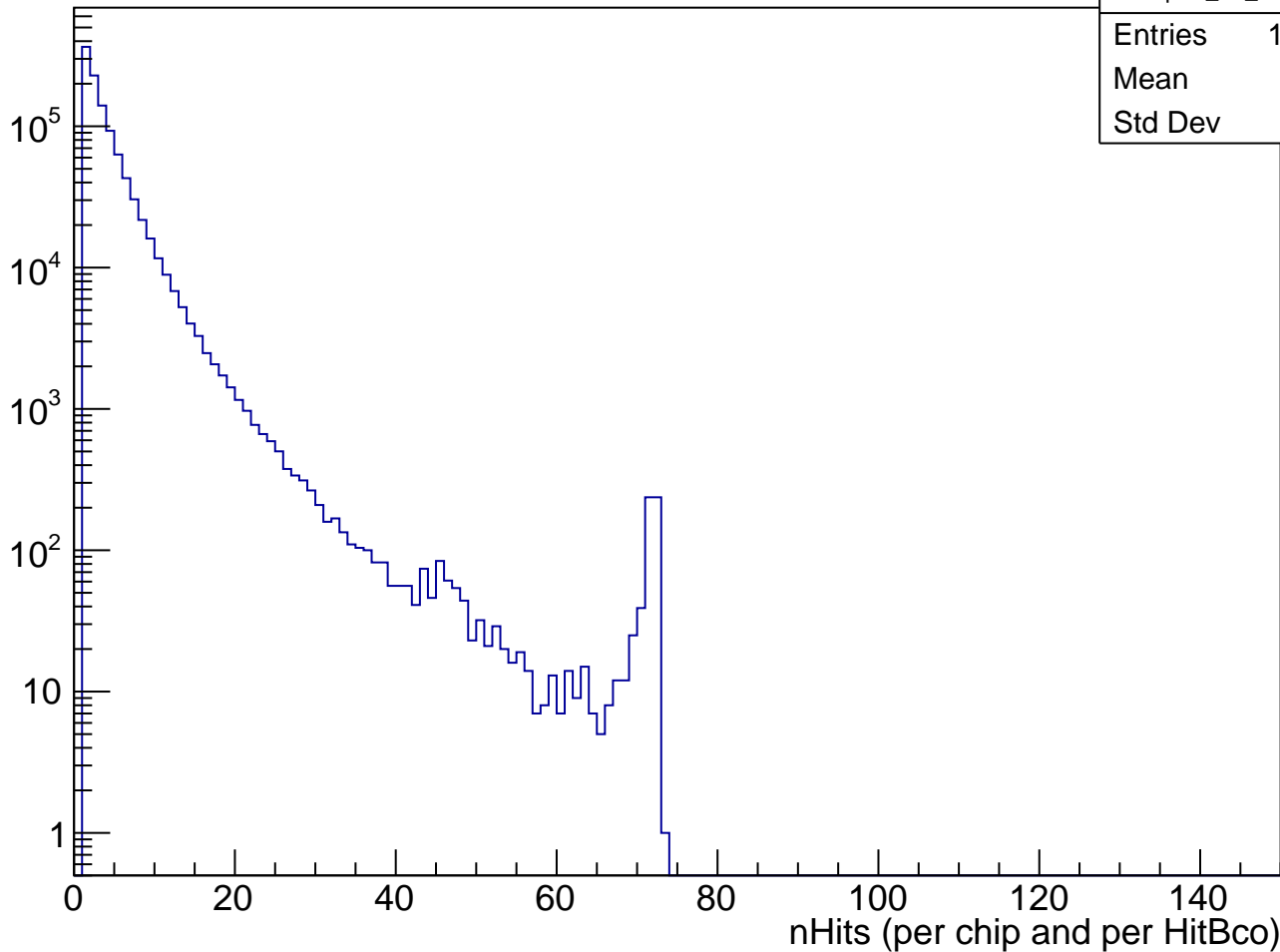
Entries 1060363

Mean 3.396

Std Dev 3.951

nChipHit_F2_Fch8_Chip9

Entries



nChipHit_F2_Fch8_Chip9

Entries 1056695

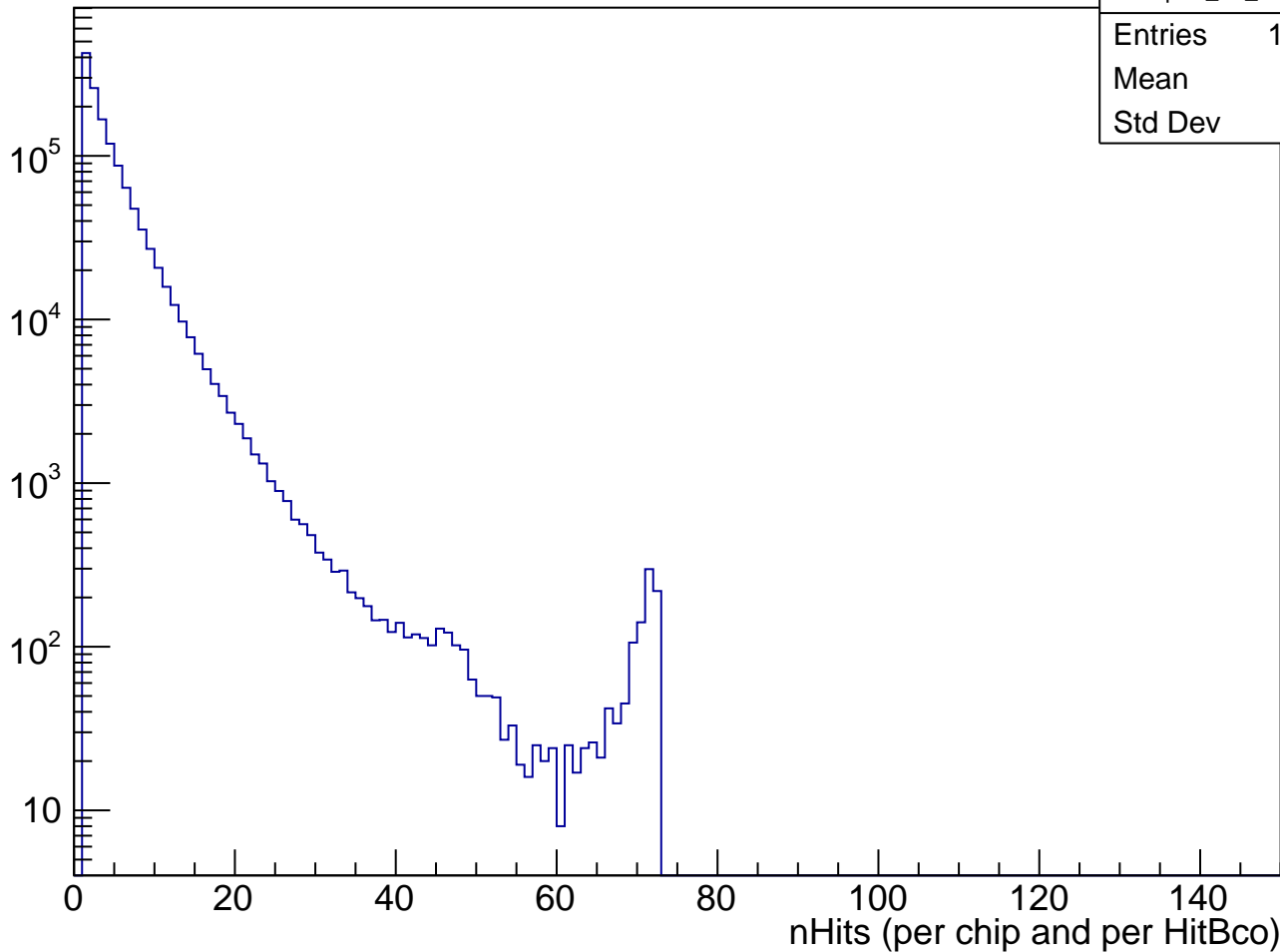
Mean 3.385

Std Dev 3.944

nHits (per chip and per HitBco)

nChipHit_F2_Fch9_Chip0

Entries



nChipHit_F2_Fch9_Chip0

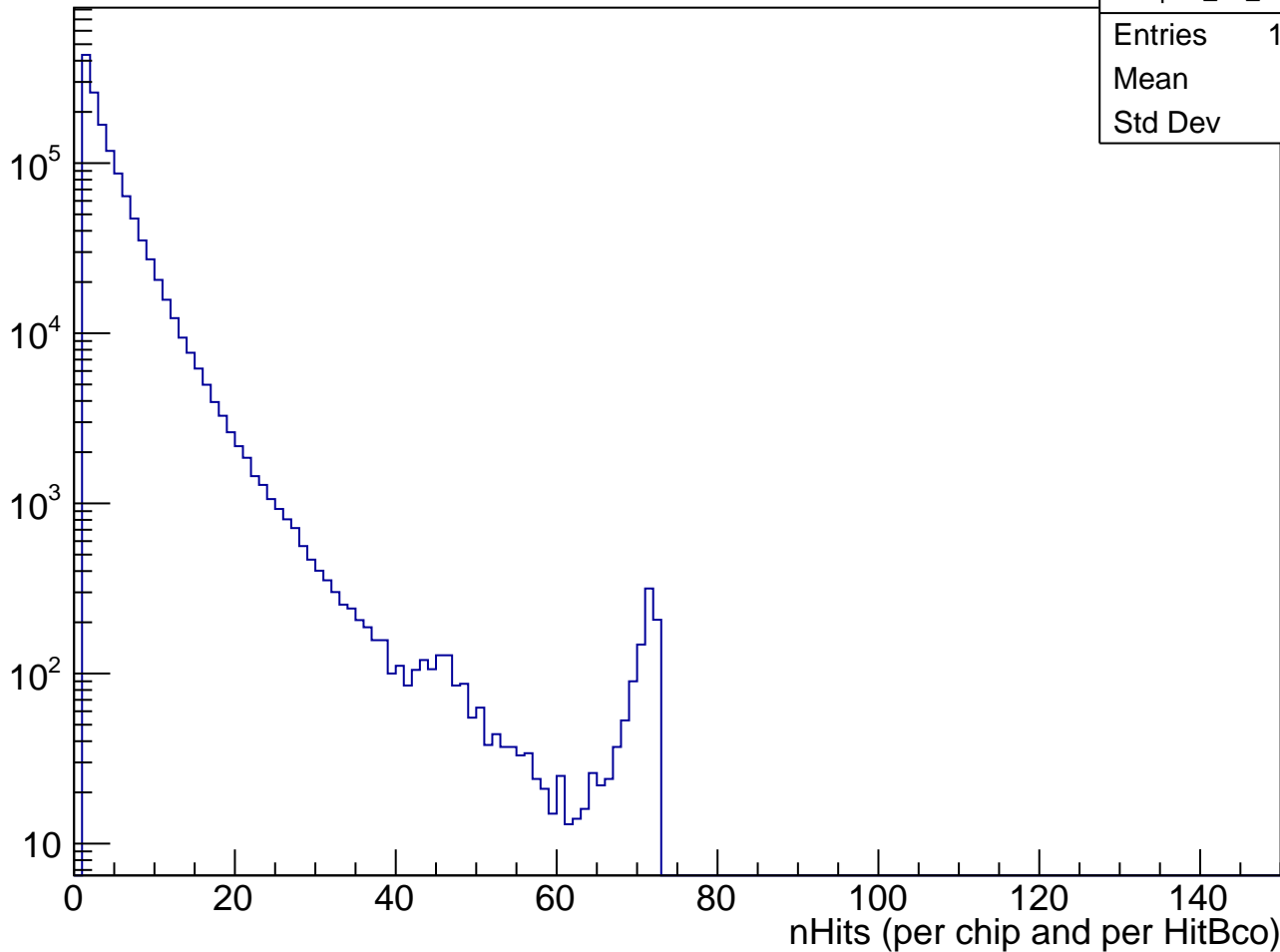
Entries 1335148

Mean 3.887

Std Dev 4.531

nChipHit_F2_Fch9_Chip1

Entries

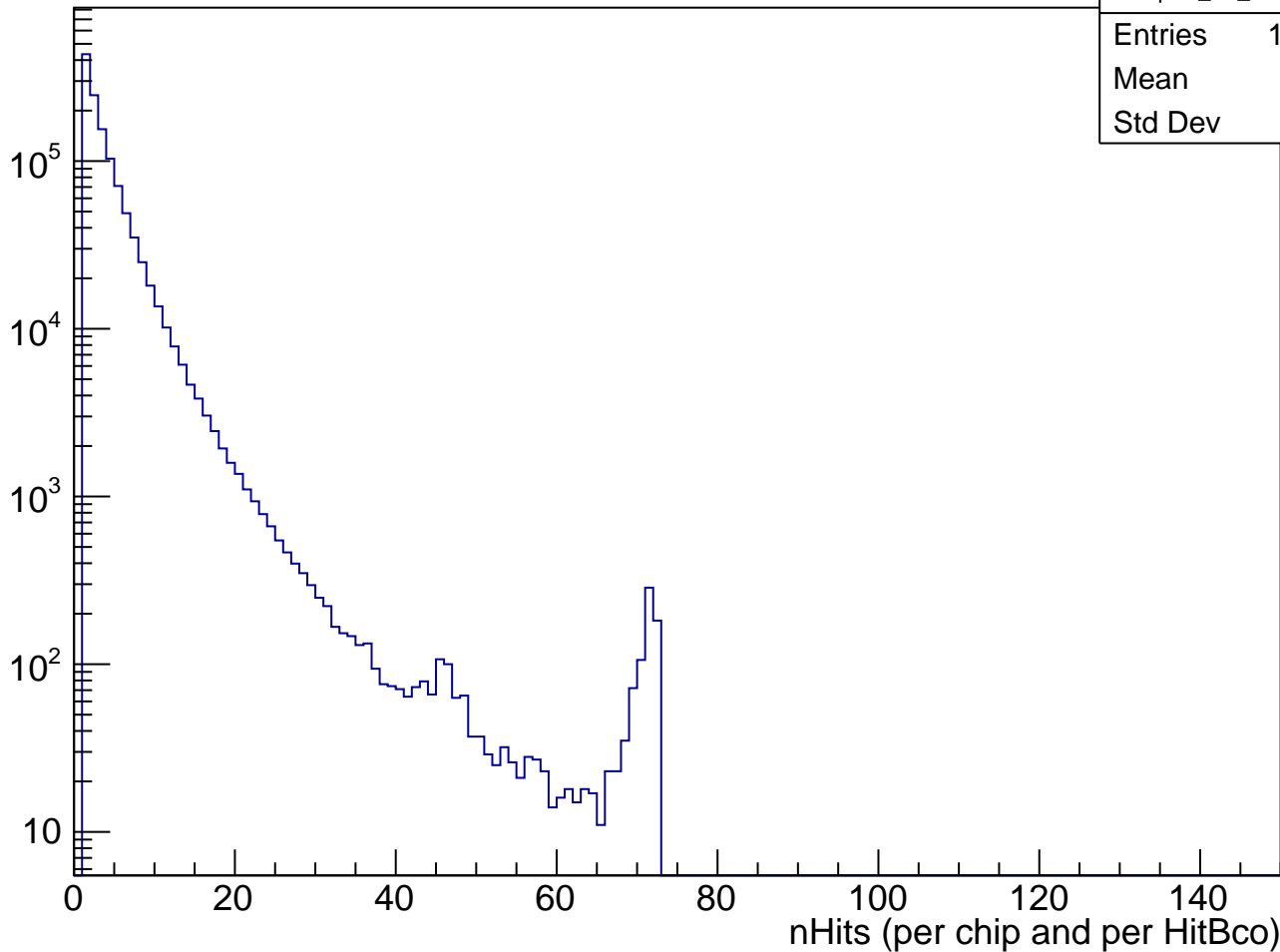


nChipHit_F2_Fch9_Chip1	
Entries	1342337
Mean	3.86
Std Dev	4.512

nHits (per chip and per HitBco)

nChipHit_F2_Fch9_Chip10

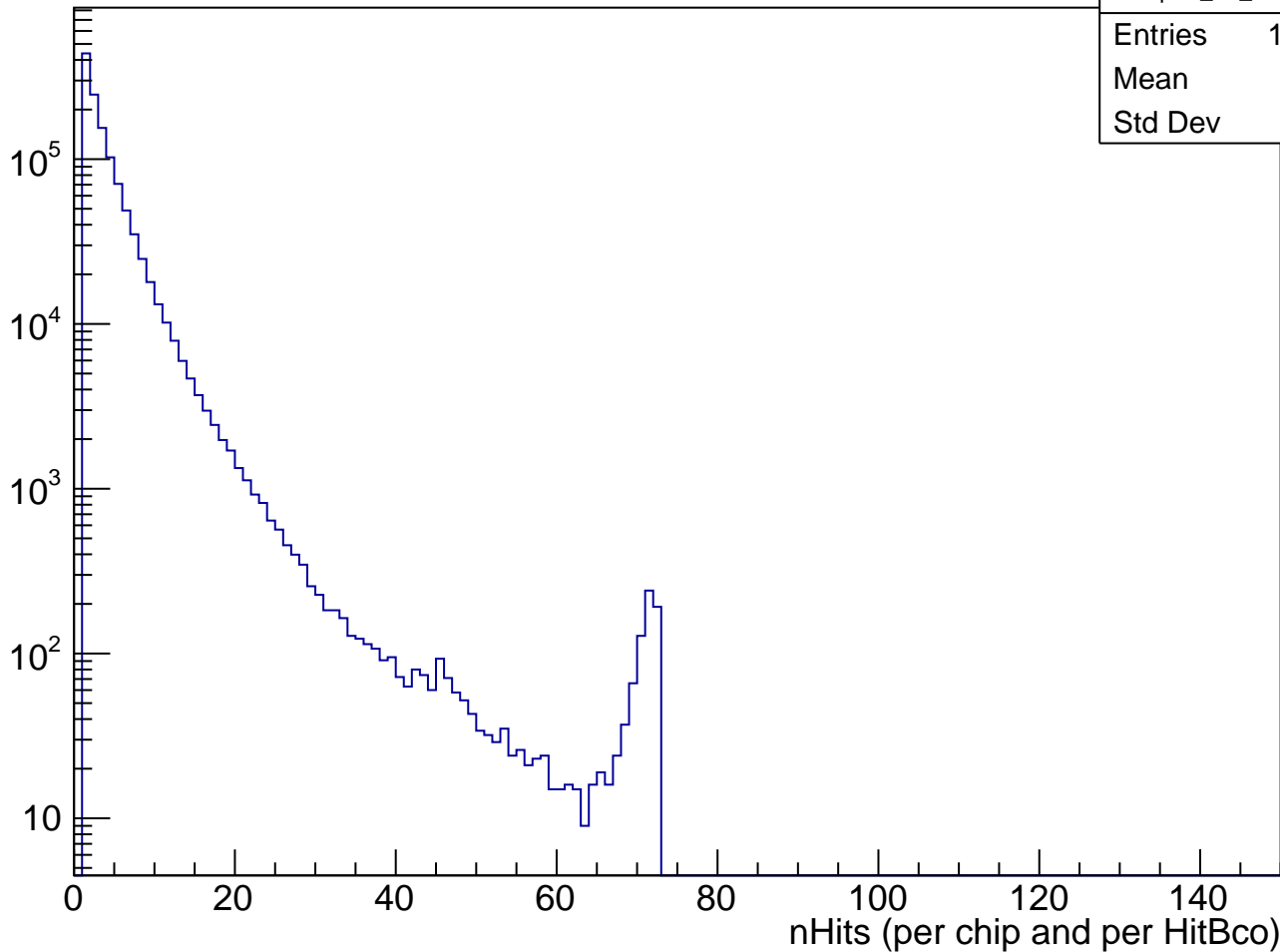
Entries



nChipHit_F2_Fch9_Chip10	
Entries	1202422
Mean	3.387
Std Dev	4.053

nChipHit_F2_Fch9_Chip11

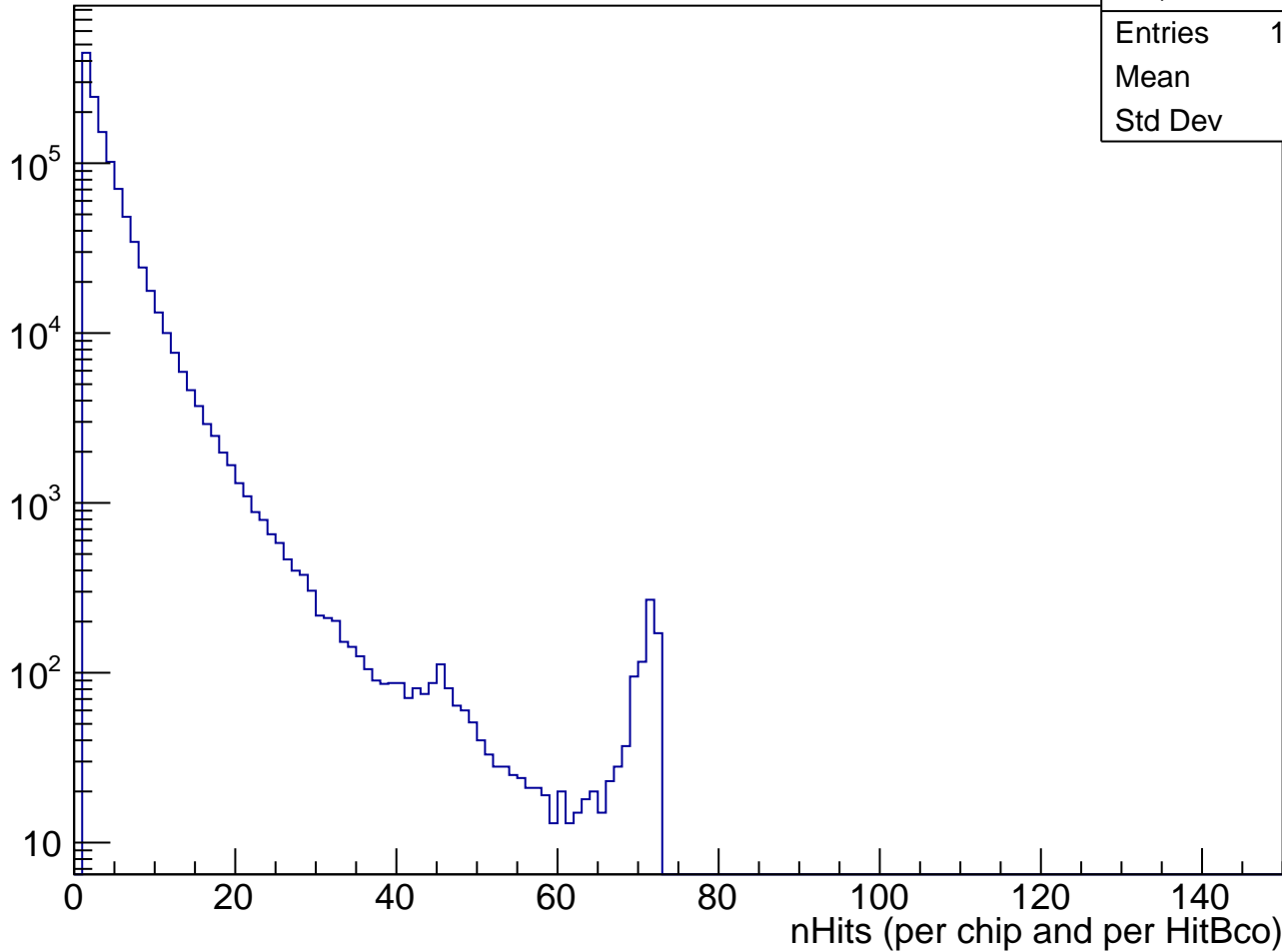
Entries



nChipHit_F2_Fch9_Chip11	
Entries	1204279
Mean	3.369
Std Dev	4.024

nChipHit_F2_Fch9_Chip12

Entries

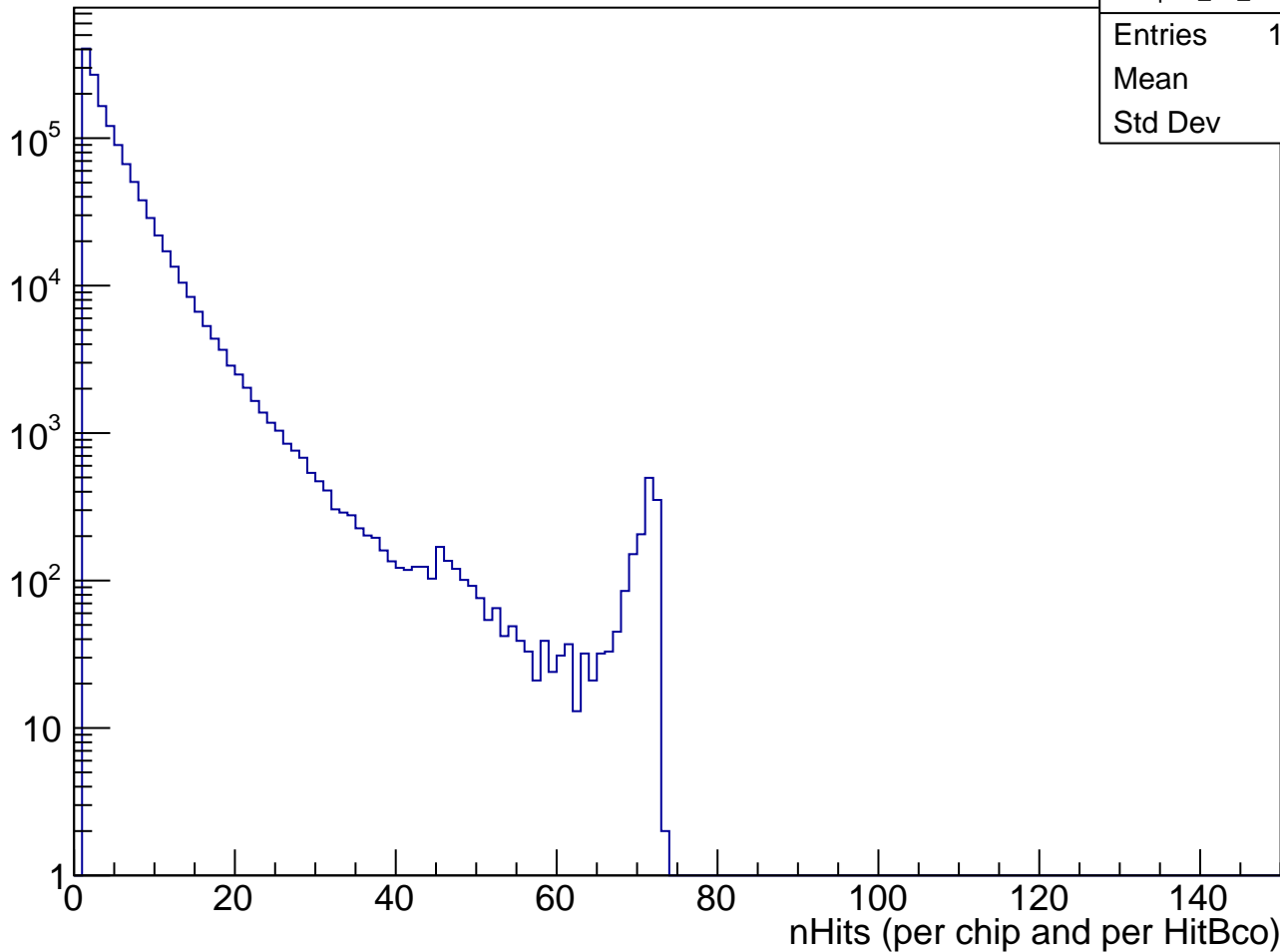


nChipHit_F2_Fch9_Chip12	
Entries	1207028
Mean	3.349
Std Dev	4.051

nHits (per chip and per HitBco)

nChipHit_F2_Fch9_Chip13

Entries



nChipHit_F2_Fch9_Chip13

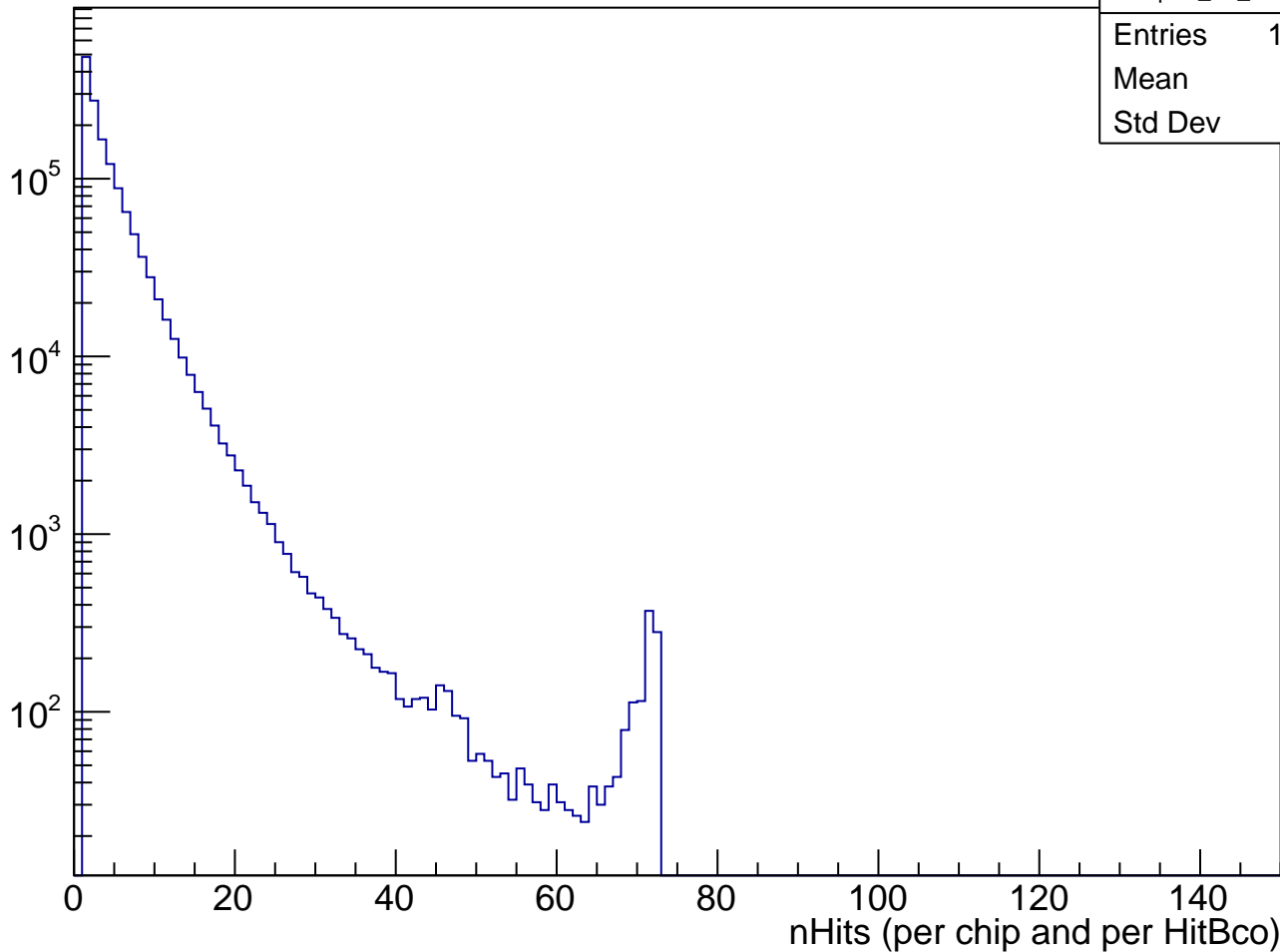
Entries 1346507

Mean 4.05

Std Dev 4.845

nChipHit_F2_Fch9_Chip14

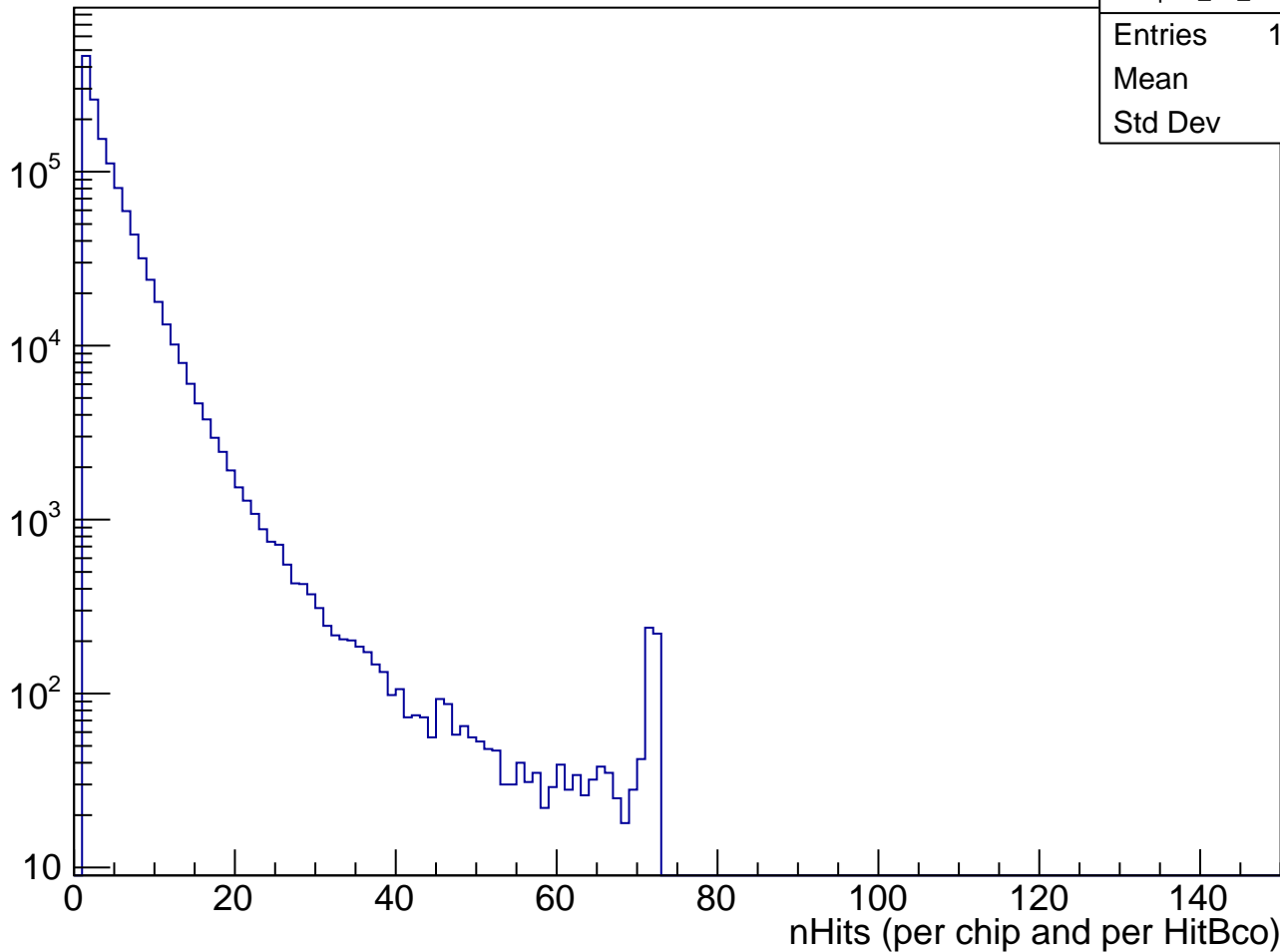
Entries



nChipHit_F2_Fch9_Chip14	
Entries	1418744
Mean	3.786
Std Dev	4.565

nChipHit_F2_Fch9_Chip15

Entries

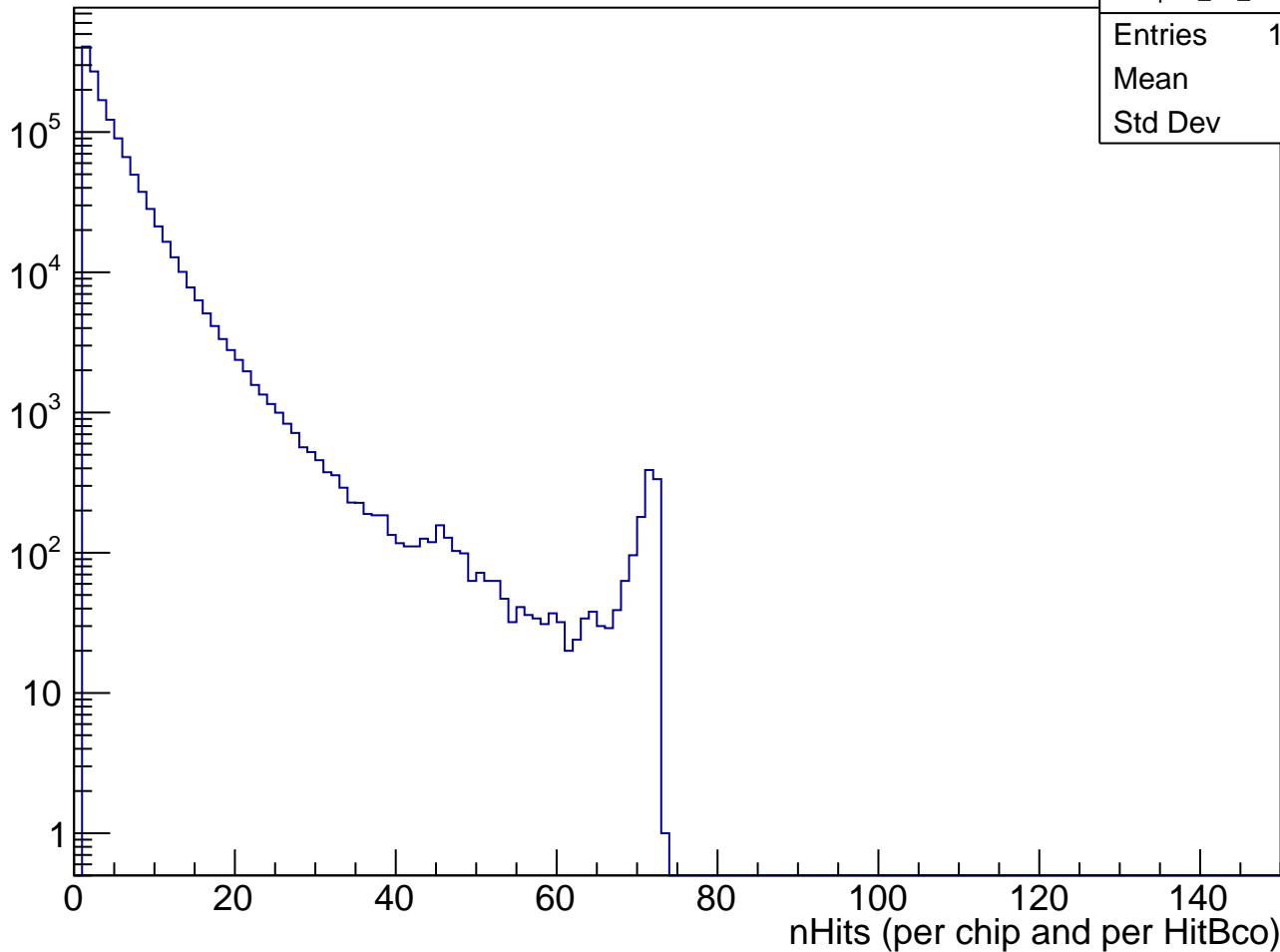


nChipHit_F2_Fch9_Chip15	
Entries	1310496
Mean	3.569
Std Dev	4.175

nHits (per chip and per HitBco)

nChipHit_F2_Fch9_Chip16

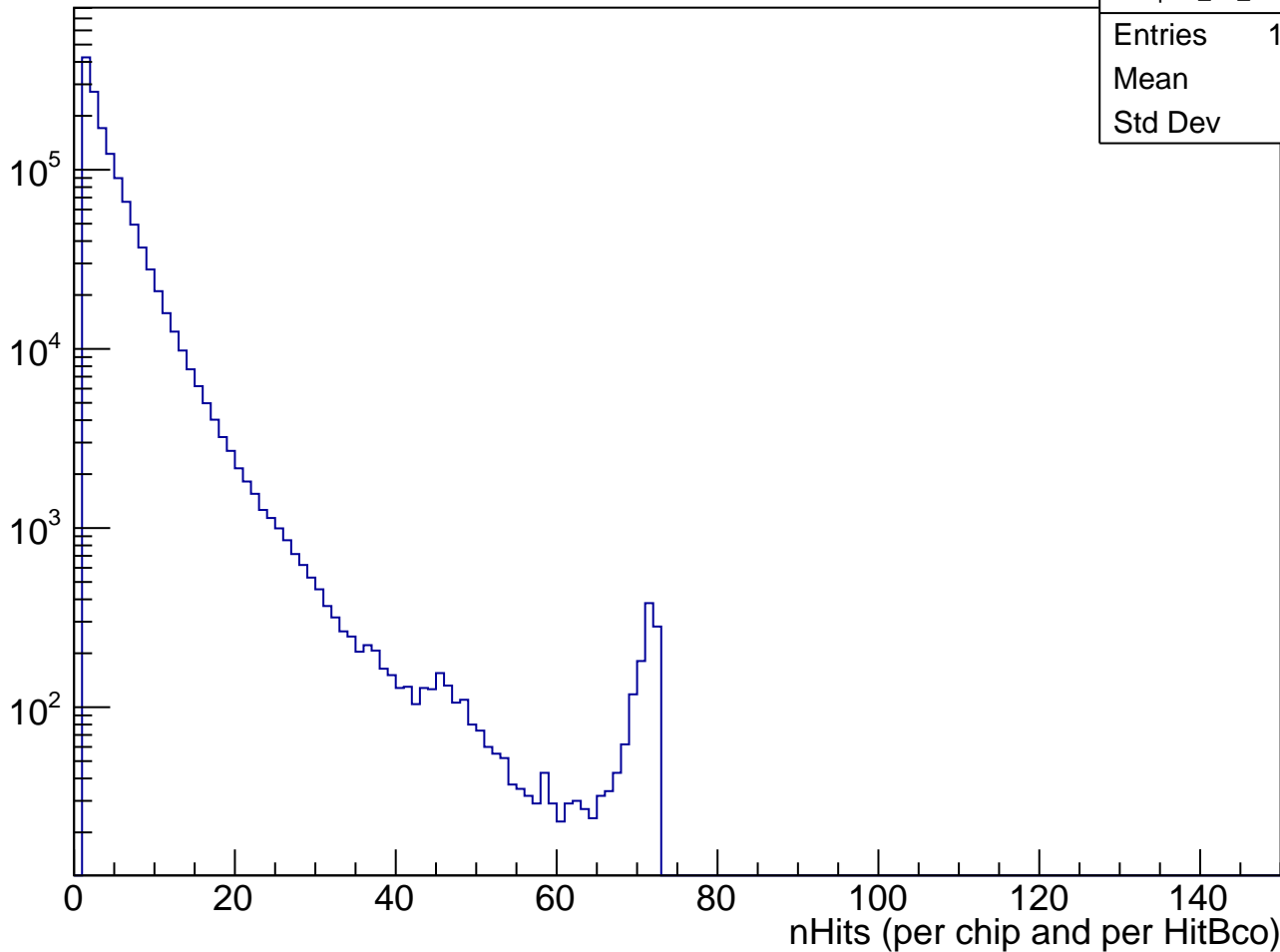
Entries



nChipHit_F2_Fch9_Chip16	
Entries	1347877
Mean	3.986
Std Dev	4.709

nChipHit_F2_Fch9_Chip17

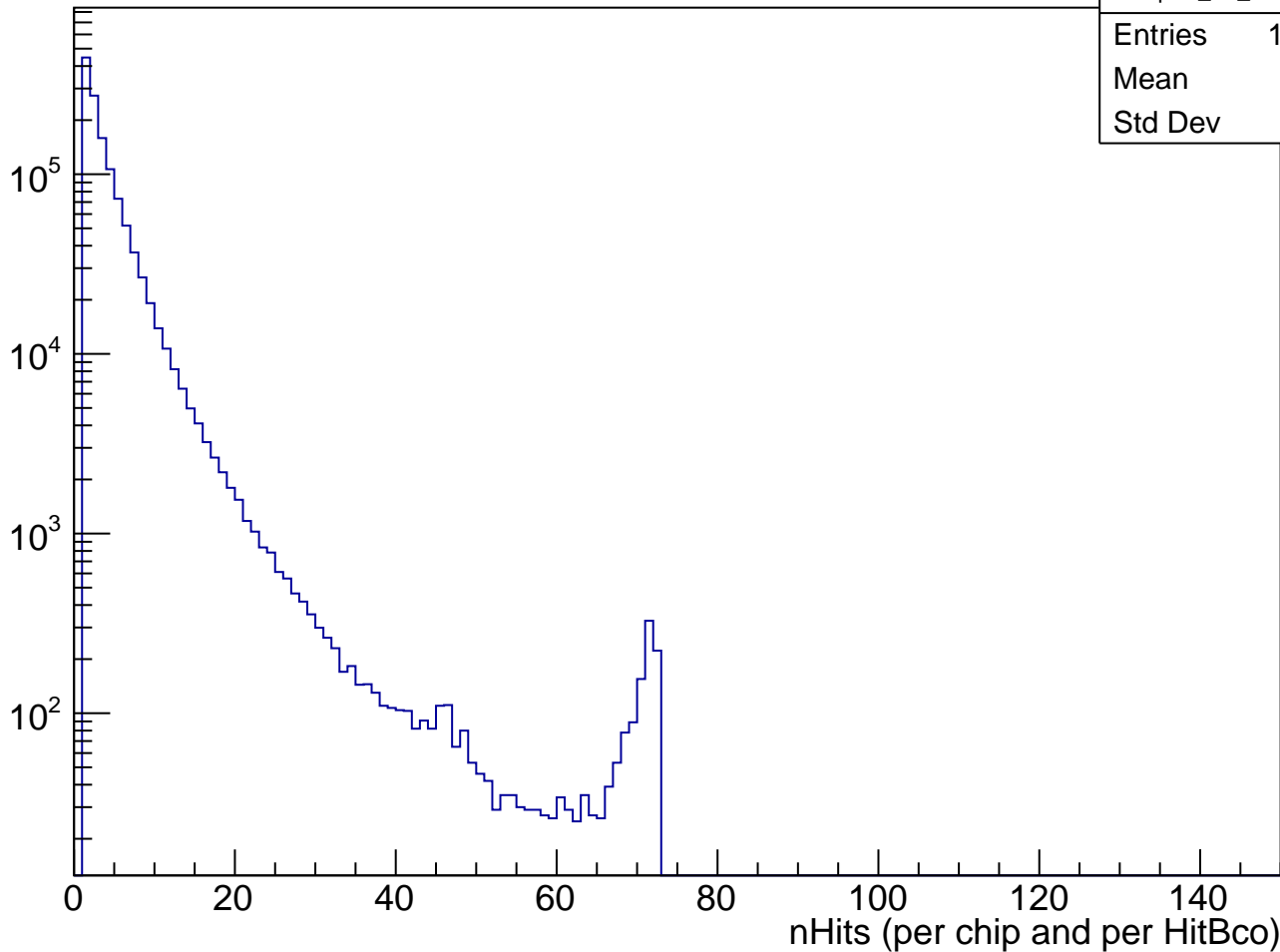
Entries



nChipHit_F2_Fch9_Chip17	
Entries	1365462
Mean	3.92
Std Dev	4.657

nChipHit_F2_Fch9_Chip18

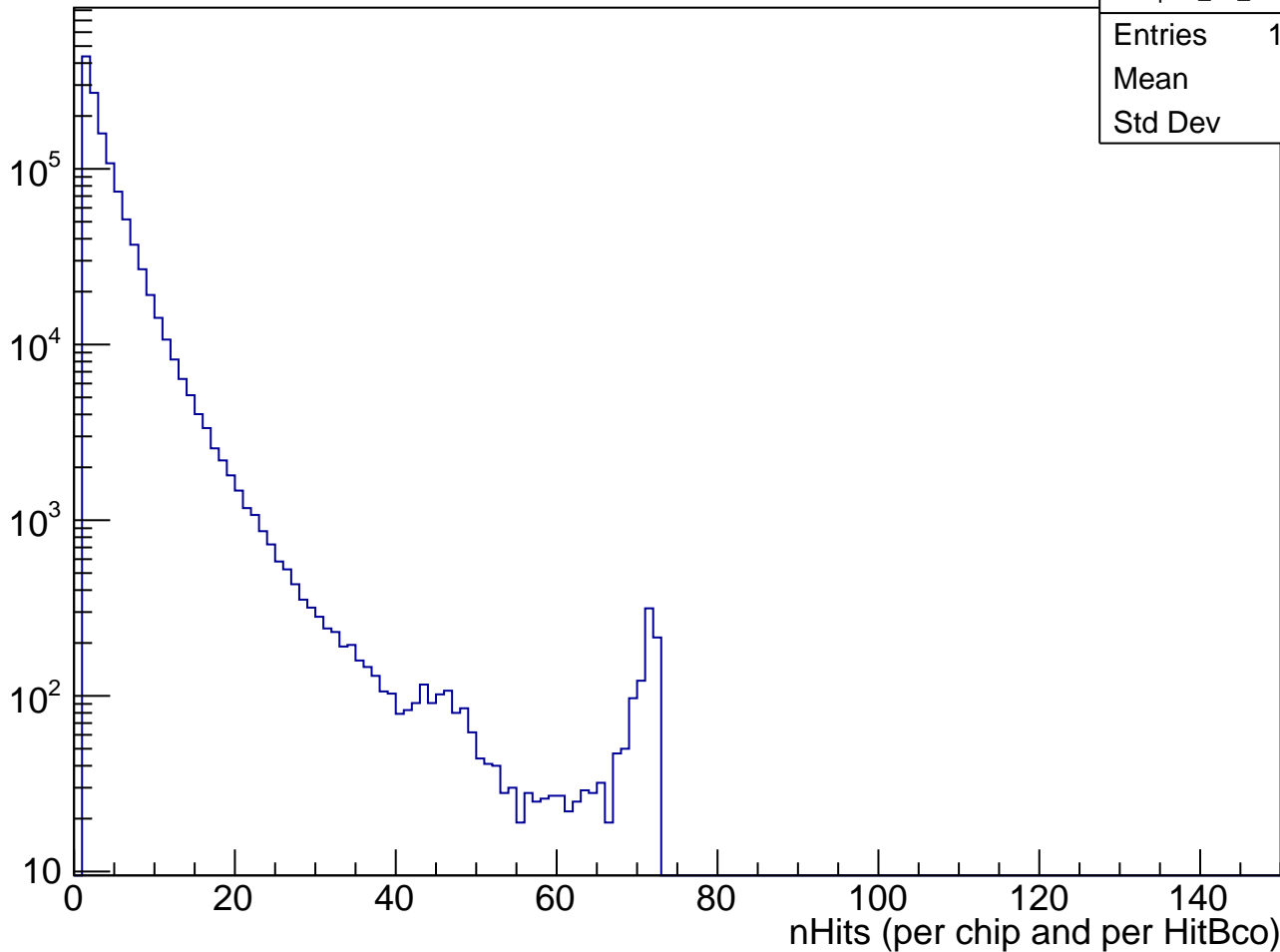
Entries



nChipHit_F2_Fch9_Chip18	
Entries	1263057
Mean	3.429
Std Dev	4.245

nChipHit_F2_Fch9_Chip19

Entries



nChipHit_F2_Fch9_Chip19

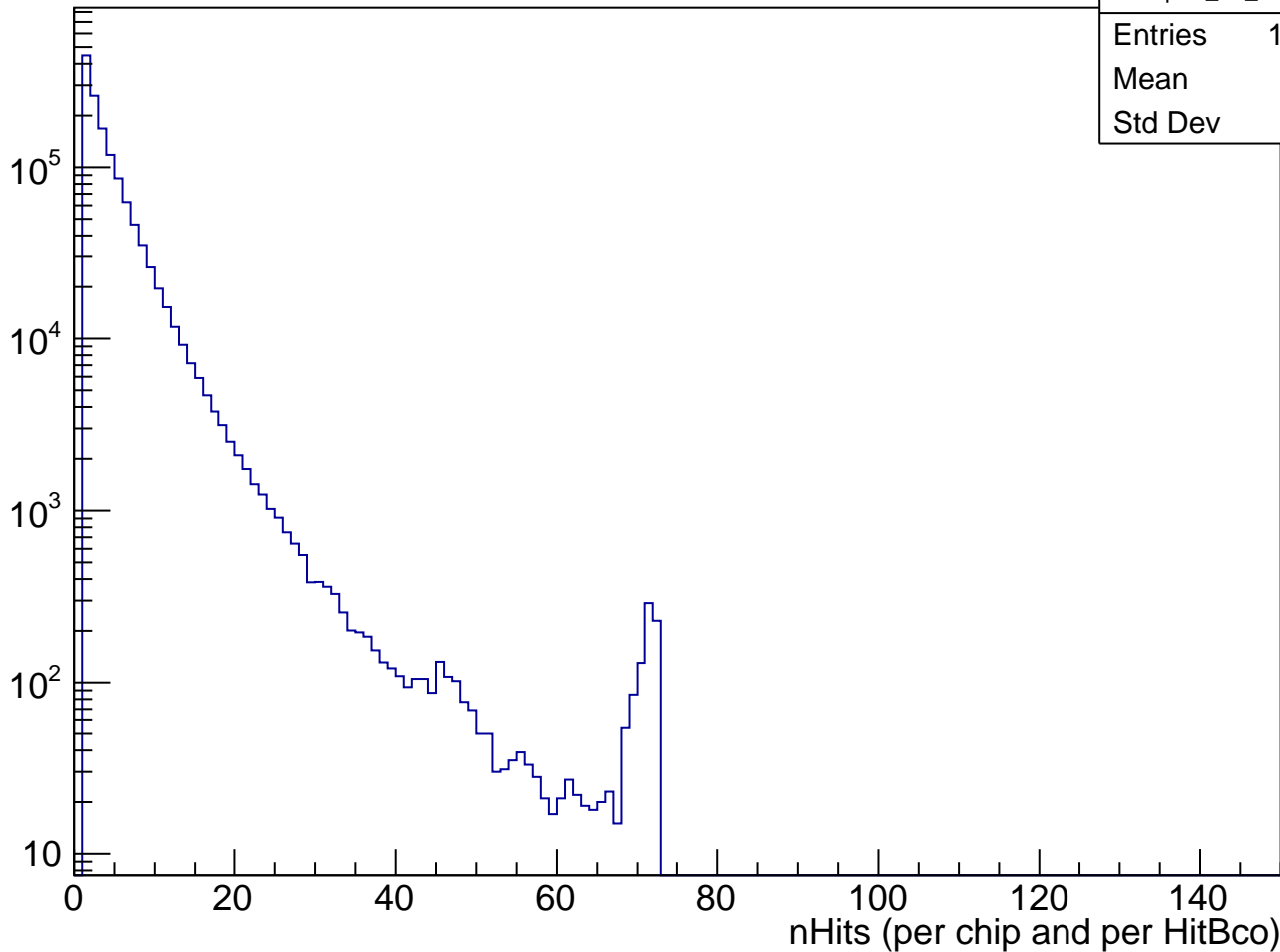
Entries 1253187

Mean 3.446

Std Dev 4.203

nChipHit_F2_Fch9_Chip2

Entries



nChipHit_F2_Fch9_Chip2

Entries 1348787

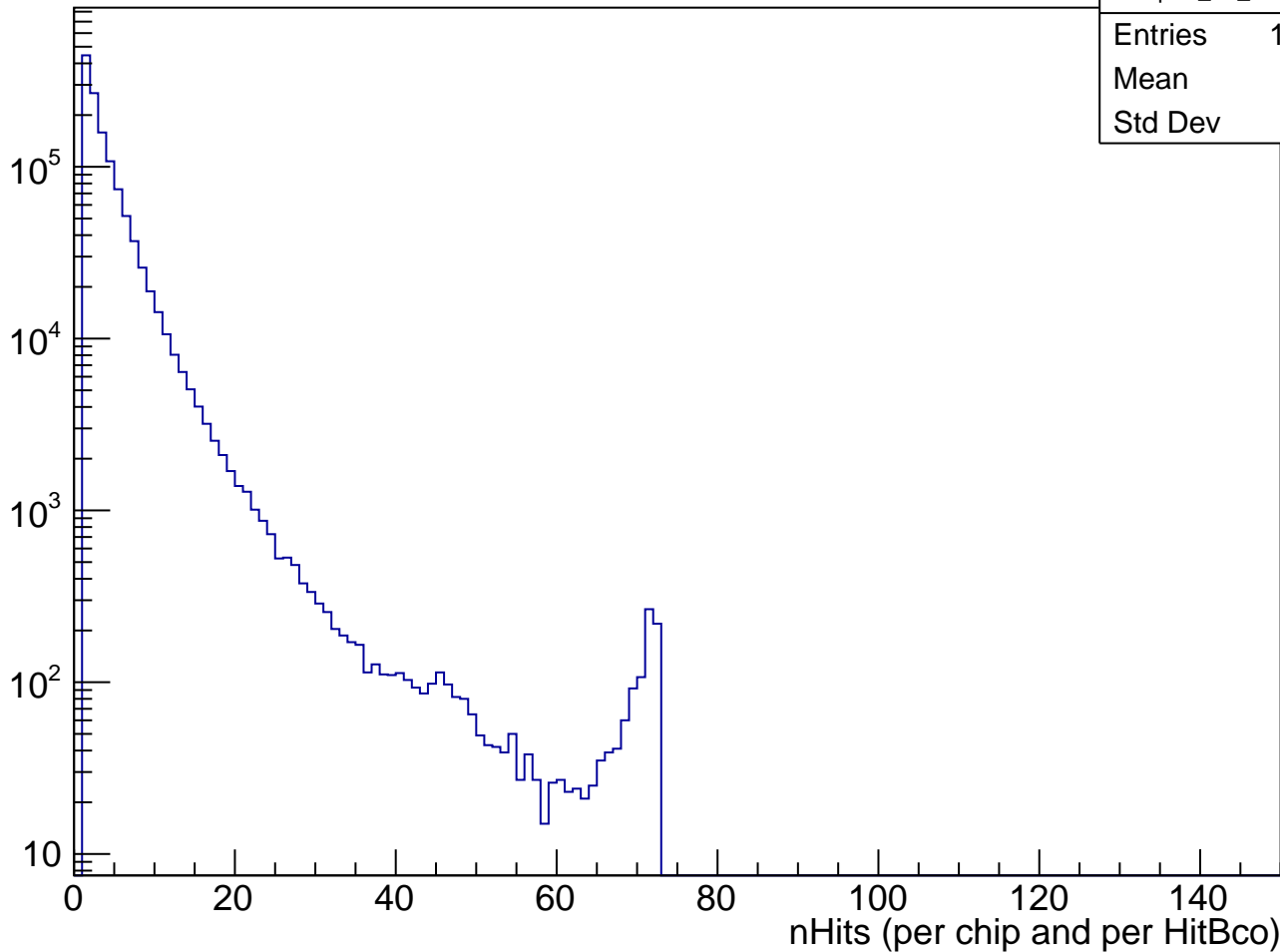
Mean 3.781

Std Dev 4.438

nHits (per chip and per HitBco)

nChipHit_F2_Fch9_Chip20

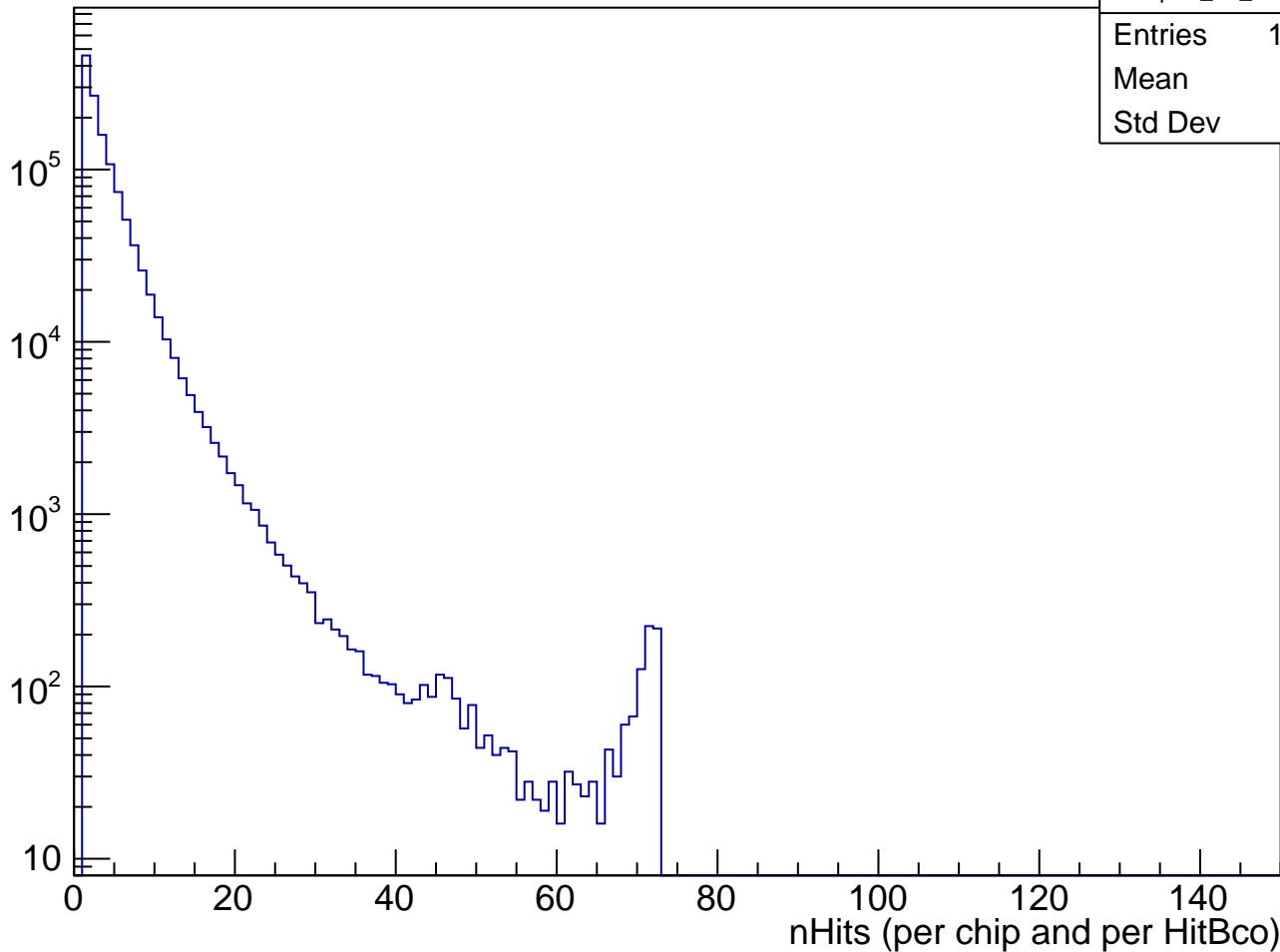
Entries



nChipHit_F2_Fch9_Chip20	
Entries	1256800
Mean	3.42
Std Dev	4.18

nChipHit_F2_Fch9_Chip21

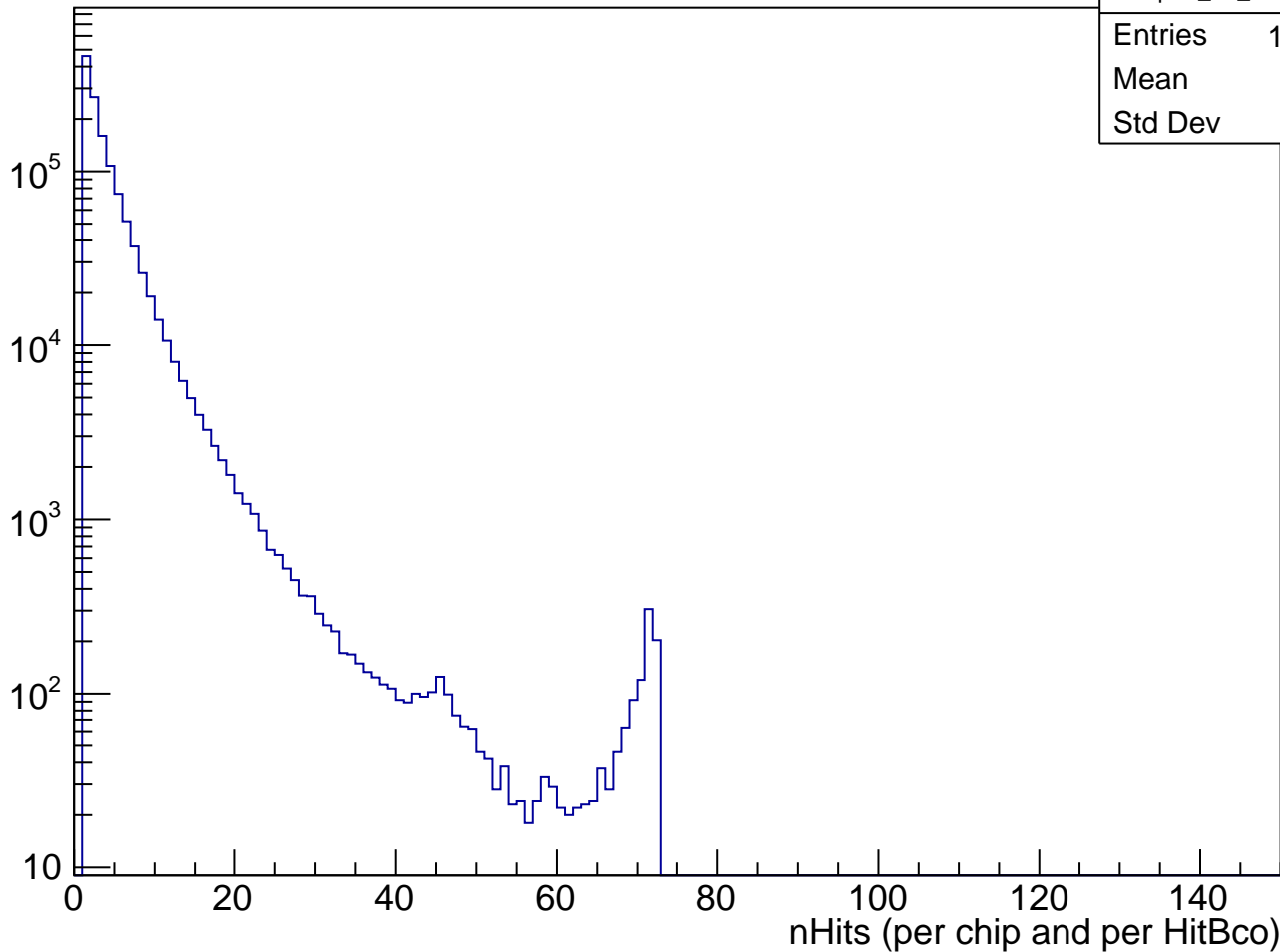
Entries



nChipHit_F2_Fch9_Chip21	
Entries	1267988
Mean	3.378
Std Dev	4.118

nChipHit_F2_Fch9_Chip22

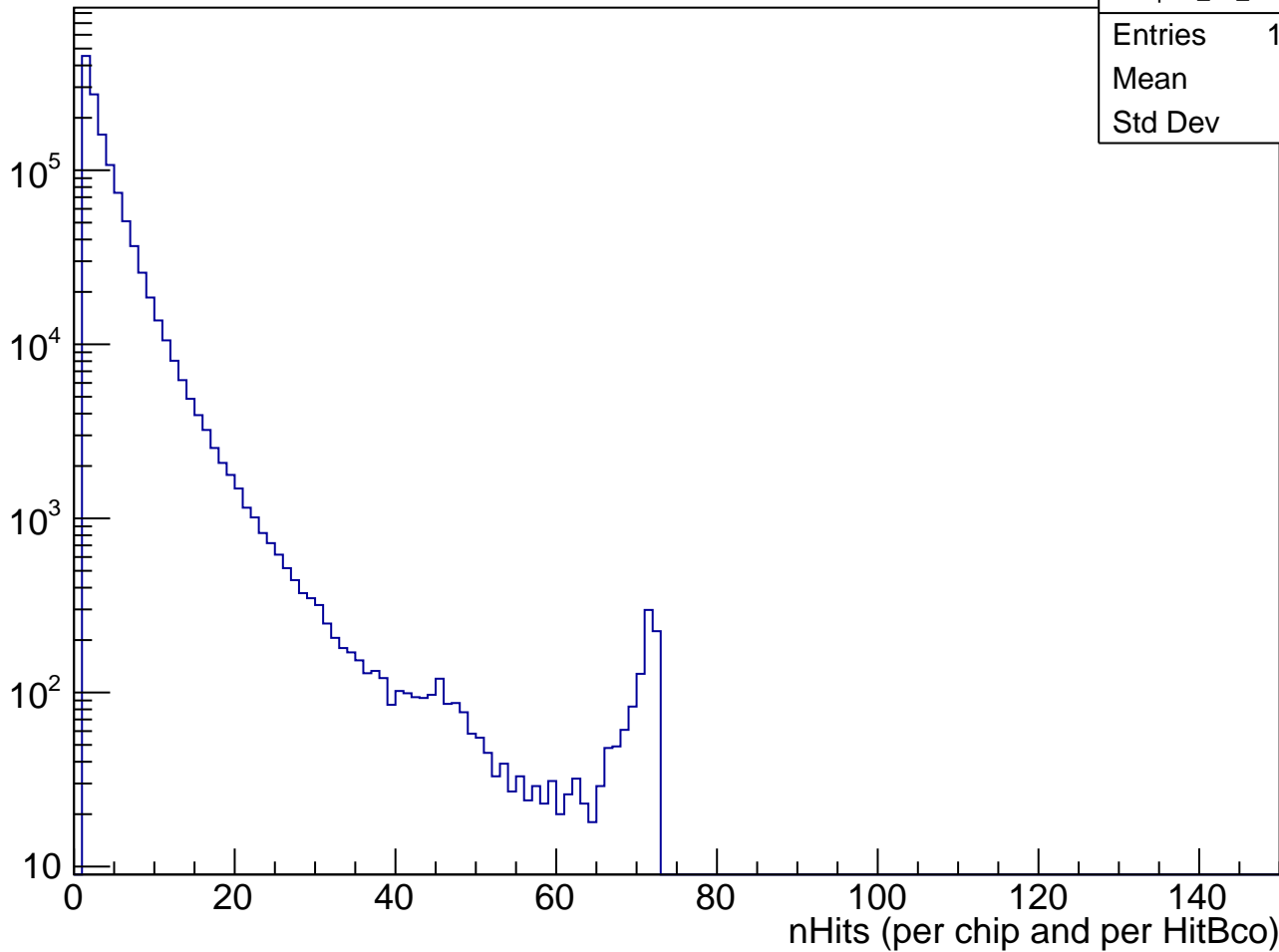
Entries



nChipHit_F2_Fch9_Chip22	
Entries	1271747
Mean	3.395
Std Dev	4.166

nChipHit_F2_Fch9_Chip23

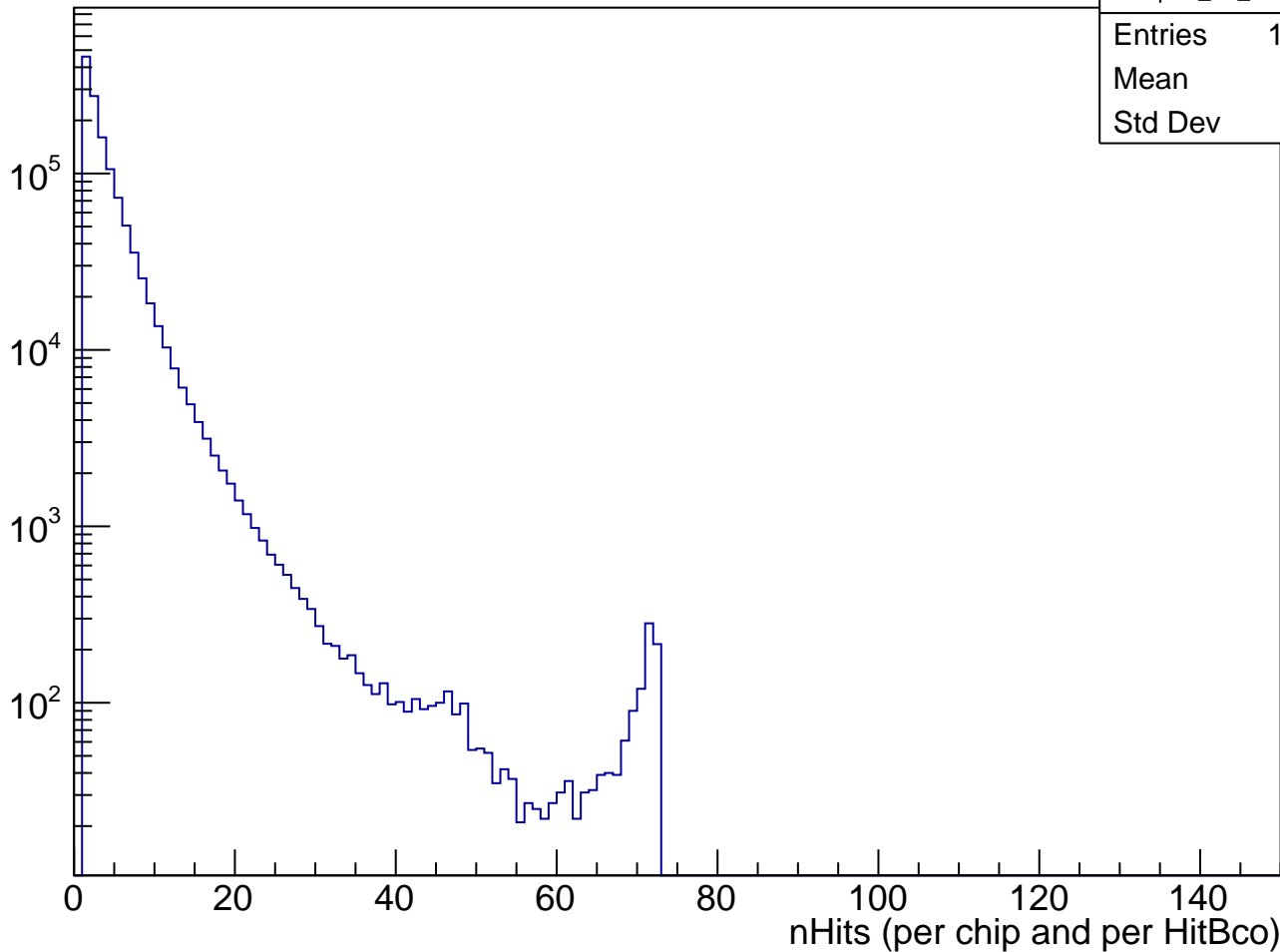
Entries



nChipHit_F2_Fch9_Chip23	
Entries	1268128
Mean	3.392
Std Dev	4.178

nChipHit_F2_Fch9_Chip24

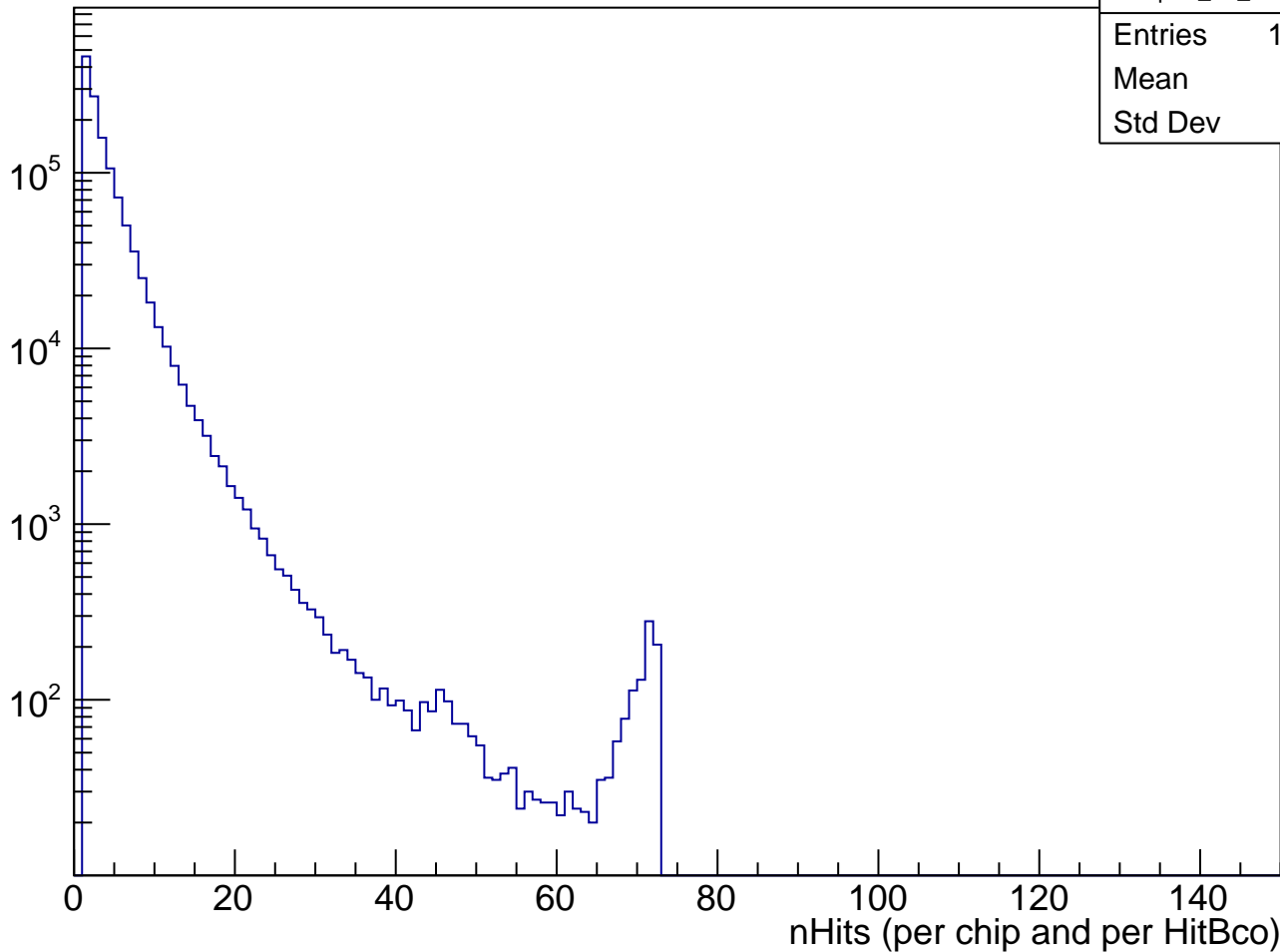
Entries



nChipHit_F2_Fch9_Chip24	
Entries	1270808
Mean	3.362
Std Dev	4.161

nChipHit_F2_Fch9_Chip25

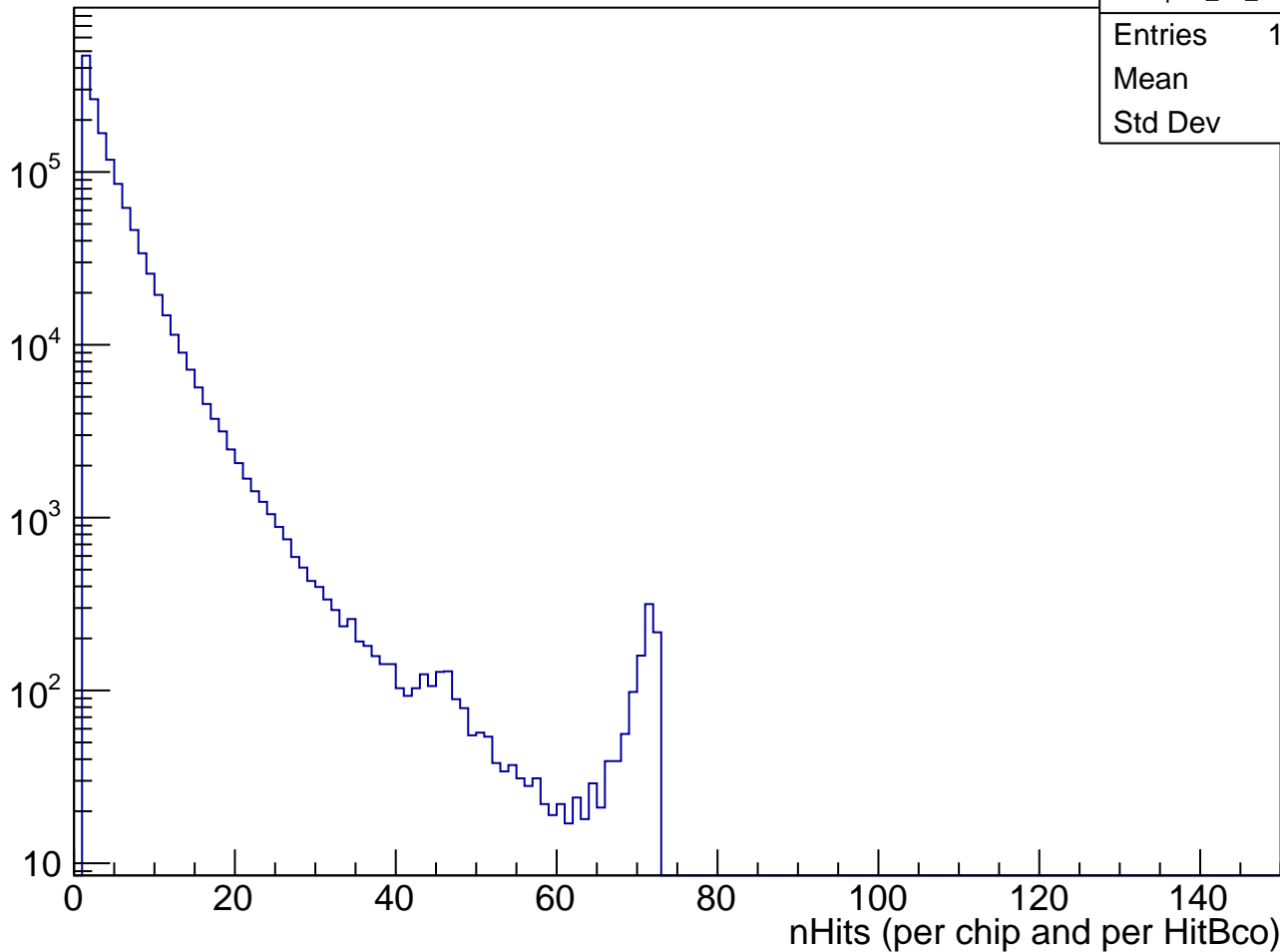
Entries



nChipHit_F2_Fch9_Chip25	
Entries	1264829
Mean	3.353
Std Dev	4.15

nChipHit_F2_Fch9_Chip3

Entries



nChipHit_F2_Fch9_Chip3

Entries 1370465

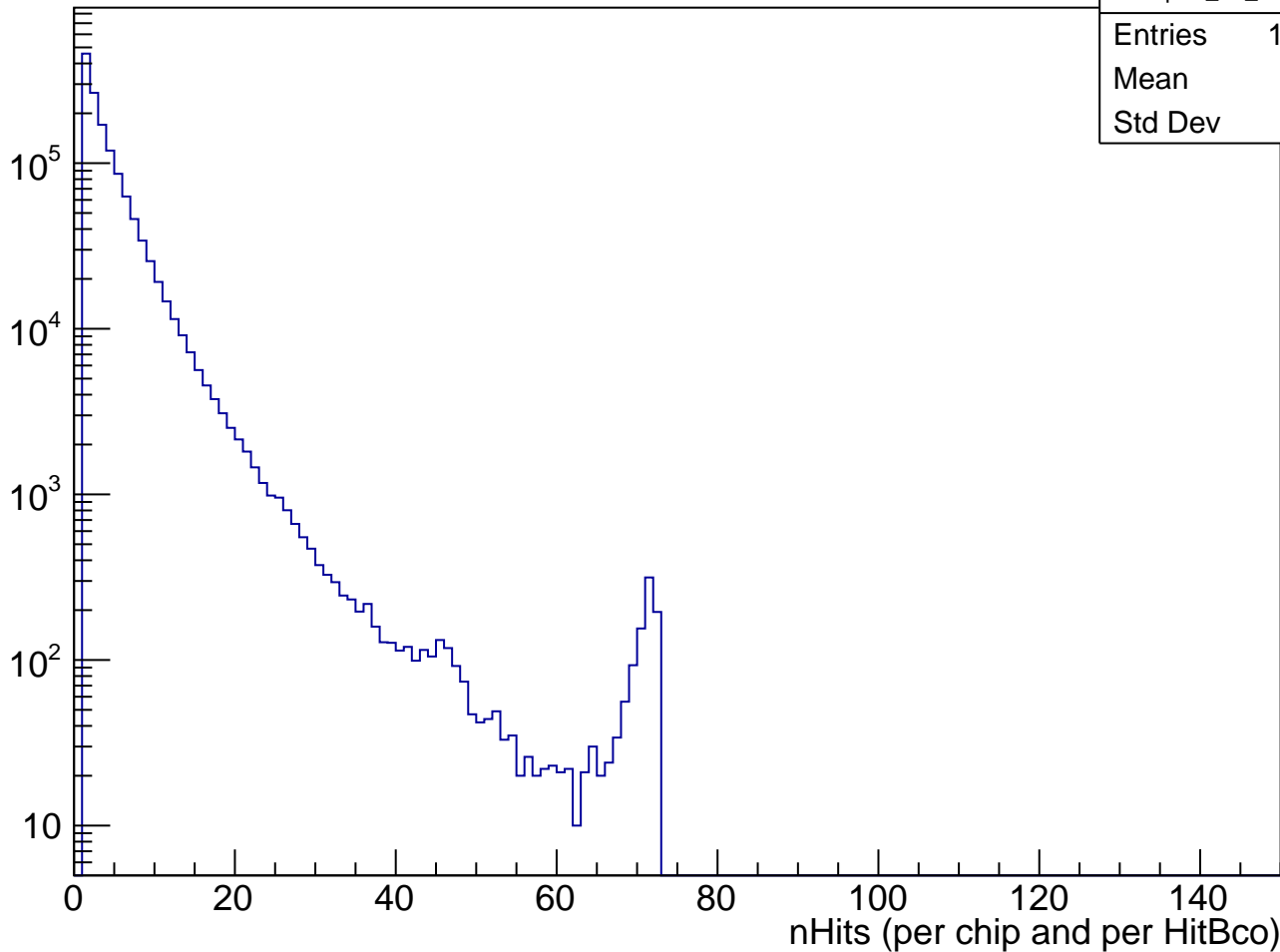
Mean 3.718

Std Dev 4.445

nHits (per chip and per HitBco)

nChipHit_F2_Fch9_Chip4

Entries



nChipHit_F2_Fch9_Chip4

Entries 1365796

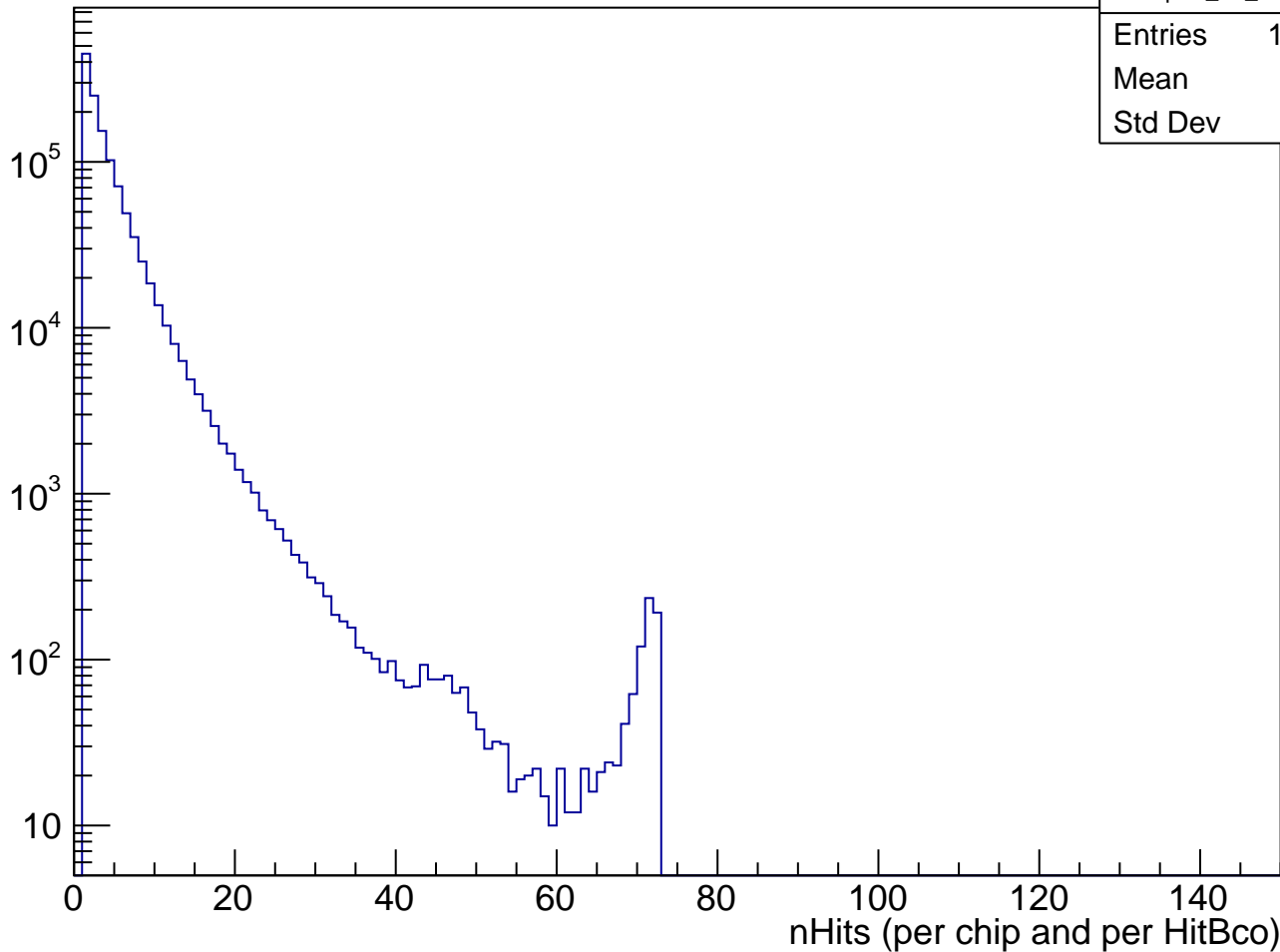
Mean 3.741

Std Dev 4.429

nHits (per chip and per HitBco)

nChipHit_F2_Fch9_Chip5

Entries



nChipHit_F2_Fch9_Chip5

Entries 1222297

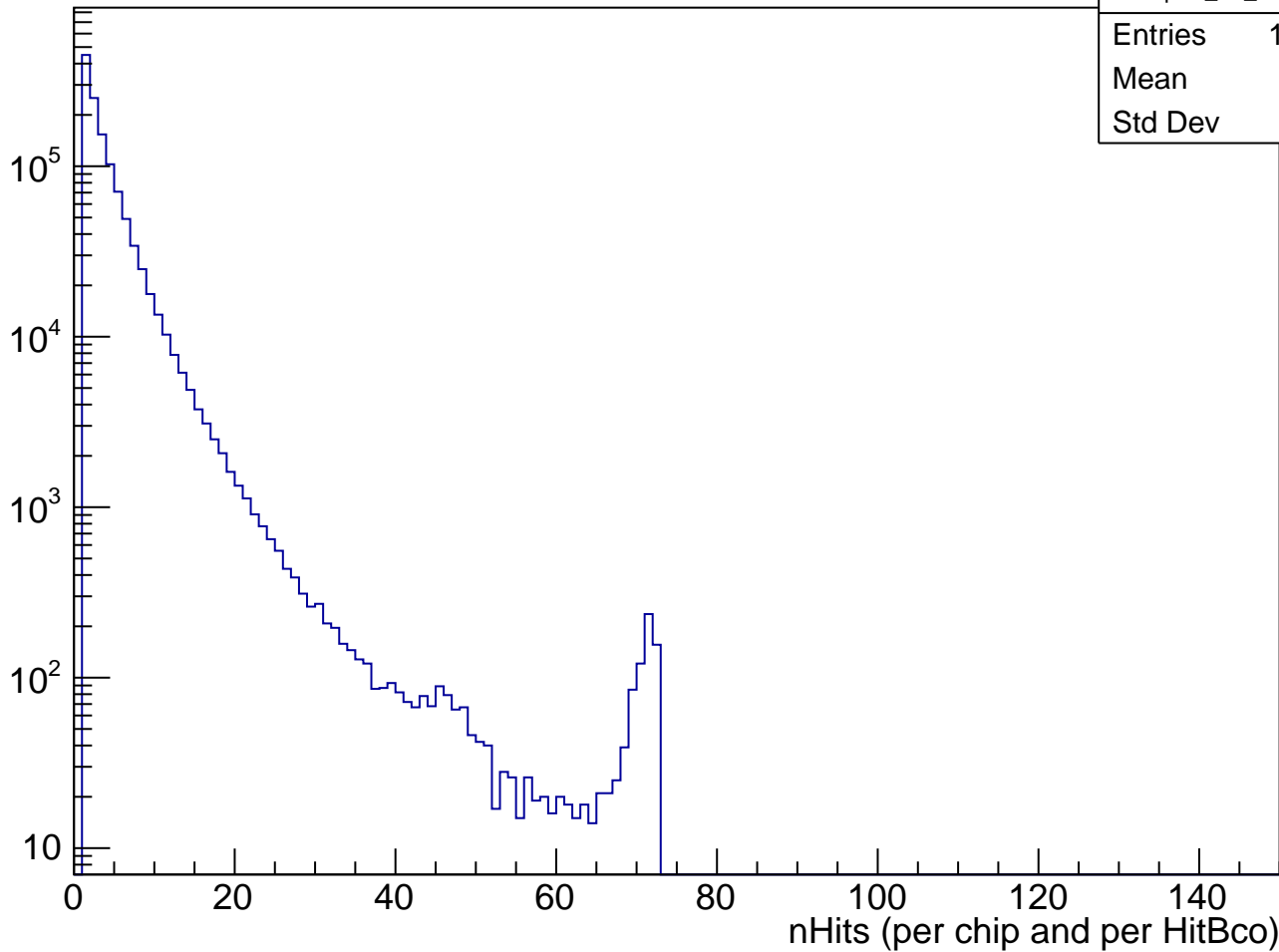
Mean 3.376

Std Dev 4.057

nHits (per chip and per HitBco)

nChipHit_F2_Fch9_Chip6

Entries



nChipHit_F2_Fch9_Chip6

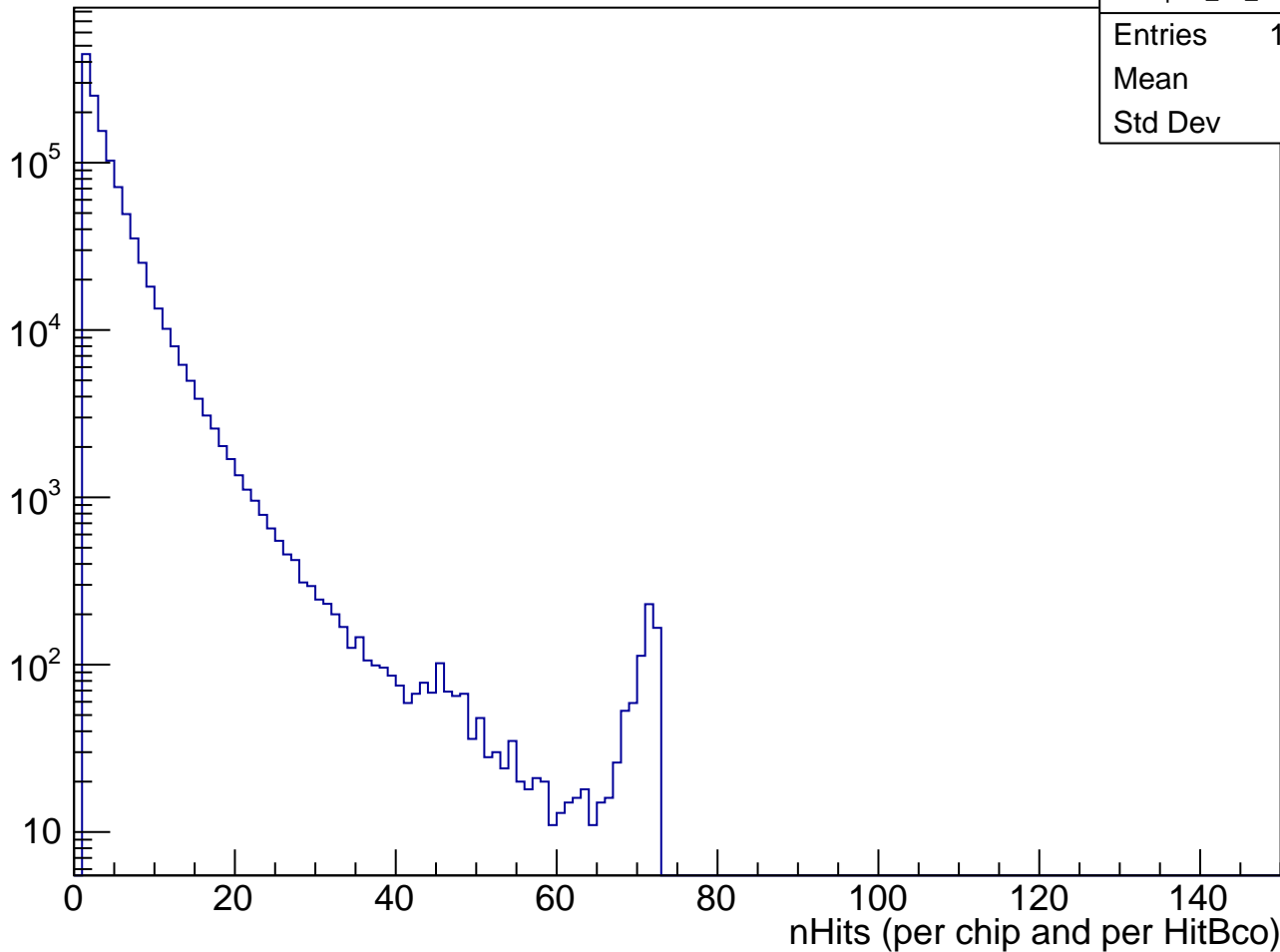
Entries 1219660

Mean 3.347

Std Dev 4.012

nChipHit_F2_Fch9_Chip7

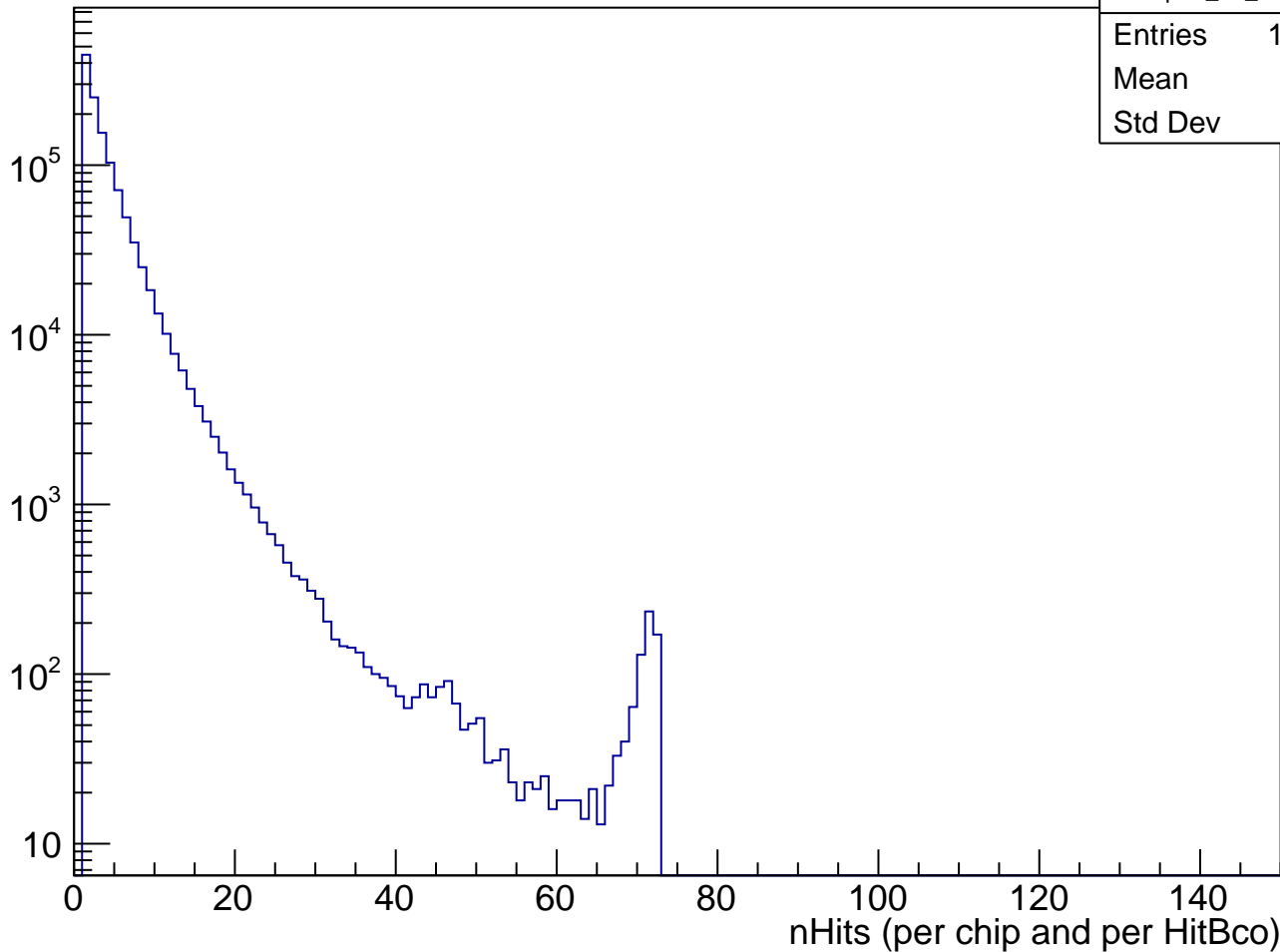
Entries



nChipHit_F2_Fch9_Chip7	
Entries	1220273
Mean	3.366
Std Dev	4.008

nChipHit_F2_Fch9_Chip8

Entries



nChipHit_F2_Fch9_Chip8

Entries 1221259

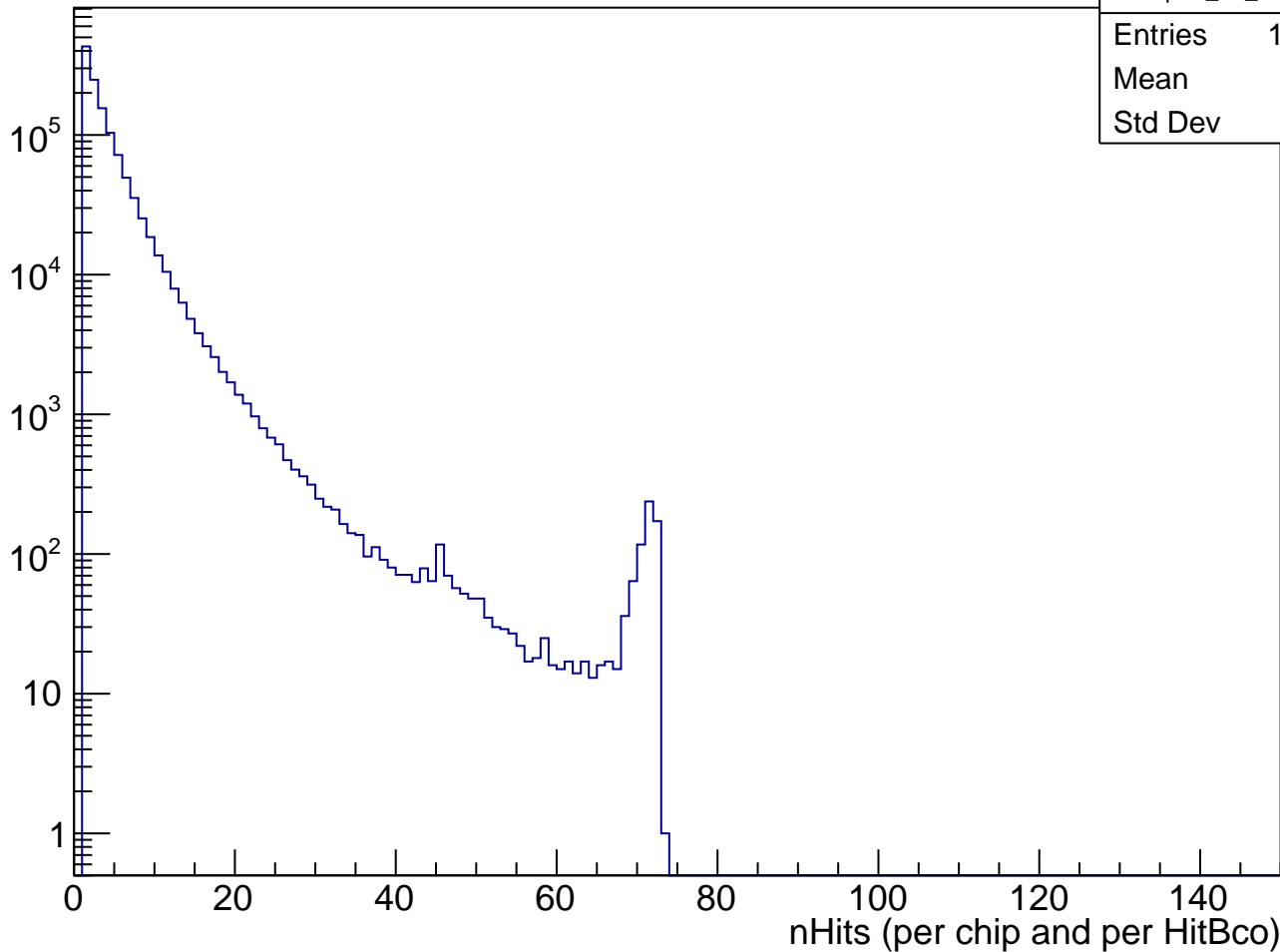
Mean 3.36

Std Dev 4.027

nHits (per chip and per HitBco)

nChipHit_F2_Fch9_Chip9

Entries



nChipHit_F2_Fch9_Chip9

Entries 1205642

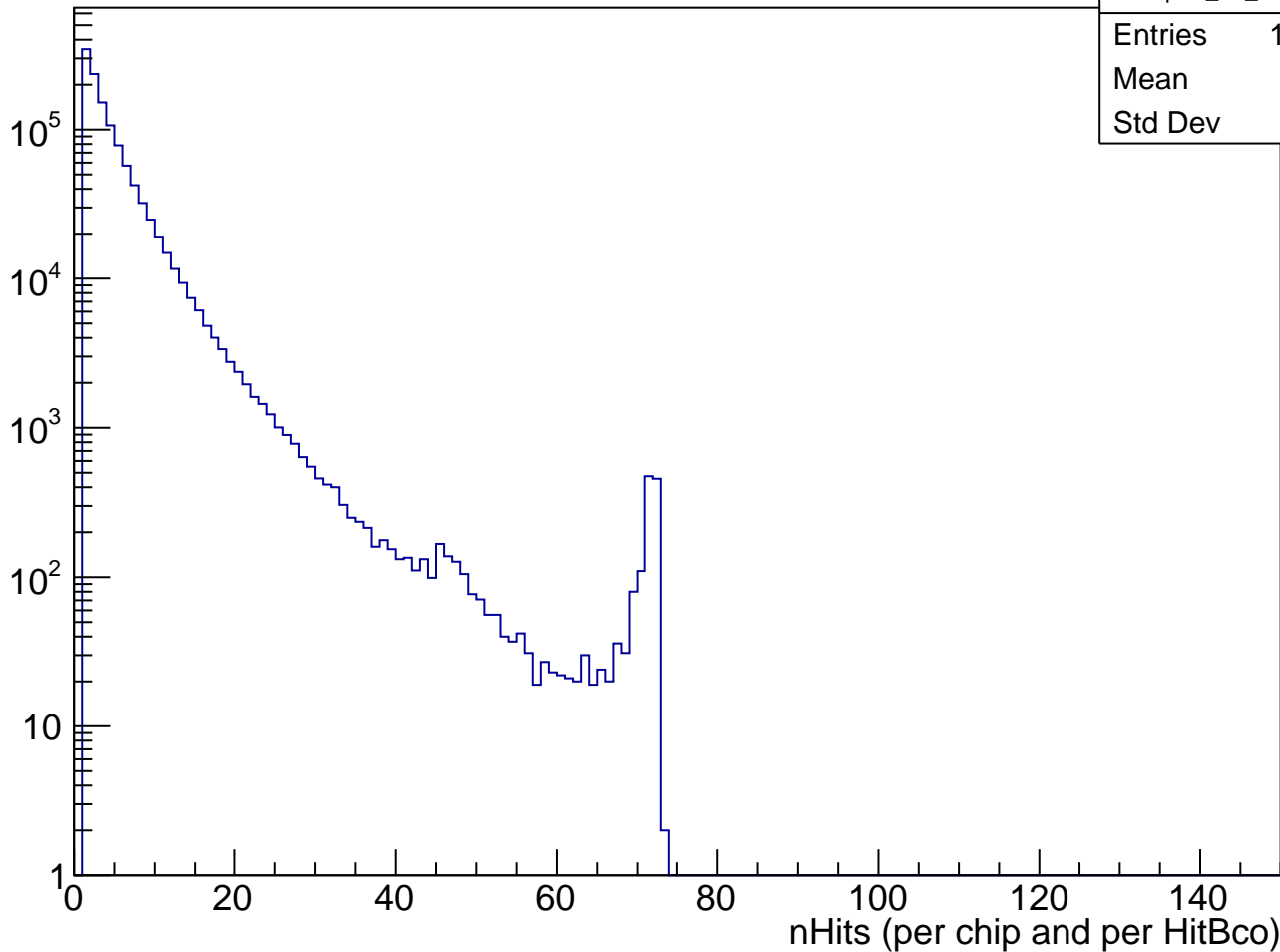
Mean 3.41

Std Dev 4.042

nHits (per chip and per HitBco)

nChipHit_F3_Fch0_Chip0

Entries



nChipHit_F3_Fch0_Chip0

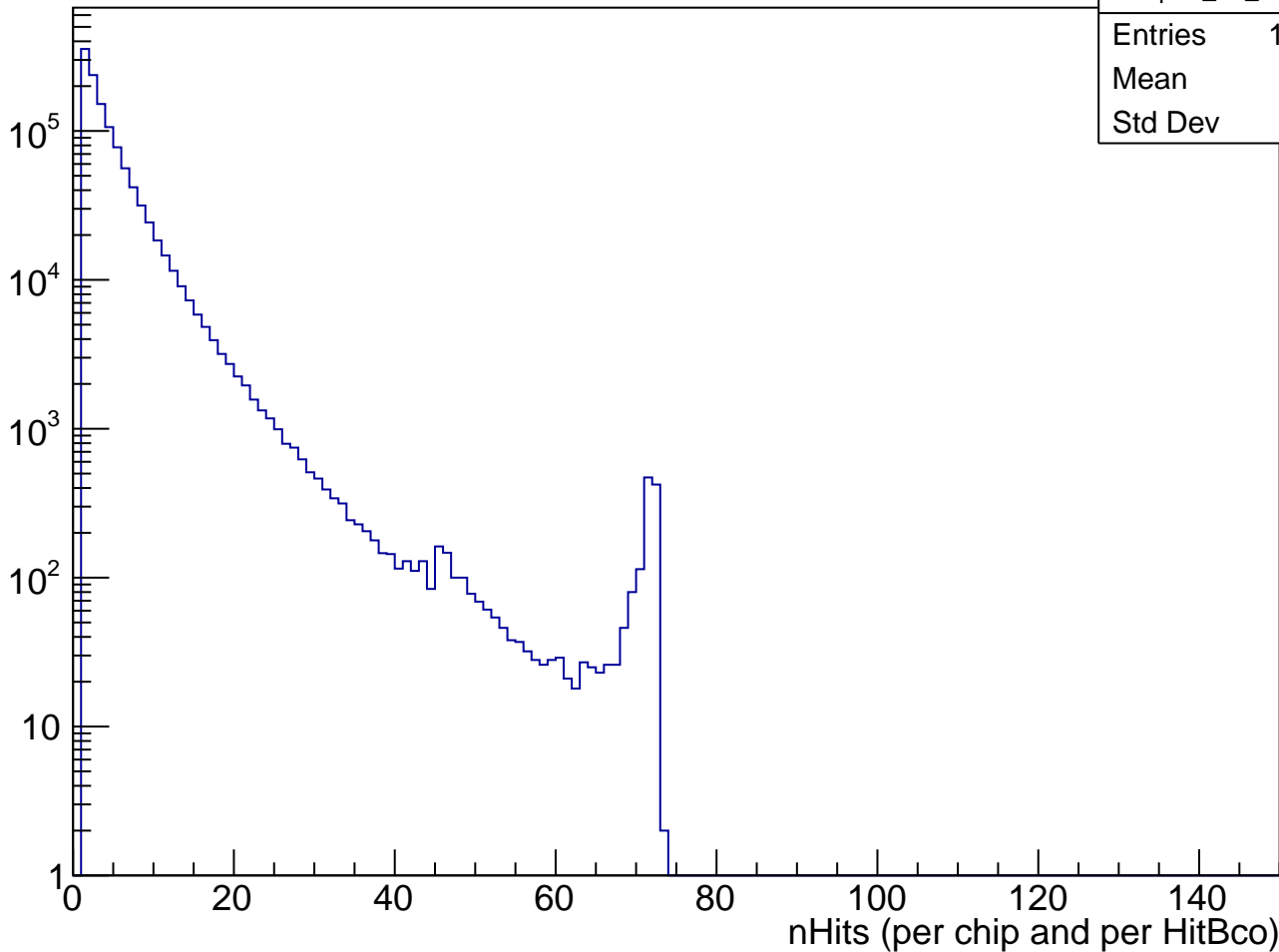
Entries 1176883

Mean 4.108

Std Dev 4.968

nChipHit_F3_Fch0_Chip1

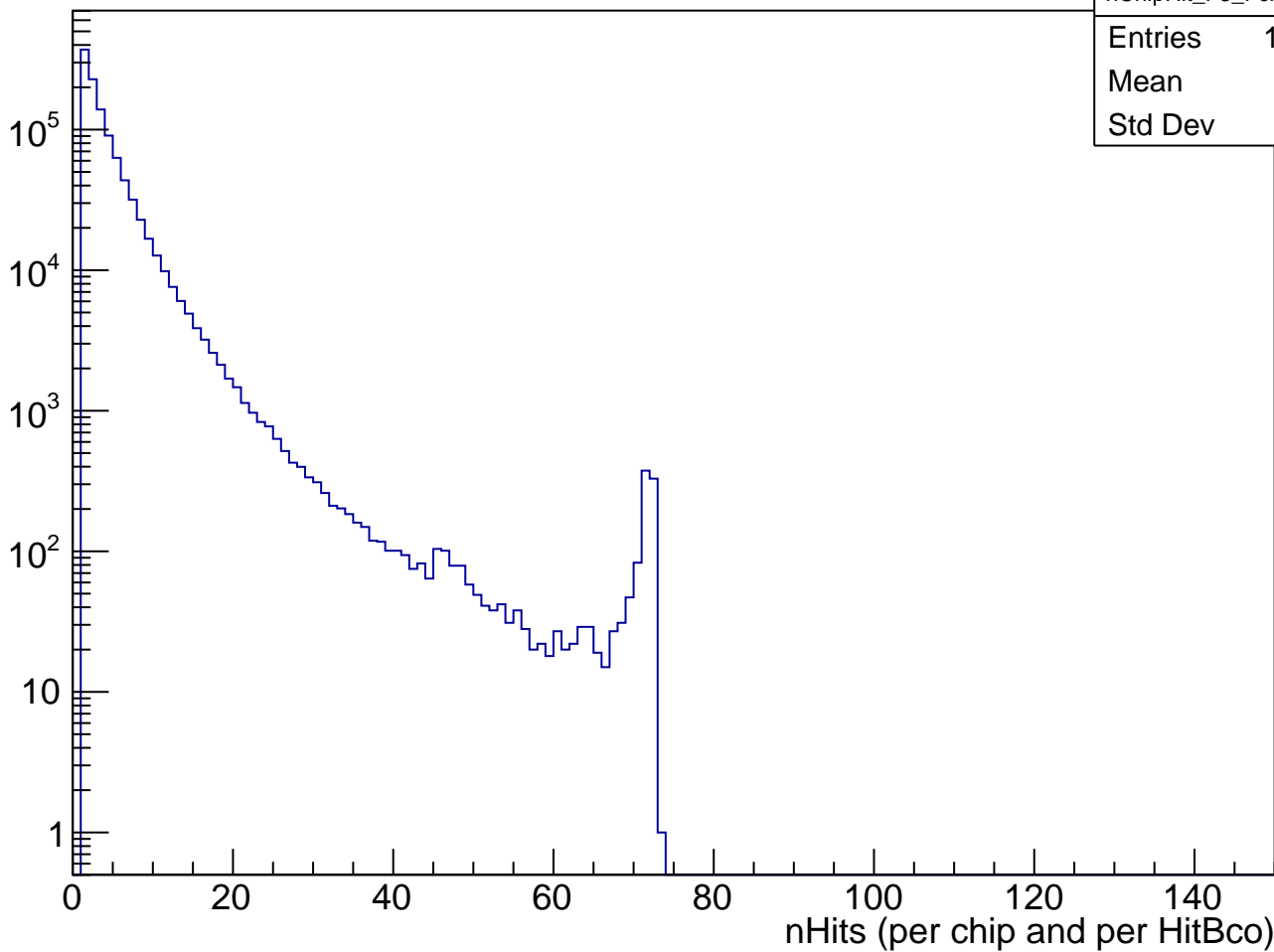
Entries



nChipHit_F3_Fch0_Chip1	
Entries	1180860
Mean	4.043
Std Dev	4.911

nChipHit_F3_Fch0_Chip10

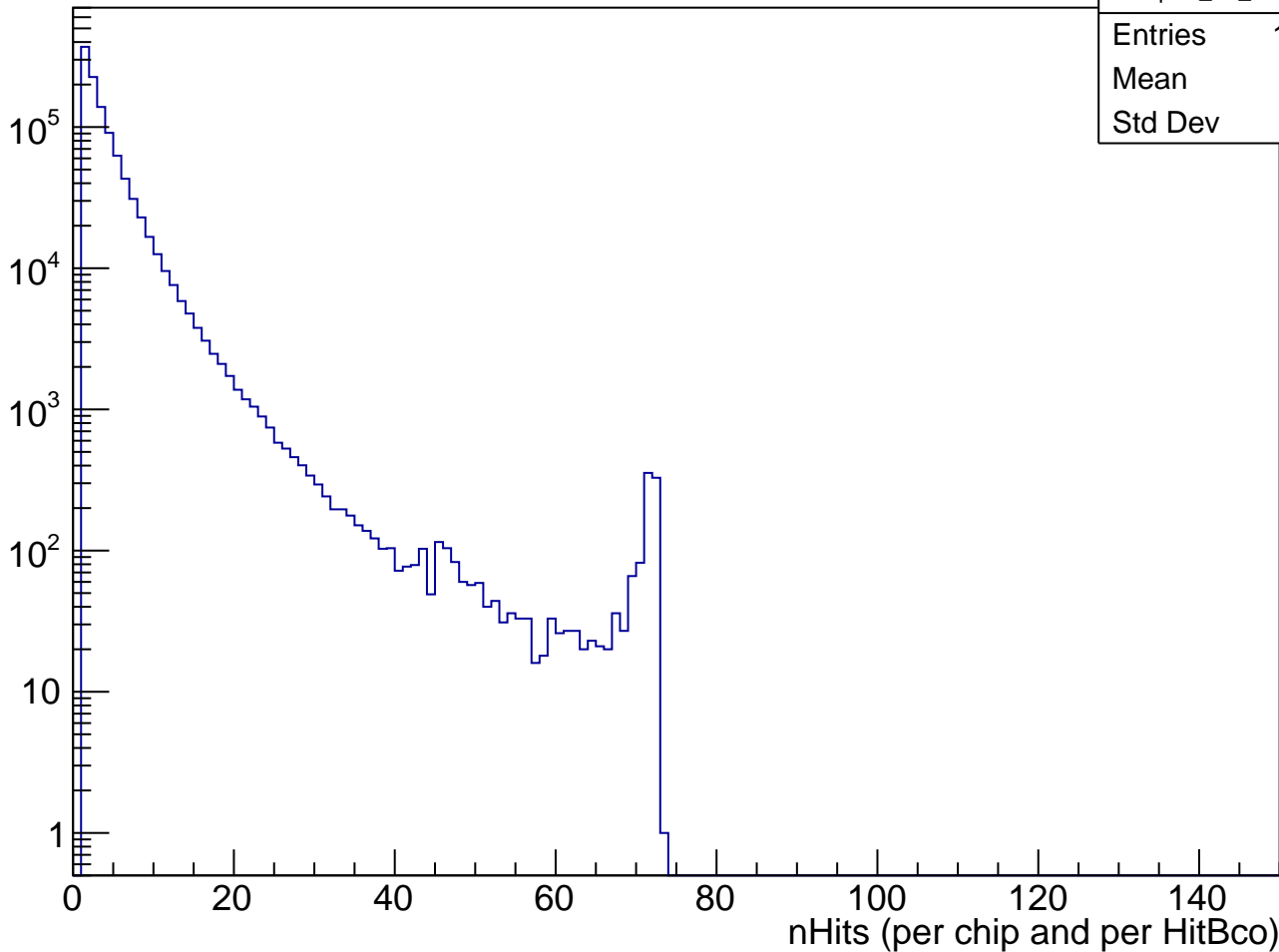
Entries



nChipHit_F3_Fch0_Chip10	
Entries	1072553
Mean	3.542
Std Dev	4.445

nChipHit_F3_Fch0_Chip11

Entries

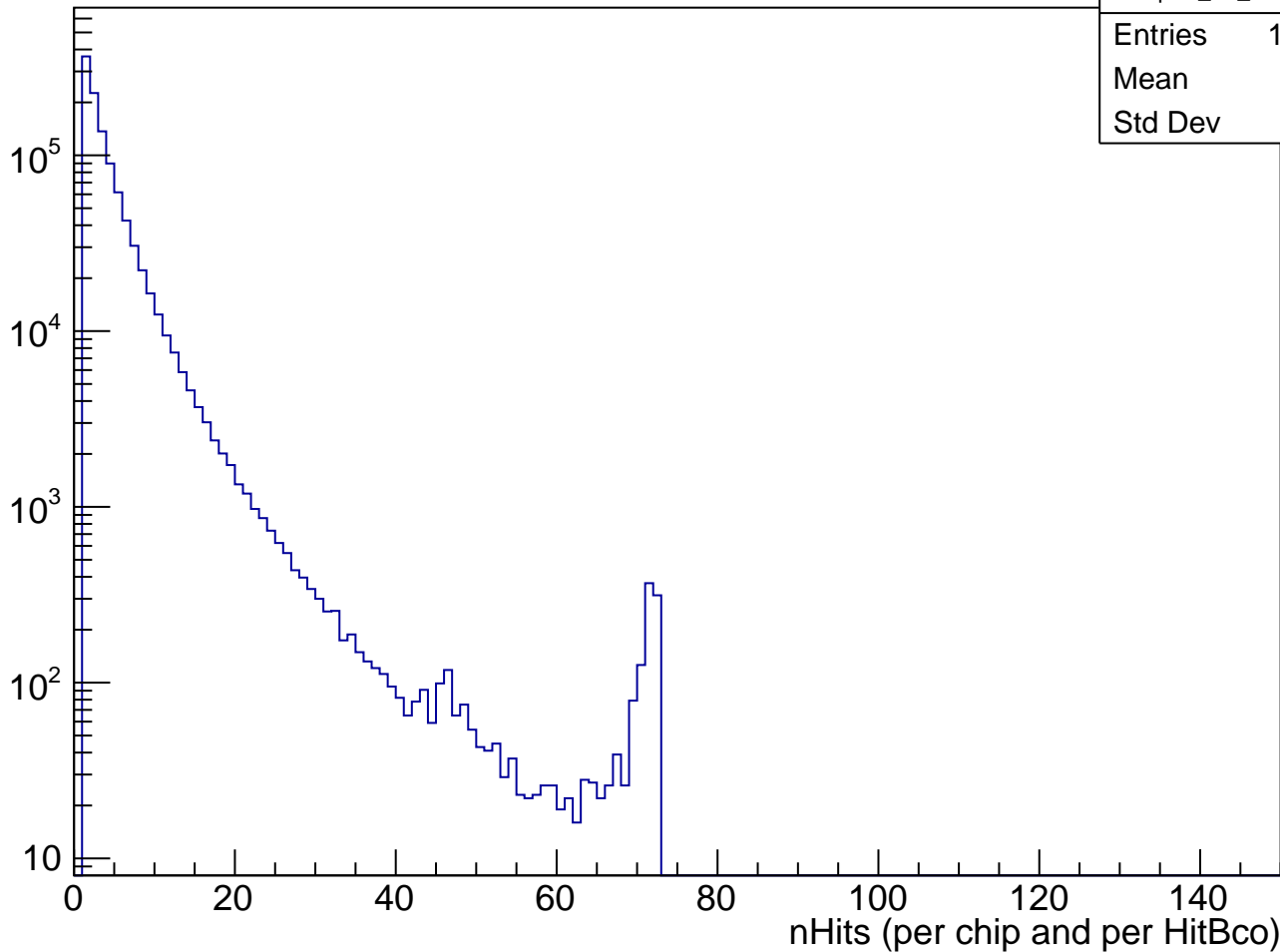


nChipHit_F3_Fch0_Chip11	
Entries	1067181
Mean	3.532
Std Dev	4.438

nHits (per chip and per HitBco)

nChipHit_F3_Fch0_Chip12

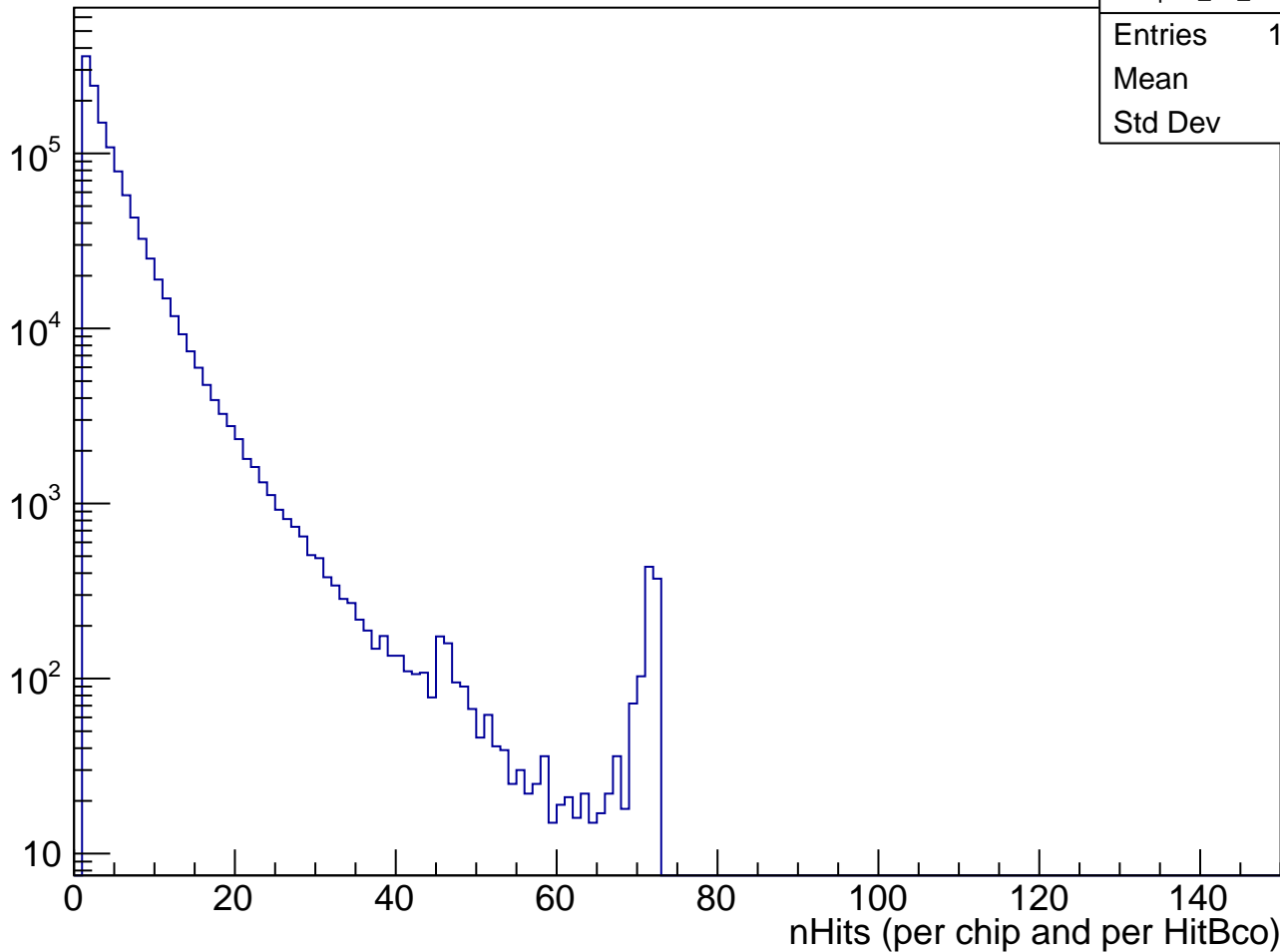
Entries



nChipHit_F3_Fch0_Chip12	
Entries	1055274
Mean	3.529
Std Dev	4.463

nChipHit_F3_Fch0_Chip13

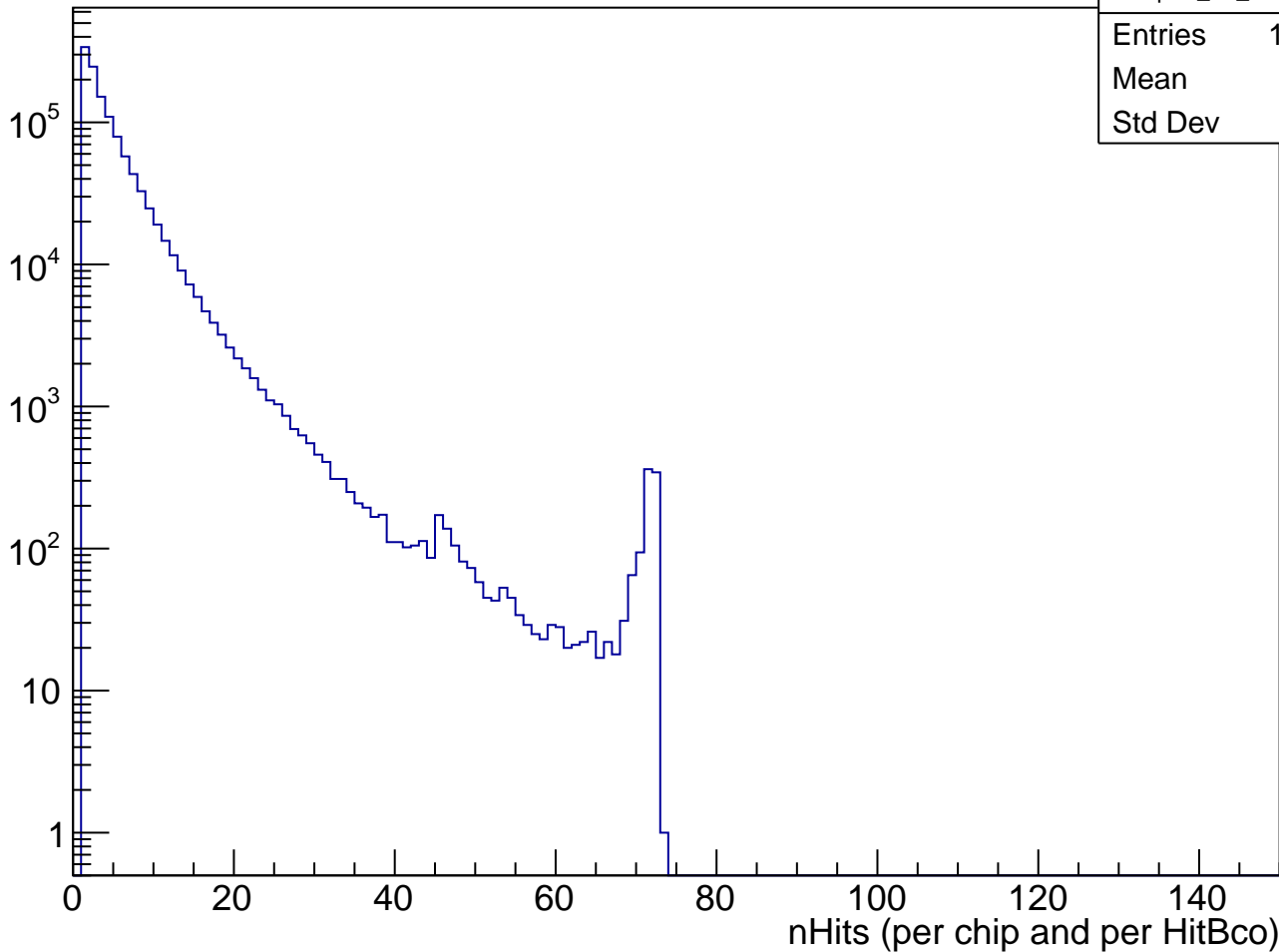
Entries



nChipHit_F3_Fch0_Chip13	
Entries	1198035
Mean	4.031
Std Dev	4.807

nChipHit_F3_Fch0_Chip14

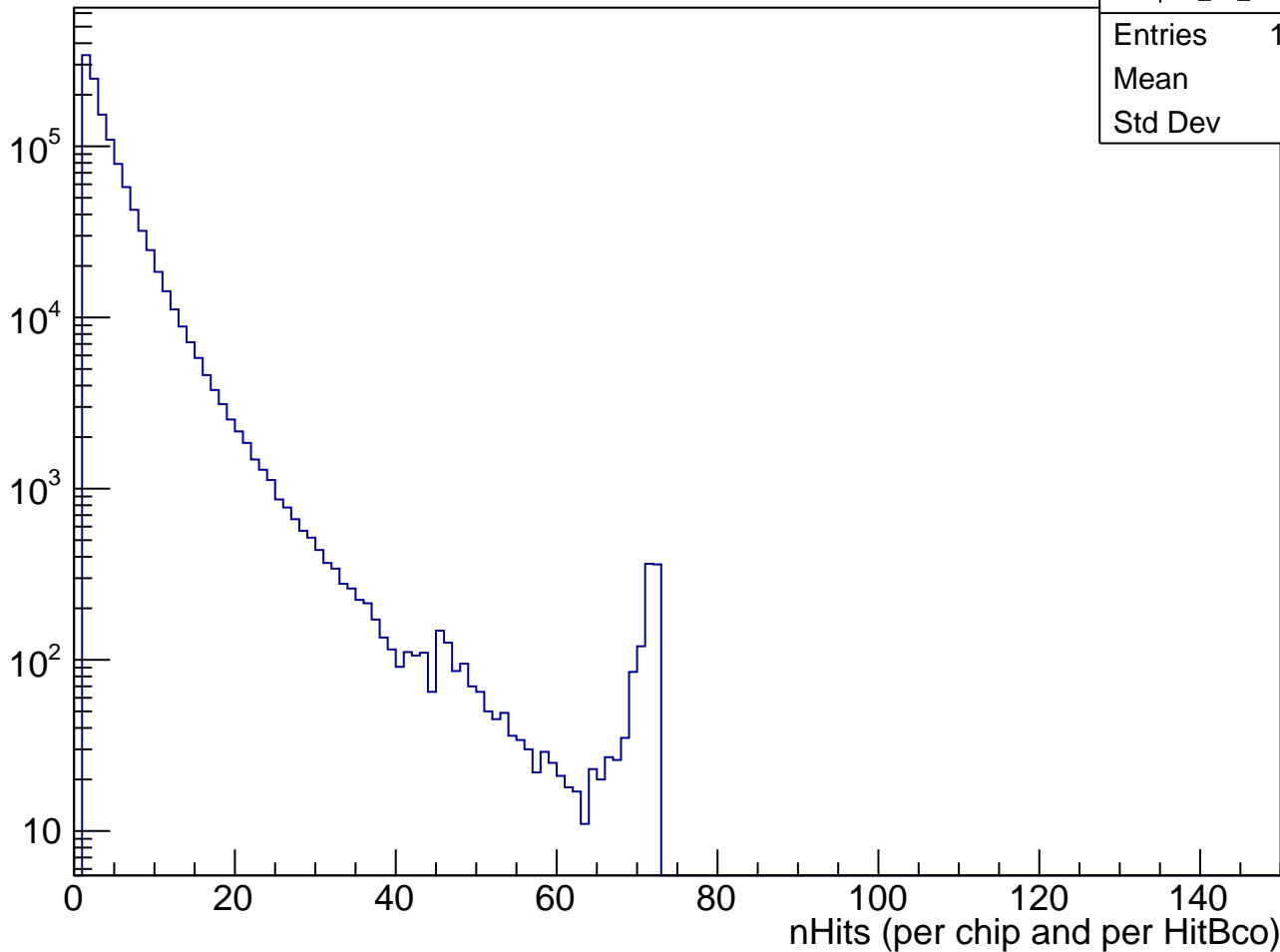
Entries



nChipHit_F3_Fch0_Chip14	
Entries	1183010
Mean	4.062
Std Dev	4.775

nChipHit_F3_Fch0_Chip15

Entries

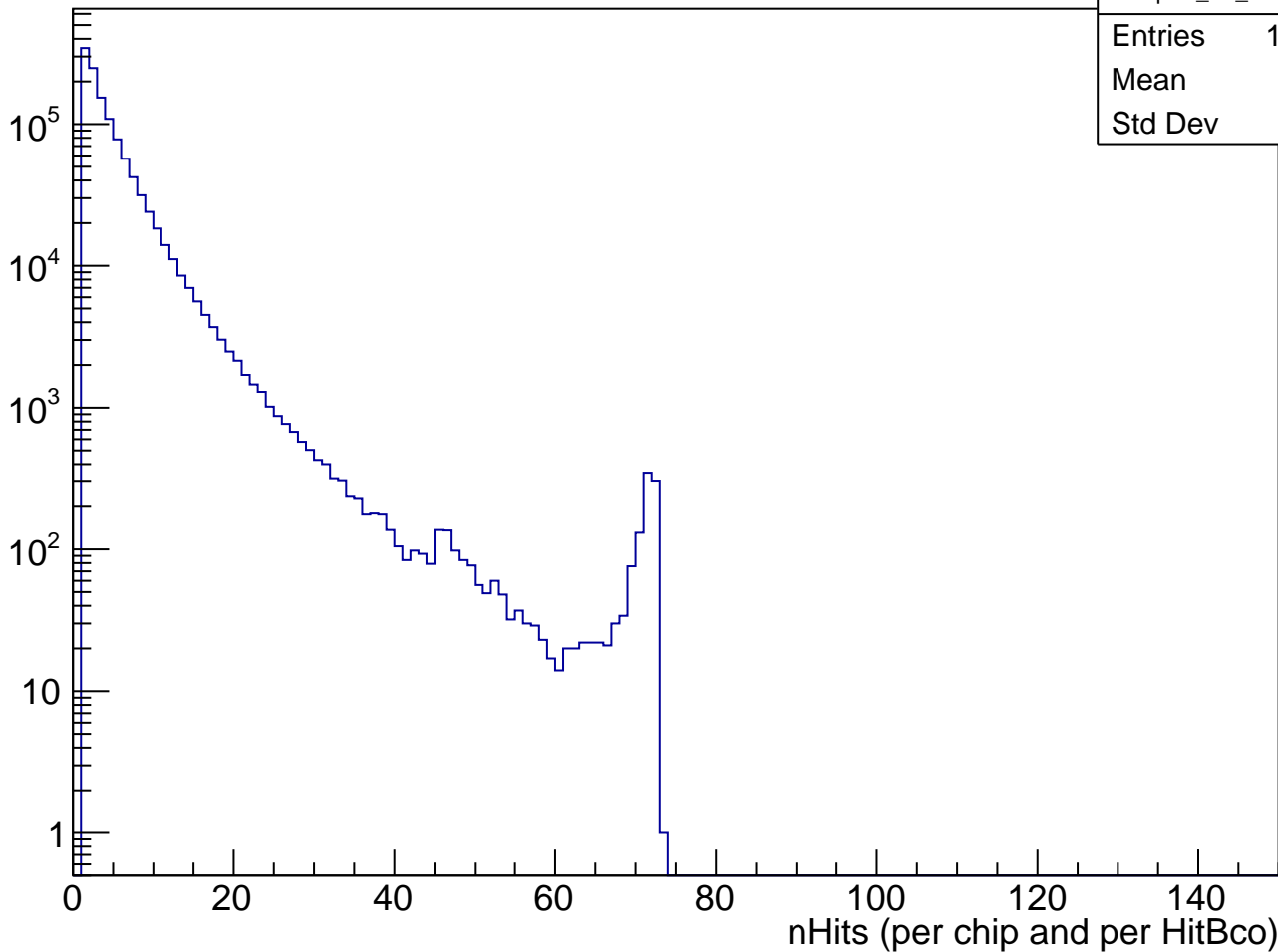


nChipHit_F3_Fch0_Chip15	
Entries	1183483
Mean	4.024
Std Dev	4.75

nHits (per chip and per HitBco)

nChipHit_F3_Fch0_Chip16

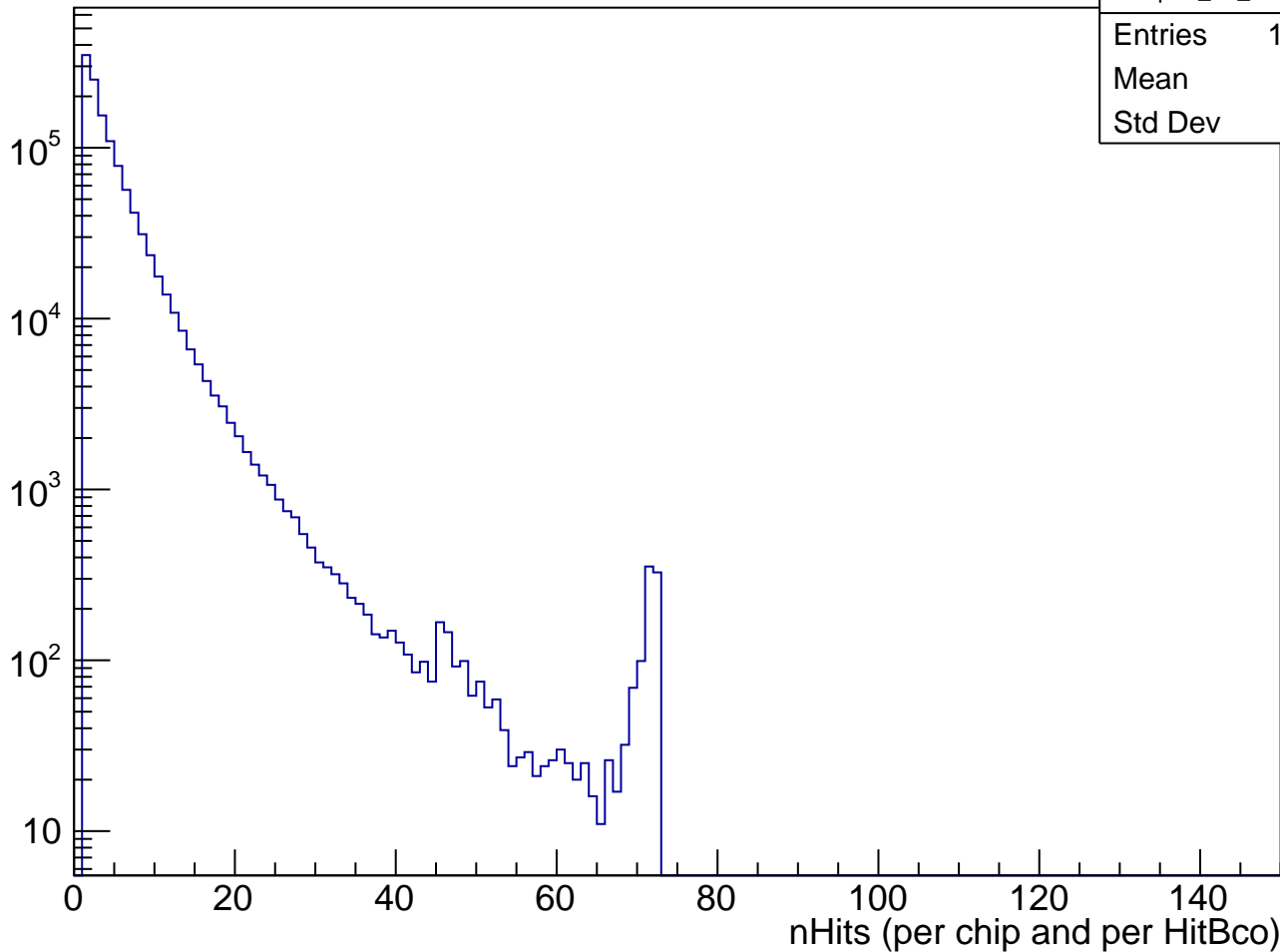
Entries



nChipHit_F3_Fch0_Chip16	
Entries	1182405
Mean	3.986
Std Dev	4.704

nChipHit_F3_Fch0_Chip17

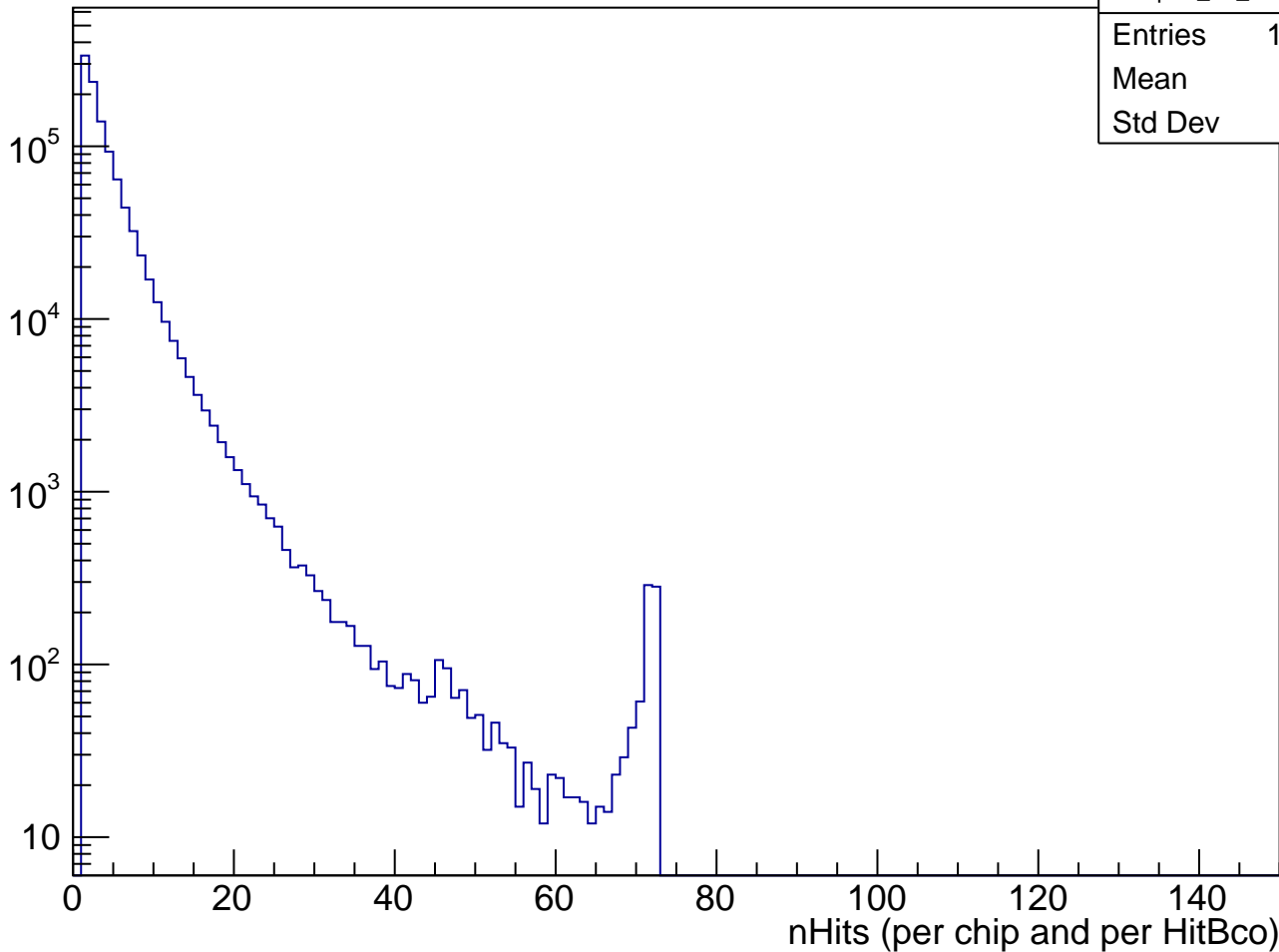
Entries



nChipHit_F3_Fch0_Chip17	
Entries	1186978
Mean	3.941
Std Dev	4.659

nChipHit_F3_Fch0_Chip18

Entries

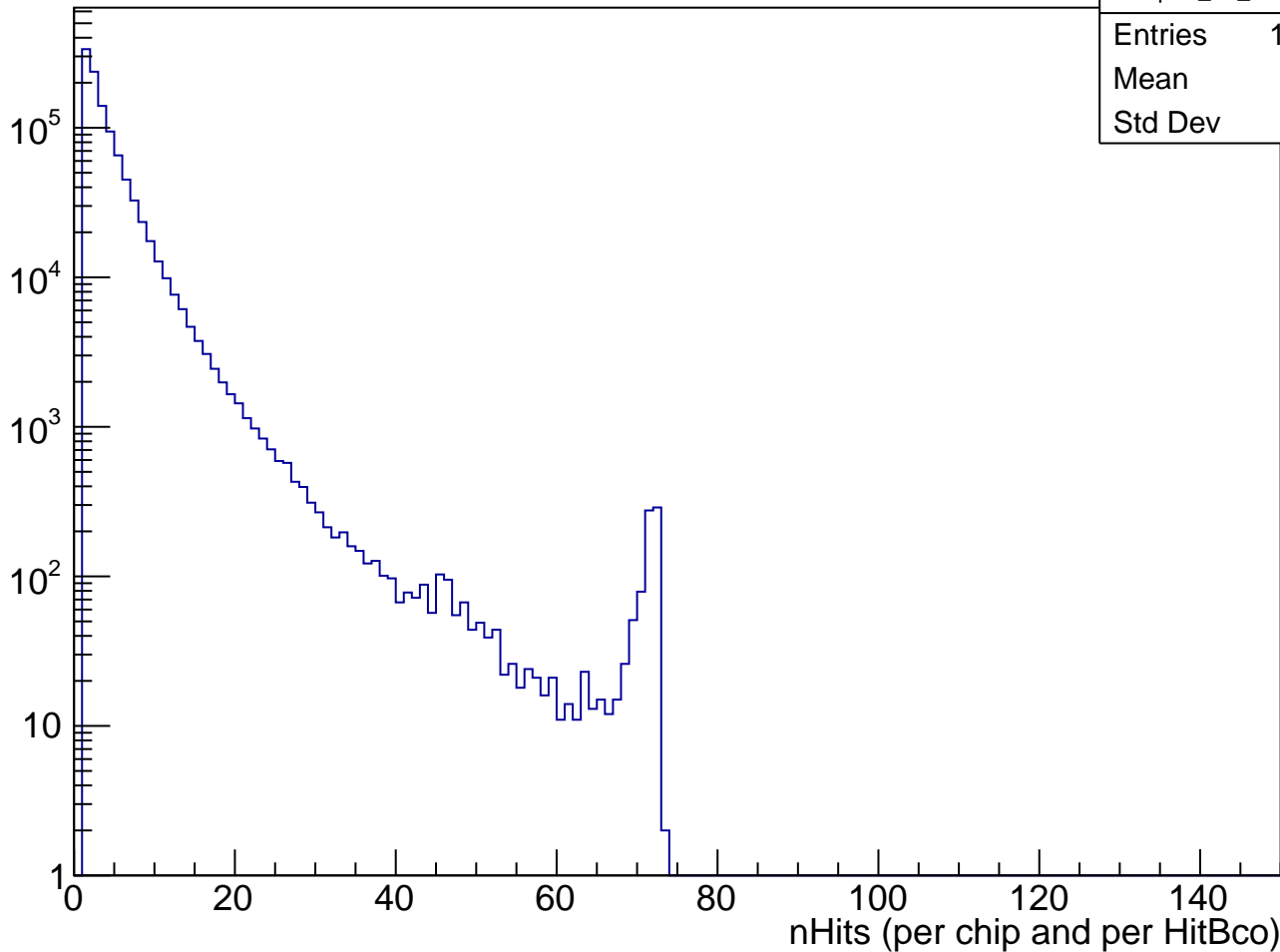


nChipHit_F3_Fch0_Chip18	
Entries	1047389
Mean	3.573
Std Dev	4.281

nHits (per chip and per HitBco)

nChipHit_F3_Fch0_Chip19

Entries

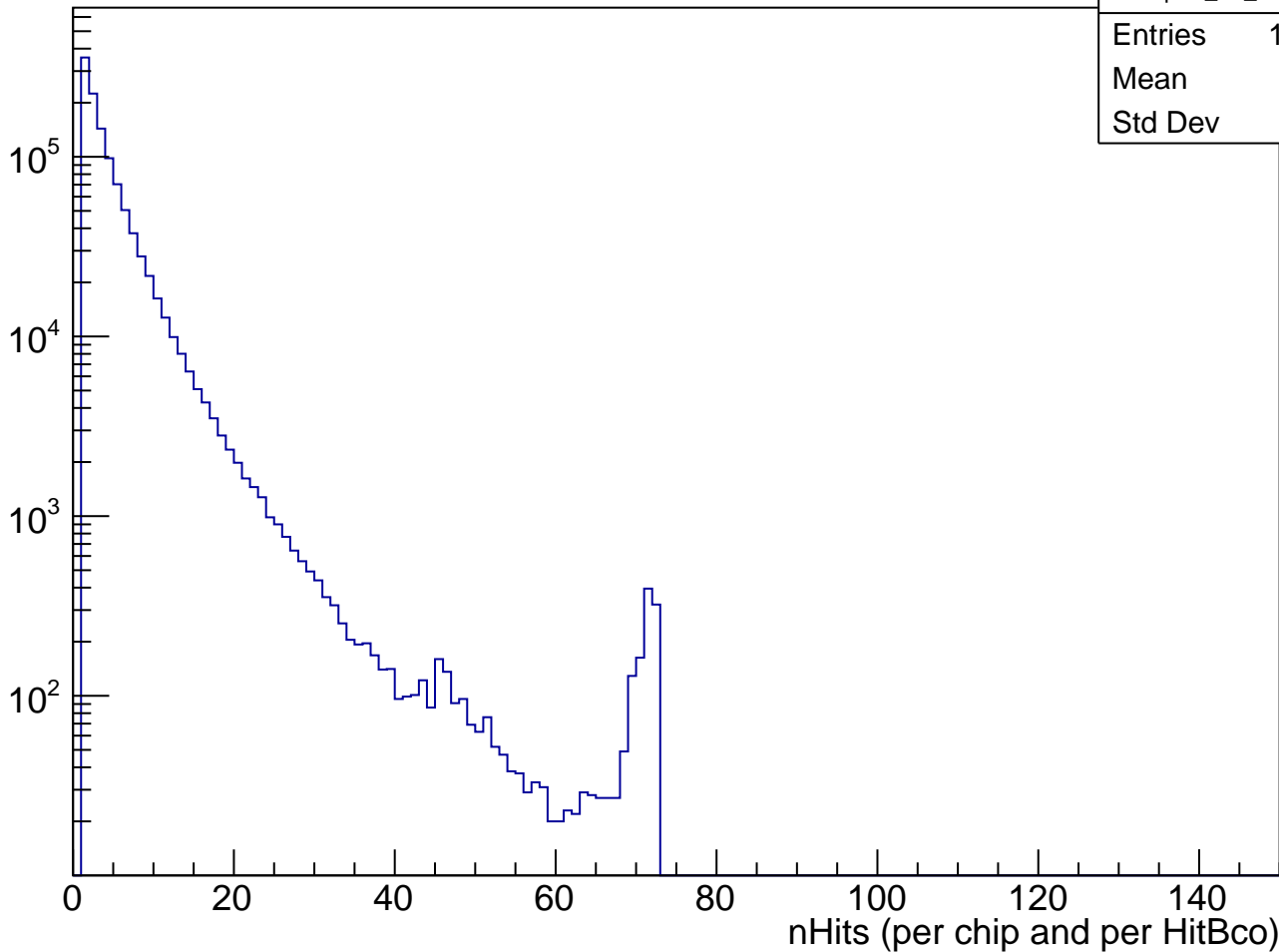


nChipHit_F3_Fch0_Chip19	
Entries	1055660
Mean	3.596
Std Dev	4.297

nHits (per chip and per HitBco)

nChipHit_F3_Fch0_Chip2

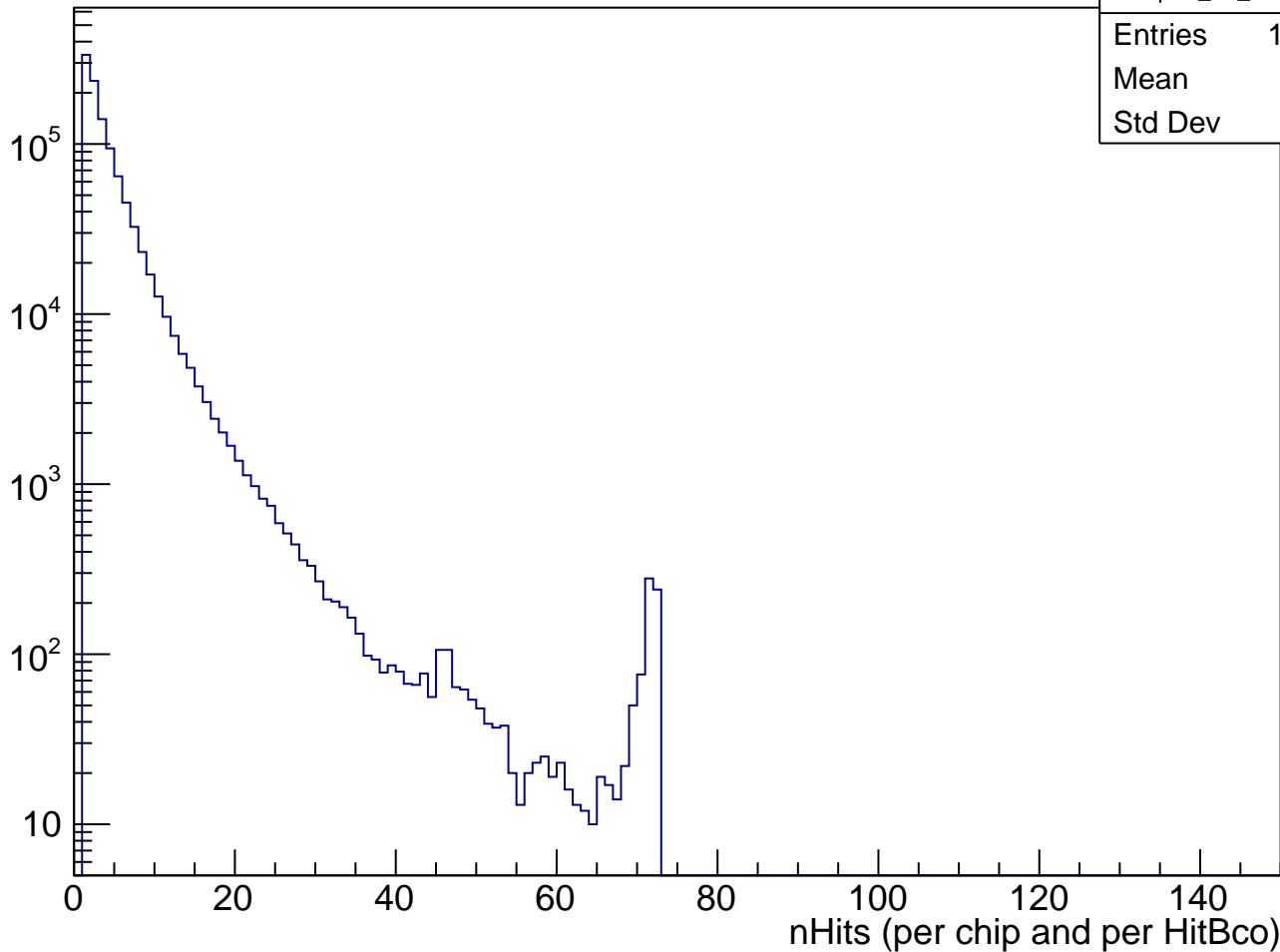
Entries



nChipHit_F3_Fch0_Chip2	
Entries	1118870
Mean	3.904
Std Dev	4.843

nChipHit_F3_Fch0_Chip20

Entries

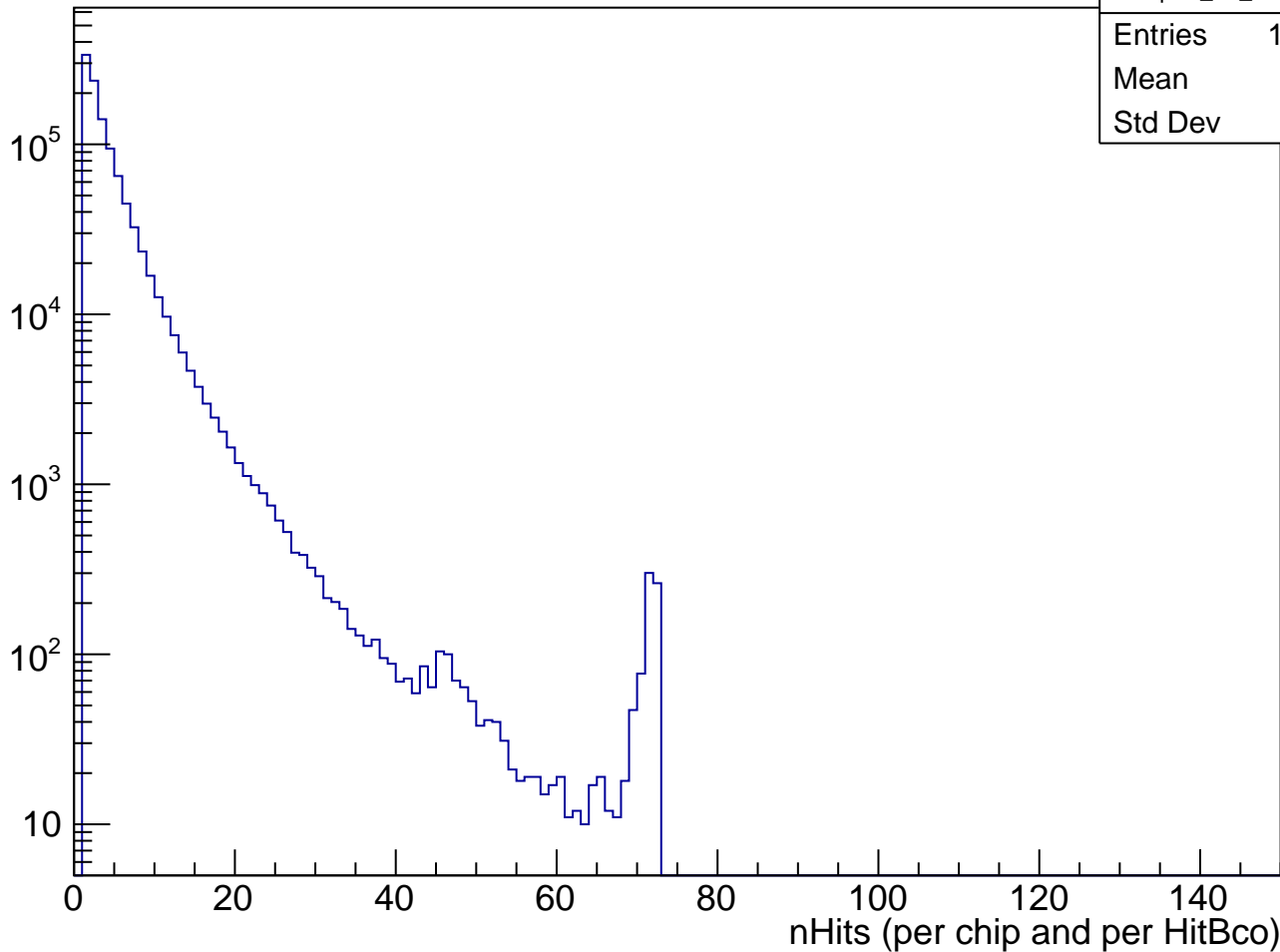


nChipHit_F3_Fch0_Chip20	
Entries	1050537
Mean	3.585
Std Dev	4.262

nHits (per chip and per HitBco)

nChipHit_F3_Fch0_Chip21

Entries

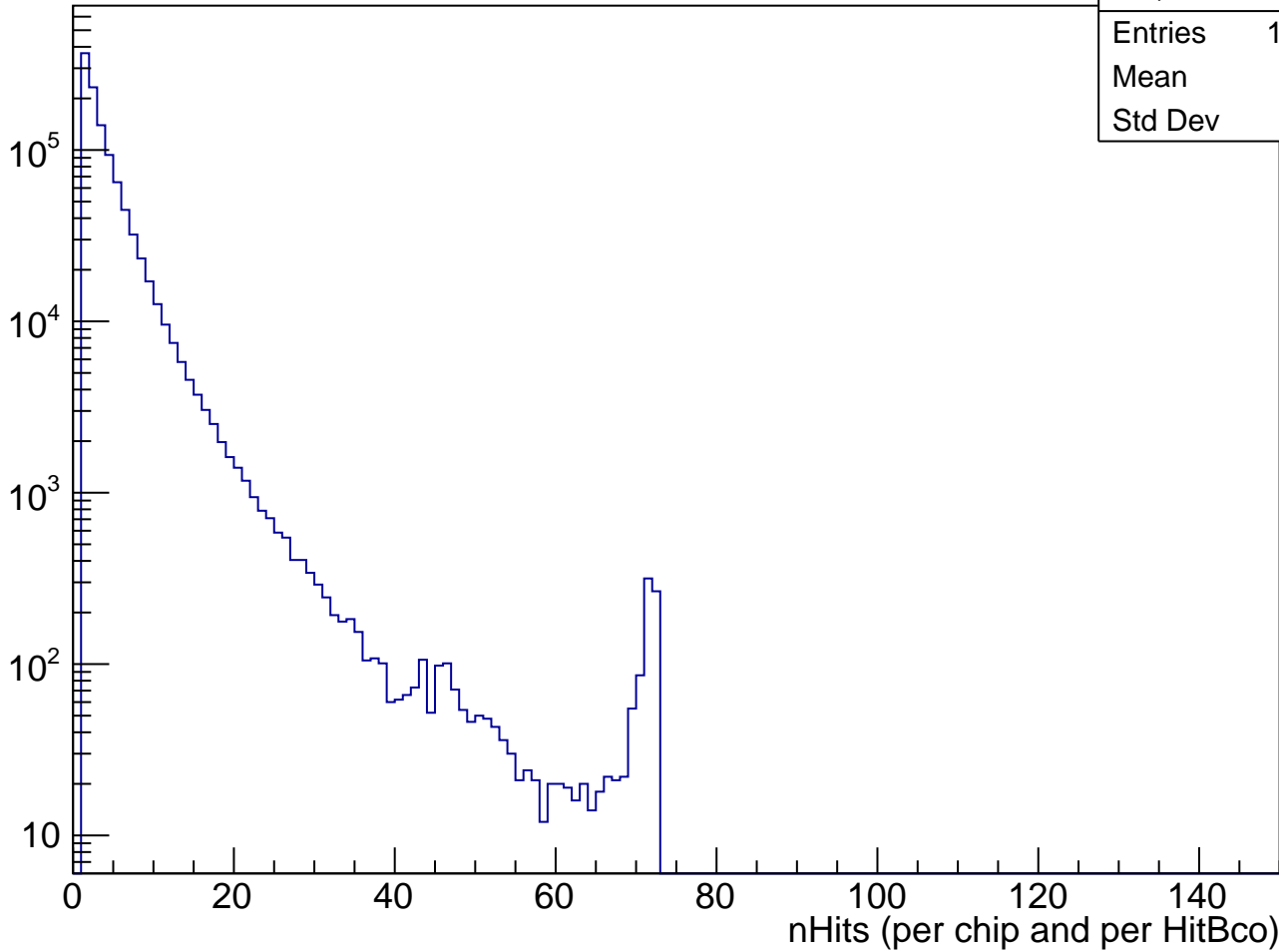


nChipHit_F3_Fch0_Chip21	
Entries	1054432
Mean	3.581
Std Dev	4.272

nHits (per chip and per HitBco)

nChipHit_F3_Fch0_Chip22

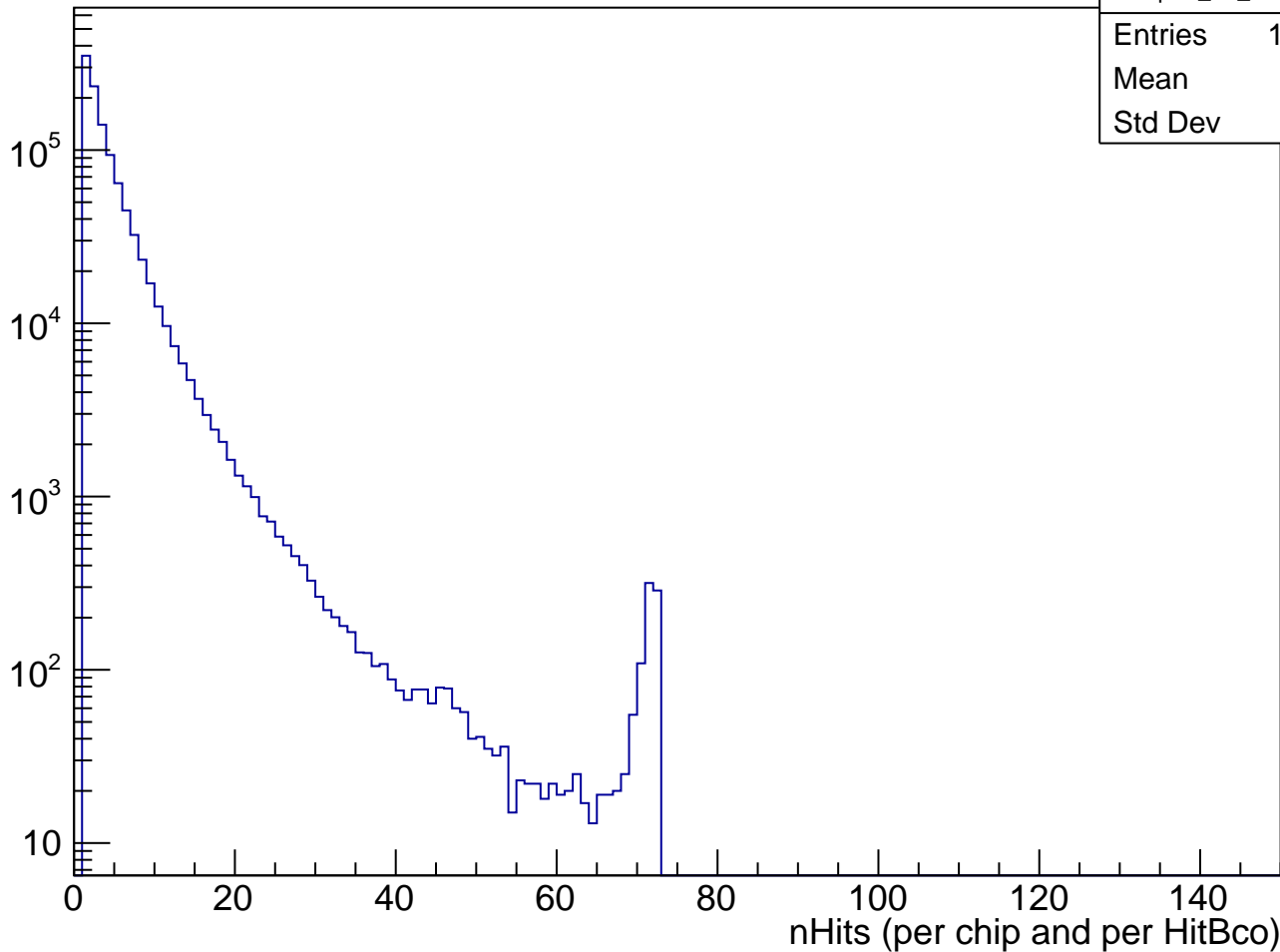
Entries



nChipHit_F3_Fch0_Chip22	
Entries	1078266
Mean	3.515
Std Dev	4.287

nChipHit_F3_Fch0_Chip23

Entries

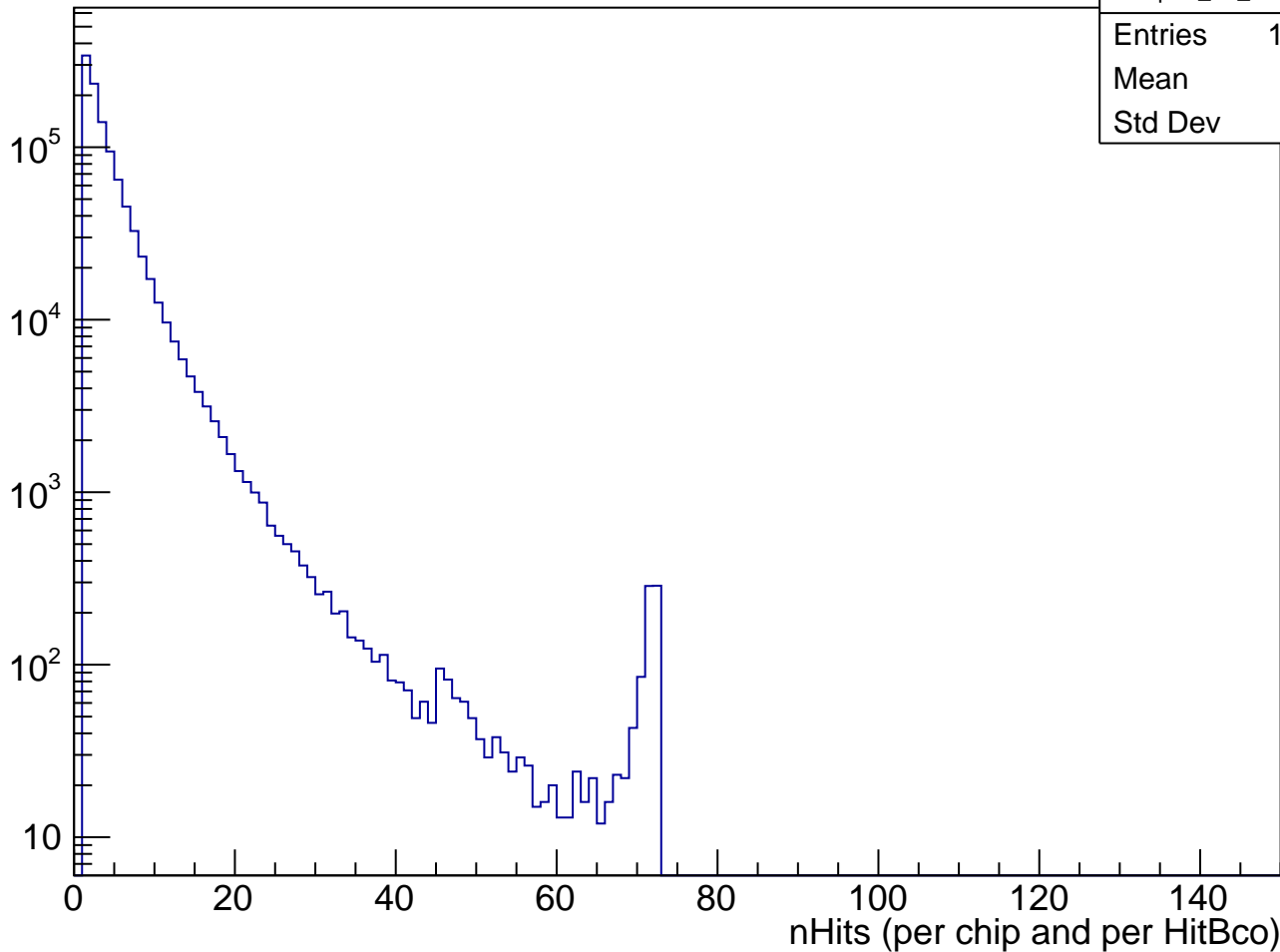


nChipHit_F3_Fch0_Chip23	
Entries	1062856
Mean	3.551
Std Dev	4.308

nHits (per chip and per HitBco)

nChipHit_F3_Fch0_Chip24

Entries

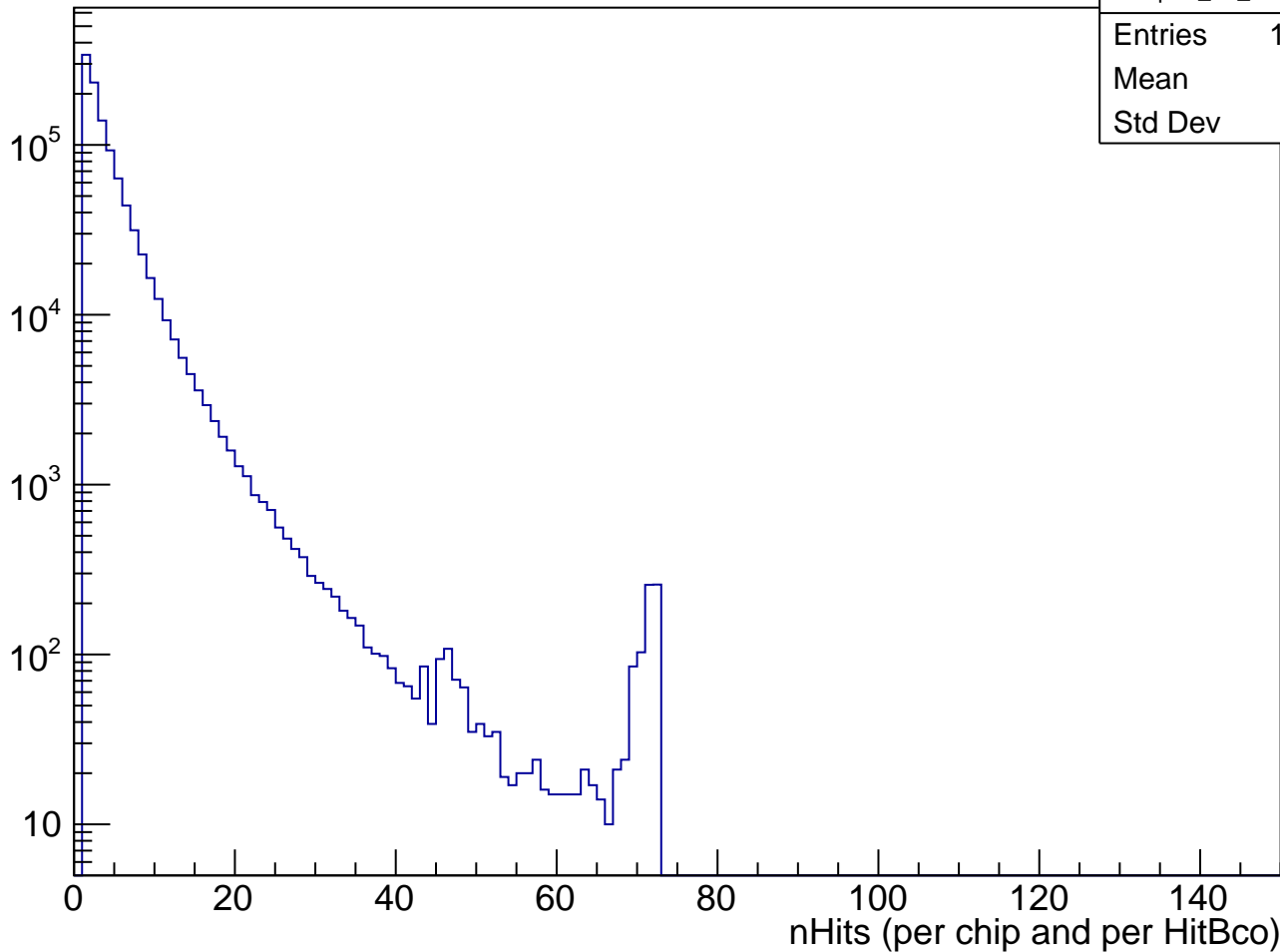


nChipHit_F3_Fch0_Chip24	
Entries	1055328
Mean	3.581
Std Dev	4.287

nHits (per chip and per HitBco)

nChipHit_F3_Fch0_Chip25

Entries

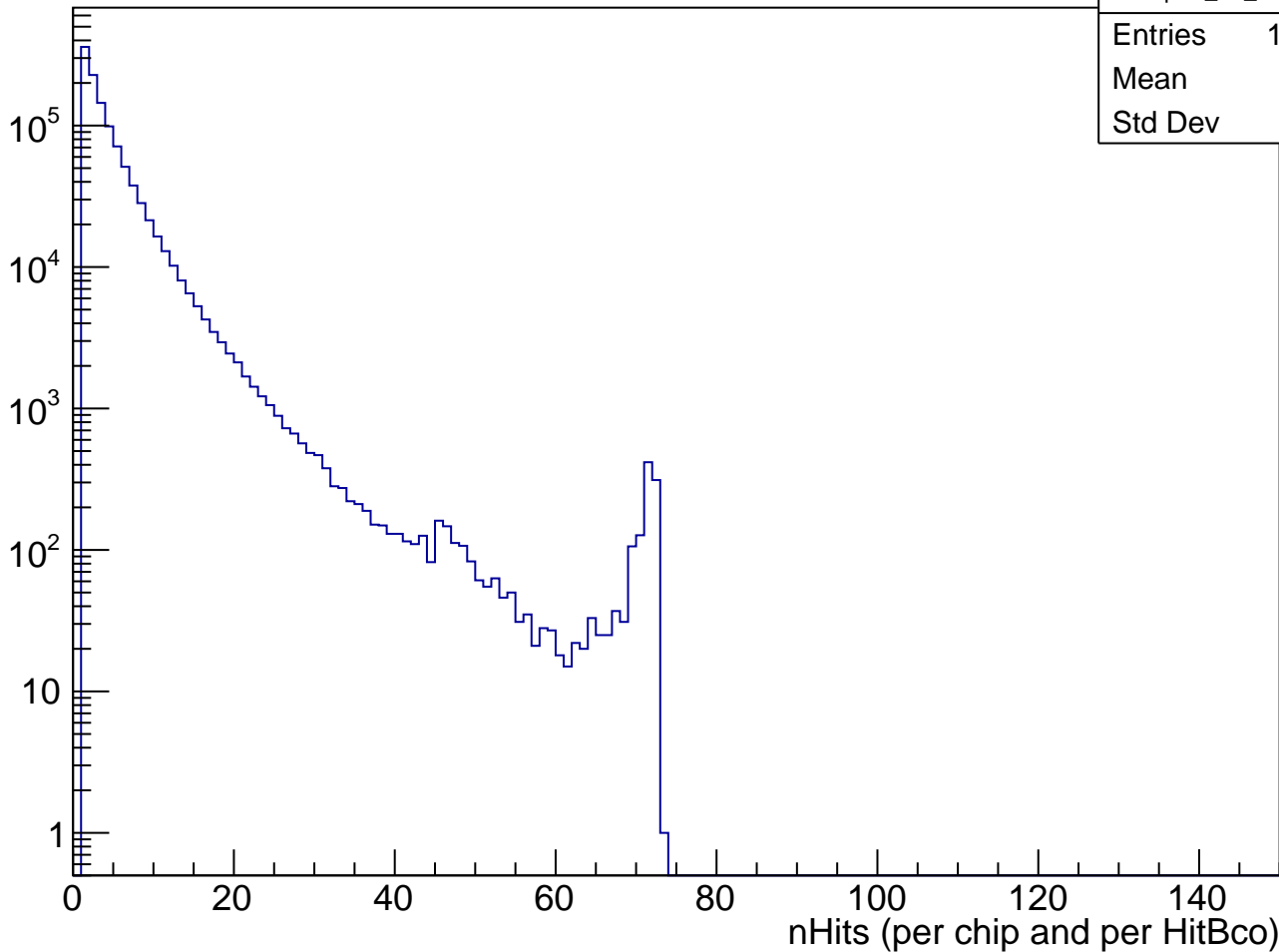


nChipHit_F3_Fch0_Chip25	
Entries	1043893
Mean	3.539
Std Dev	4.252

nHits (per chip and per HitBco)

nChipHit_F3_Fch0_Chip3

Entries



nChipHit_F3_Fch0_Chip3

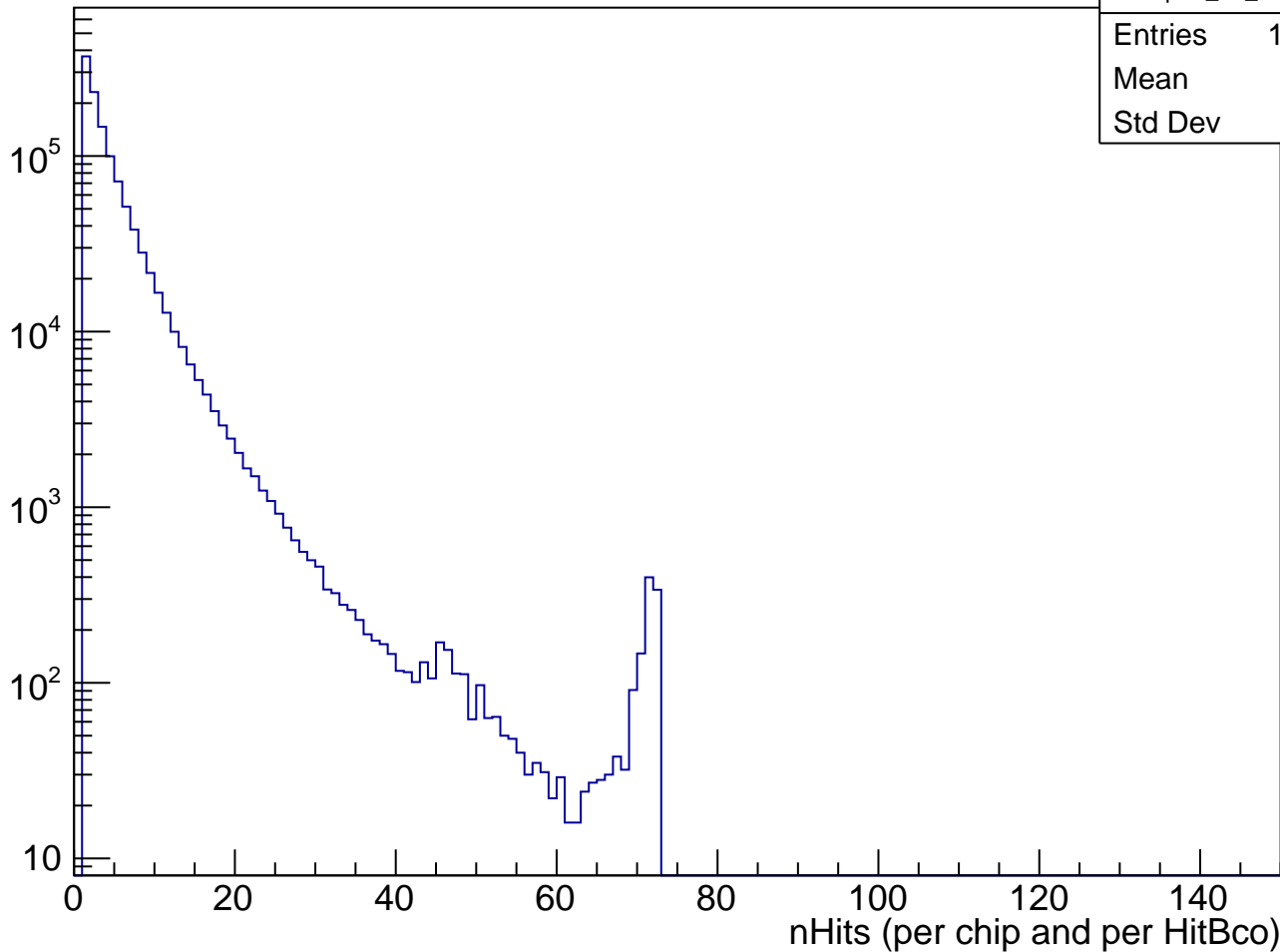
Entries 1129239

Mean 3.906

Std Dev 4.83

nChipHit_F3_Fch0_Chip4

Entries



nChipHit_F3_Fch0_Chip4

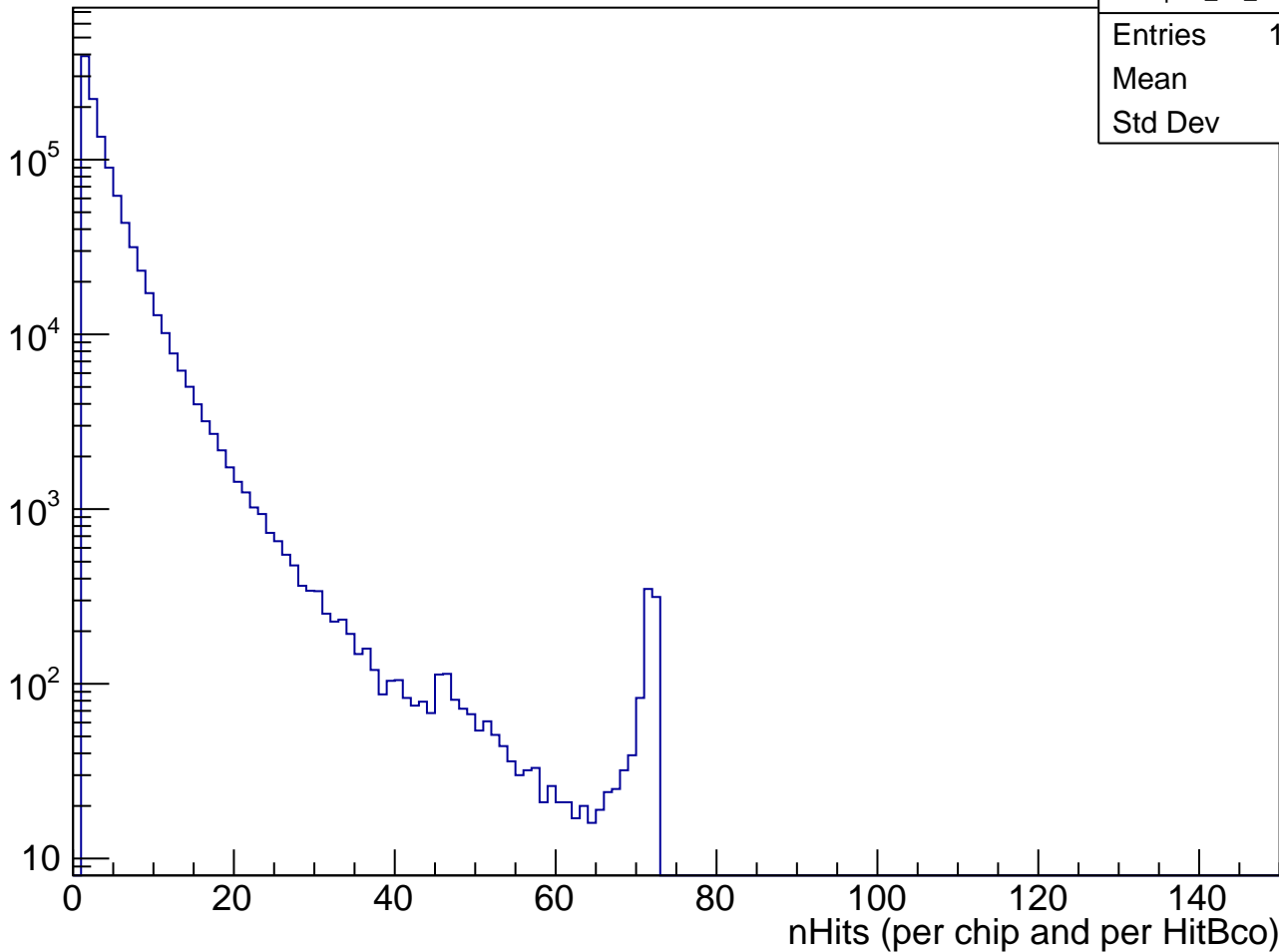
Entries 1147125

Mean 3.888

Std Dev 4.843

nChipHit_F3_Fch0_Chip5

Entries

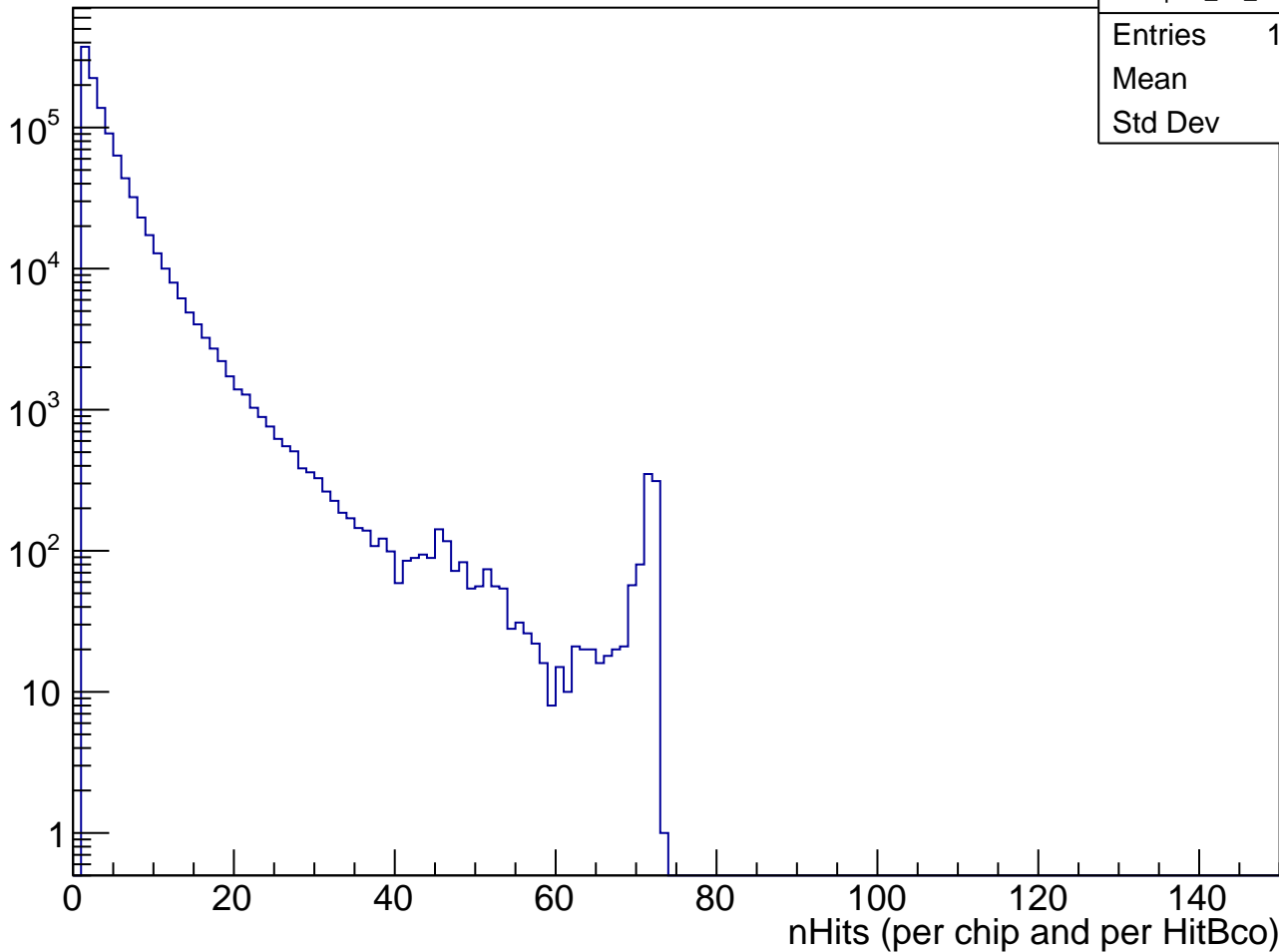


nChipHit_F3_Fch0_Chip5	
Entries	1083950
Mean	3.521
Std Dev	4.449

nHits (per chip and per HitBco)

nChipHit_F3_Fch0_Chip6

Entries



nChipHit_F3_Fch0_Chip6

Entries 1072869

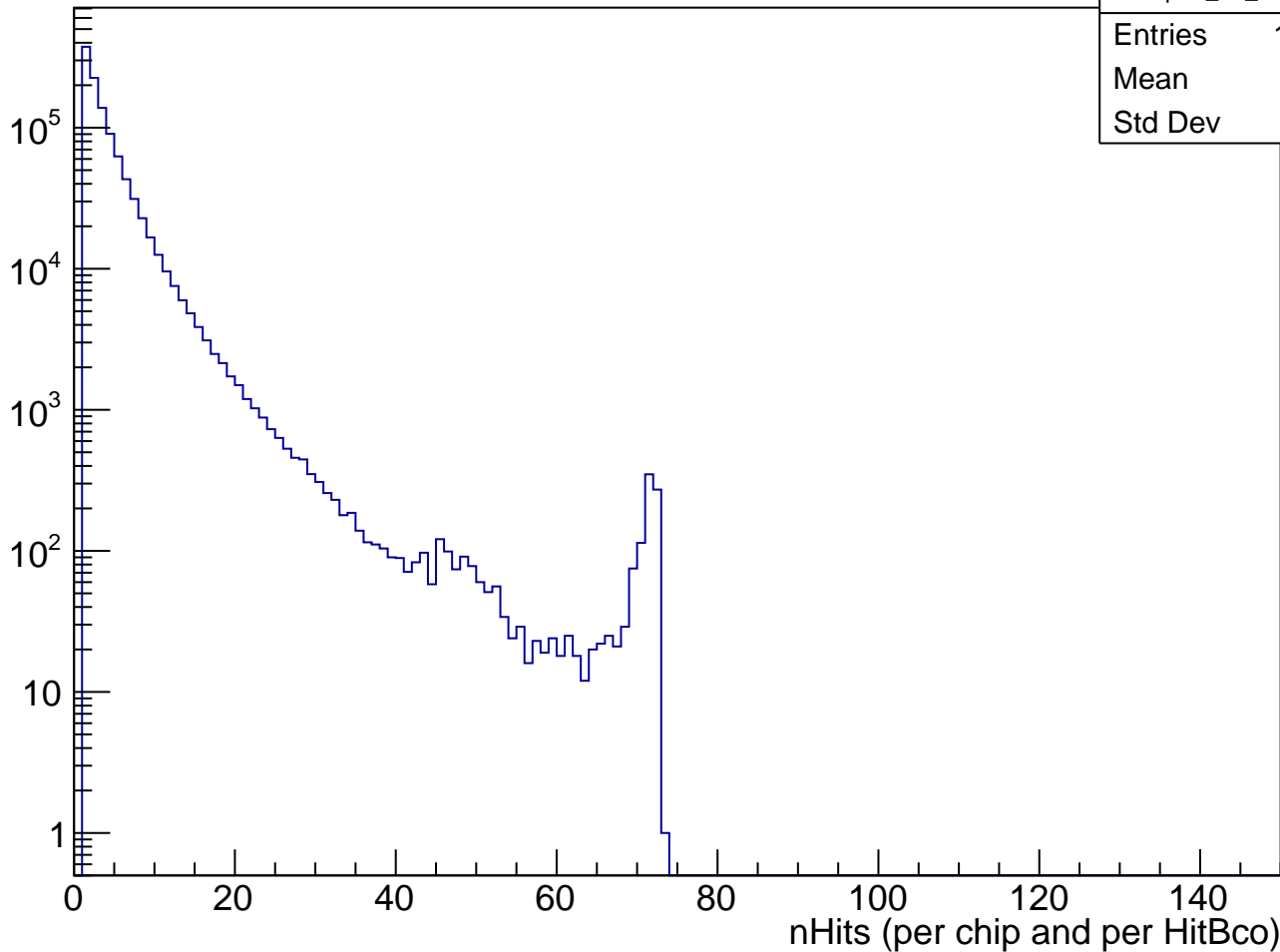
Mean 3.561

Std Dev 4.449

nHits (per chip and per HitBco)

nChipHit_F3_Fch0_Chip7

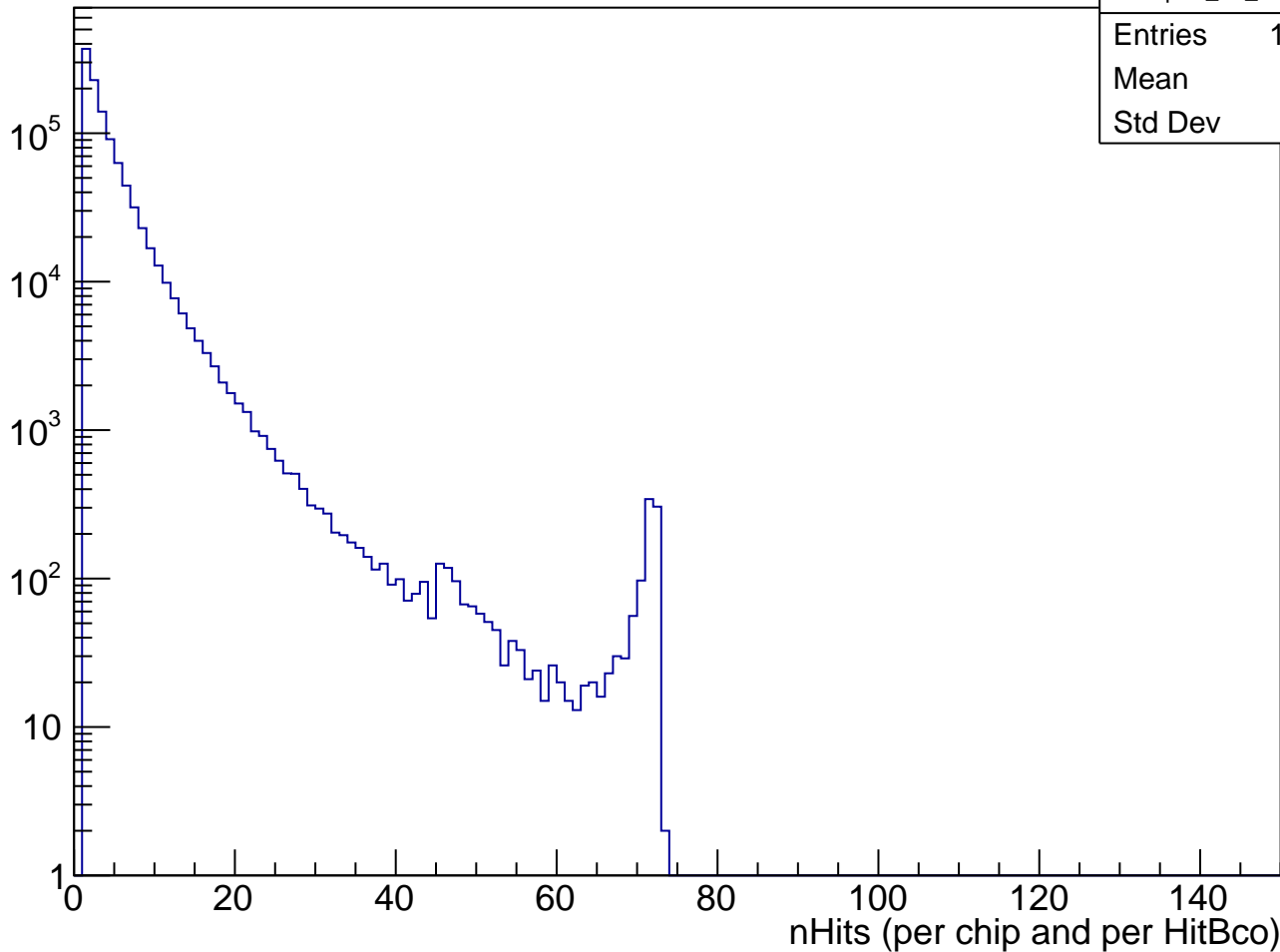
Entries



nChipHit_F3_Fch0_Chip7	
Entries	1070551
Mean	3.528
Std Dev	4.427

nChipHit_F3_Fch0_Chip8

Entries



nChipHit_F3_Fch0_Chip8

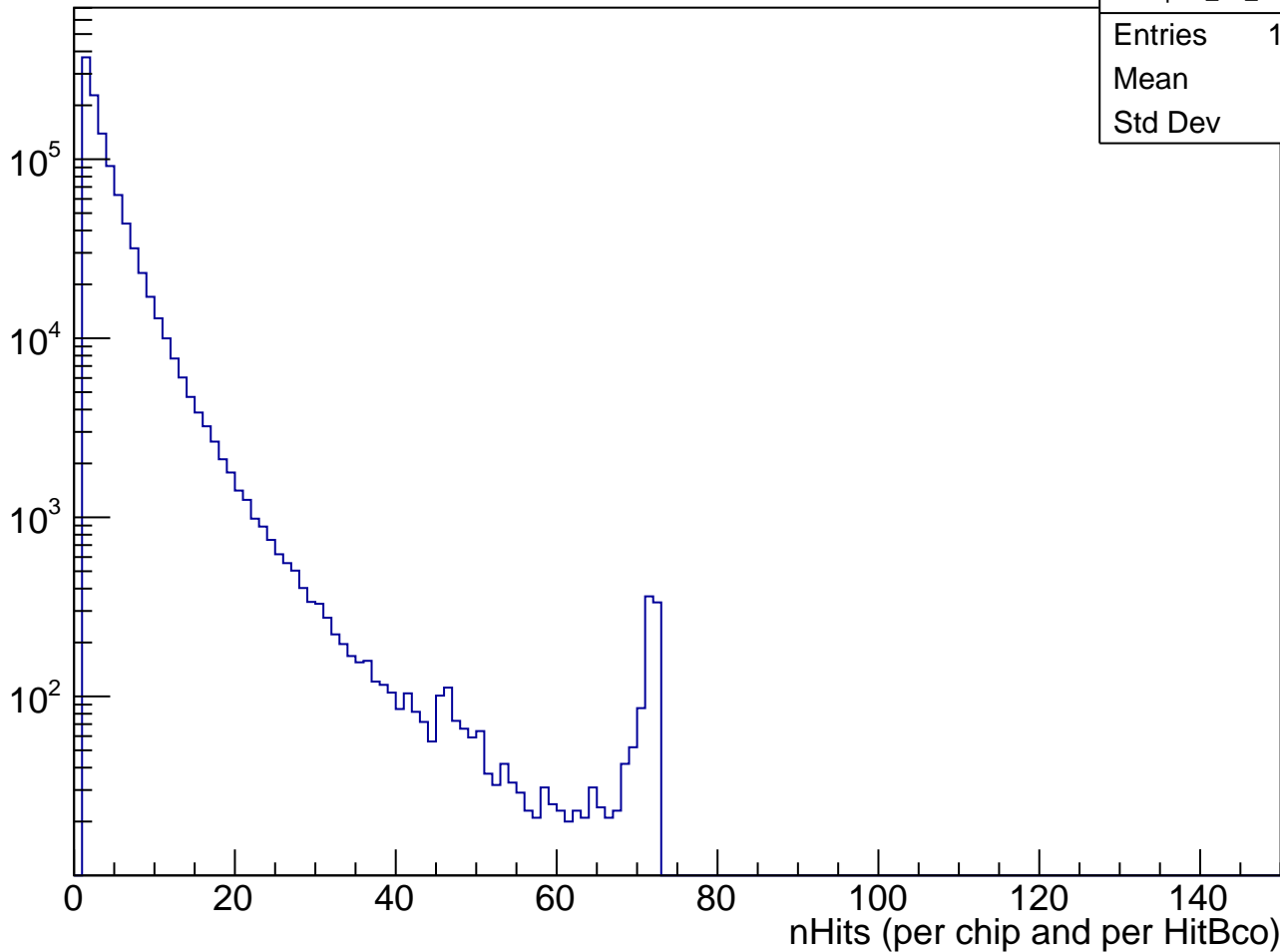
Entries 1075040

Mean 3.555

Std Dev 4.438

nChipHit_F3_Fch0_Chip9

Entries



nChipHit_F3_Fch0_Chip9

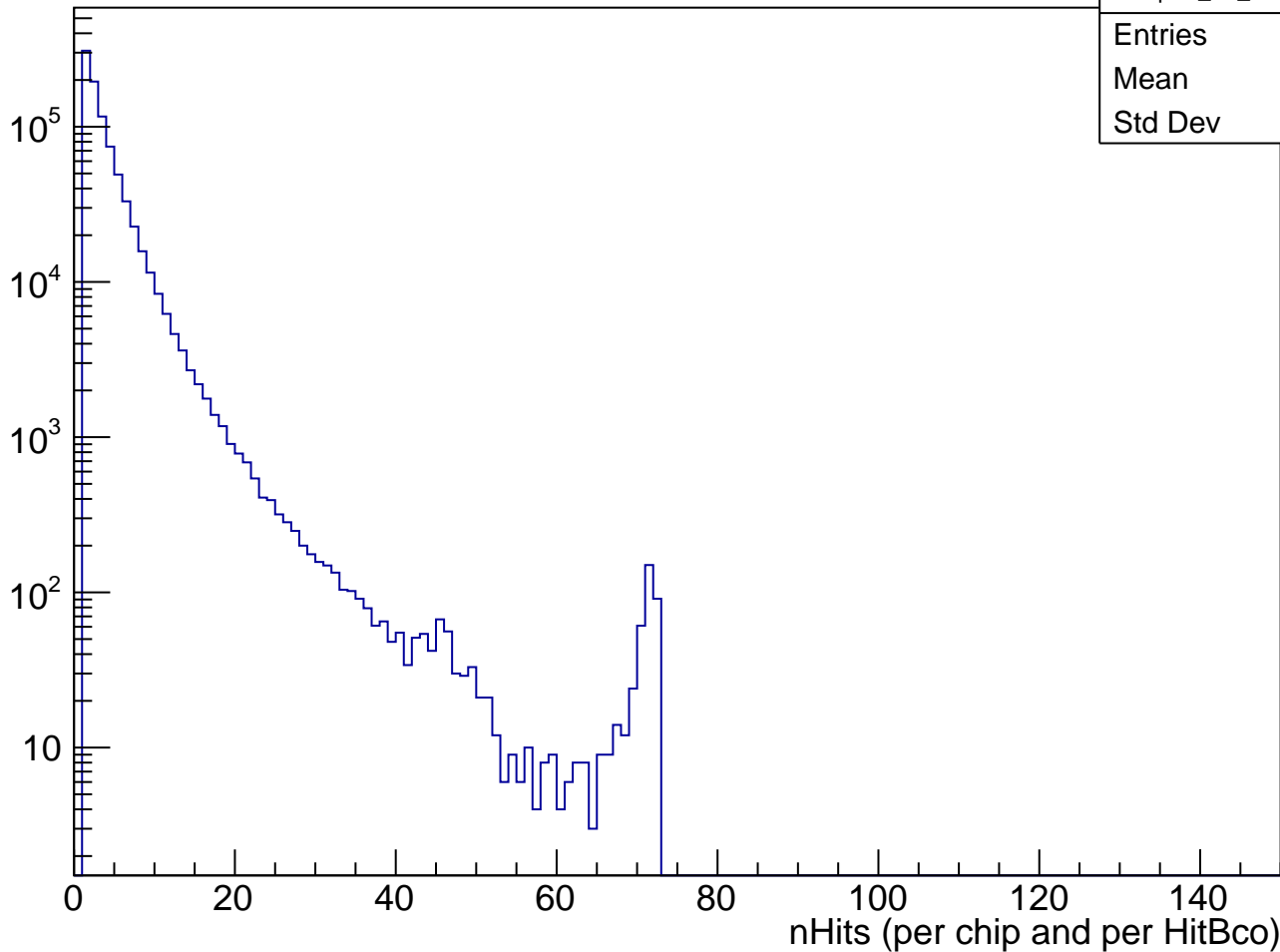
Entries 1074548

Mean 3.555

Std Dev 4.461

nChipHit_F3_Fch10_Chip0

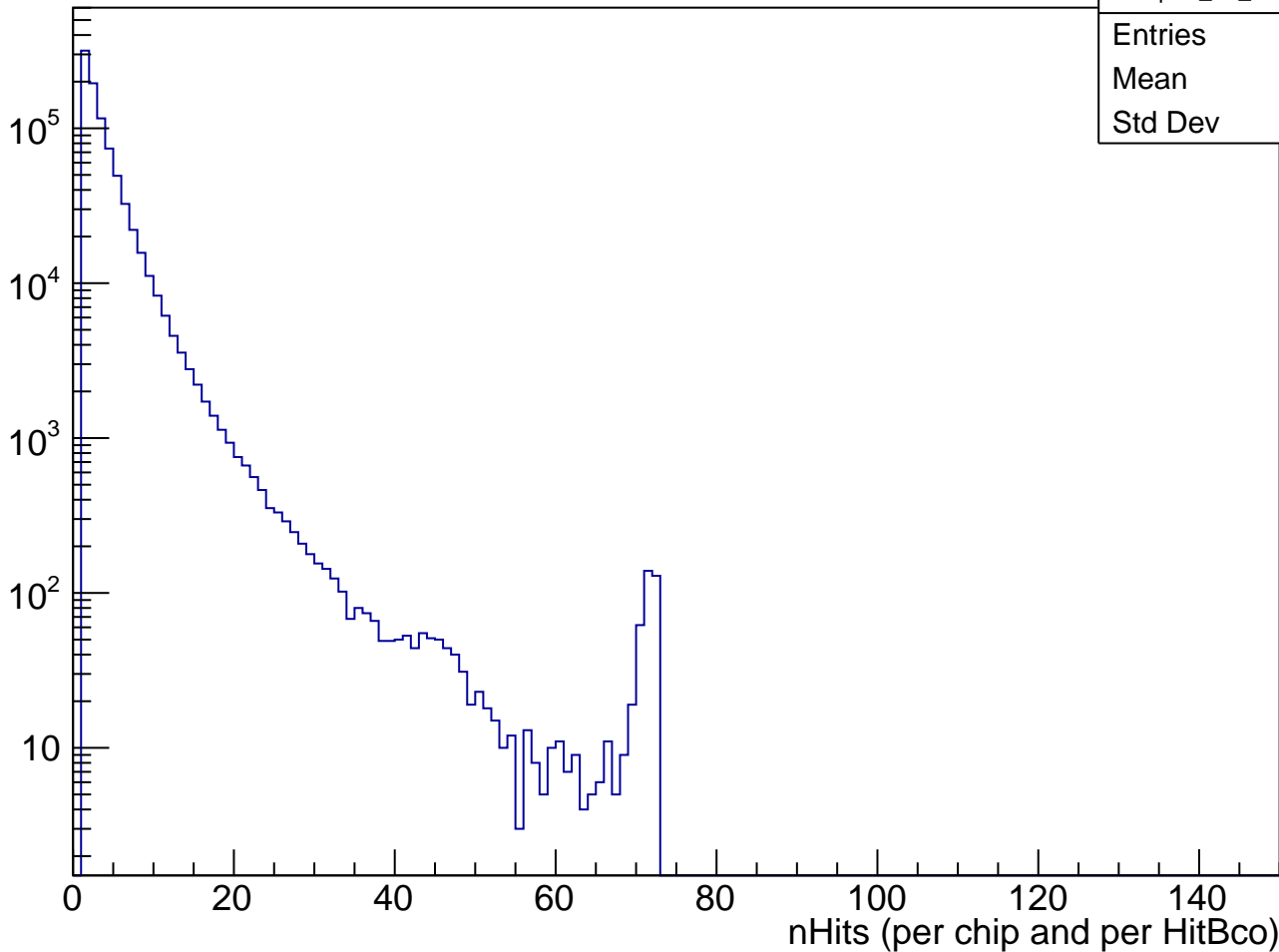
Entries



nChipHit_F3_Fch10_Chip0	
Entries	866213
Mean	3.204
Std Dev	3.693

nChipHit_F3_Fch10_Chip1

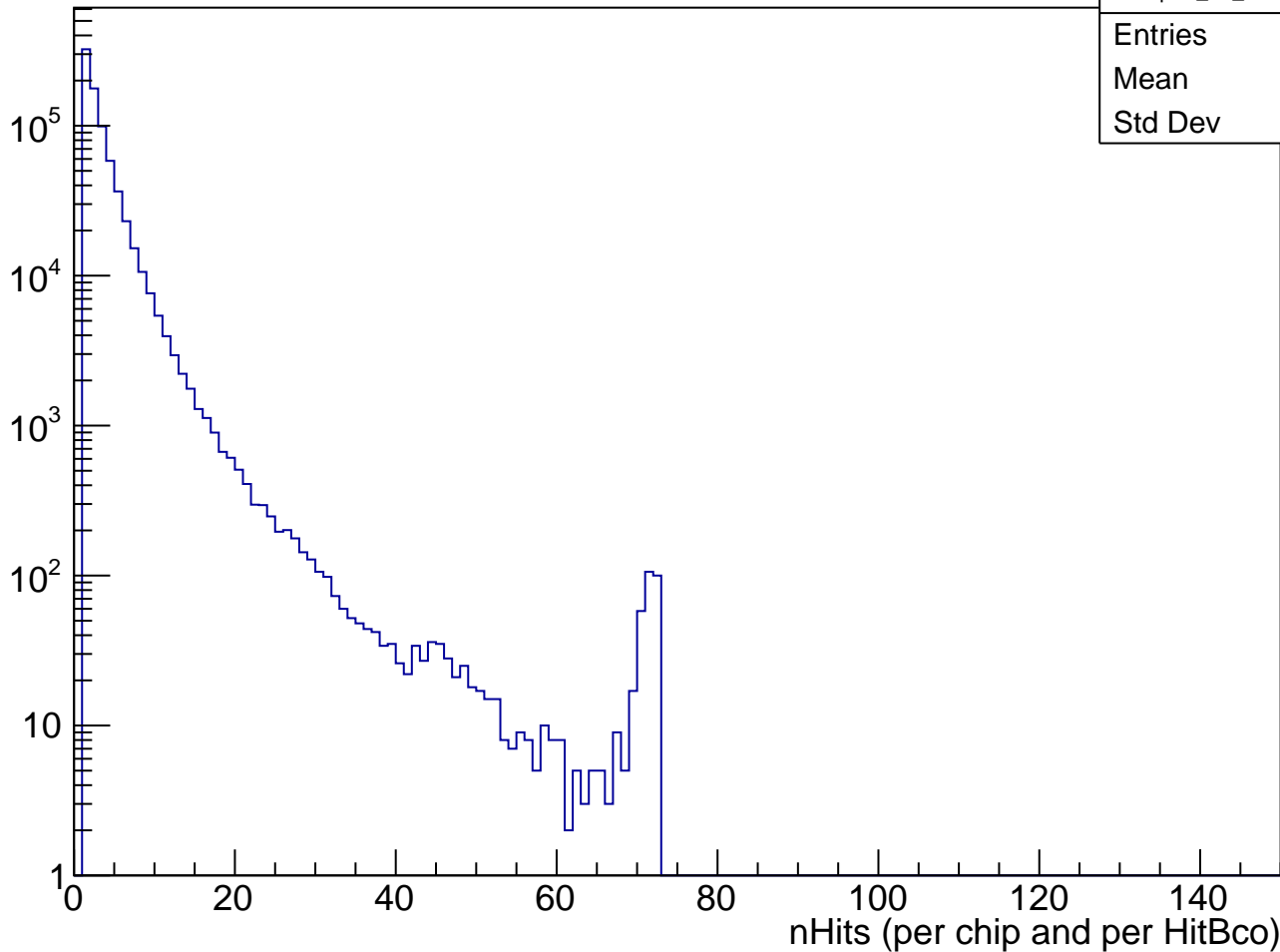
Entries



nChipHit_F3_Fch10_Chip1	
Entries	871863
Mean	3.173
Std Dev	3.682

nChipHit_F3_Fch10_Chip10

Entries

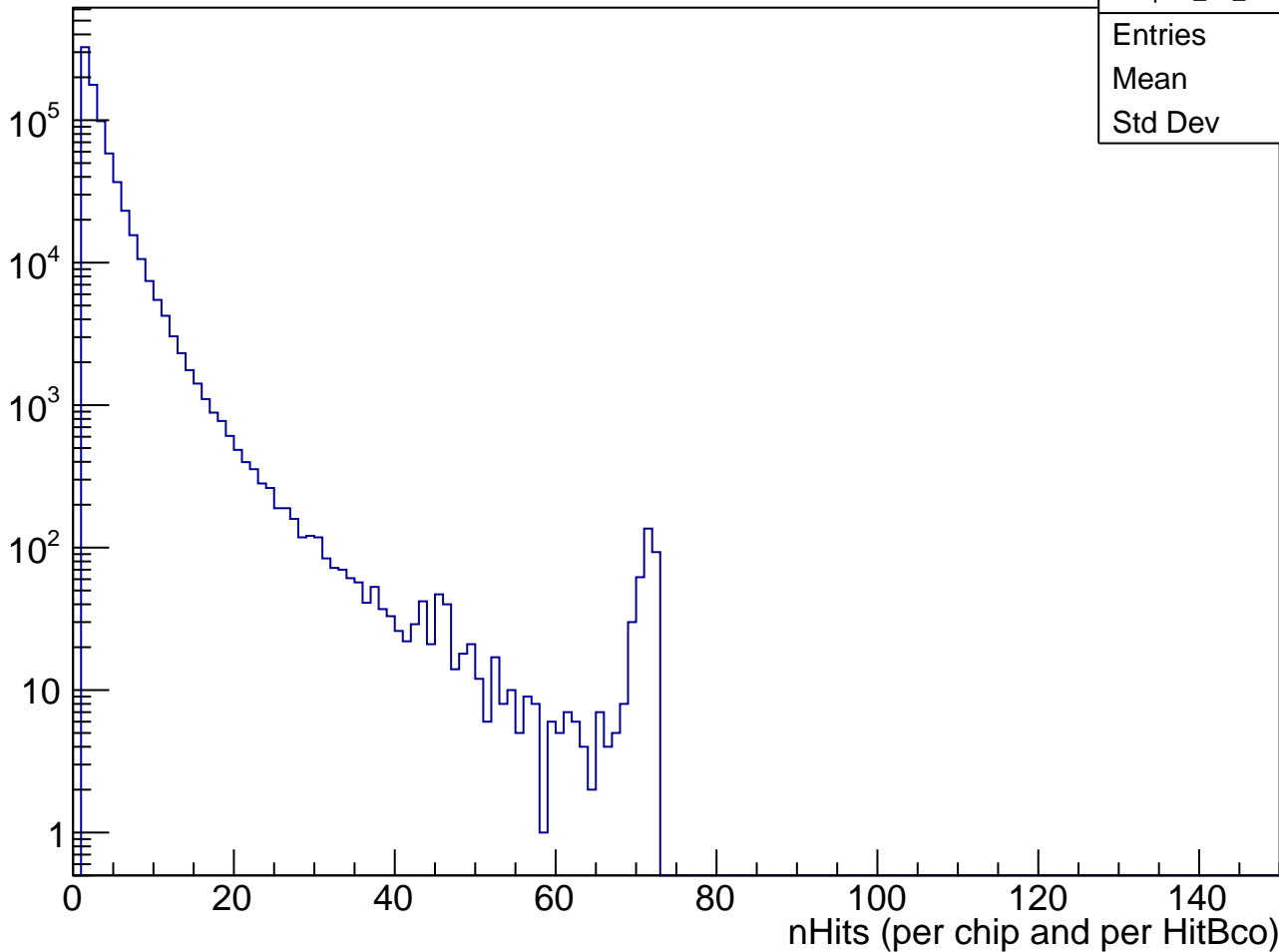


nChipHit_F3_Fch10_Chip10	
Entries	775649
Mean	2.8
Std Dev	3.316

nHits (per chip and per HitBco)

nChipHit_F3_Fch10_Chip11

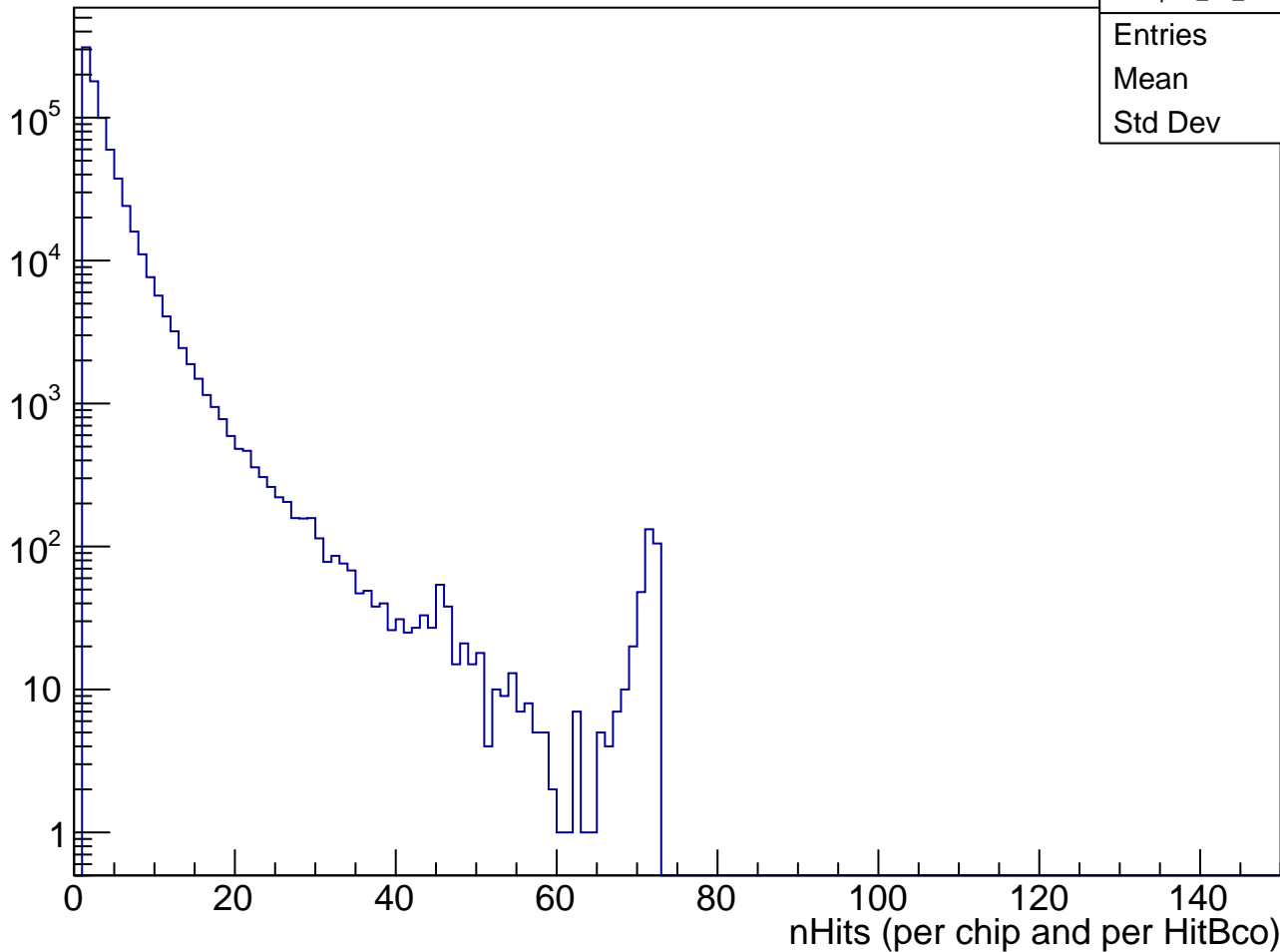
Entries



nChipHit_F3_Fch10_Chip11	
Entries	778254
Mean	2.81
Std Dev	3.357

nChipHit_F3_Fch10_Chip12

Entries



nChipHit_F3_Fch10_Chip12

Entries 771410

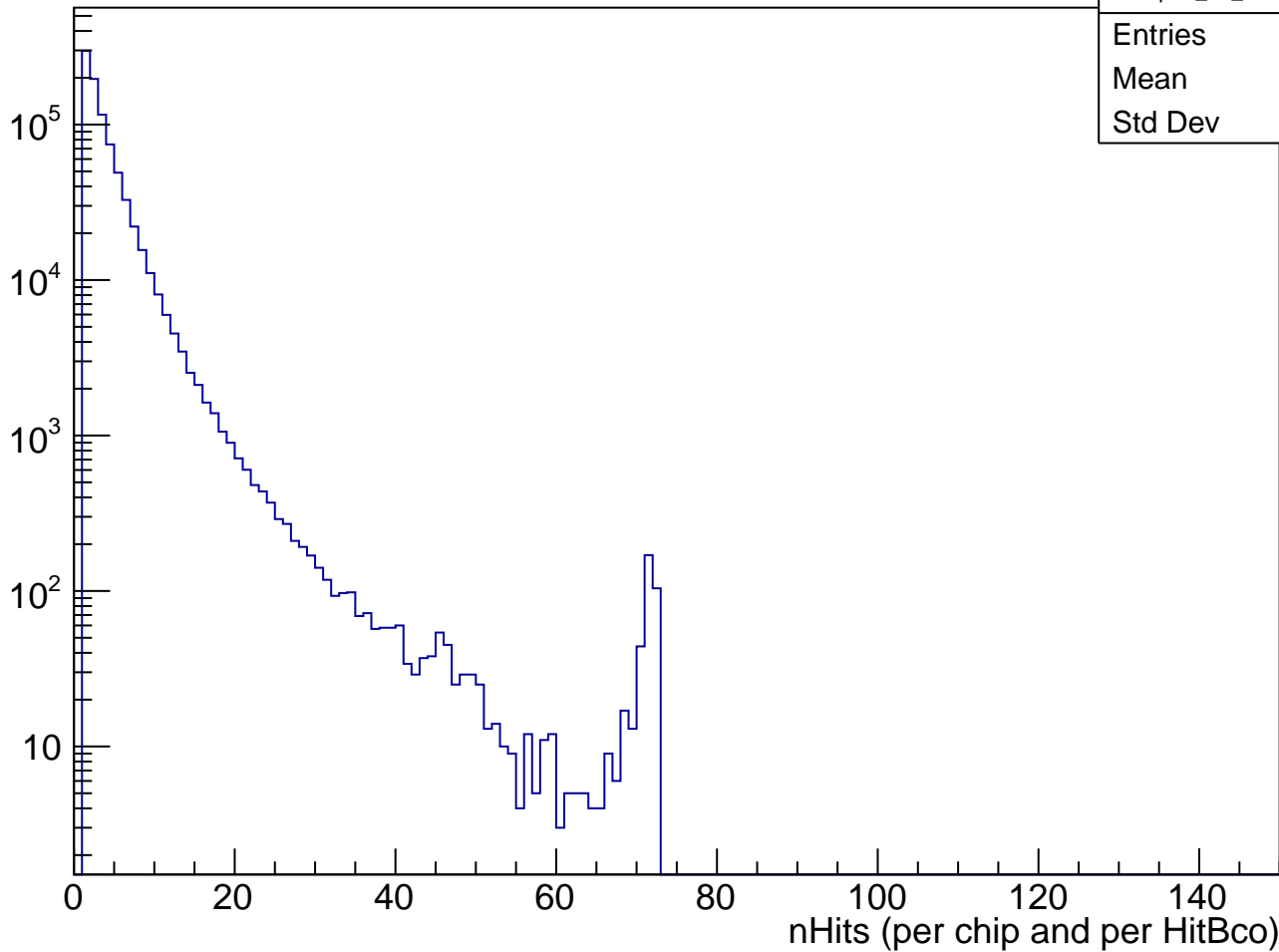
Mean 2.87

Std Dev 3.383

nHits (per chip and per HitBco)

nChipHit_F3_Fch10_Chip13

Entries



nChipHit_F3_Fch10_Chip13

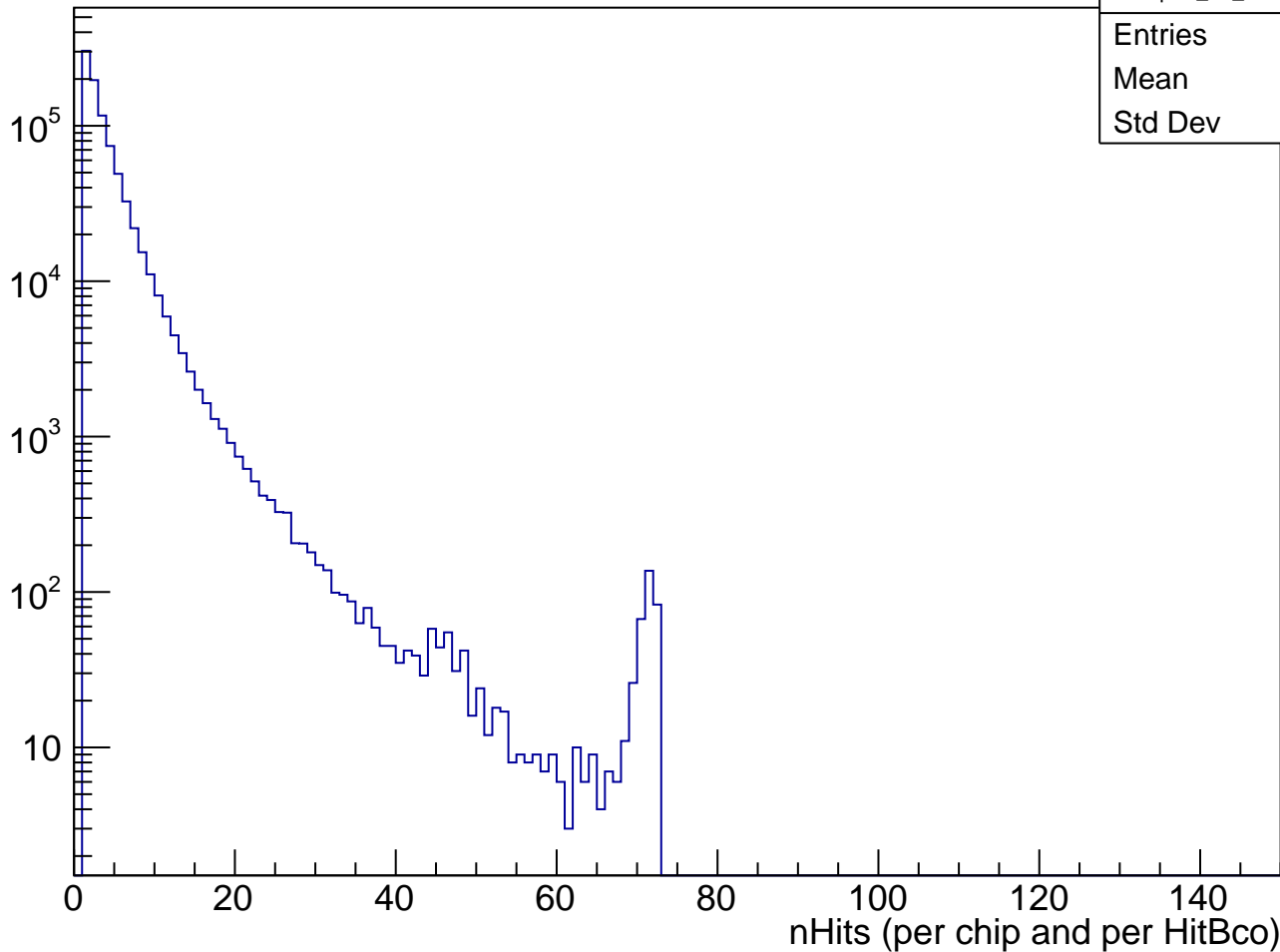
Entries 852924

Mean 3.192

Std Dev 3.632

nChipHit_F3_Fch10_Chip14

Entries



nChipHit_F3_Fch10_Chip14

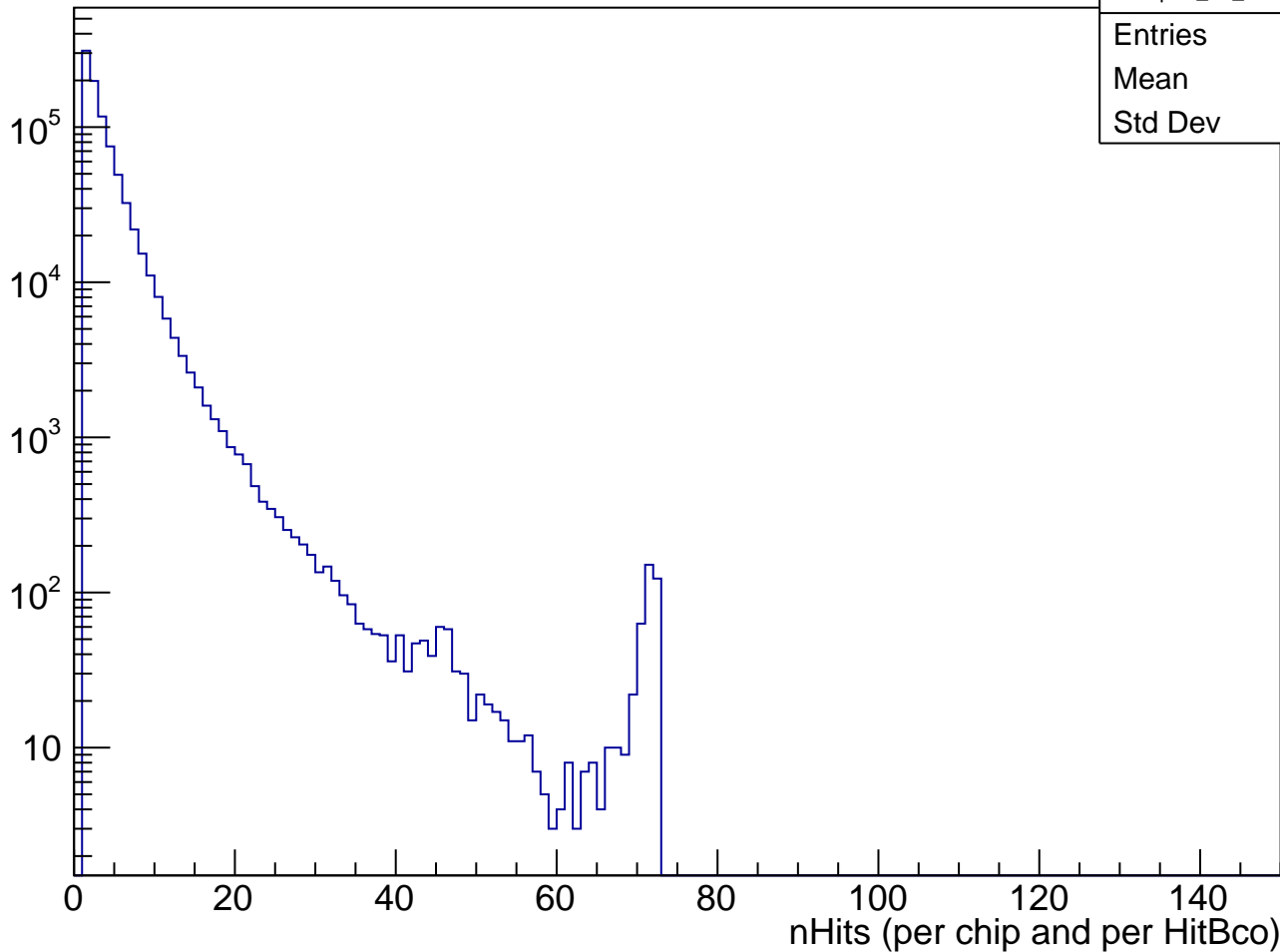
Entries 858478

Mean 3.177

Std Dev 3.626

nChipHit_F3_Fch10_Chip15

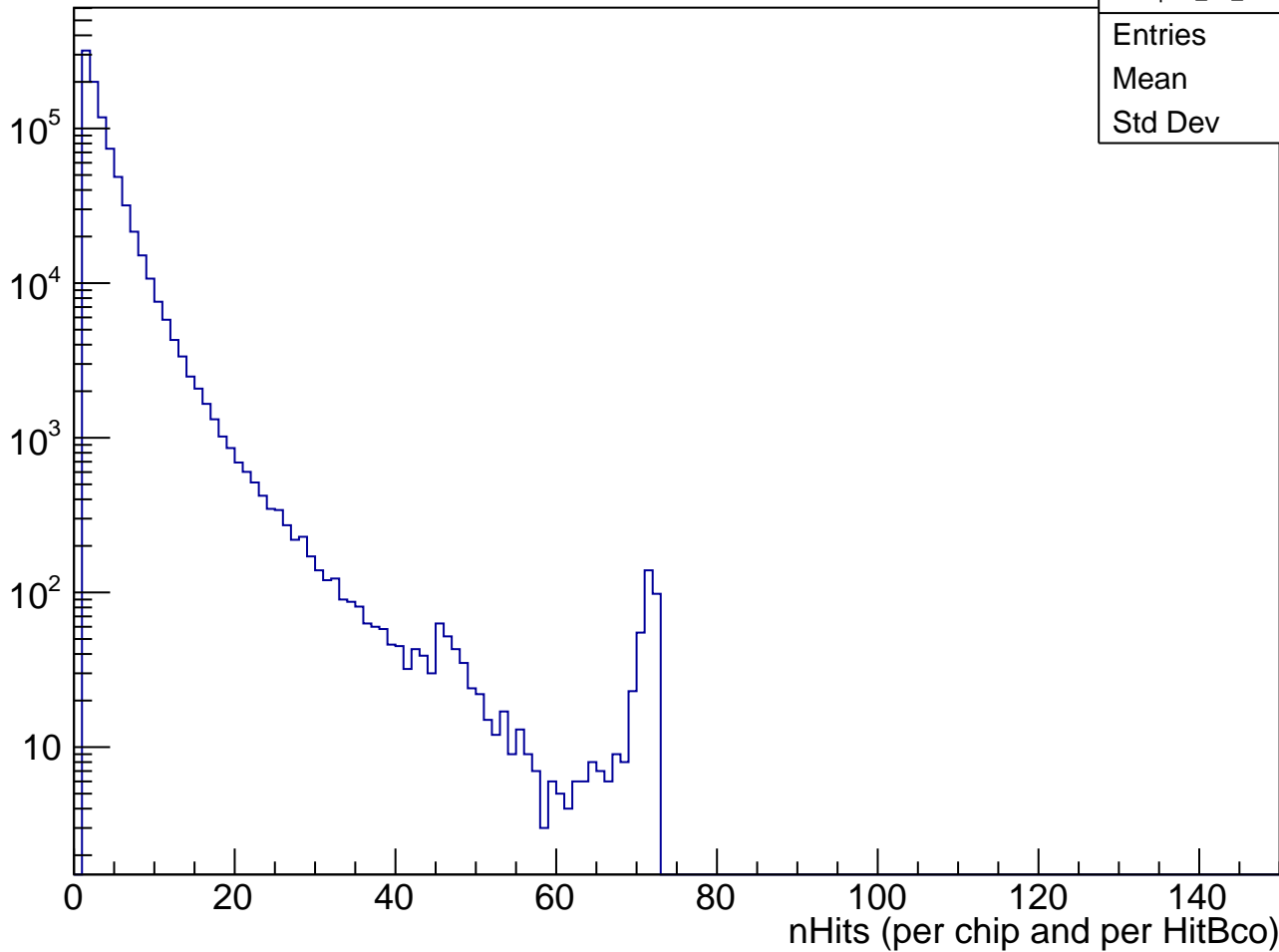
Entries



nChipHit_F3_Fch10_Chip15	
Entries	868148
Mean	3.155
Std Dev	3.633

nChipHit_F3_Fch10_Chip16

Entries



nChipHit_F3_Fch10_Chip16

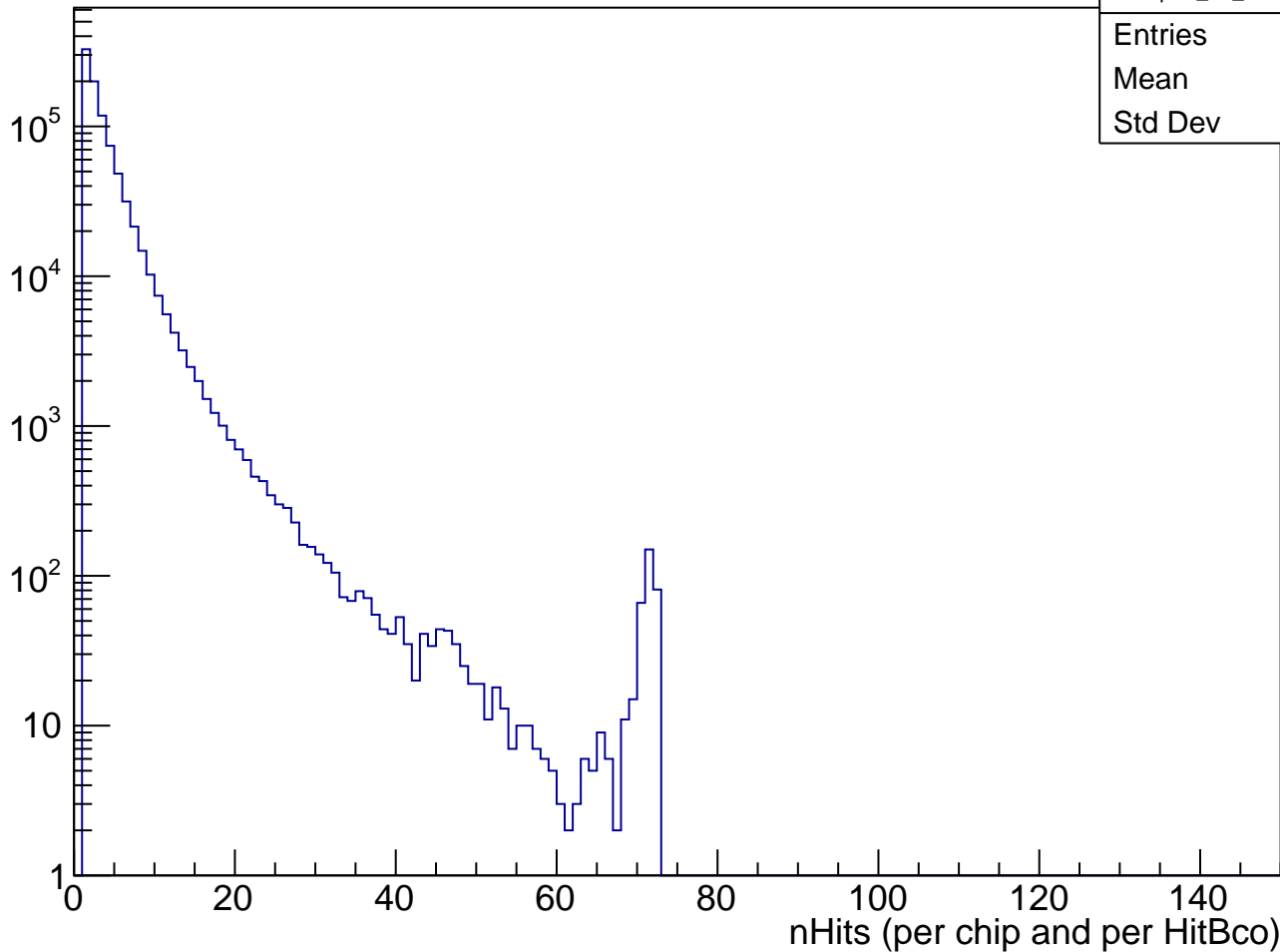
Entries 874730

Mean 3.113

Std Dev 3.581

nChipHit_F3_Fch10_Chip17

Entries



nChipHit_F3_Fch10_Chip17

Entries 880428

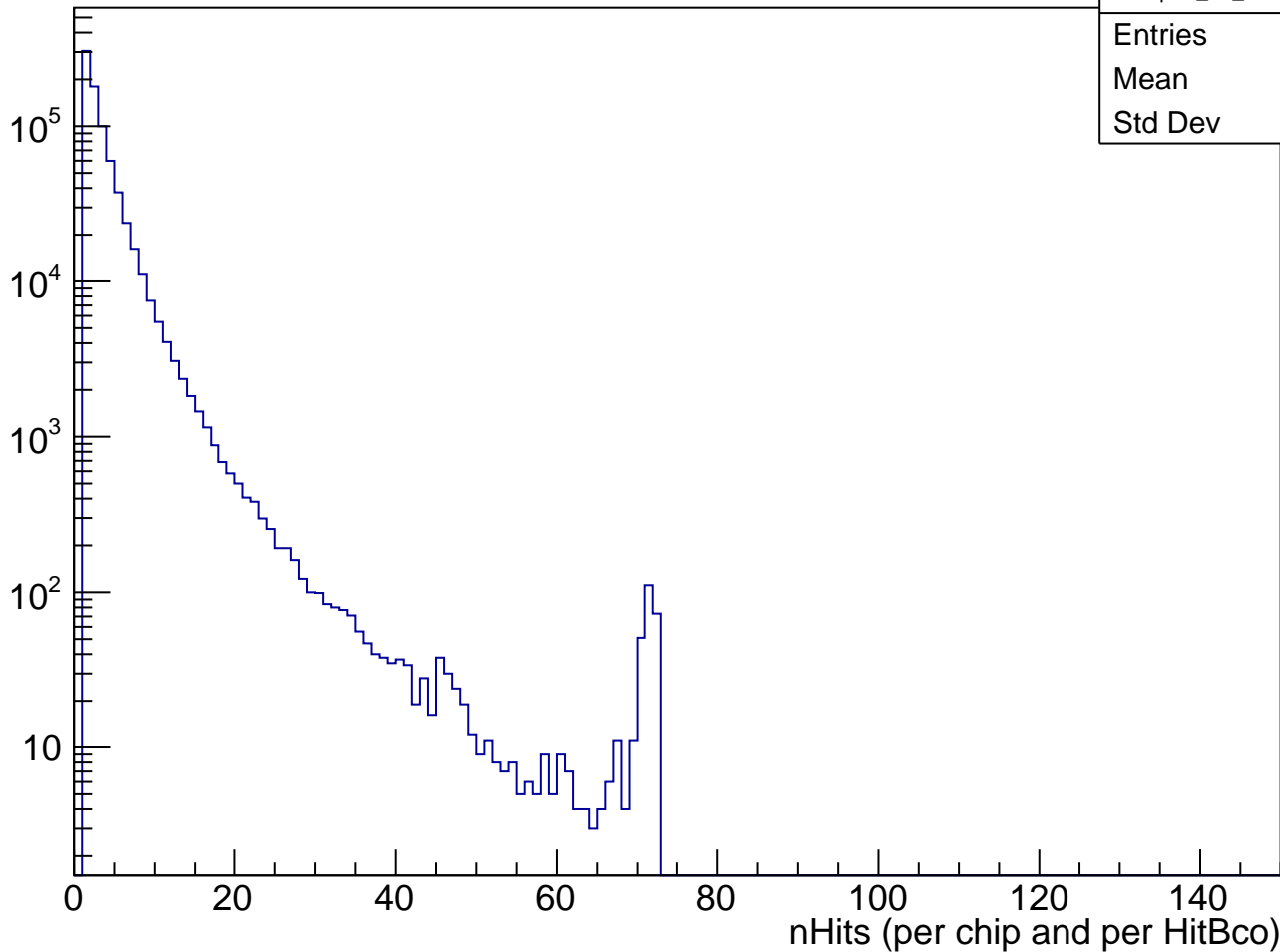
Mean 3.064

Std Dev 3.499

nHits (per chip and per HitBco)

nChipHit_F3_Fch10_Chip18

Entries



nChipHit_F3_Fch10_Chip18

Entries 766340

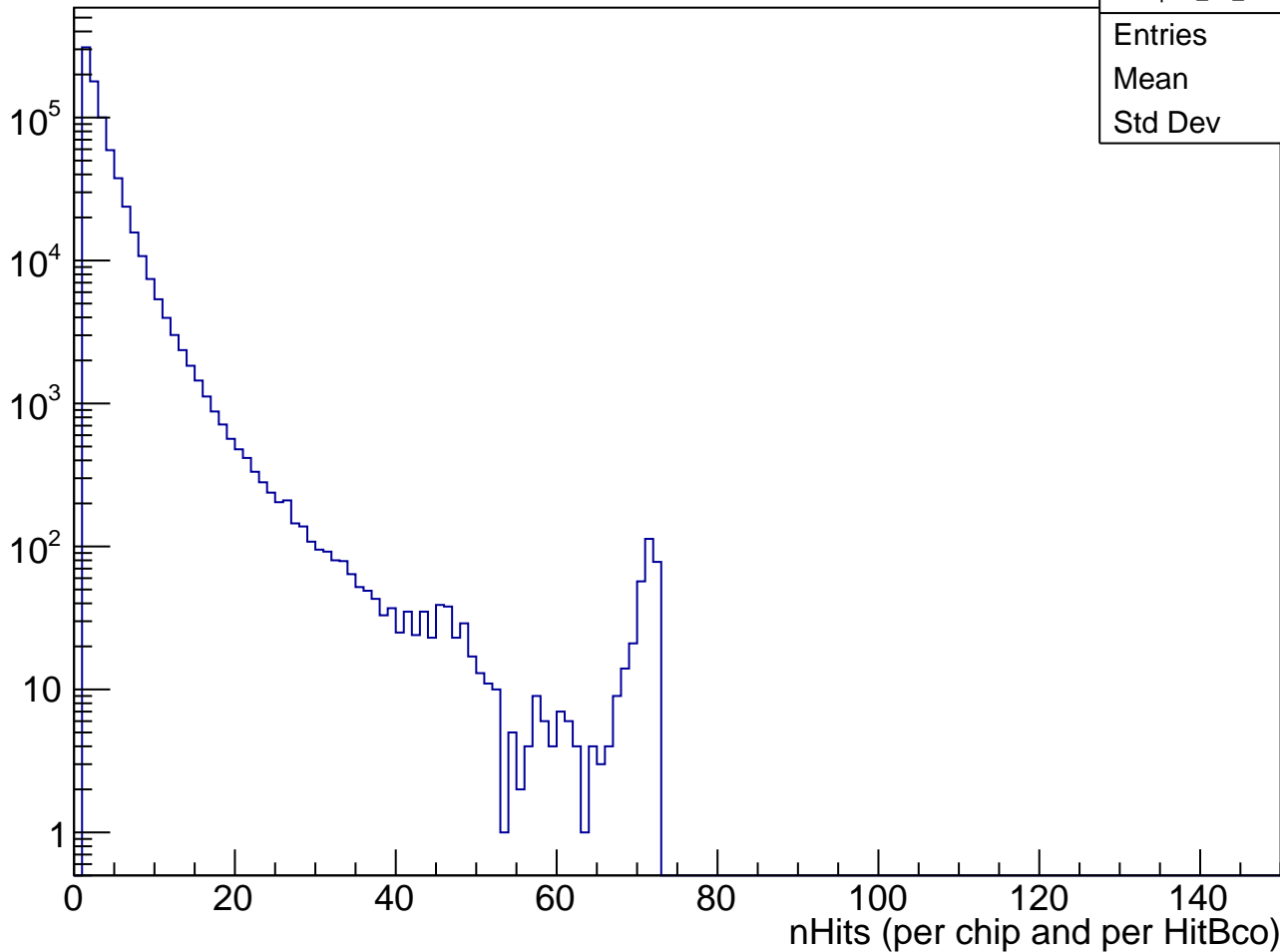
Mean 2.86

Std Dev 3.3

nHits (per chip and per HitBco)

nChipHit_F3_Fch10_Chip19

Entries



nChipHit_F3_Fch10_Chip19

Entries 768432

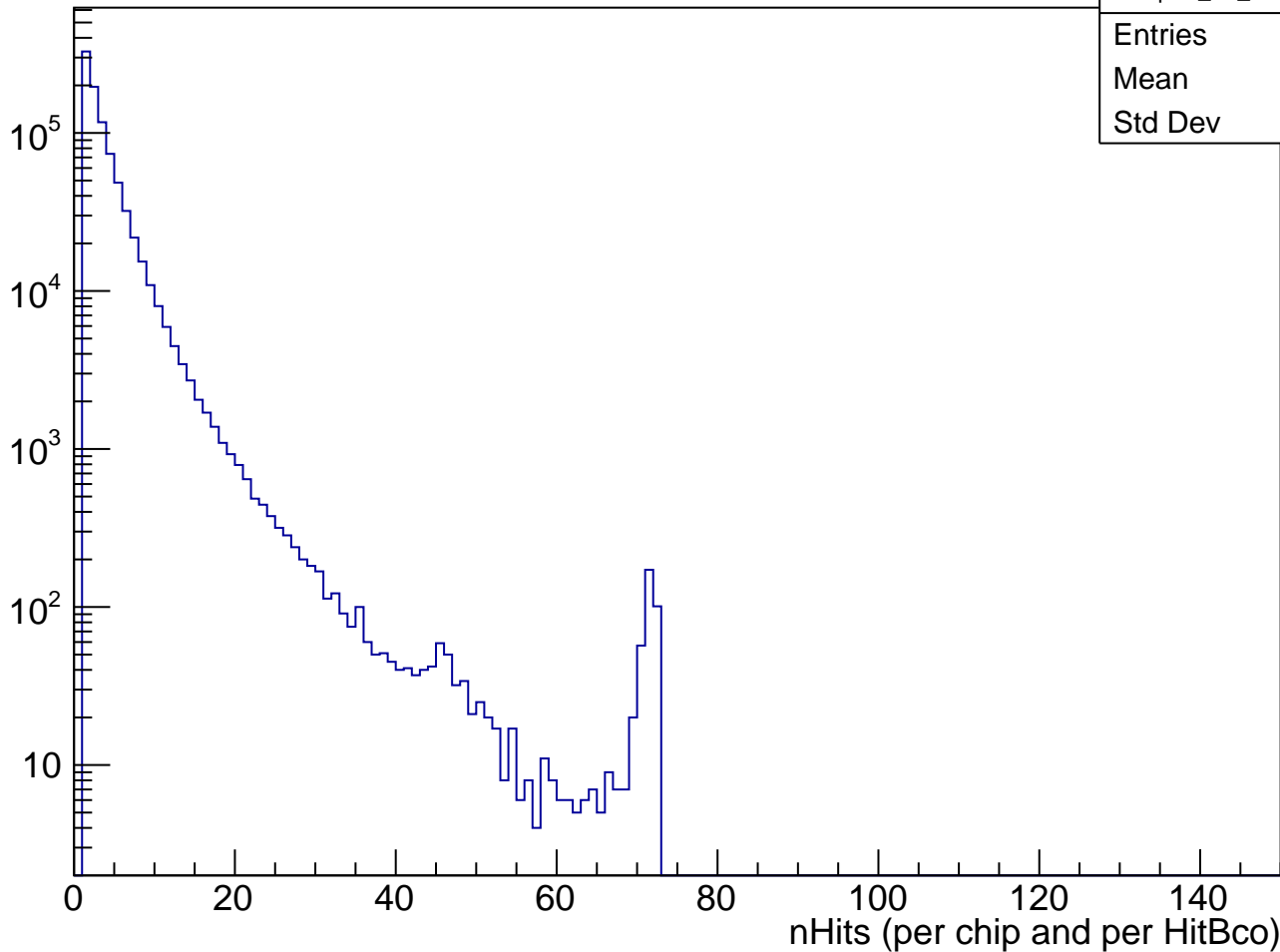
Mean 2.843

Std Dev 3.317

nHits (per chip and per HitBco)

nChipHit_F3_Fch10_Chip2

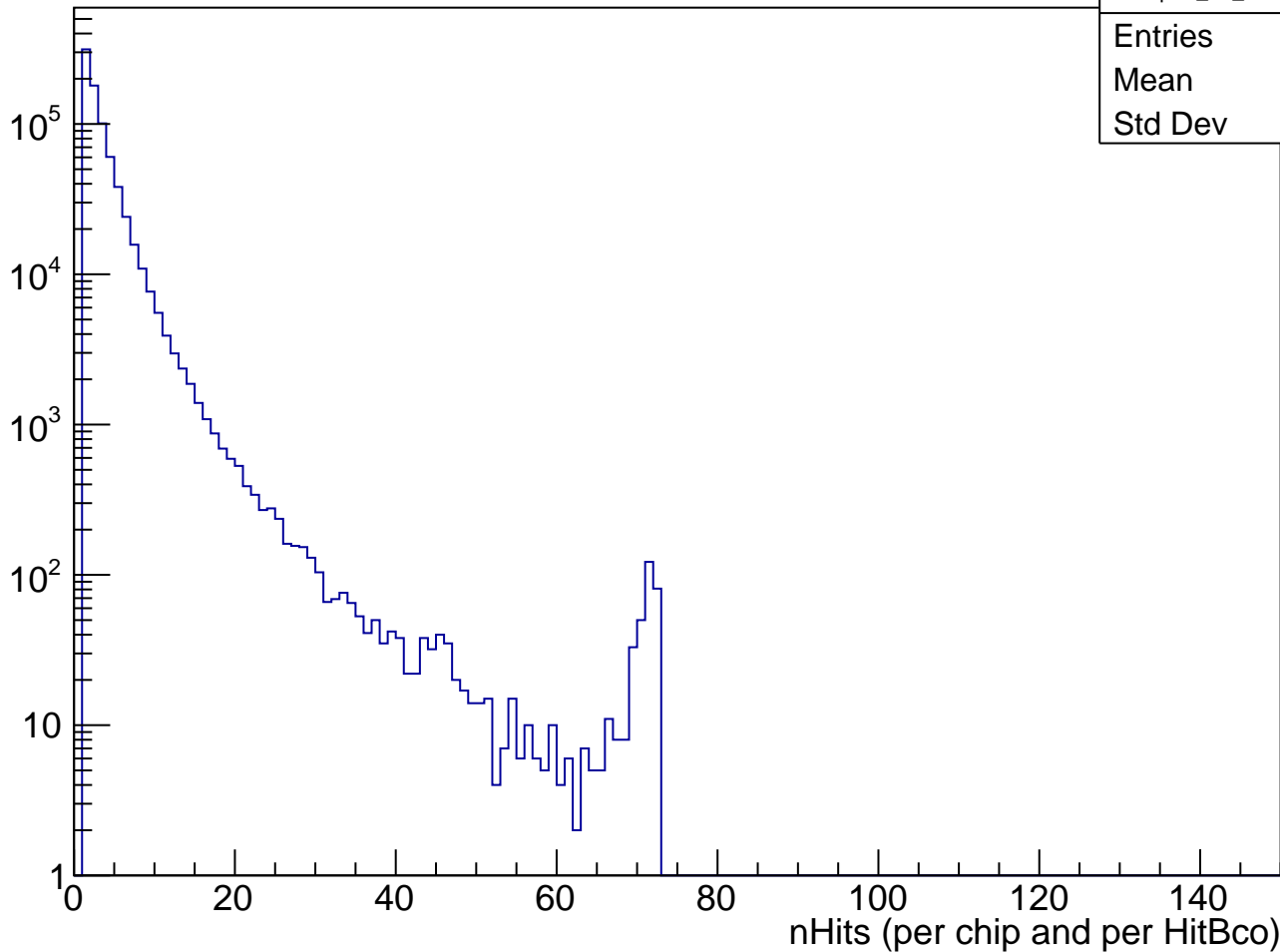
Entries



nChipHit_F3_Fch10_Chip2	
Entries	880479
Mean	3.121
Std Dev	3.631

nChipHit_F3_Fch10_Chip20

Entries



nChipHit_F3_Fch10_Chip20

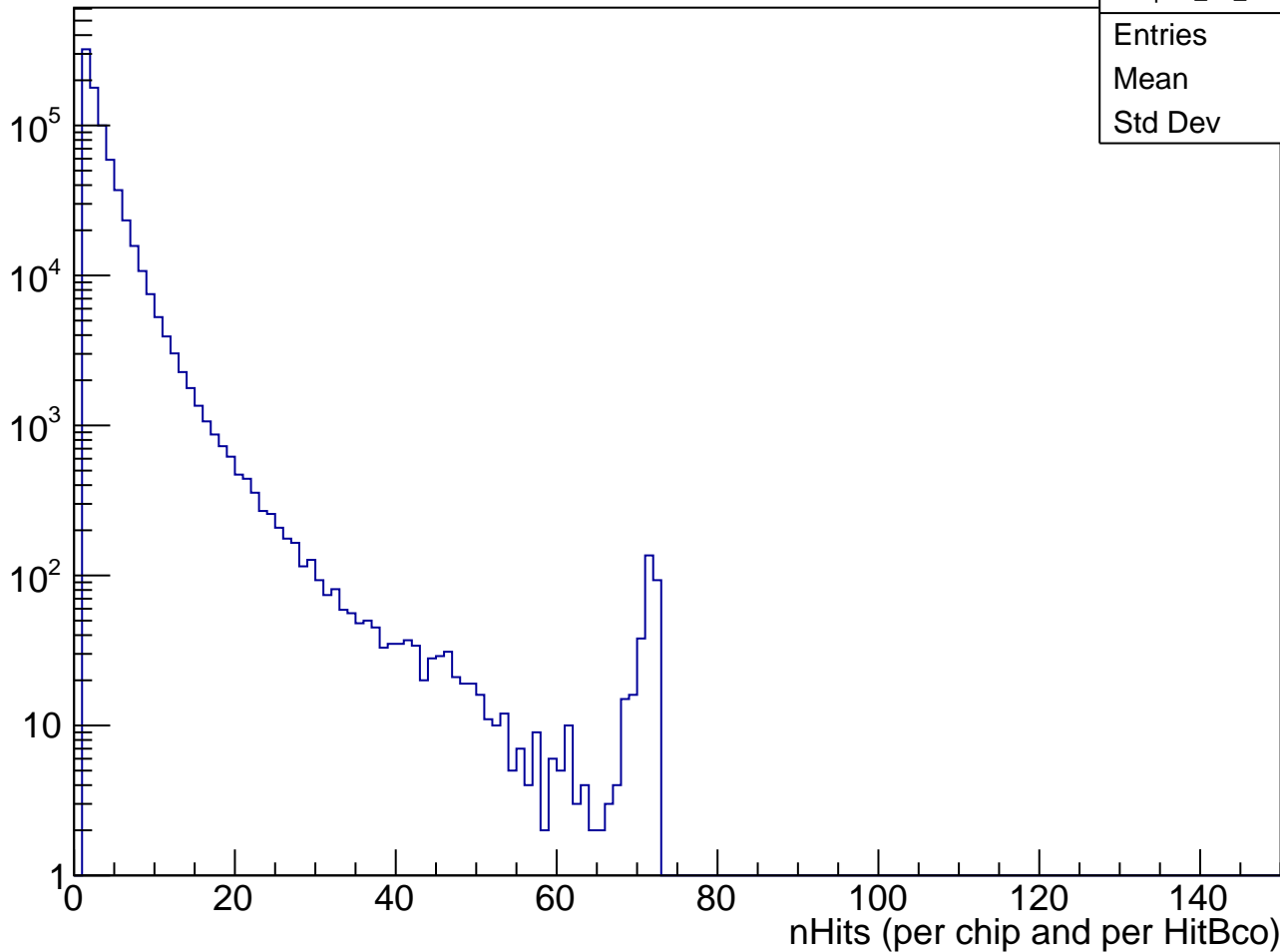
Entries 776921

Mean 2.845

Std Dev 3.337

nChipHit_F3_Fch10_Chip21

Entries

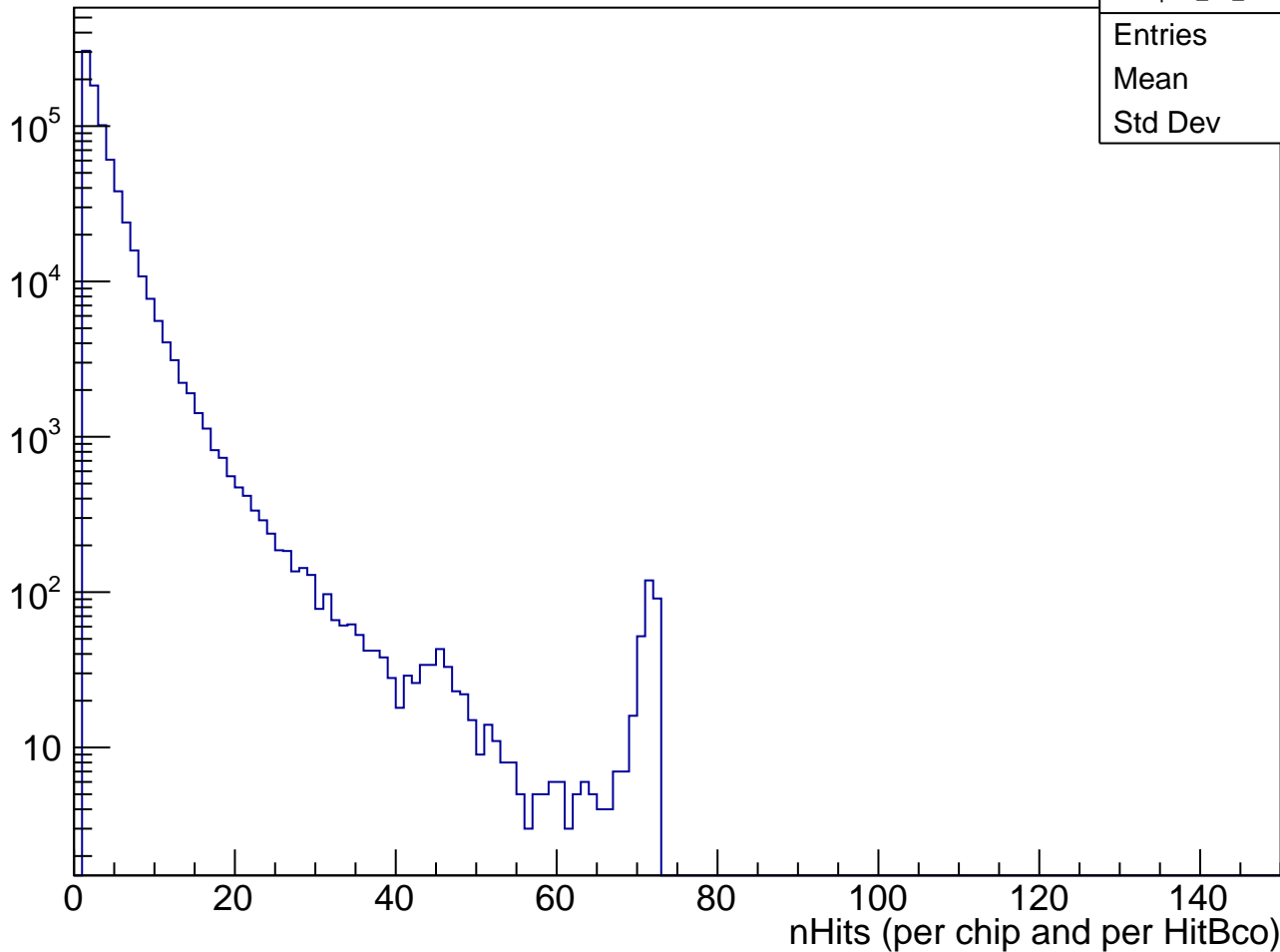


nChipHit_F3_Fch10_Chip21	
Entries	778355
Mean	2.808
Std Dev	3.302

nHits (per chip and per HitBco)

nChipHit_F3_Fch10_Chip22

Entries



nChipHit_F3_Fch10_Chip22

Entries 771910

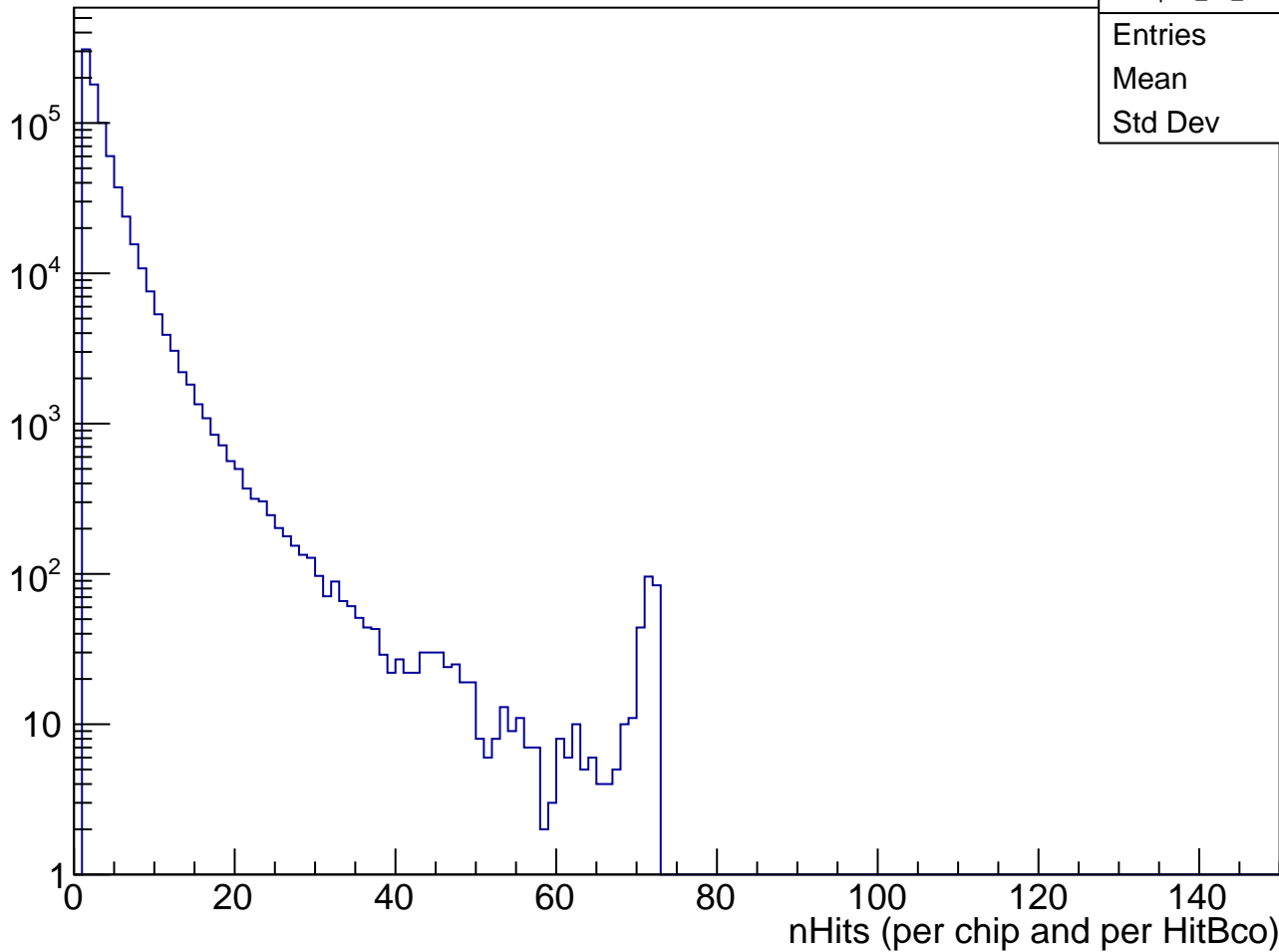
Mean 2.857

Std Dev 3.306

nHits (per chip and per HitBco)

nChipHit_F3_Fch10_Chip23

Entries



nChipHit_F3_Fch10_Chip23

Entries 769763

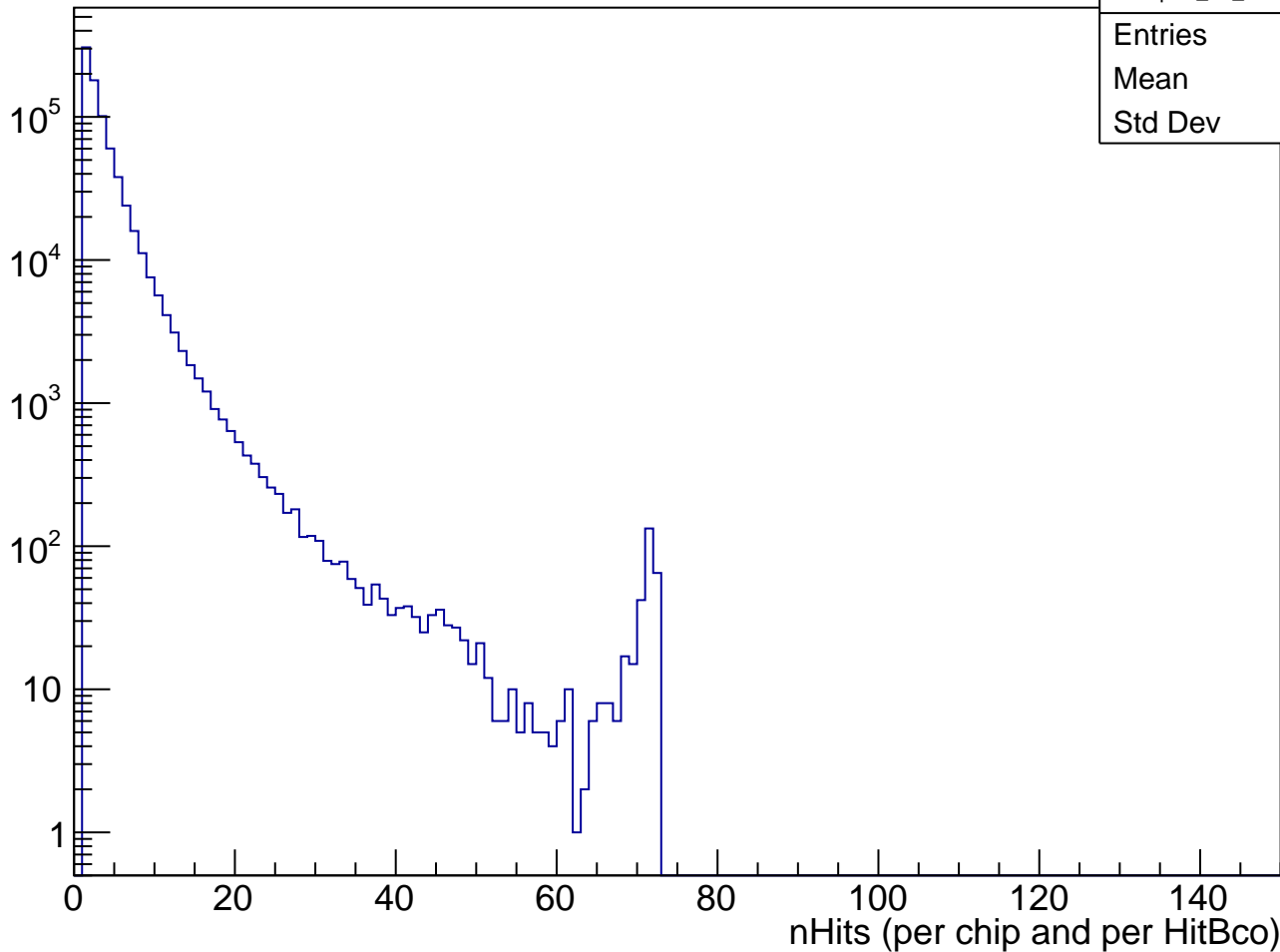
Mean 2.833

Std Dev 3.258

nHits (per chip and per HitBco)

nChipHit_F3_Fch10_Chip24

Entries



nChipHit_F3_Fch10_Chip24

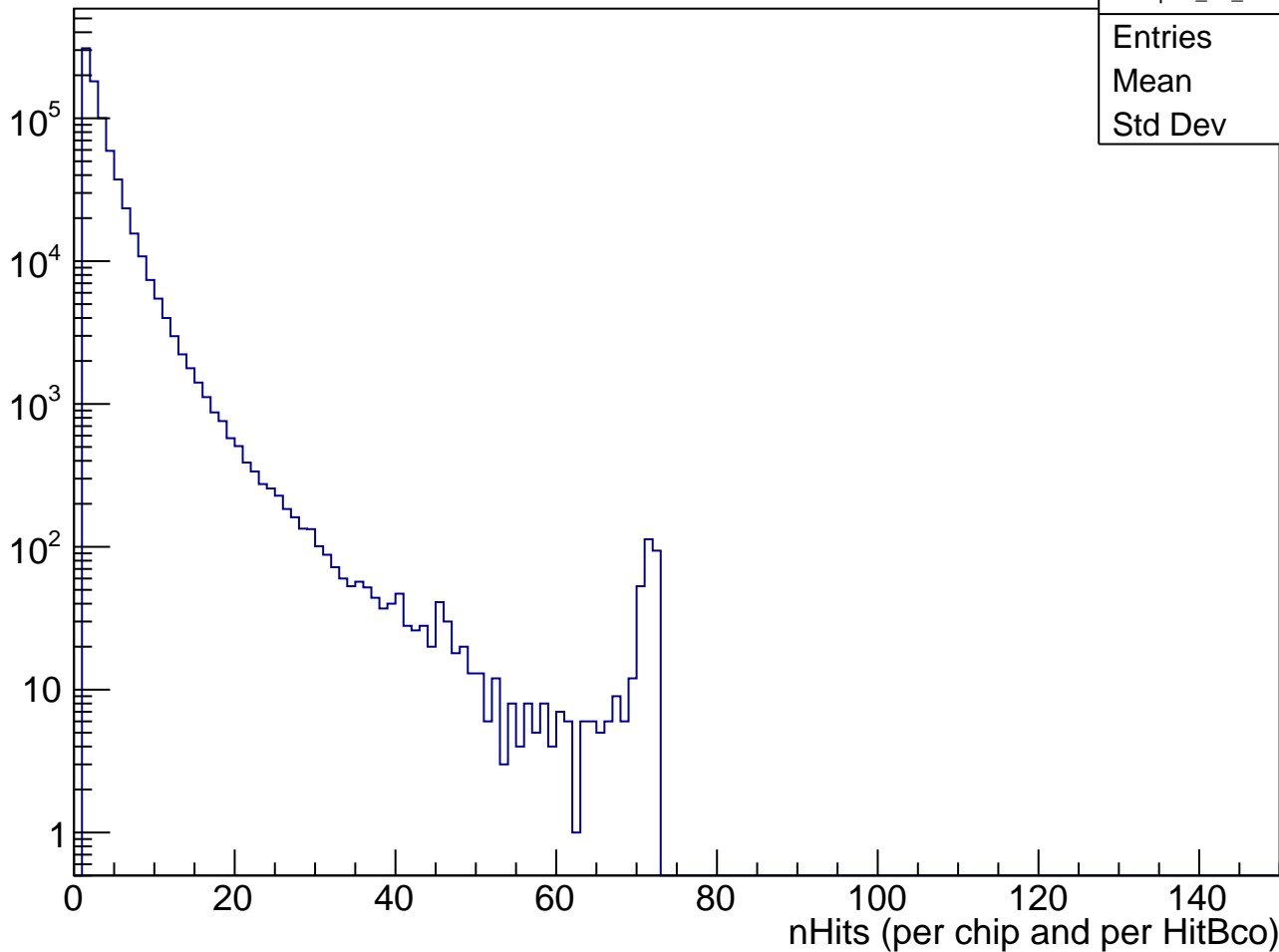
Entries 770668

Mean 2.875

Std Dev 3.348

nChipHit_F3_Fch10_Chip25

Entries



nChipHit_F3_Fch10_Chip25

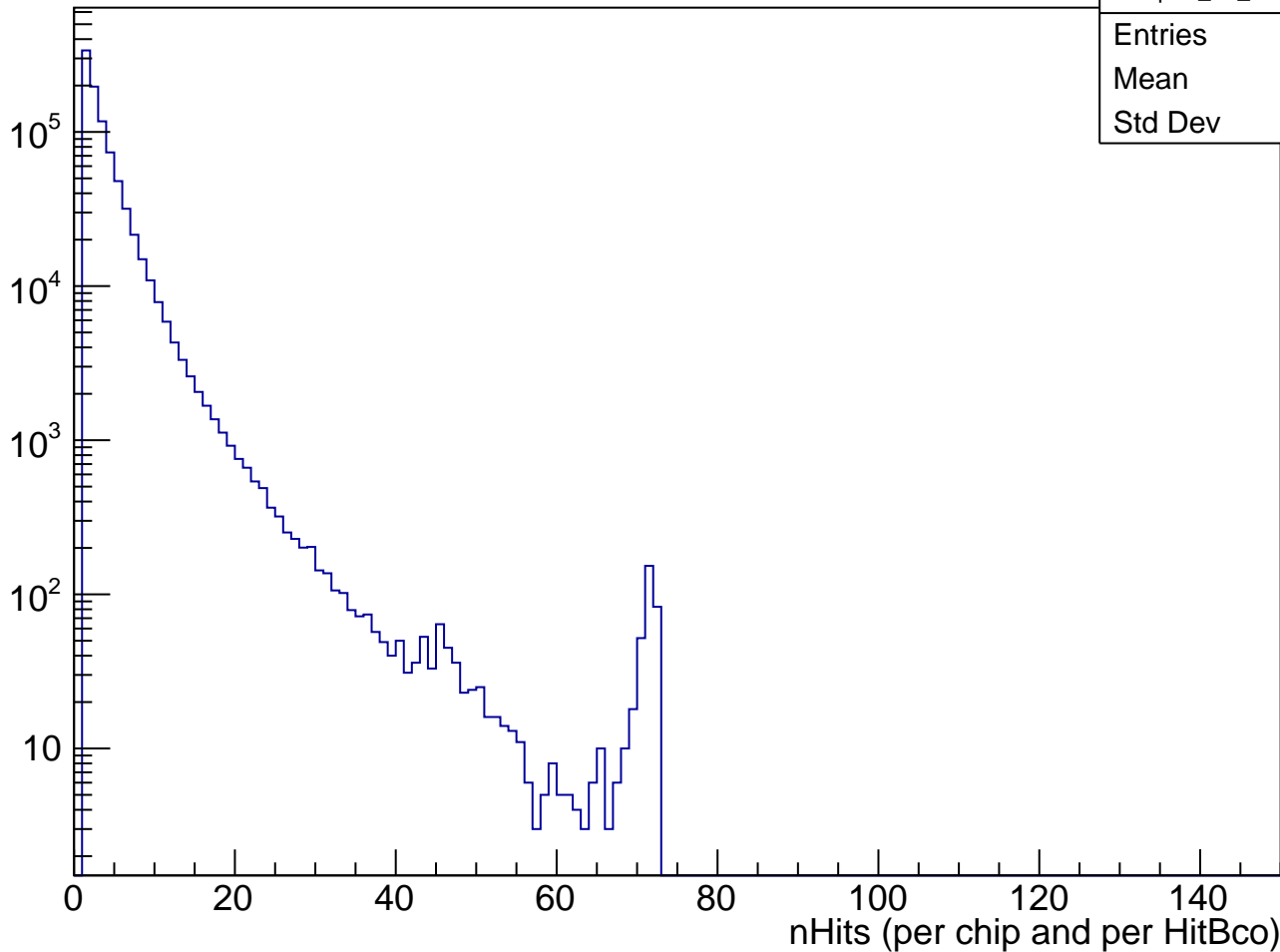
Entries 769361

Mean 2.839

Std Dev 3.312

nChipHit_F3_Fch10_Chip3

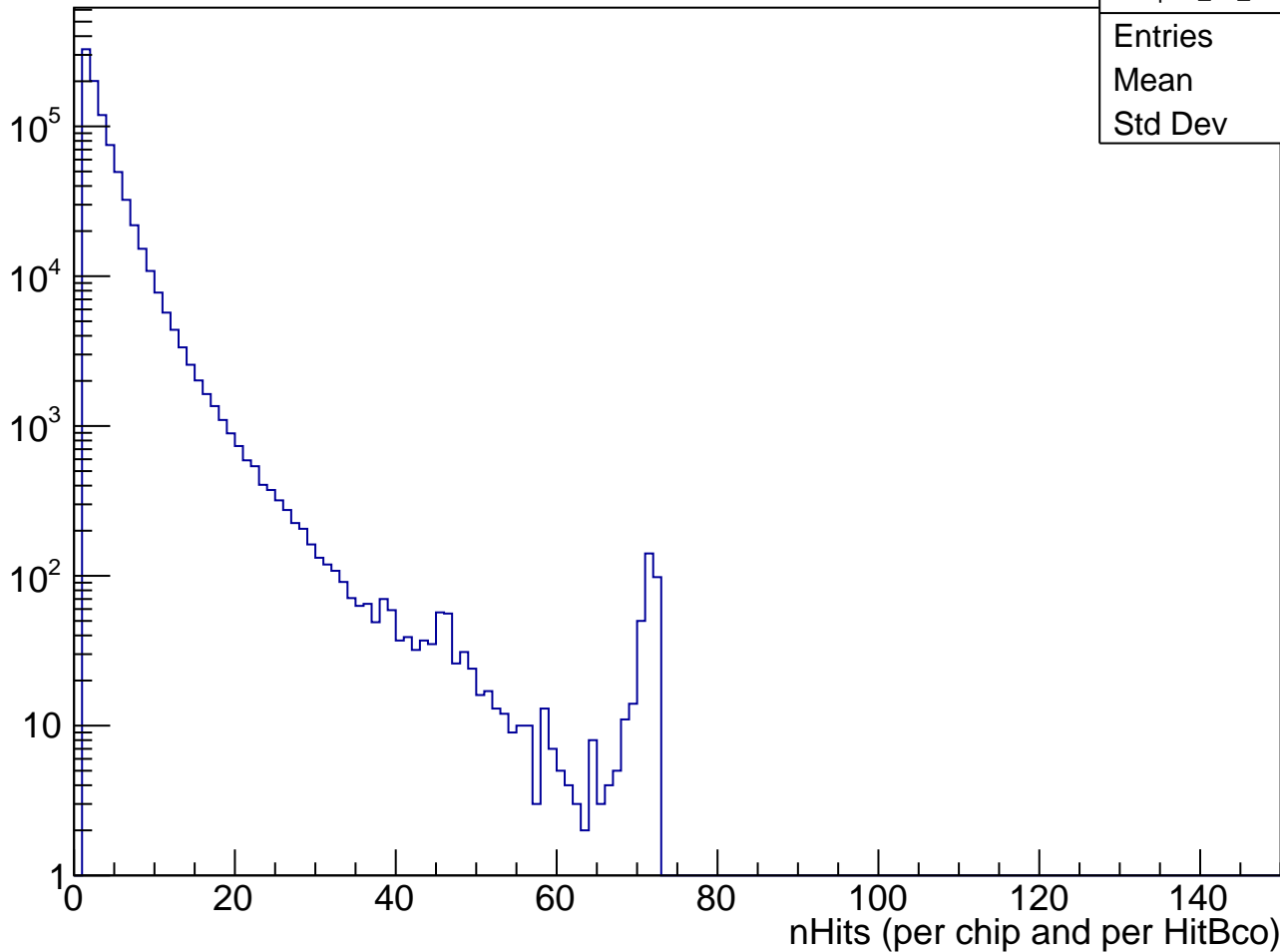
Entries



nChipHit_F3_Fch10_Chip3	
Entries	889666
Mean	3.08
Std Dev	3.57

nChipHit_F3_Fch10_Chip4

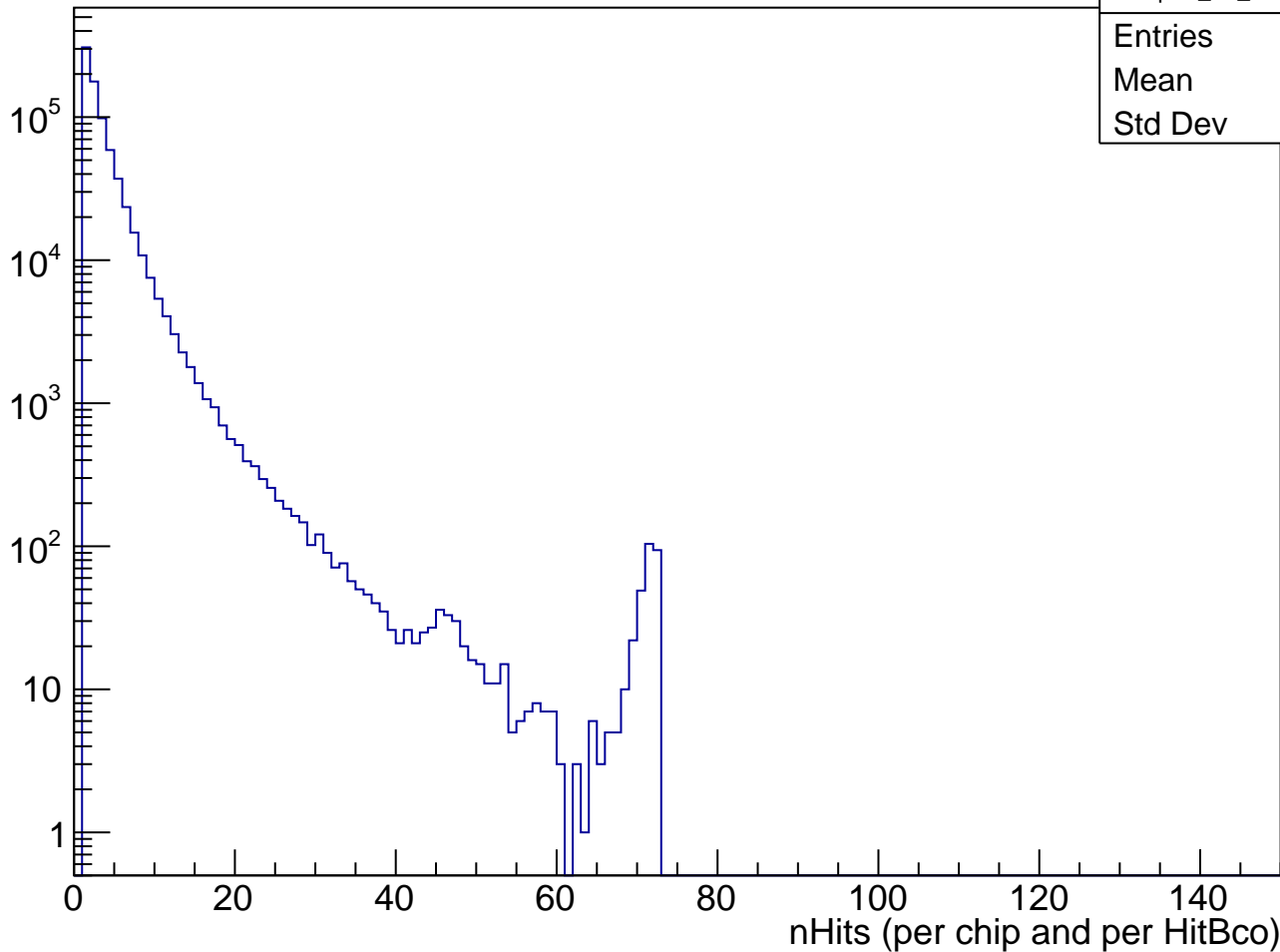
Entries



nChipHit_F3_Fch10_Chip4	
Entries	888161
Mean	3.099
Std Dev	3.538

nChipHit_F3_Fch10_Chip5

Entries

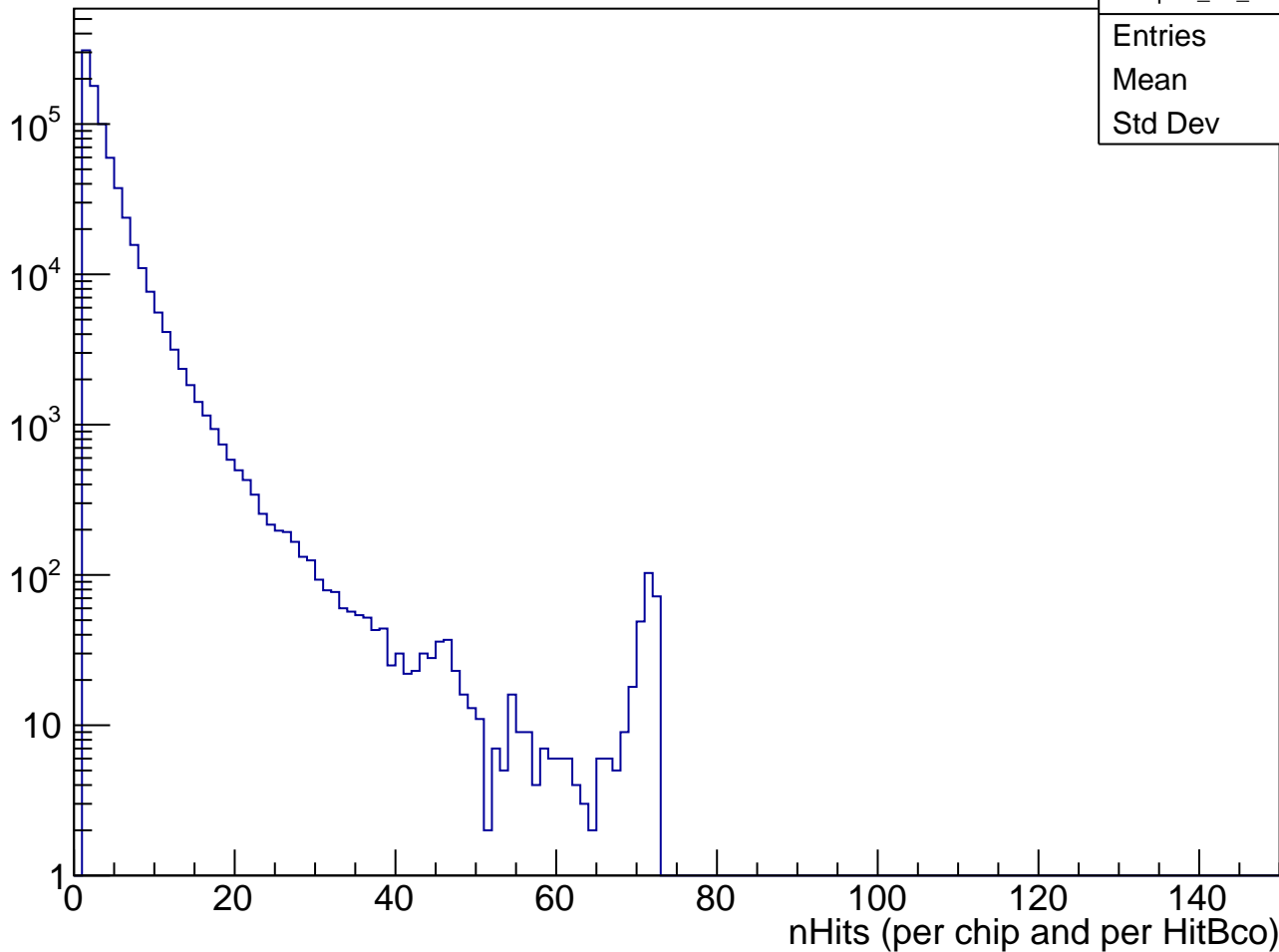


nChipHit_F3_Fch10_Chip5	
Entries	761371
Mean	2.847
Std Dev	3.32

nHits (per chip and per HitBco)

nChipHit_F3_Fch10_Chip6

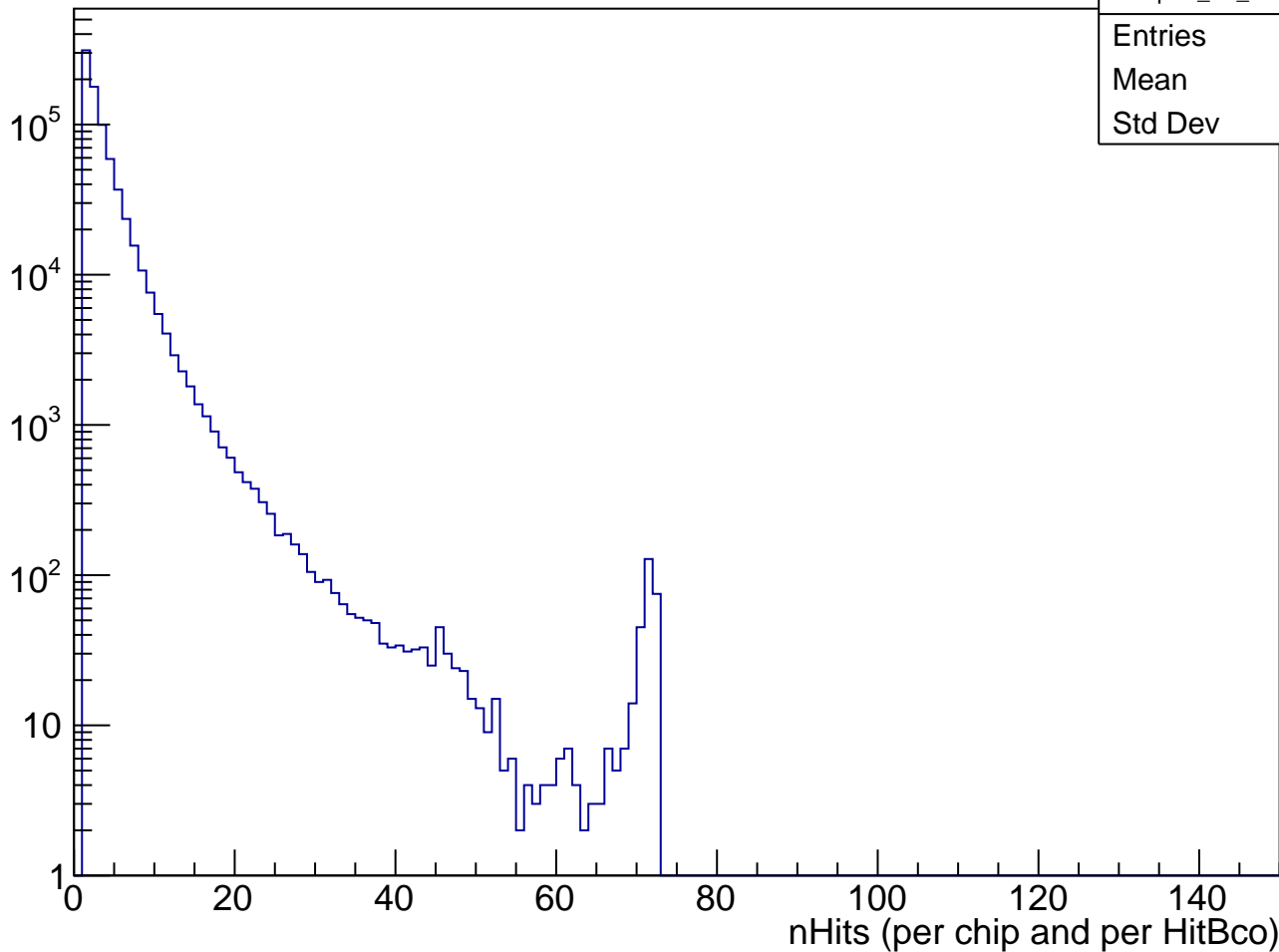
Entries



nChipHit_F3_Fch10_Chip6	
Entries	769385
Mean	2.85
Std Dev	3.289

nChipHit_F3_Fch10_Chip7

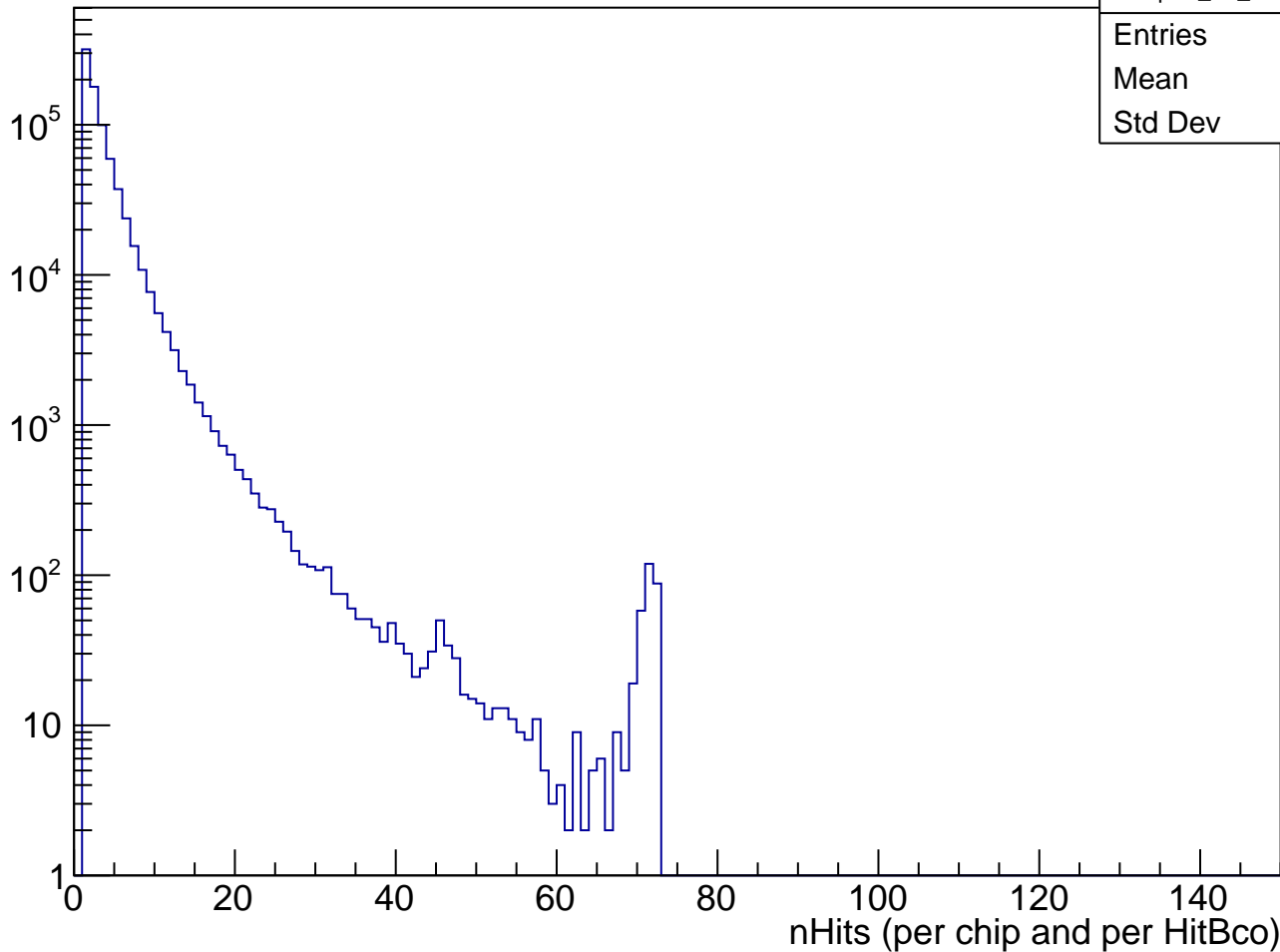
Entries



nChipHit_F3_Fch10_Chip7	
Entries	768052
Mean	2.835
Std Dev	3.304

nChipHit_F3_Fch10_Chip8

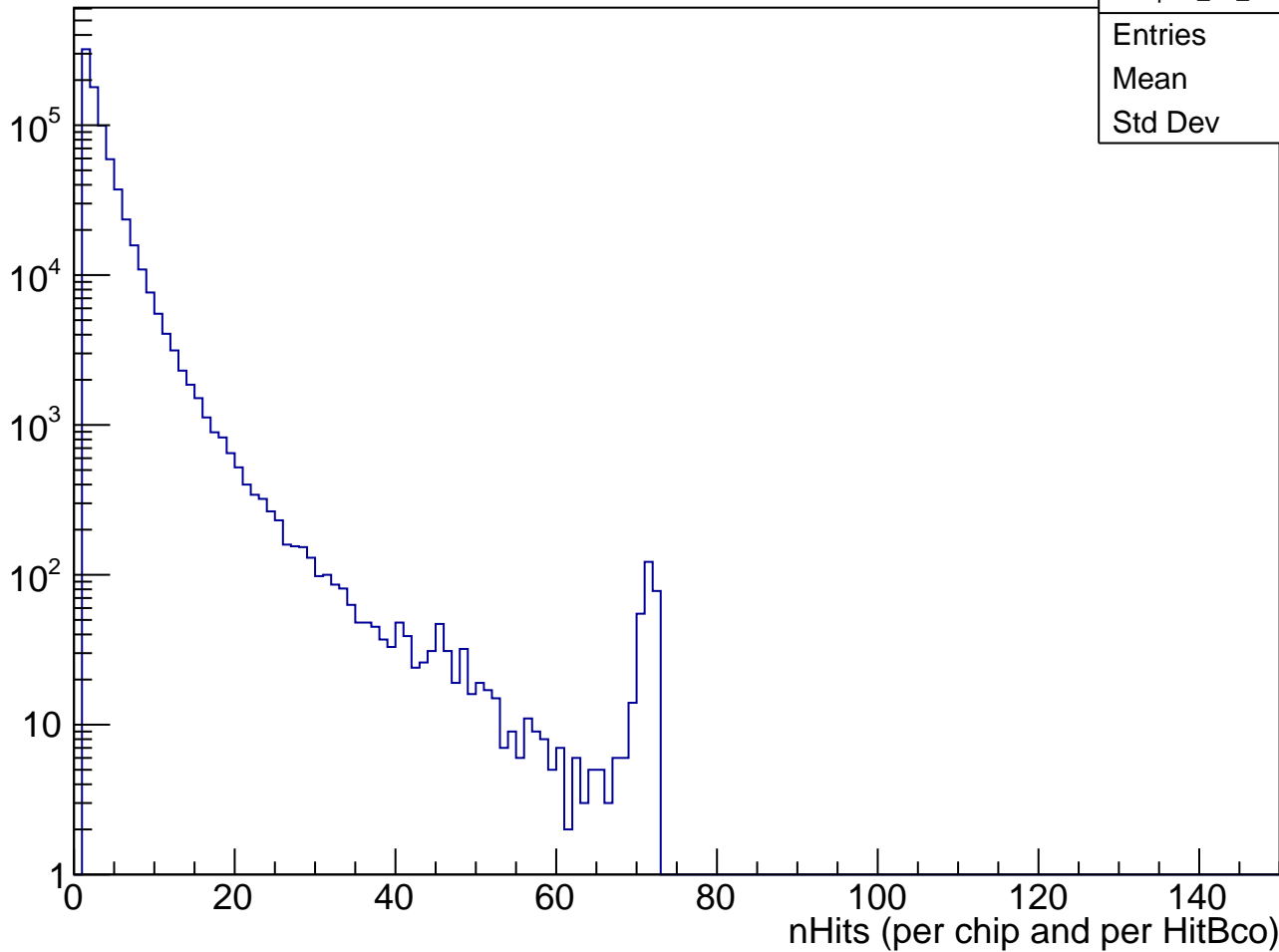
Entries



nChipHit_F3_Fch10_Chip8	
Entries	776982
Mean	2.838
Std Dev	3.352

nChipHit_F3_Fch10_Chip9

Entries

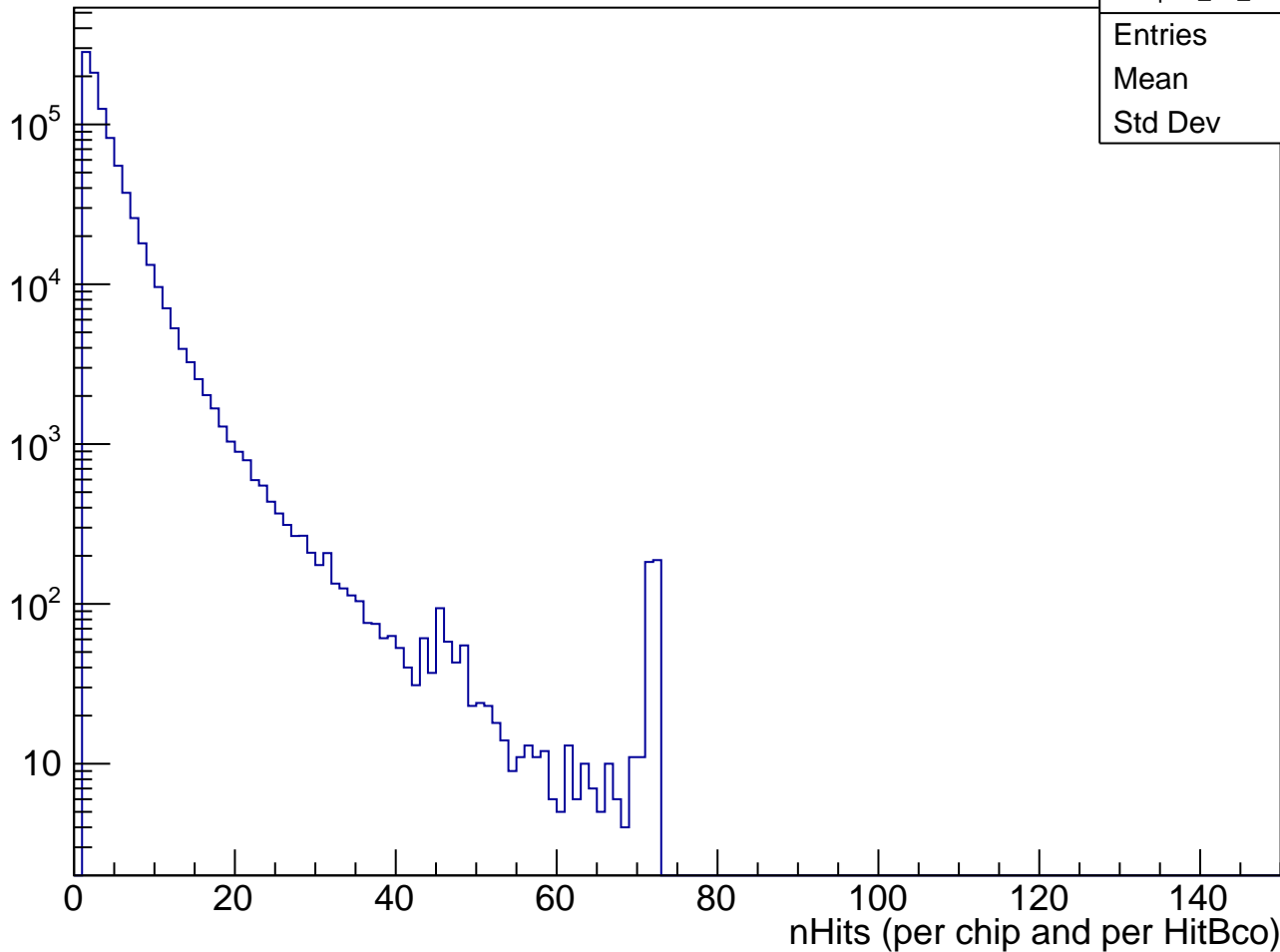


nChipHit_F3_Fch10_Chip9	
Entries	780202
Mean	2.832
Std Dev	3.35

nHits (per chip and per HitBco)

nChipHit_F3_Fch11_Chip0

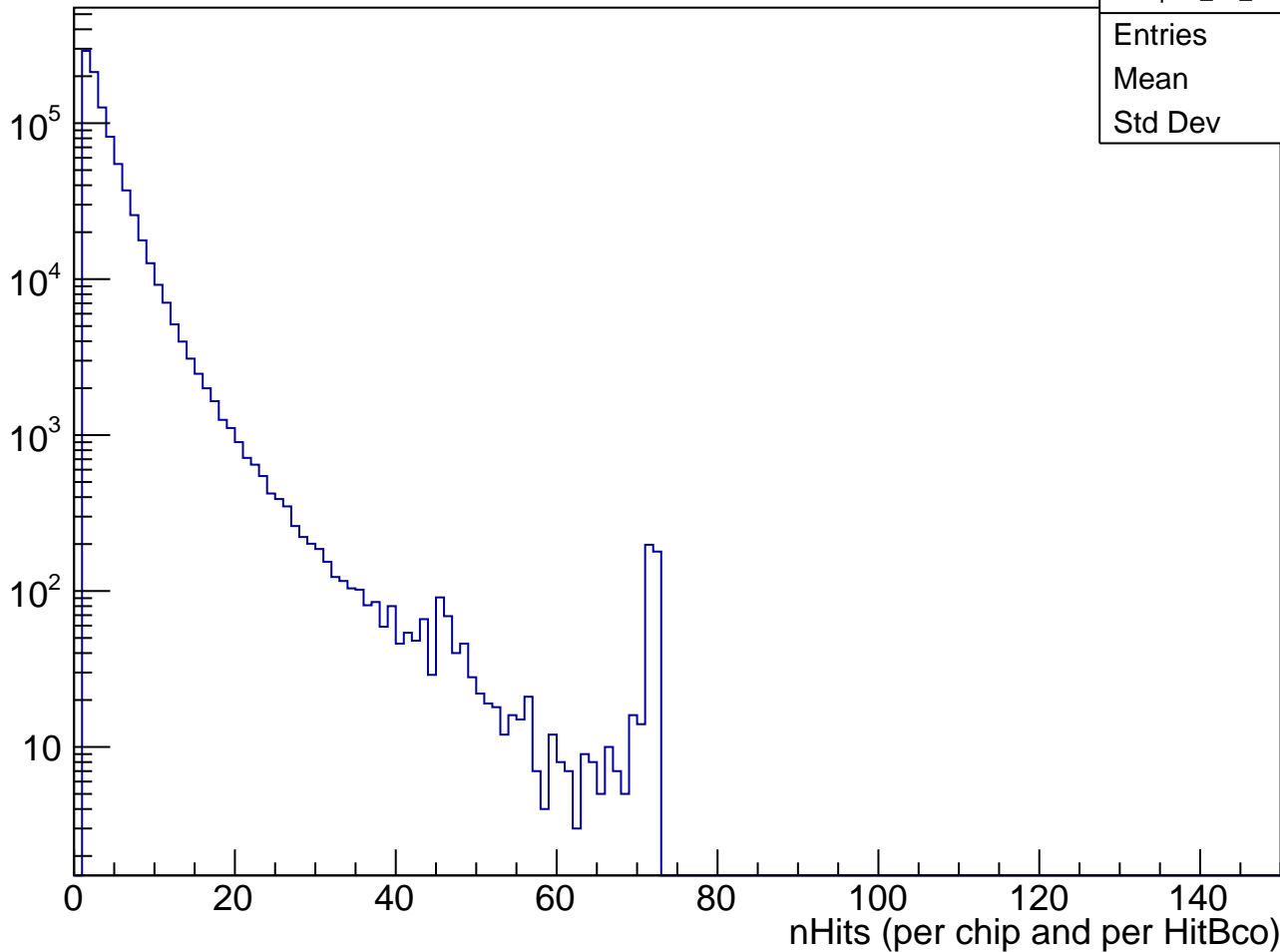
Entries



nChipHit_F3_Fch11_Chip0	
Entries	896190
Mean	3.391
Std Dev	3.841

nChipHit_F3_Fch11_Chip1

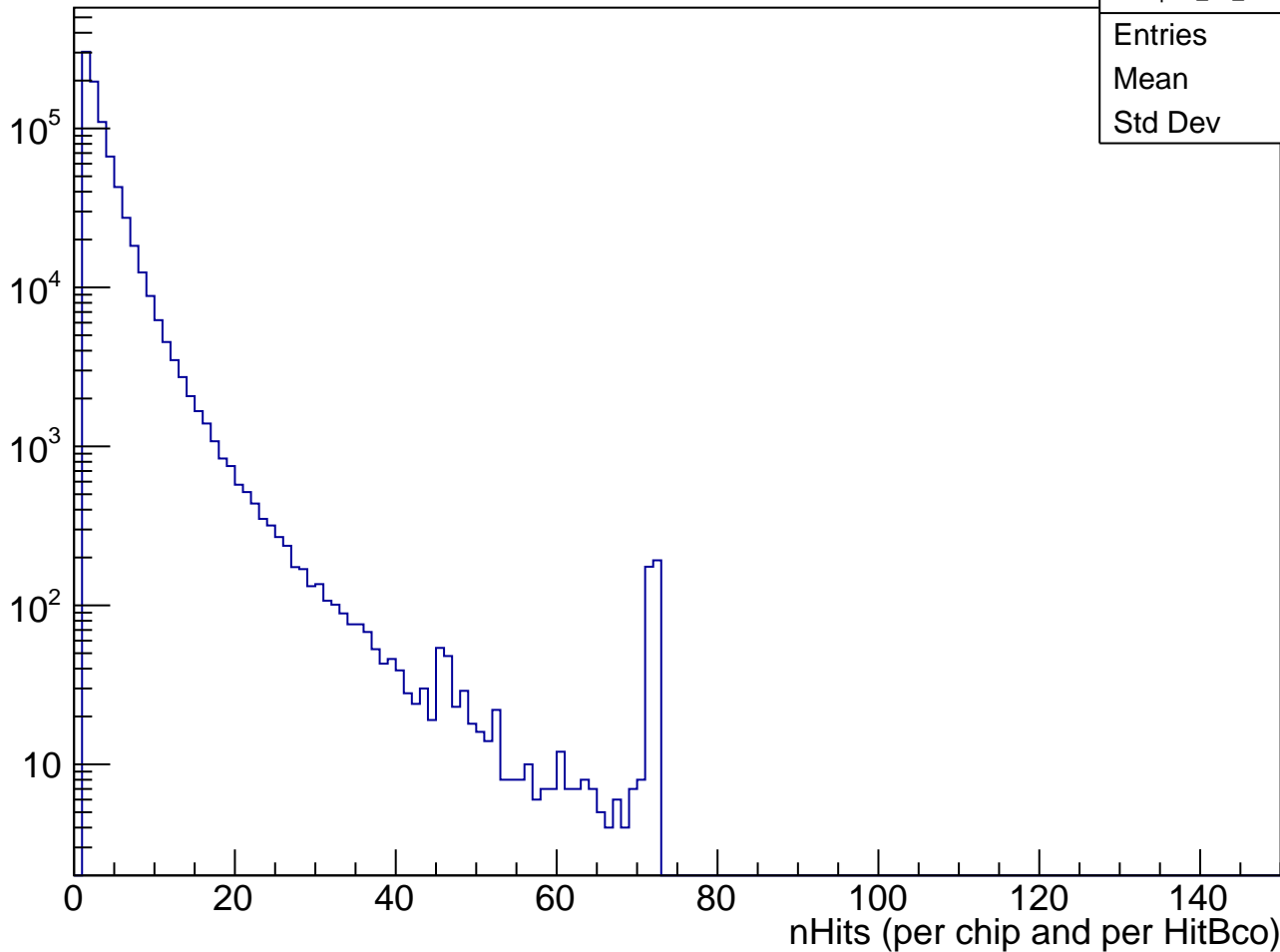
Entries



nChipHit_F3_Fch11_Chip1	
Entries	903026
Mean	3.354
Std Dev	3.828

nChipHit_F3_Fch11_Chip10

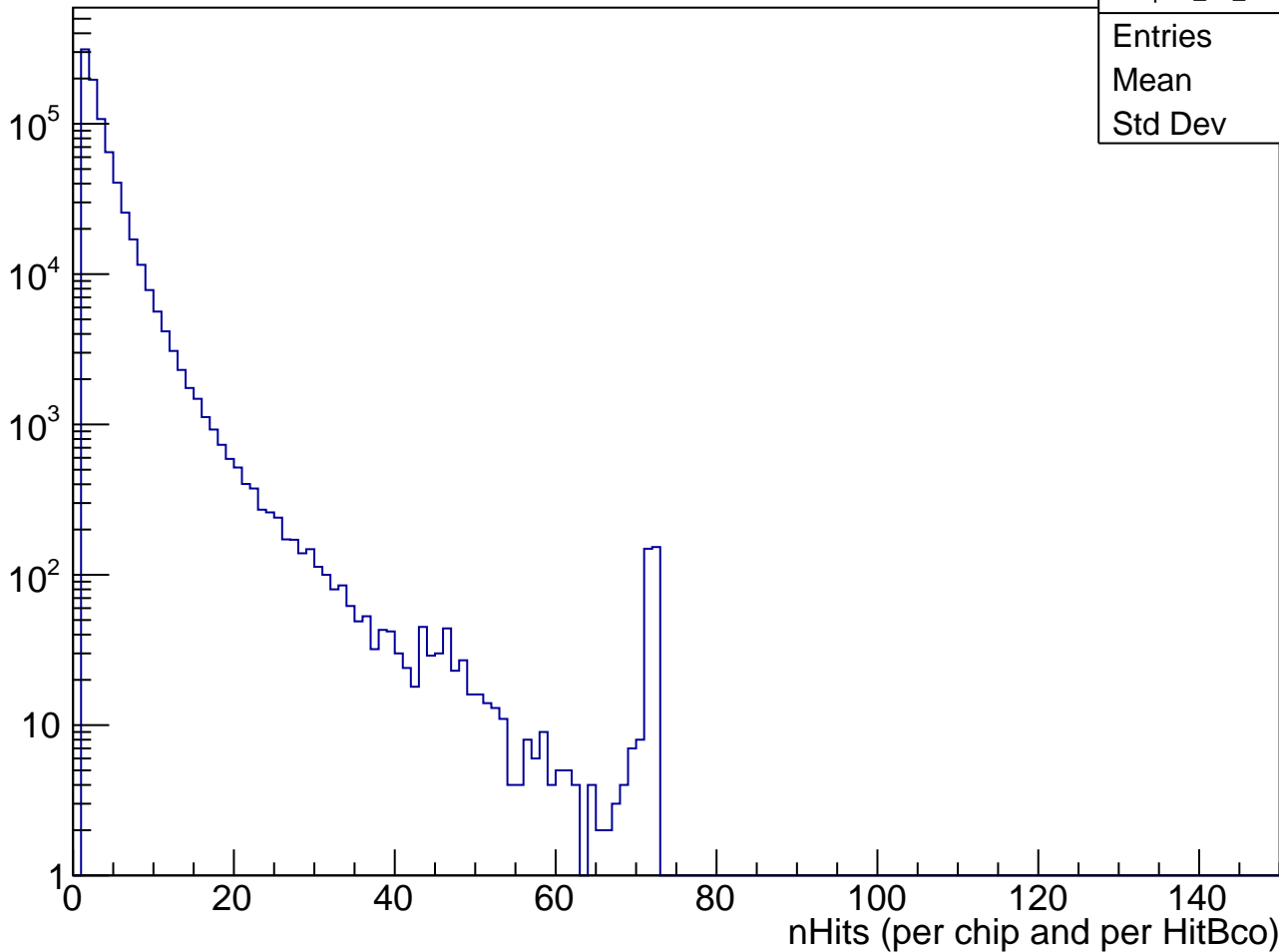
Entries



nChipHit_F3_Fch11_Chip10	
Entries	816418
Mean	2.992
Std Dev	3.529

nChipHit_F3_Fch11_Chip11

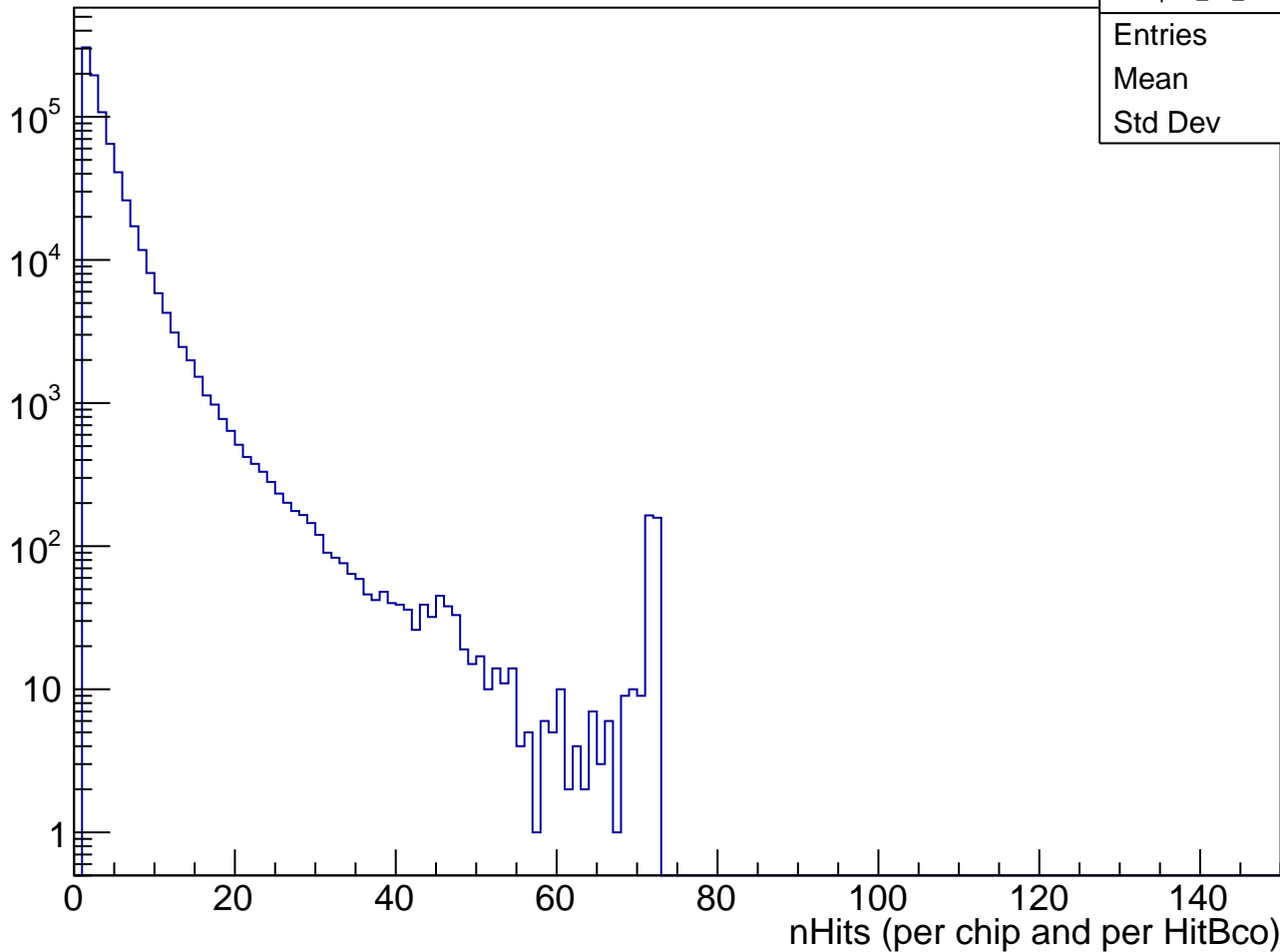
Entries



nChipHit_F3_Fch11_Chip11	
Entries	809831
Mean	2.872
Std Dev	3.32

nChipHit_F3_Fch11_Chip12

Entries



nChipHit_F3_Fch11_Chip12

Entries 804366

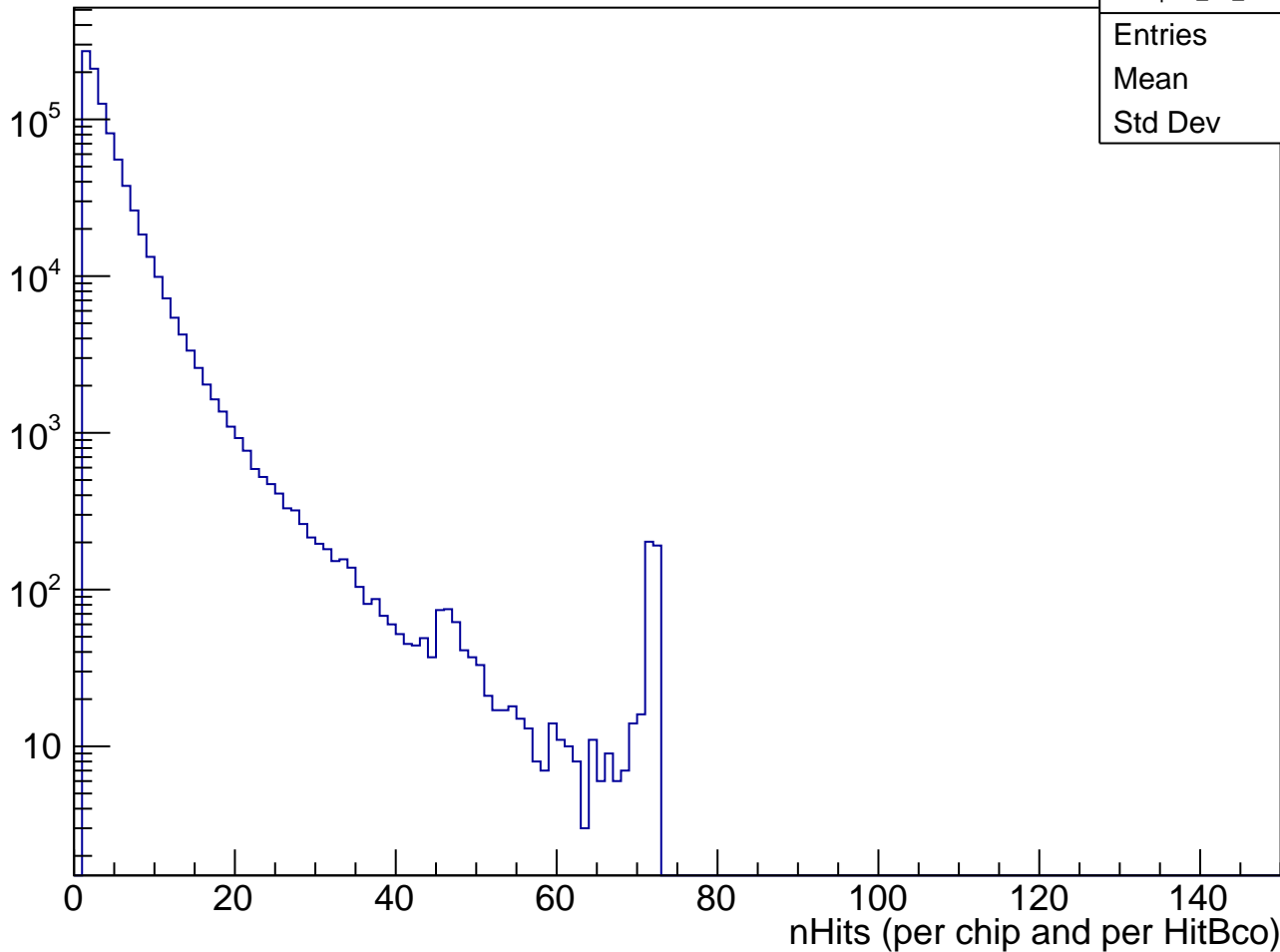
Mean 2.918

Std Dev 3.404

nHits (per chip and per HitBco)

nChipHit_F3_Fch11_Chip13

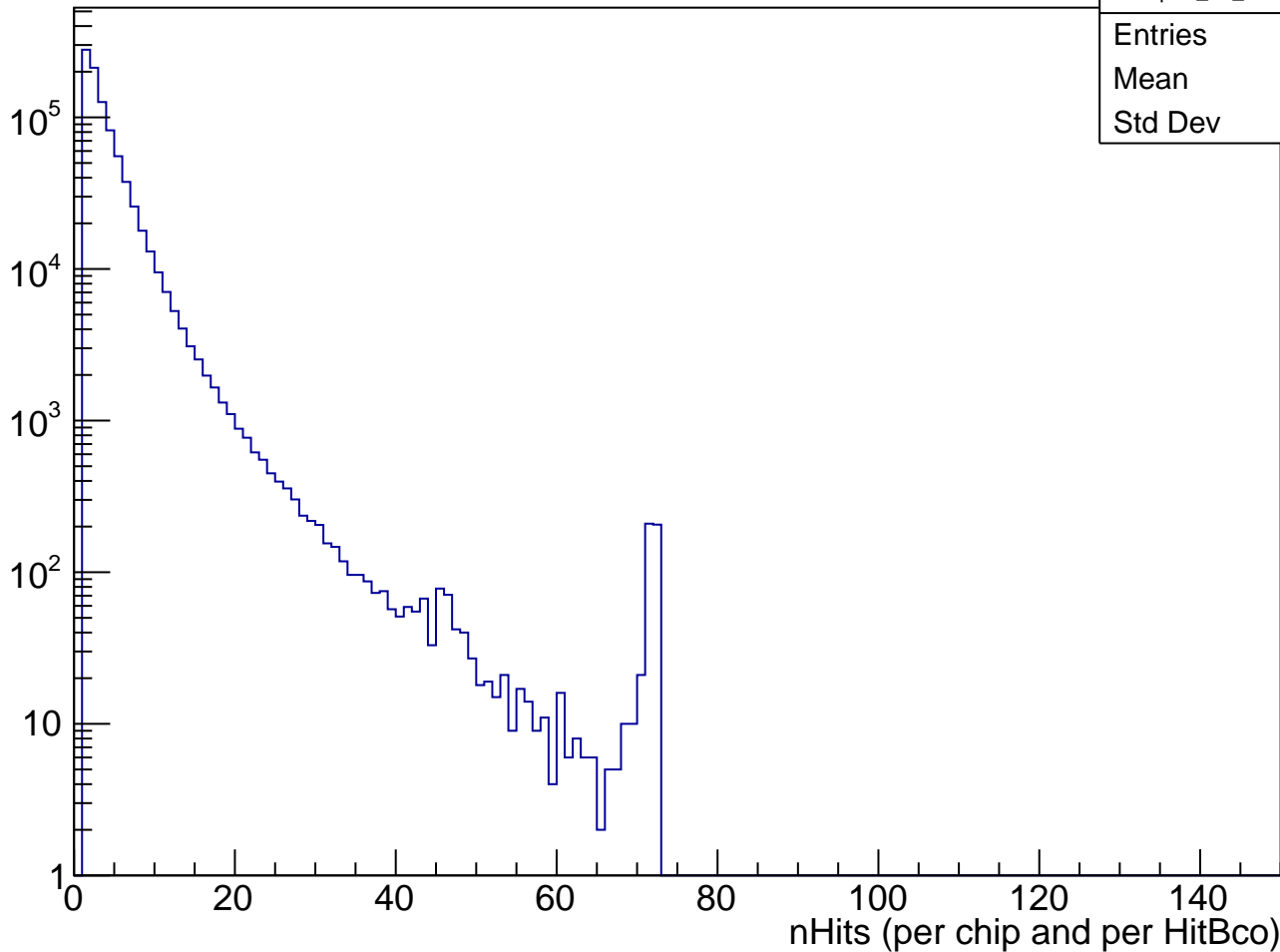
Entries



nChipHit_F3_Fch11_Chip13	
Entries	887328
Mean	3.448
Std Dev	3.924

nChipHit_F3_Fch11_Chip14

Entries



nChipHit_F3_Fch11_Chip14

Entries 894320

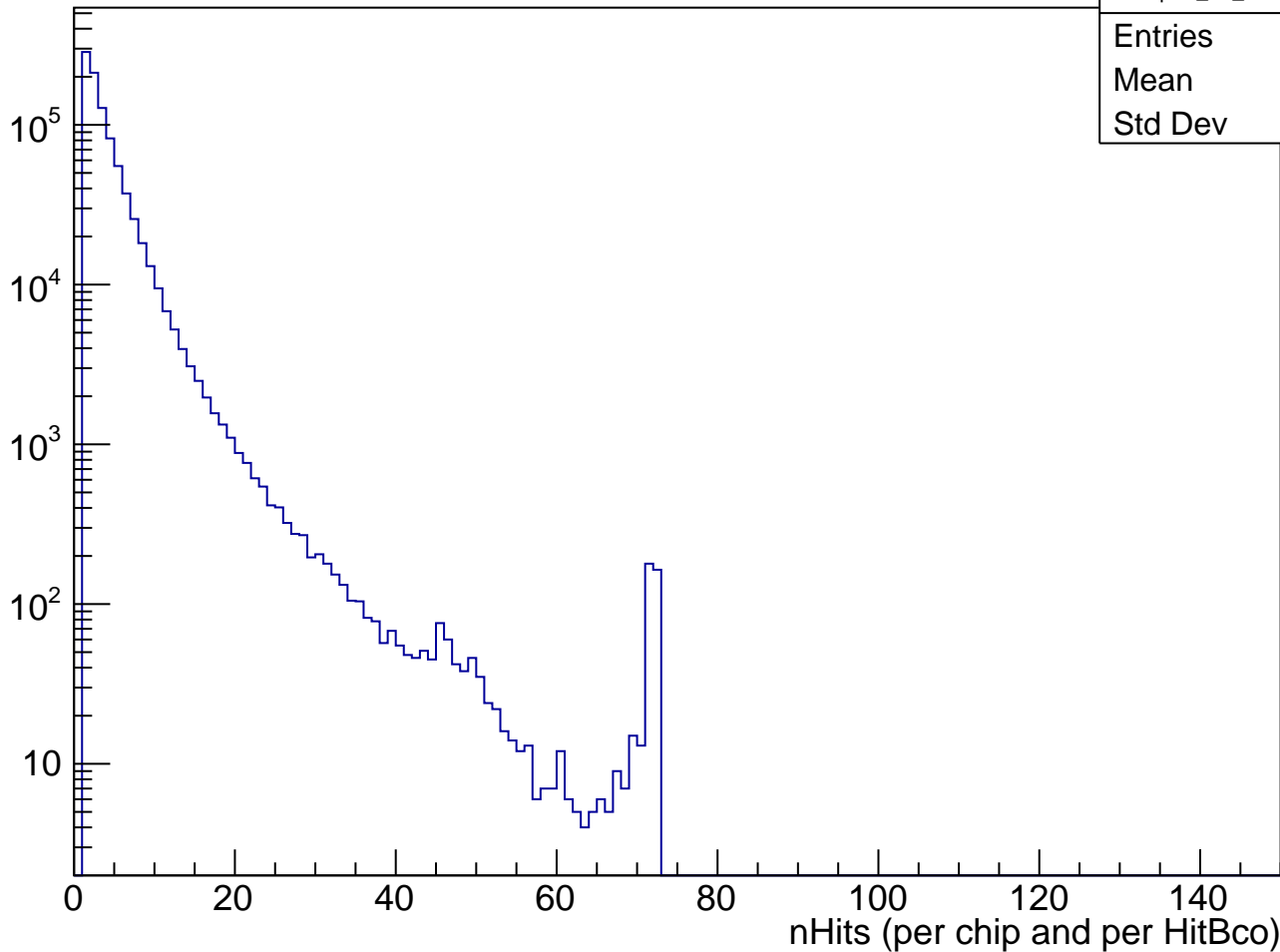
Mean 3.403

Std Dev 3.882

nHits (per chip and per HitBco)

nChipHit_F3_Fch11_Chip15

Entries



nChipHit_F3_Fch11_Chip15

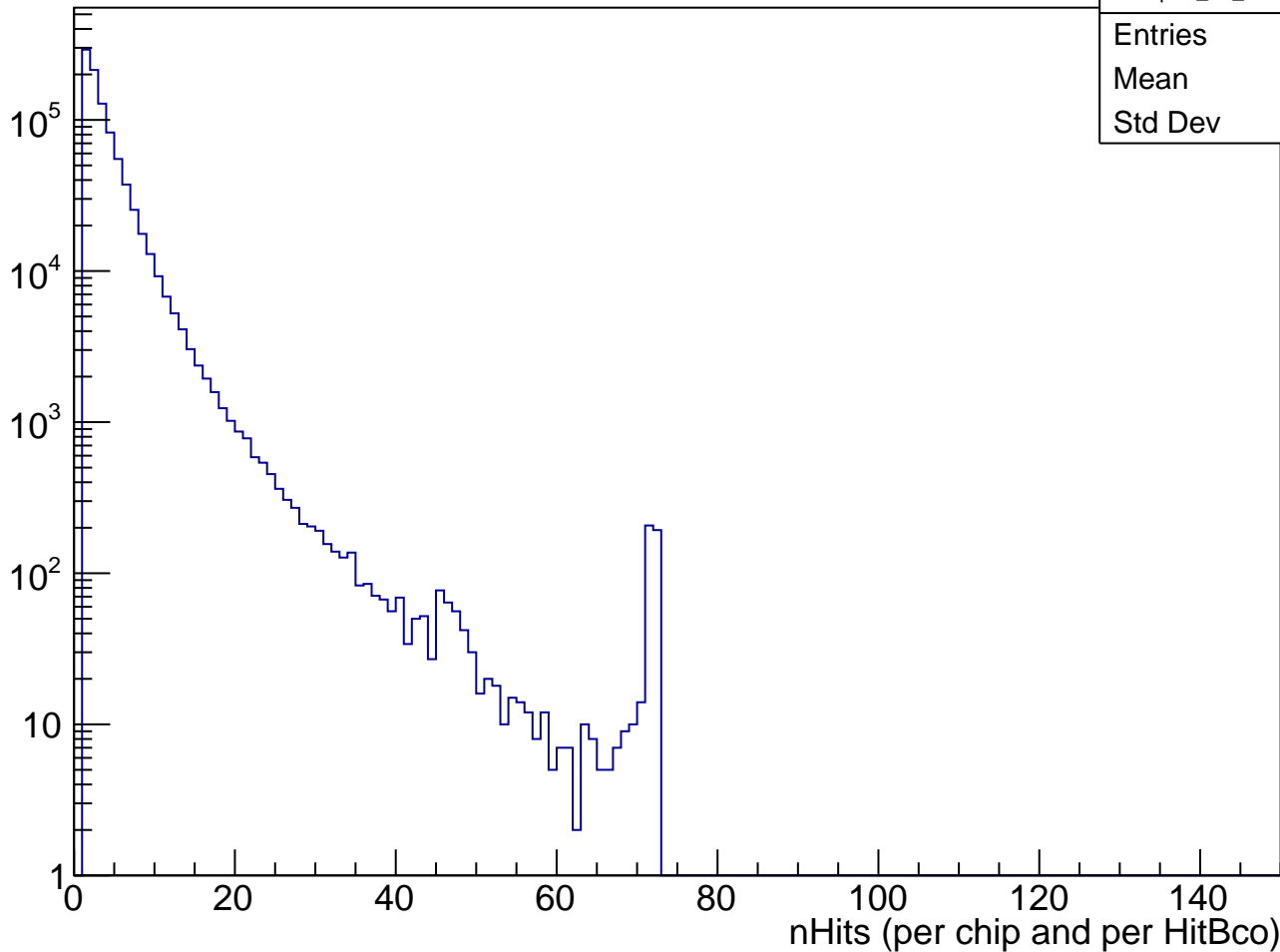
Entries 901151

Mean 3.374

Std Dev 3.816

nChipHit_F3_Fch11_Chip16

Entries



nChipHit_F3_Fch11_Chip16

Entries 908610

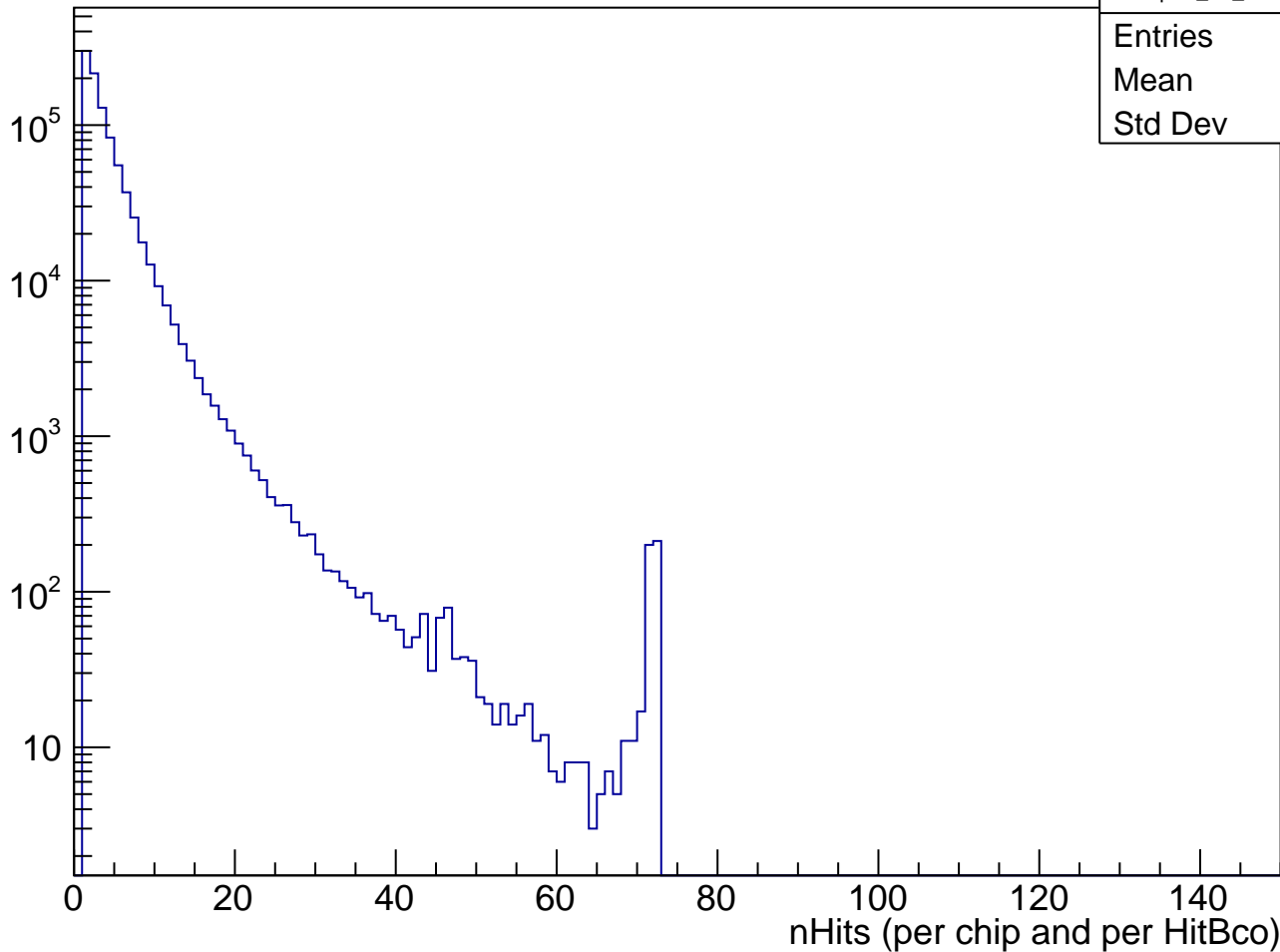
Mean 3.343

Std Dev 3.807

nHits (per chip and per HitBco)

nChipHit_F3_Fch11_Chip17

Entries



nChipHit_F3_Fch11_Chip17

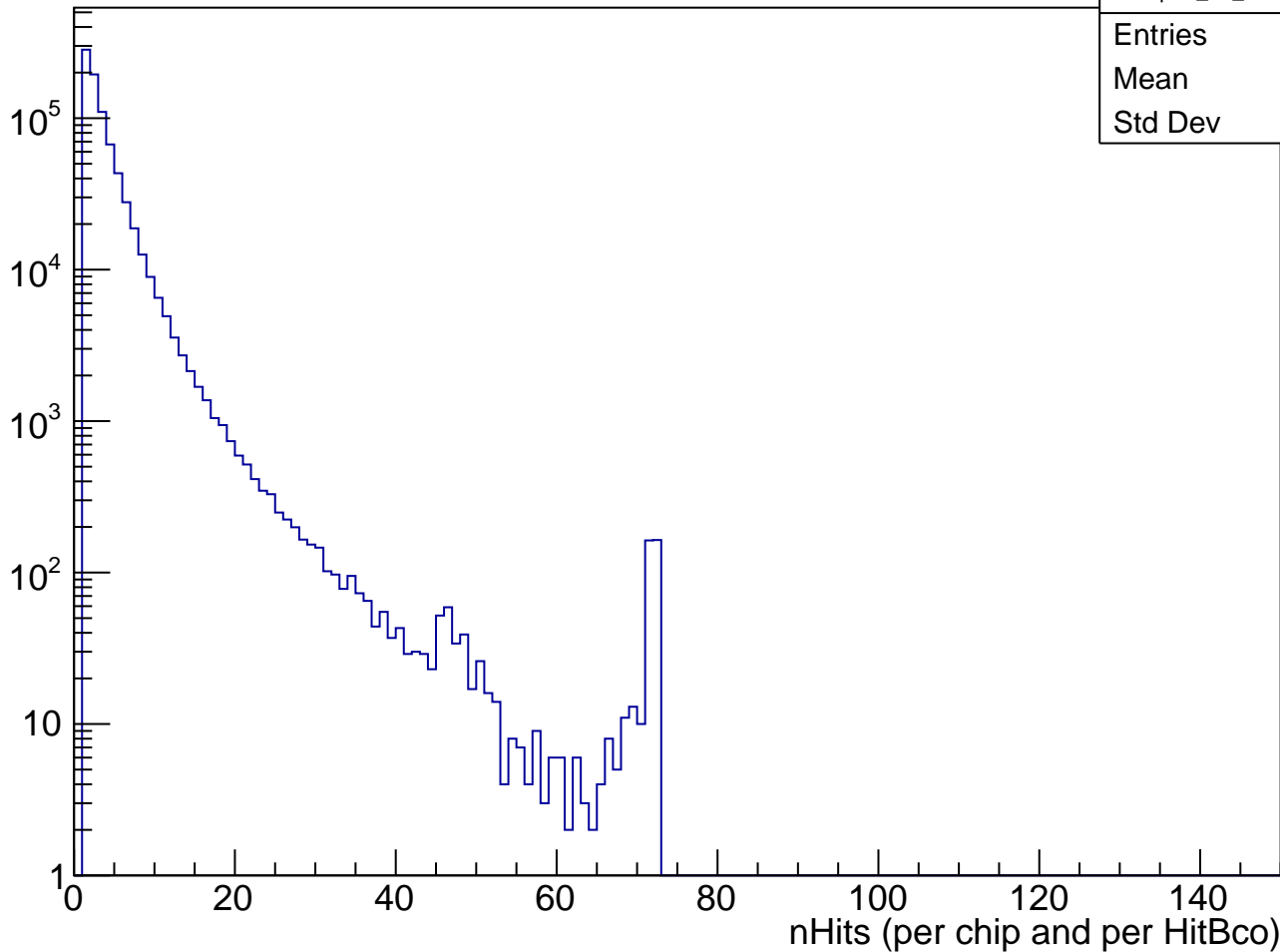
Entries 919301

Mean 3.321

Std Dev 3.814

nChipHit_F3_Fch11_Chip18

Entries



nChipHit_F3_Fch11_Chip18

Entries 796878

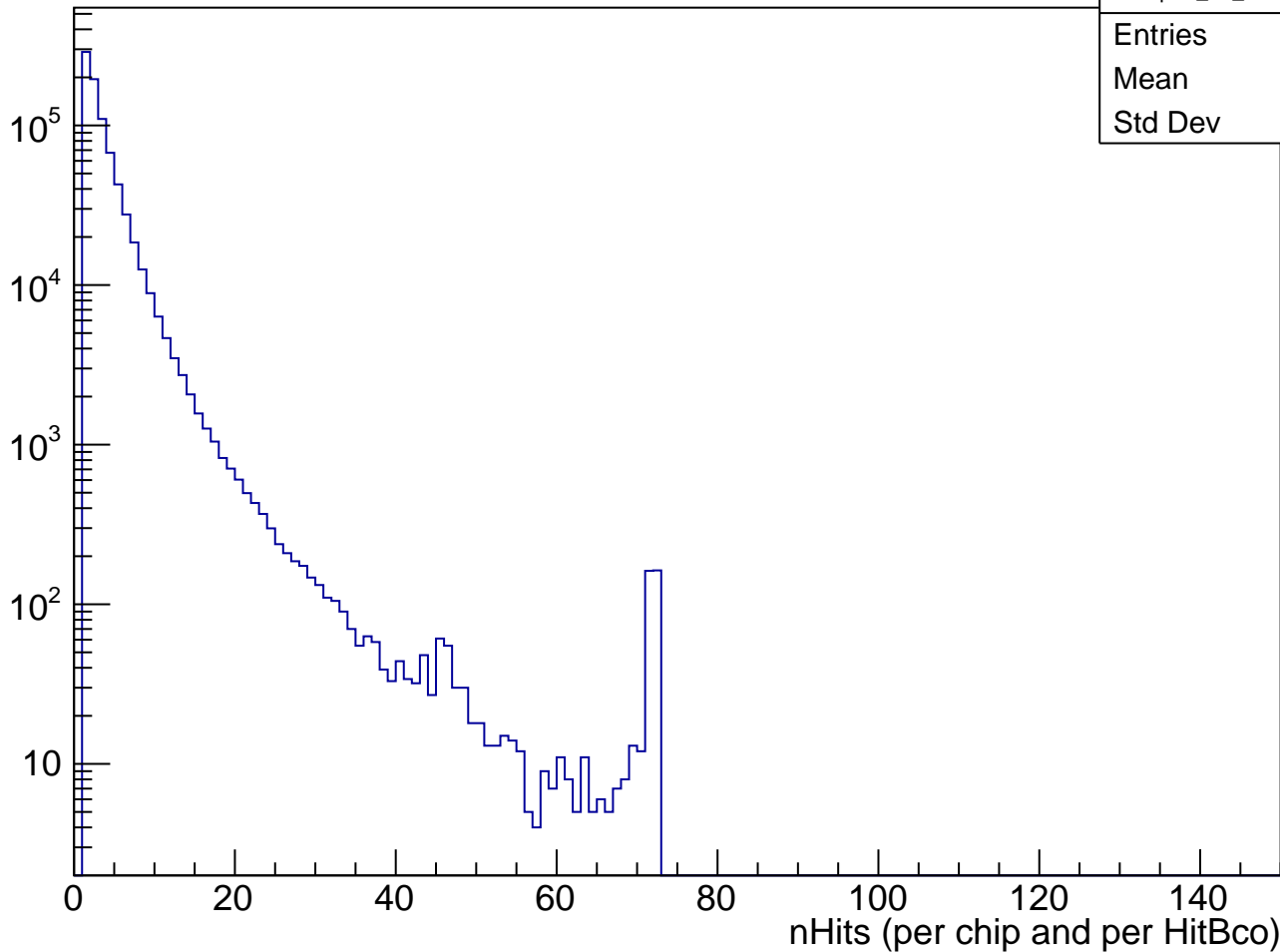
Mean 3.062

Std Dev 3.548

nHits (per chip and per HitBco)

nChipHit_F3_Fch11_Chip19

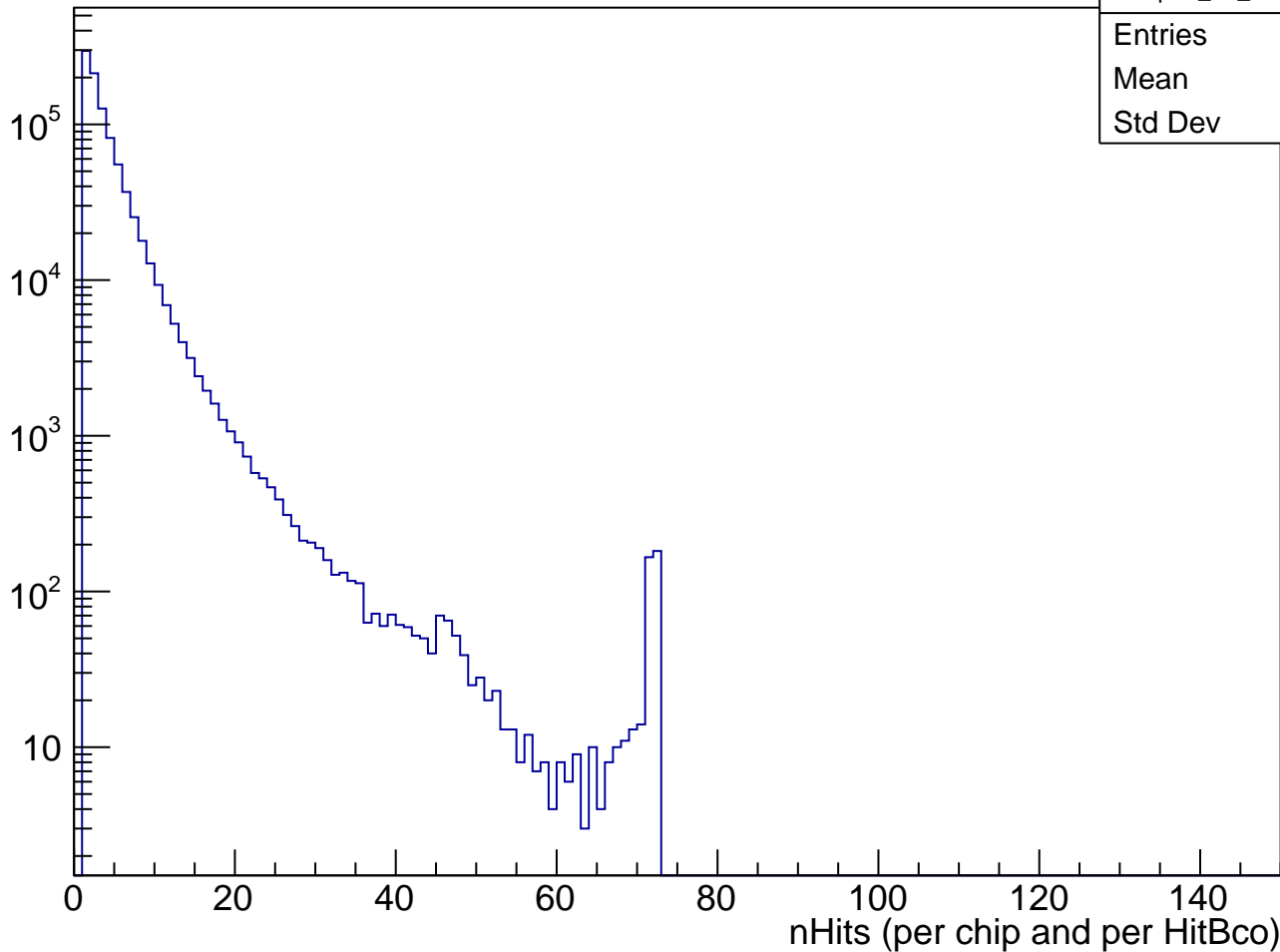
Entries



nChipHit_F3_Fch11_Chip19	
Entries	800498
Mean	3.032
Std Dev	3.536

nChipHit_F3_Fch11_Chip2

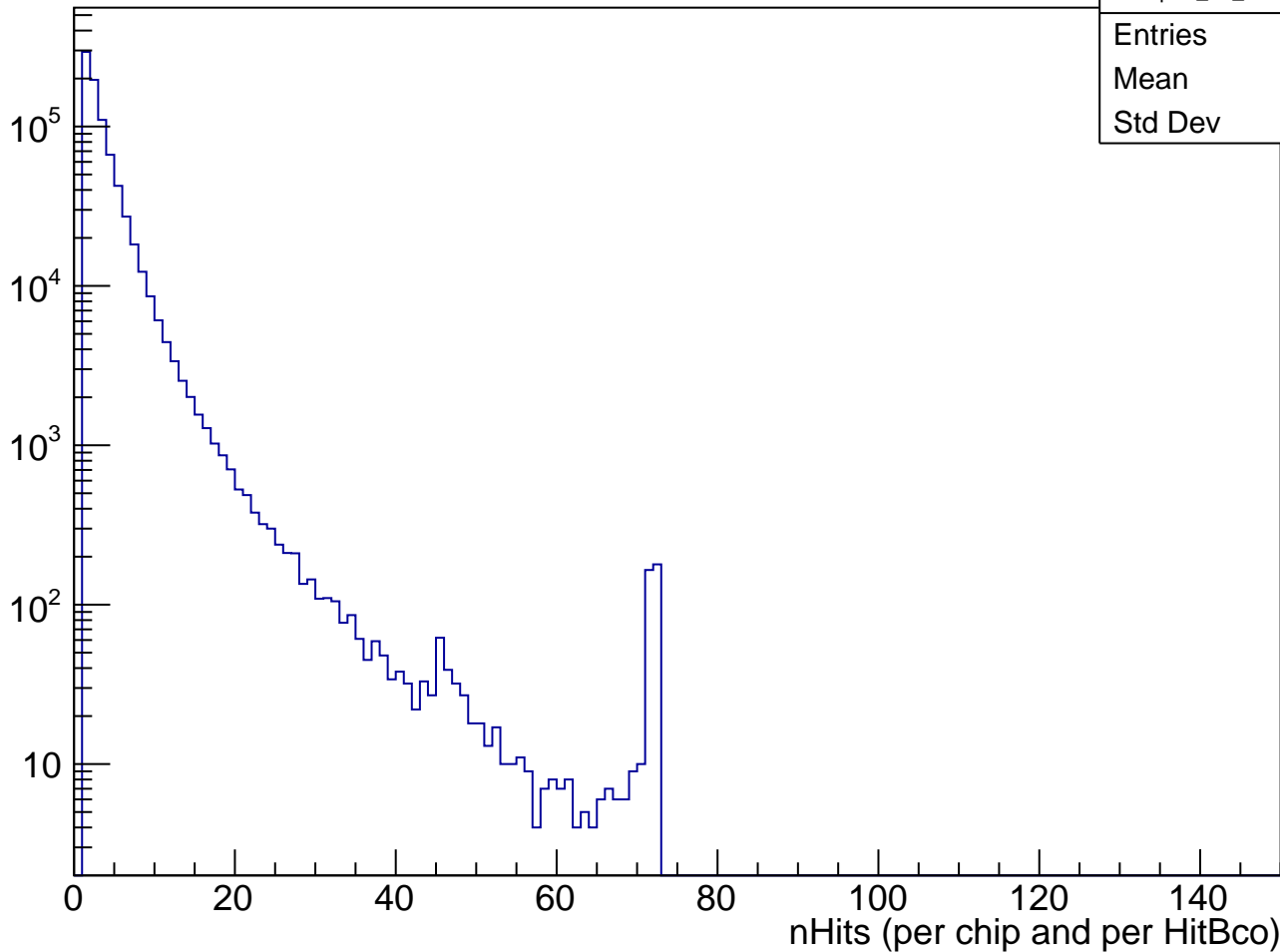
Entries



nChipHit_F3_Fch11_Chip2	
Entries	909680
Mean	3.334
Std Dev	3.79

nChipHit_F3_Fch11_Chip20

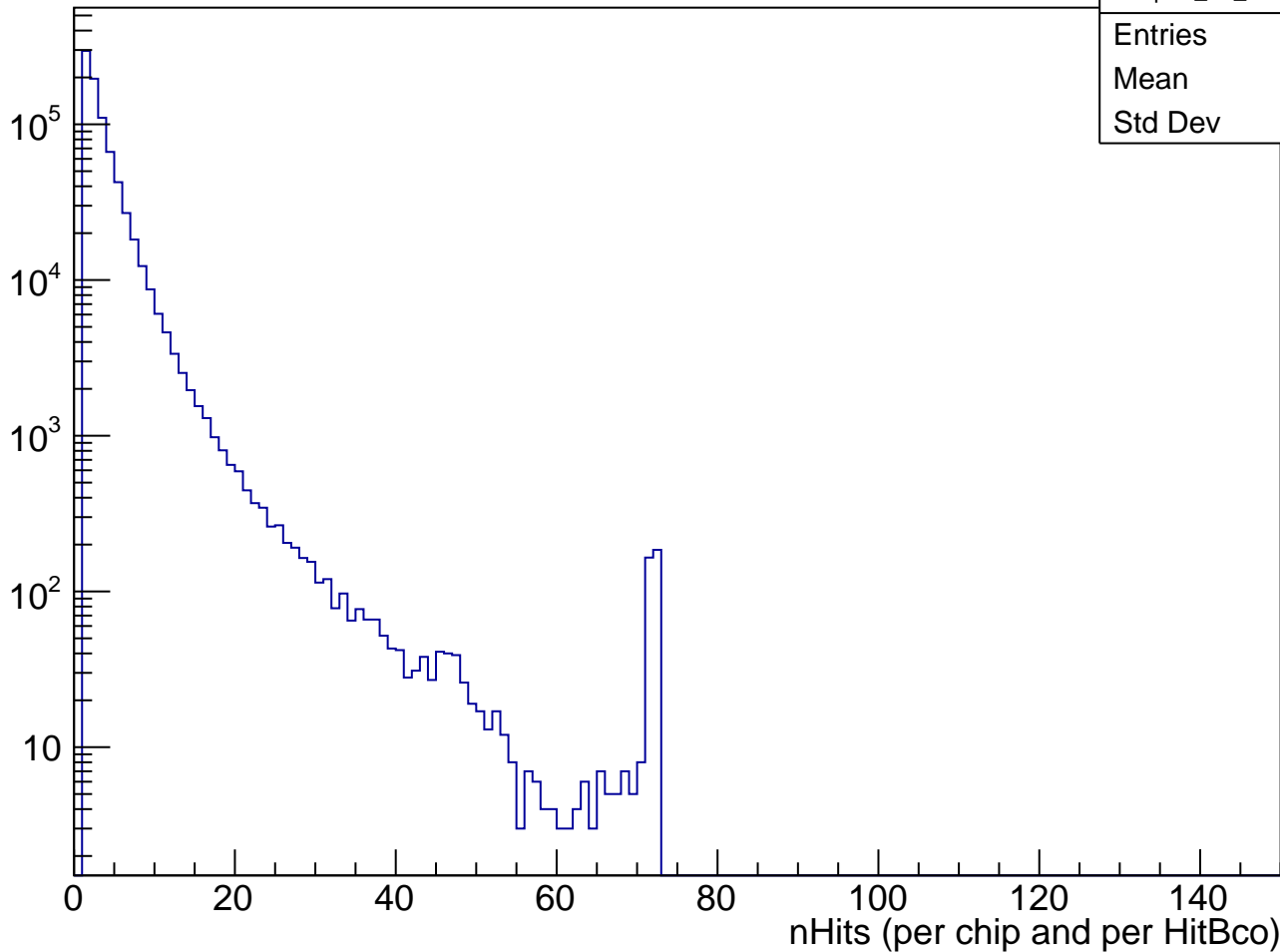
Entries



nChipHit_F3_Fch11_Chip20	
Entries	803905
Mean	2.992
Std Dev	3.49

nChipHit_F3_Fch11_Chip21

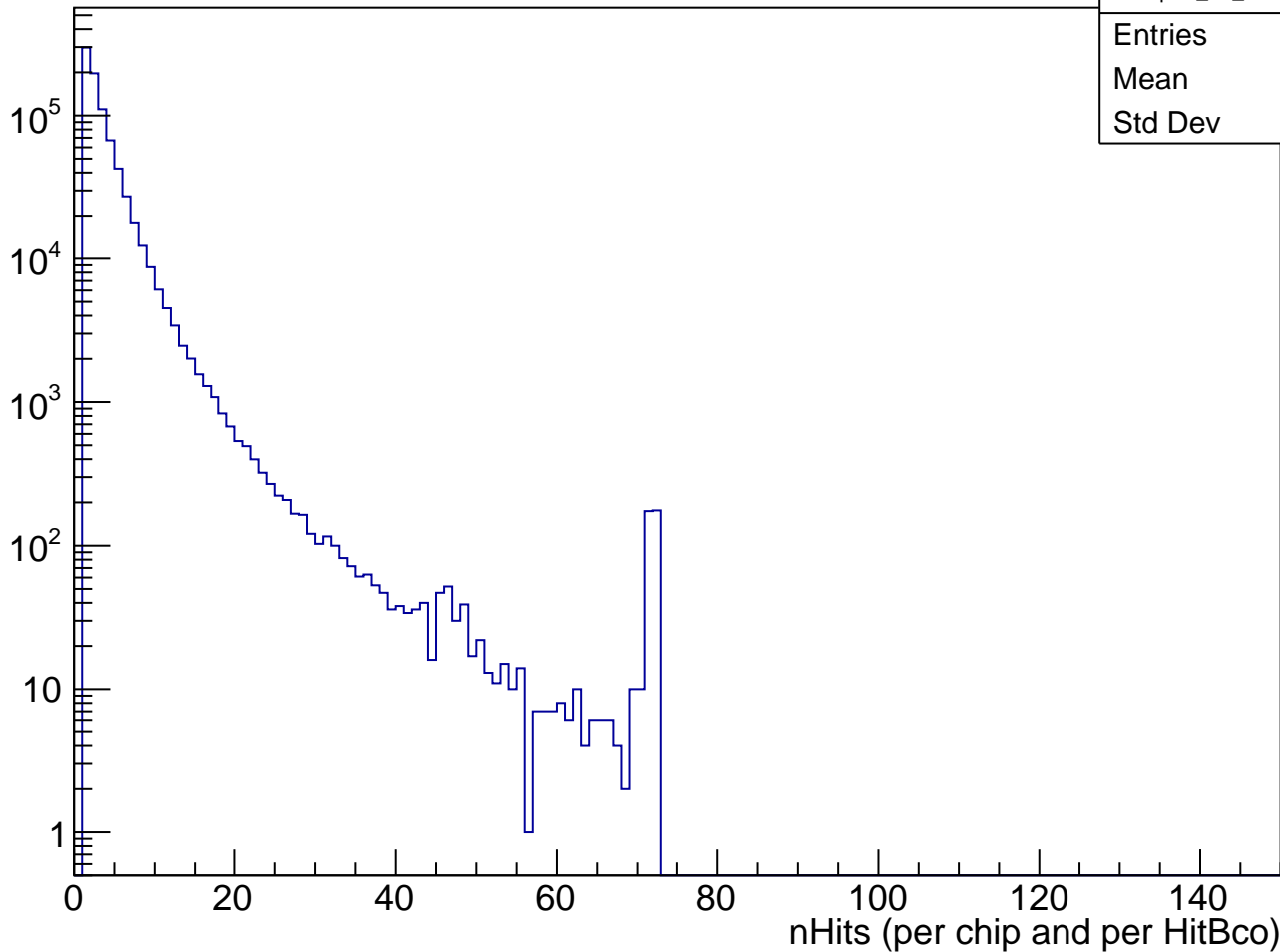
Entries



nChipHit_F3_Fch11_Chip21	
Entries	806177
Mean	2.986
Std Dev	3.48

nChipHit_F3_Fch11_Chip22

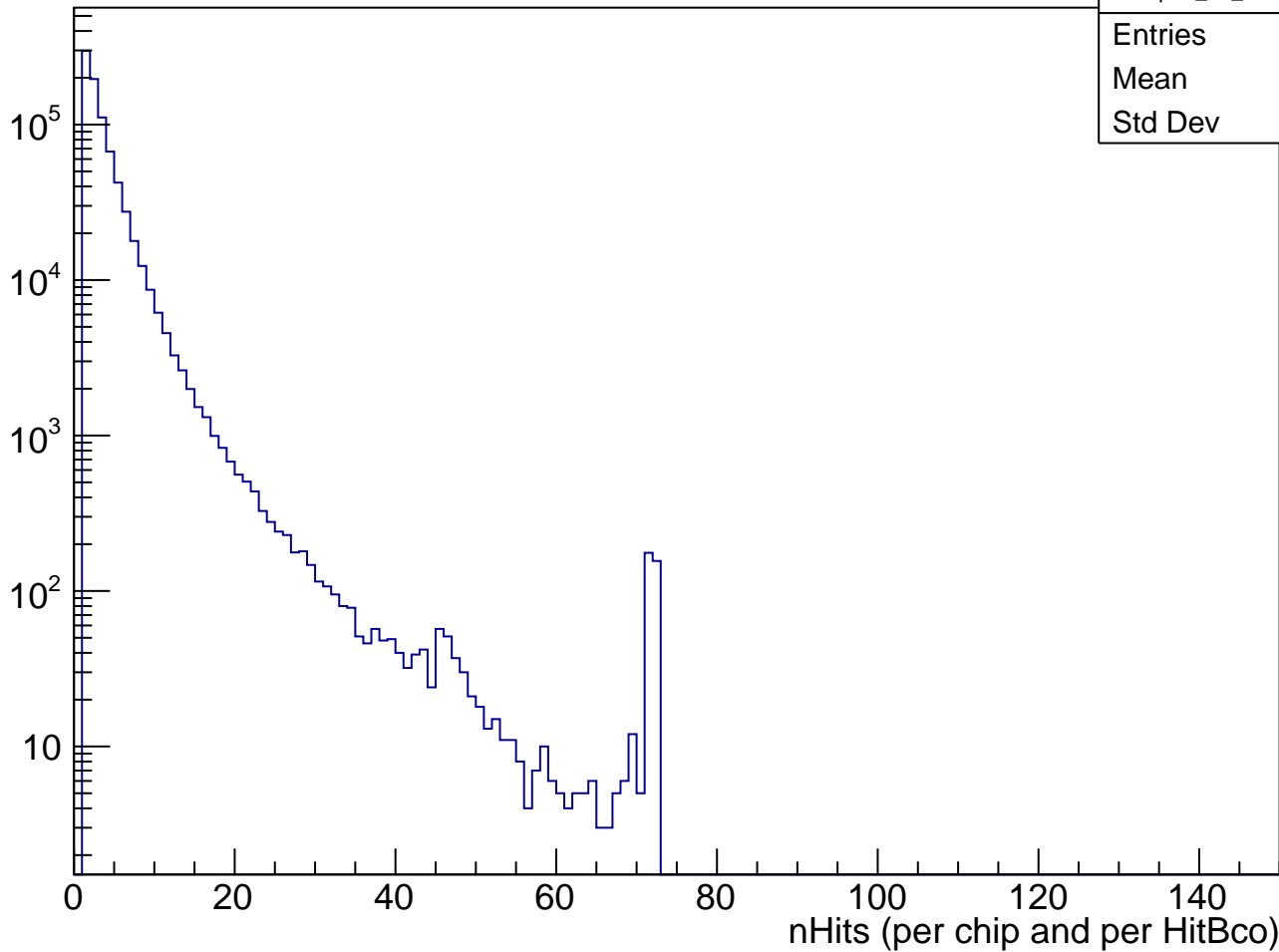
Entries



nChipHit_F3_Fch11_Chip22	
Entries	810030
Mean	2.983
Std Dev	3.485

nChipHit_F3_Fch11_Chip23

Entries



nChipHit_F3_Fch11_Chip23

Entries 810063

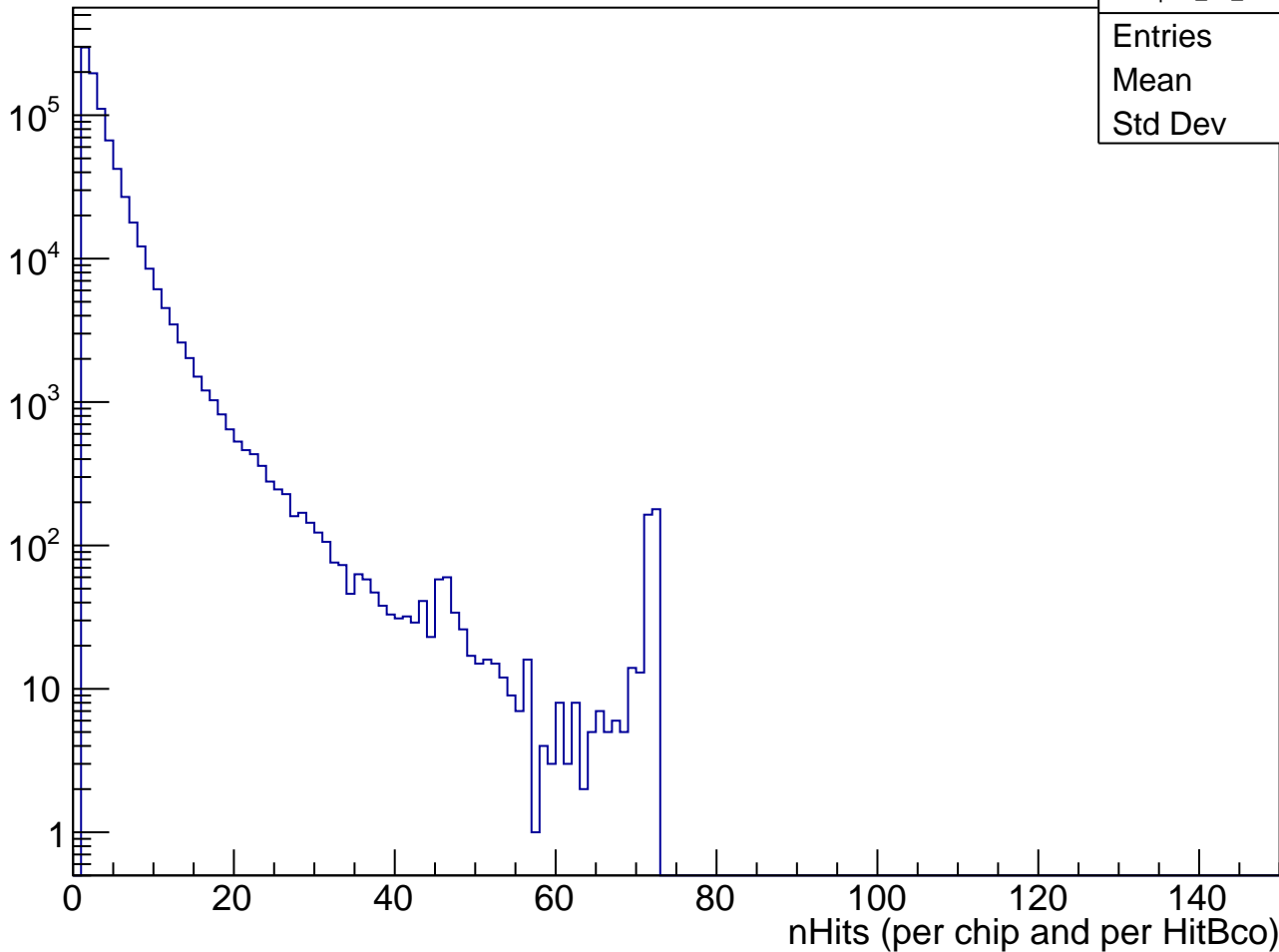
Mean 2.985

Std Dev 3.479

nHits (per chip and per HitBco)

nChipHit_F3_Fch11_Chip24

Entries



nChipHit_F3_Fch11_Chip24

Entries 806771

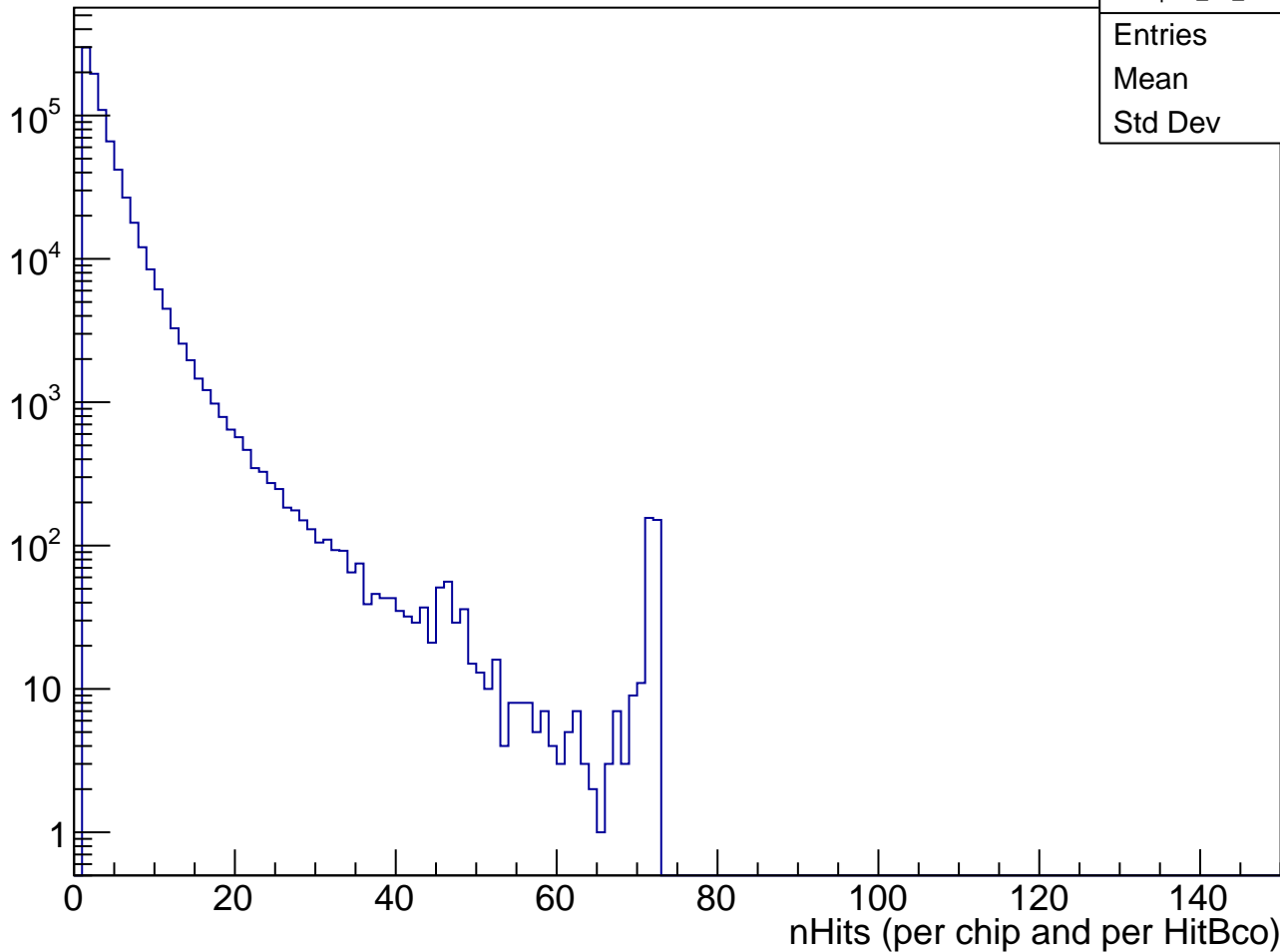
Mean 2.98

Std Dev 3.476

nHits (per chip and per HitBco)

nChipHit_F3_Fch11_Chip25

Entries



nChipHit_F3_Fch11_Chip25

Entries 804167

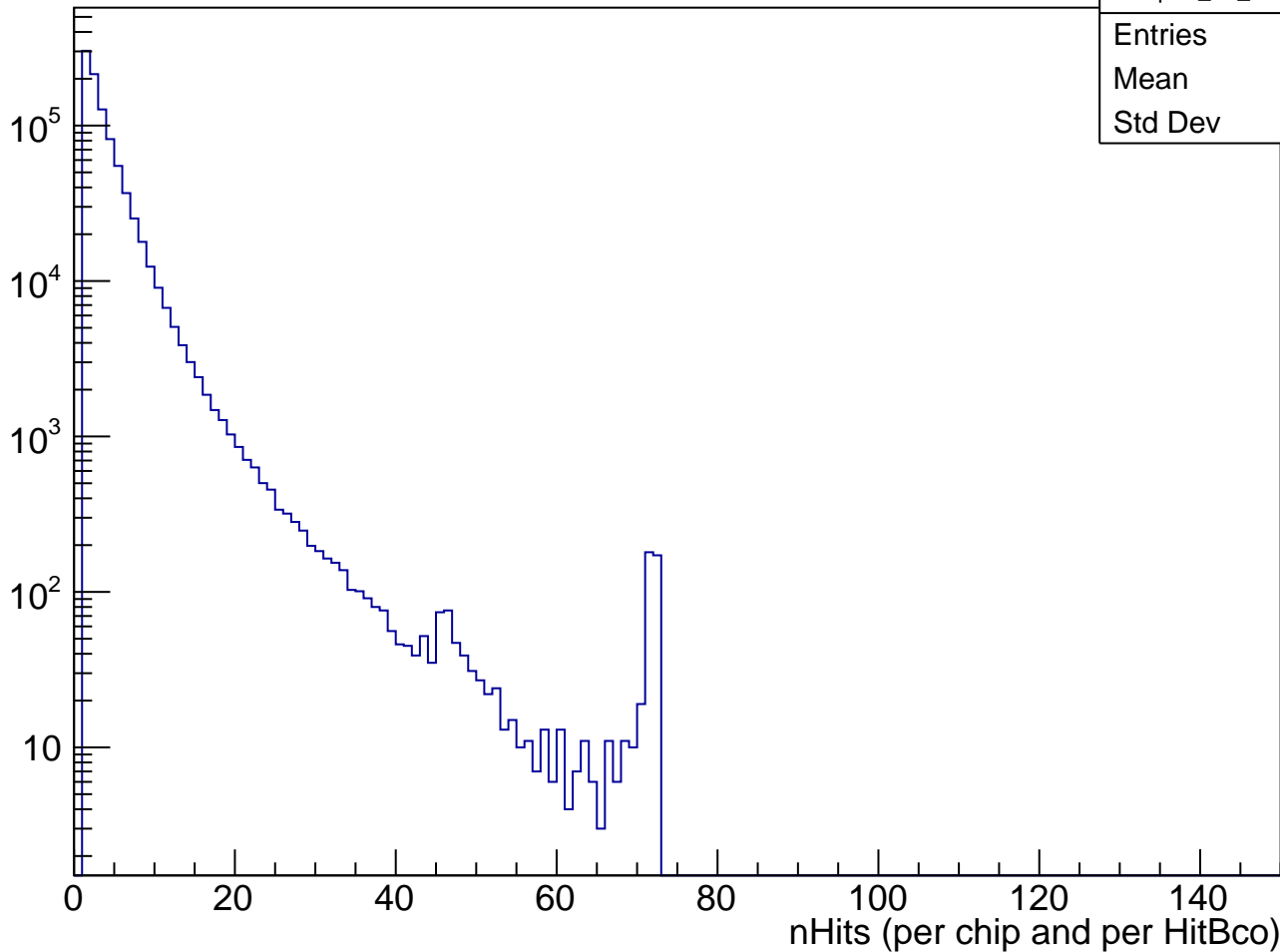
Mean 2.959

Std Dev 3.412

nHits (per chip and per HitBco)

nChipHit_F3_Fch11_Chip3

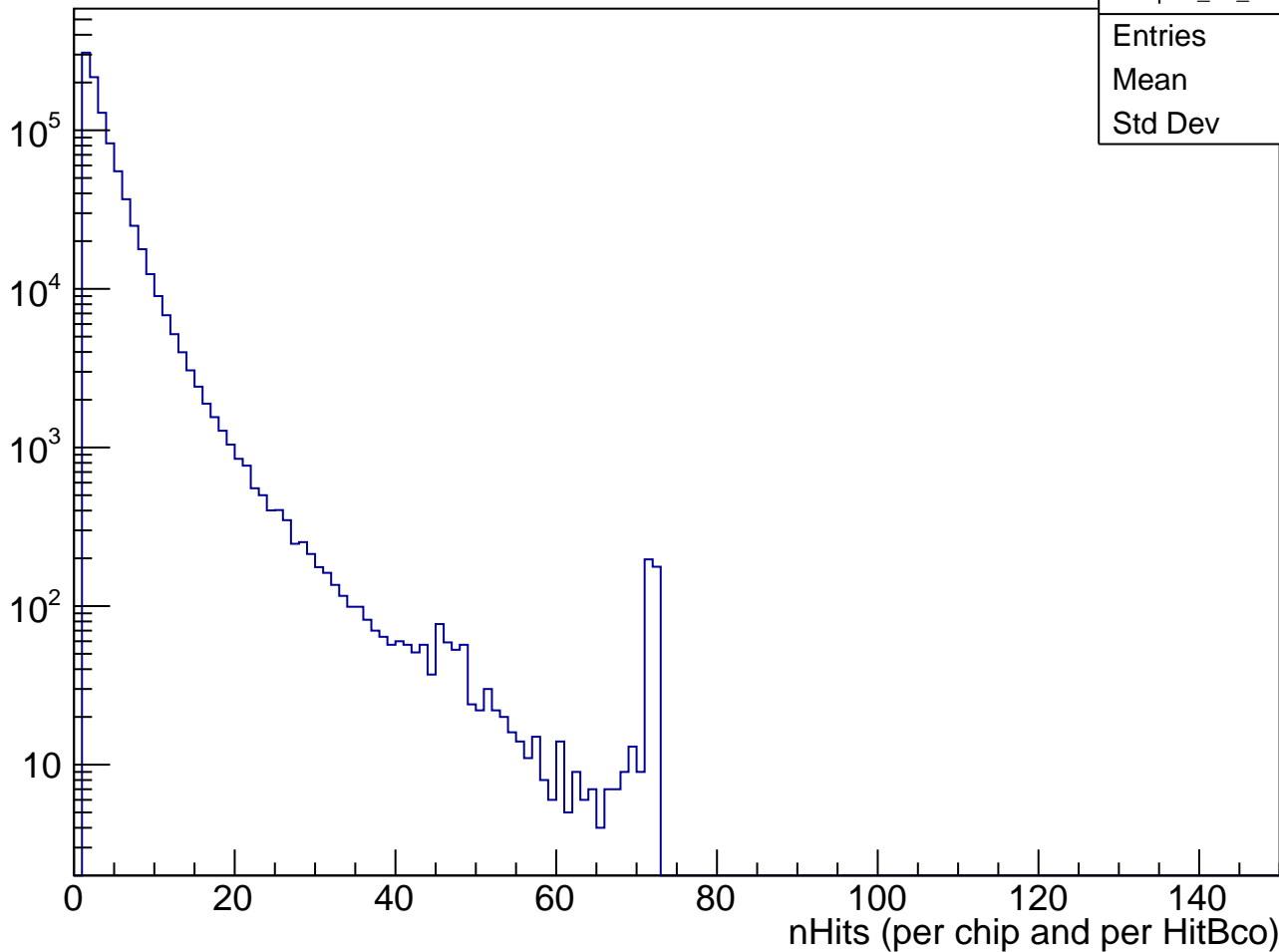
Entries



nChipHit_F3_Fch11_Chip3	
Entries	915475
Mean	3.301
Std Dev	3.773

nChipHit_F3_Fch11_Chip4

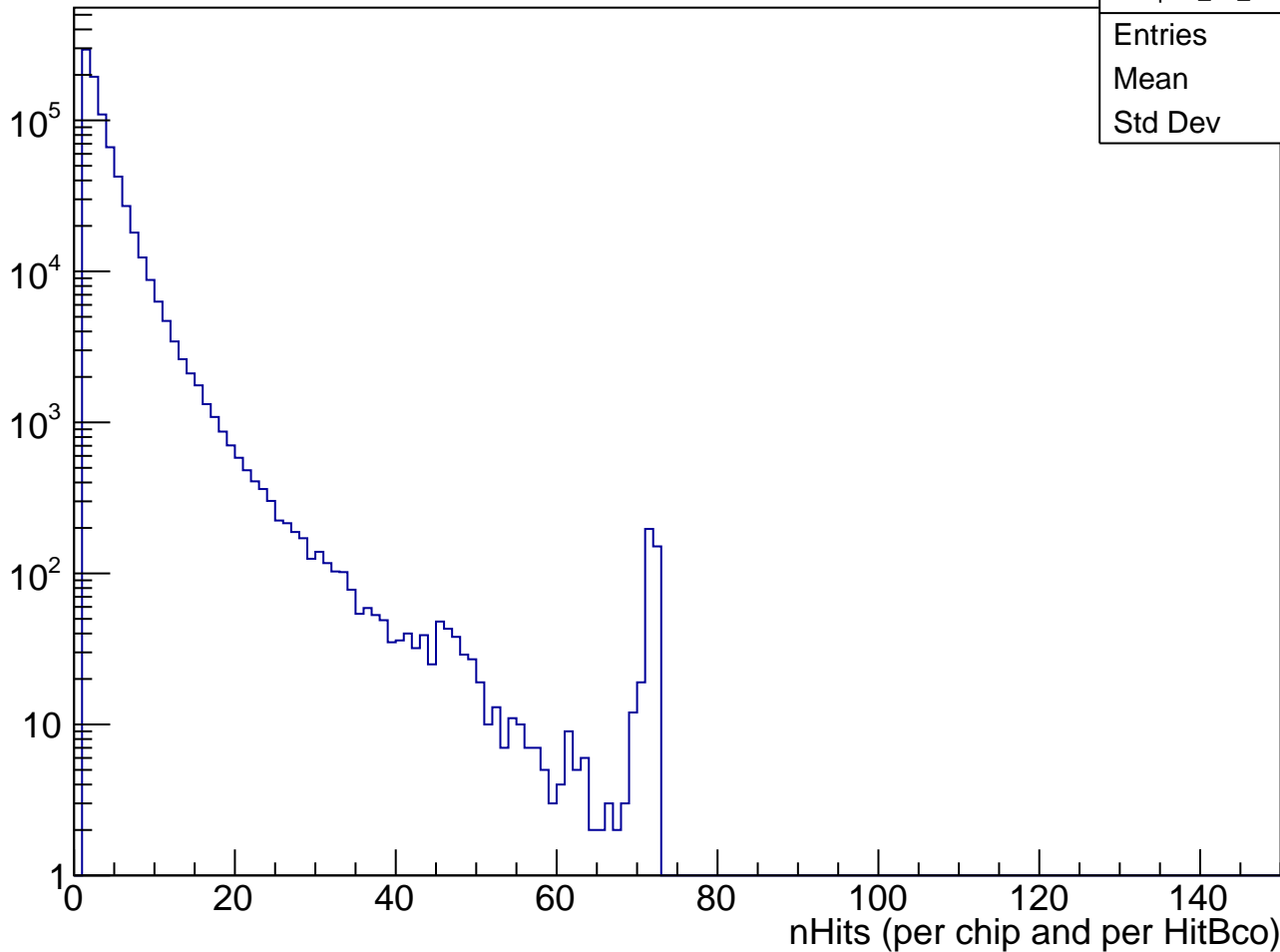
Entries



nChipHit_F3_Fch11_Chip4	
Entries	926382
Mean	3.289
Std Dev	3.776

nChipHit_F3_Fch11_Chip5

Entries

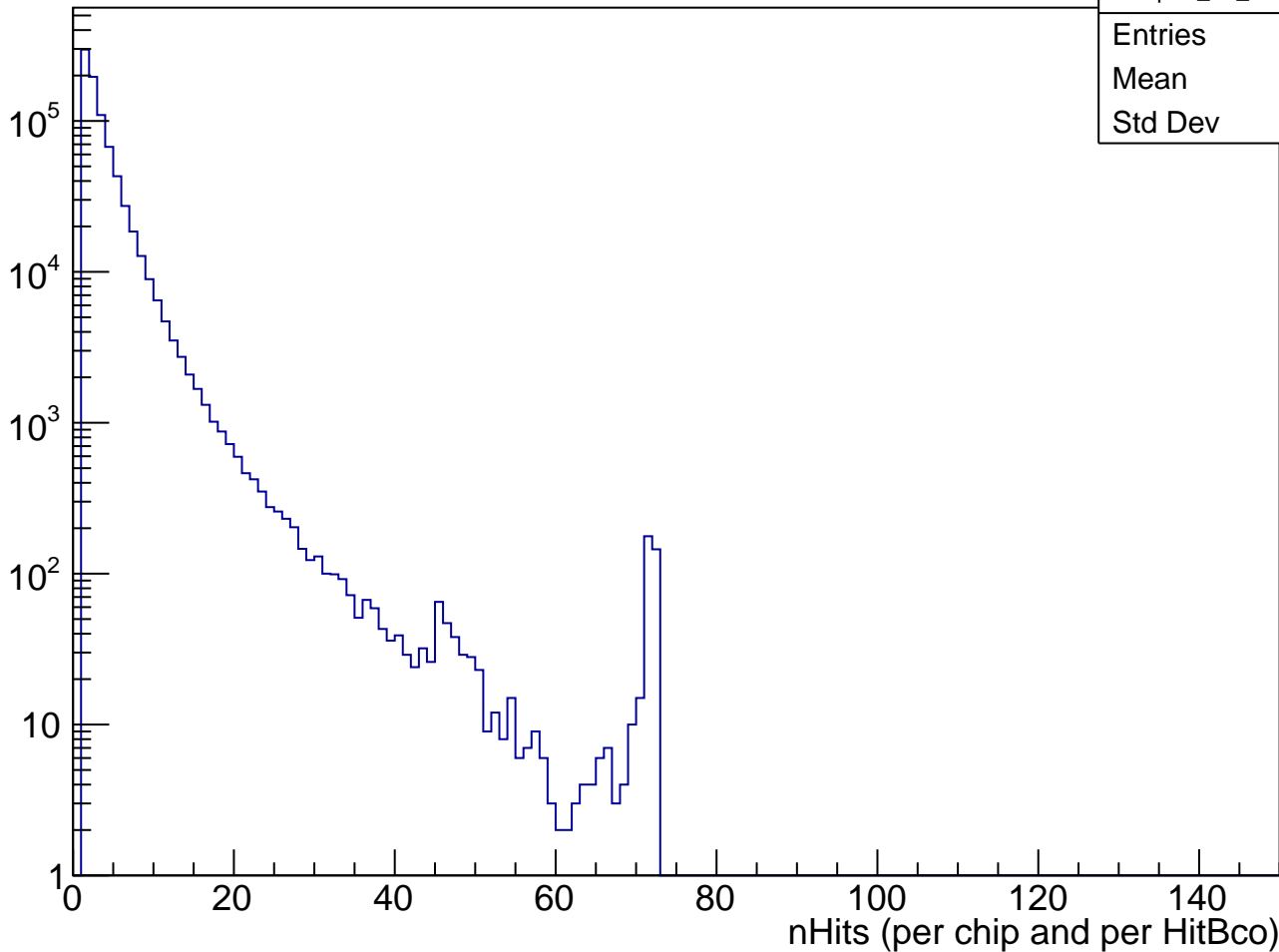


nChipHit_F3_Fch11_Chip5	
Entries	802619
Mean	3.012
Std Dev	3.527

nHits (per chip and per HitBco)

nChipHit_F3_Fch11_Chip6

Entries



nChipHit_F3_Fch11_Chip6

Entries 810427

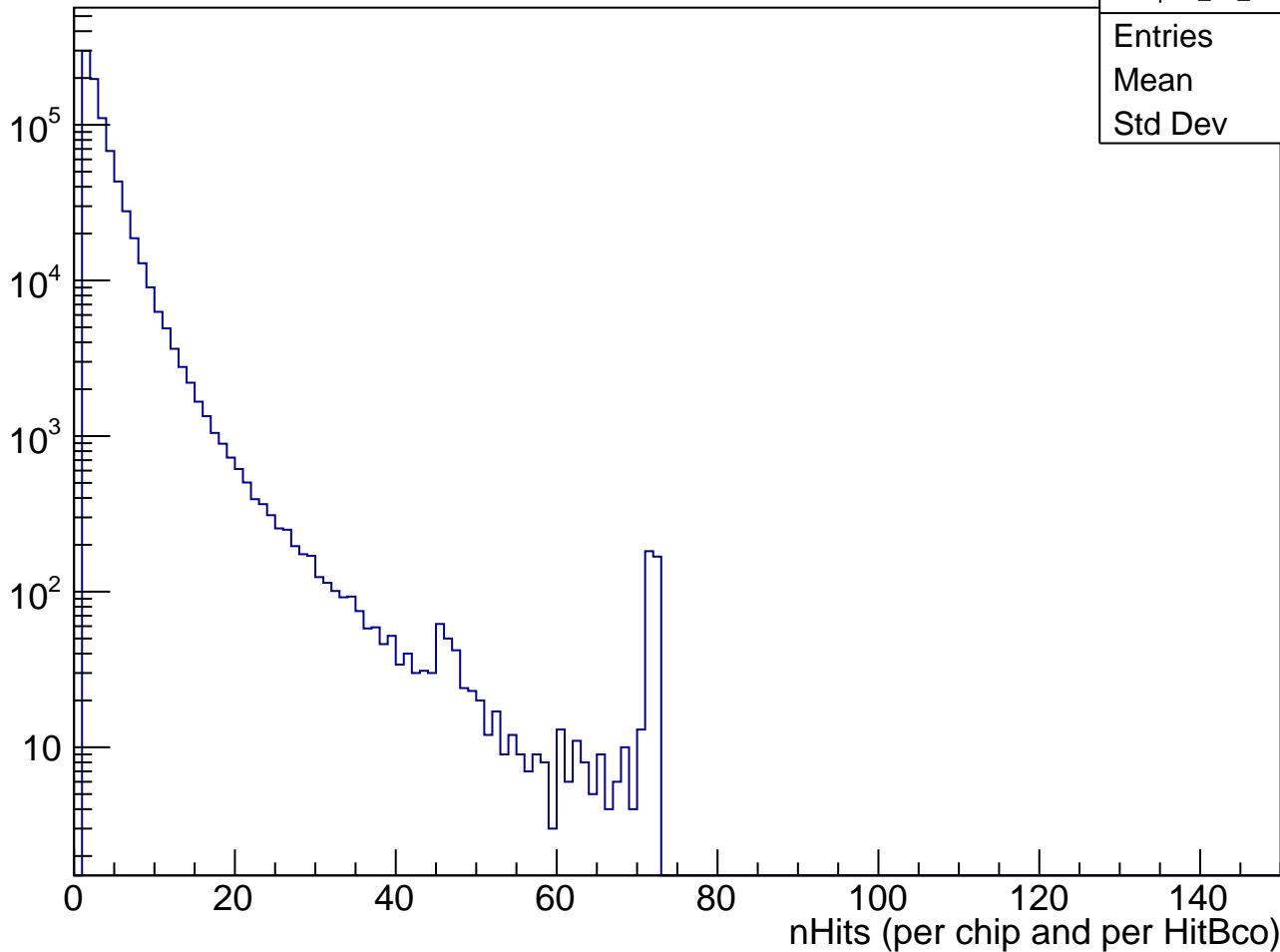
Mean 3.009

Std Dev 3.491

nHits (per chip and per HitBco)

nChipHit_F3_Fch11_Chip7

Entries

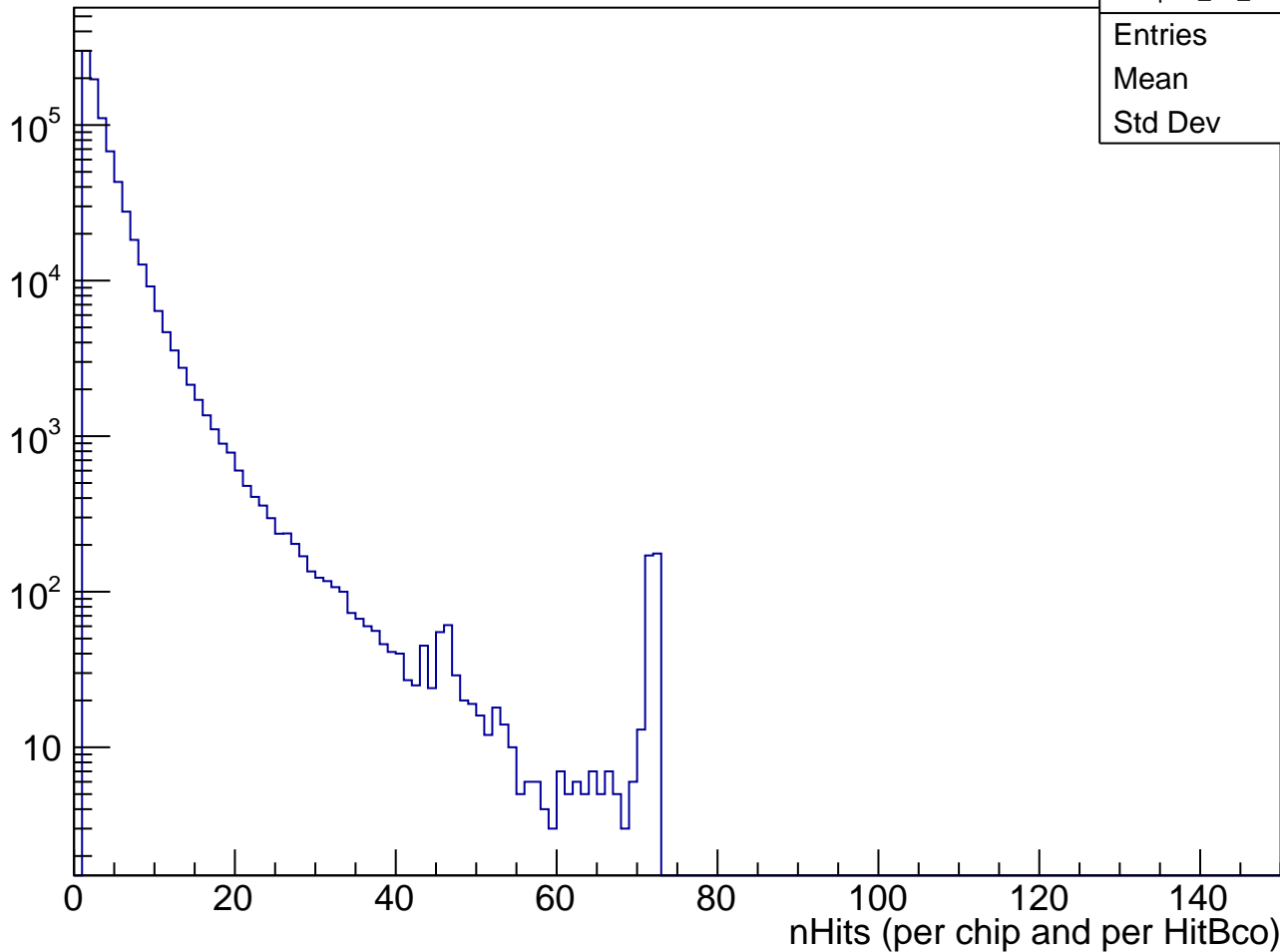


nChipHit_F3_Fch11_Chip7	
Entries	816731
Mean	3.027
Std Dev	3.562

nHits (per chip and per HitBco)

nChipHit_F3_Fch11_Chip8

Entries

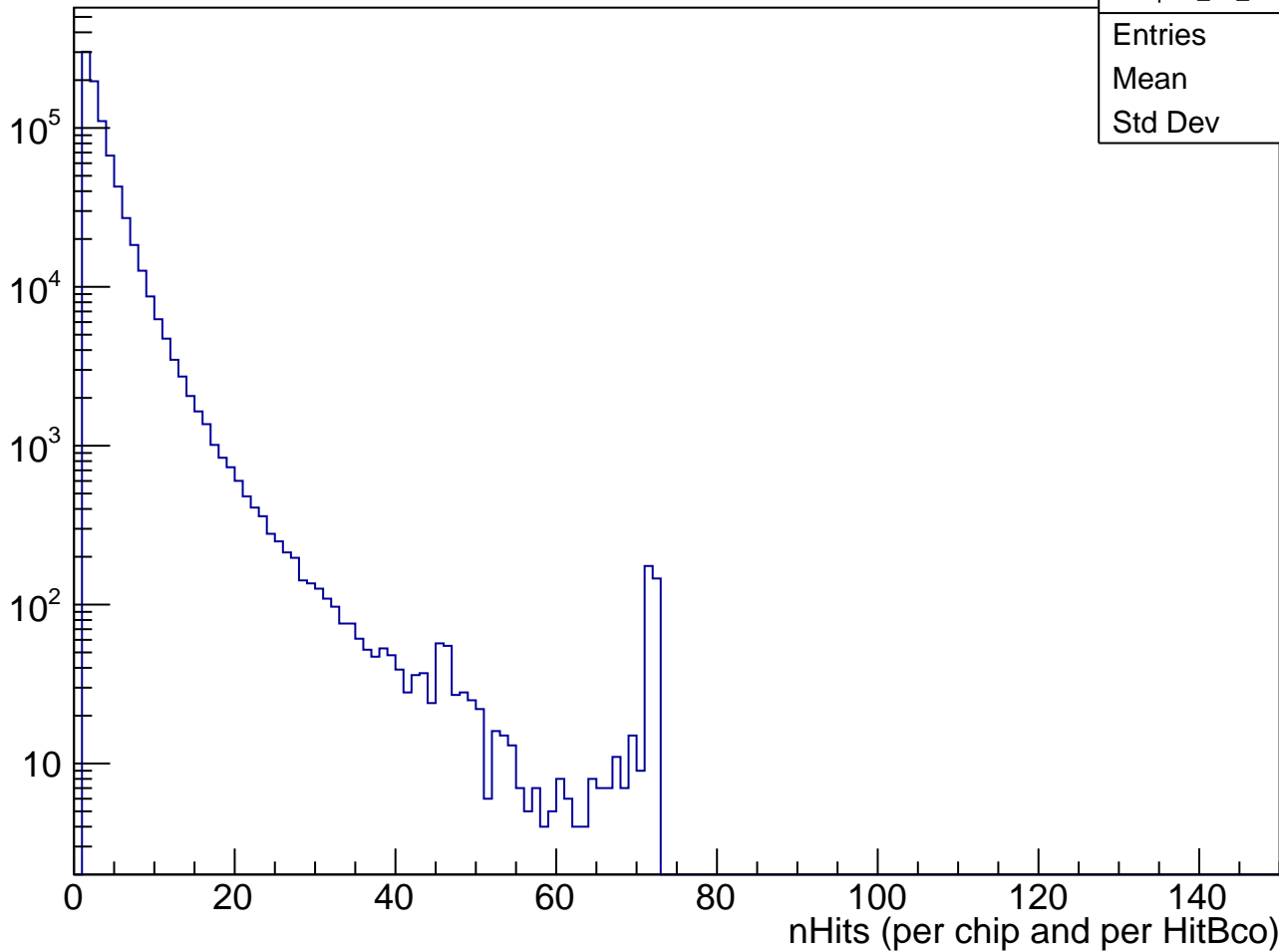


nChipHit_F3_Fch11_Chip8	
Entries	816314
Mean	3.013
Std Dev	3.521

nHits (per chip and per HitBco)

nChipHit_F3_Fch11_Chip9

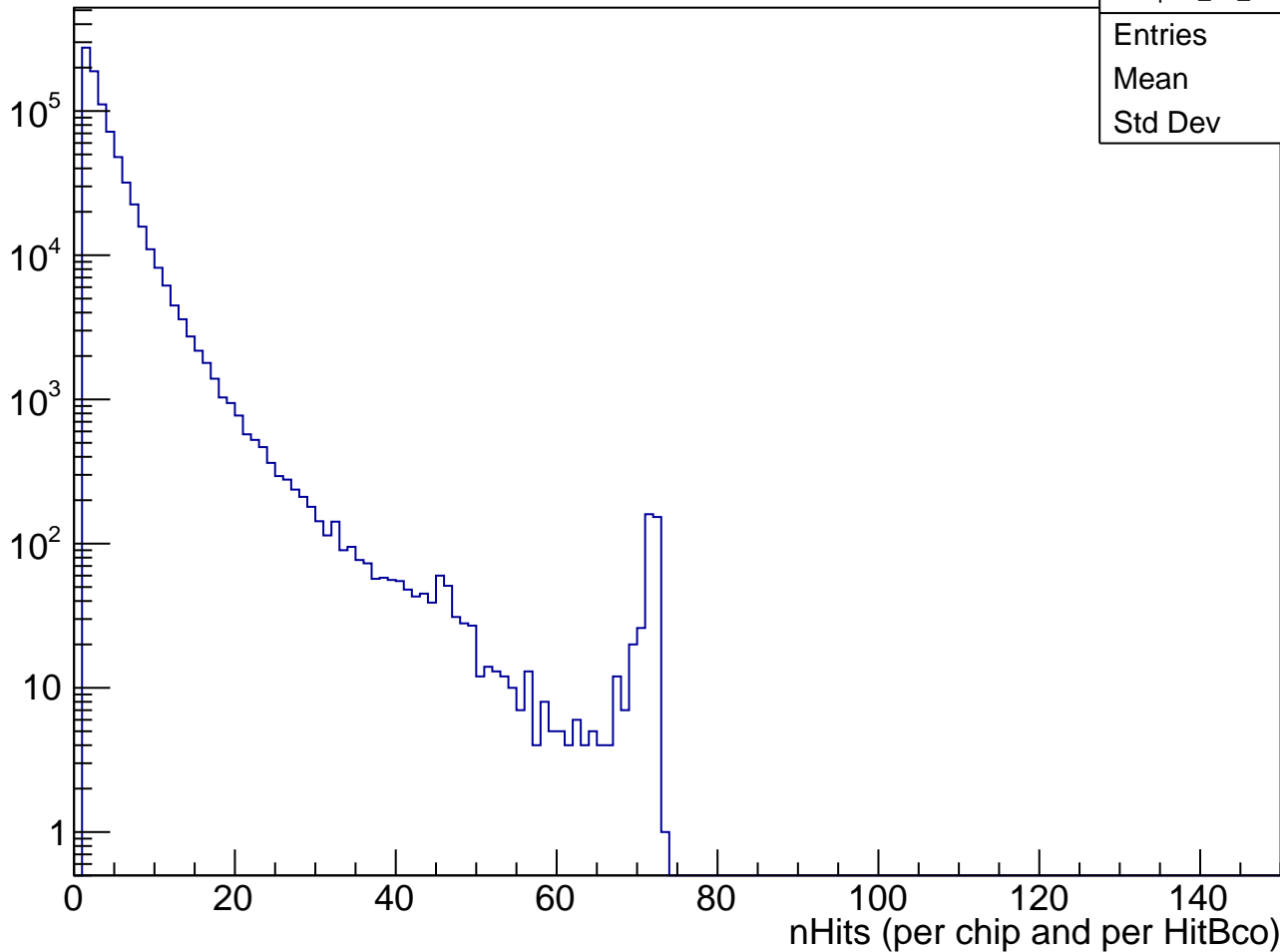
Entries



nChipHit_F3_Fch11_Chip9	
Entries	815167
Mean	2.991
Std Dev	3.493

nChipHit_F3_Fch12_Chip0

Entries

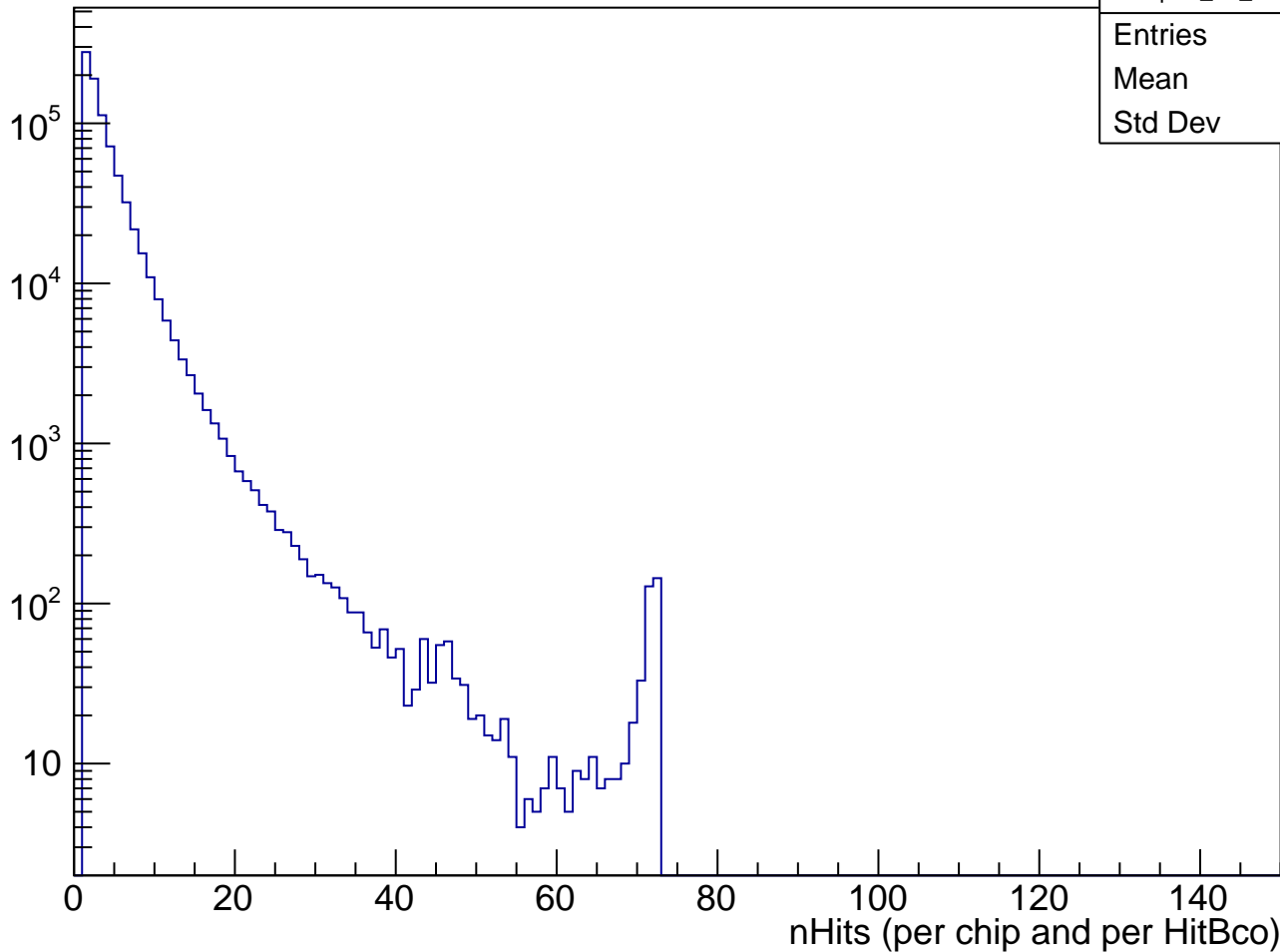


nChipHit_F3_Fch12_Chip0	
Entries	813927
Mean	3.281
Std Dev	3.752

nHits (per chip and per HitBco)

nChipHit_F3_Fch12_Chip1

Entries

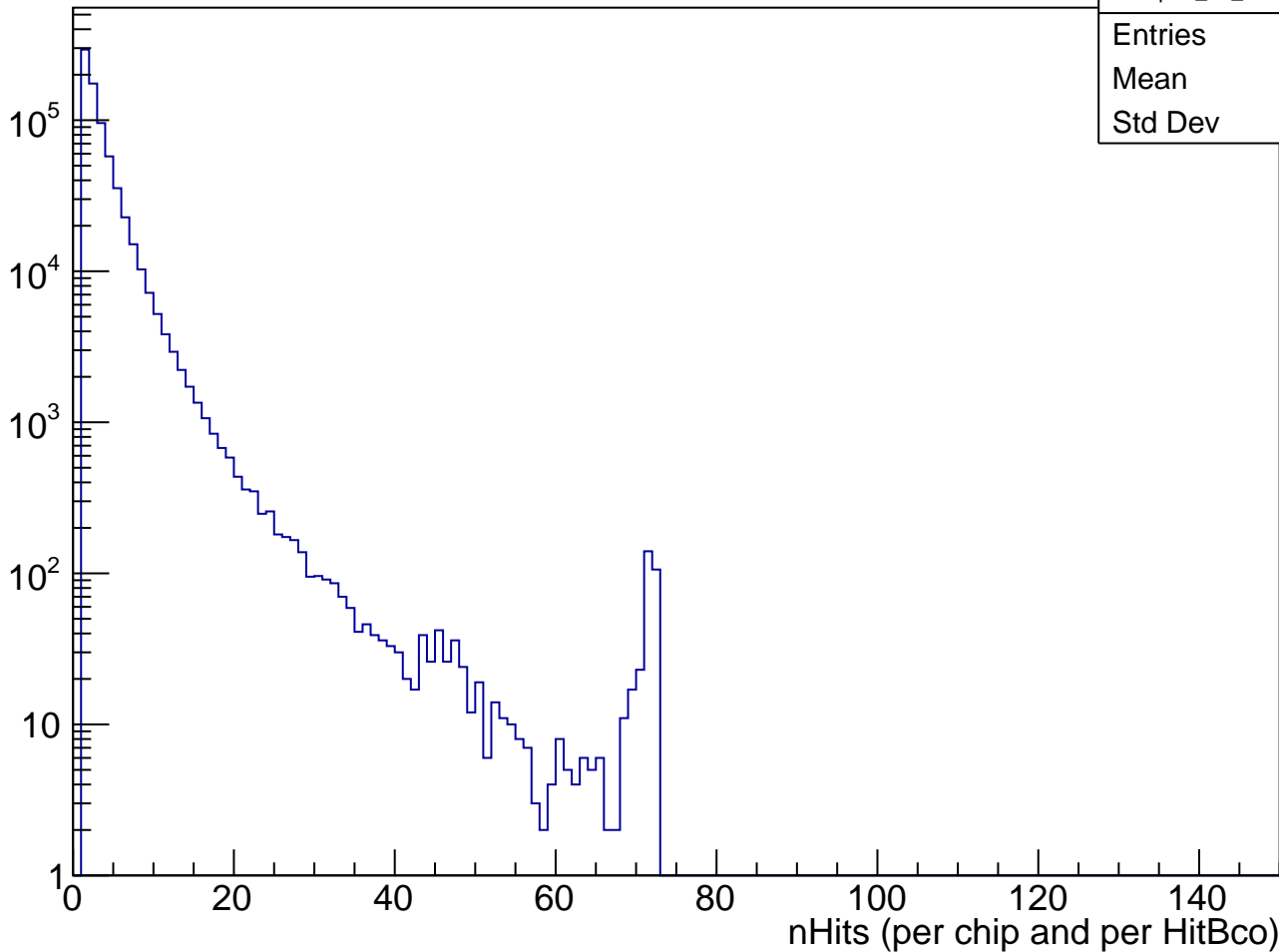


nChipHit_F3_Fch12_Chip1	
Entries	817006
Mean	3.238
Std Dev	3.692

nHits (per chip and per HitBco)

nChipHit_F3_Fch12_Chip10

Entries



nChipHit_F3_Fch12_Chip10

Entries 736509

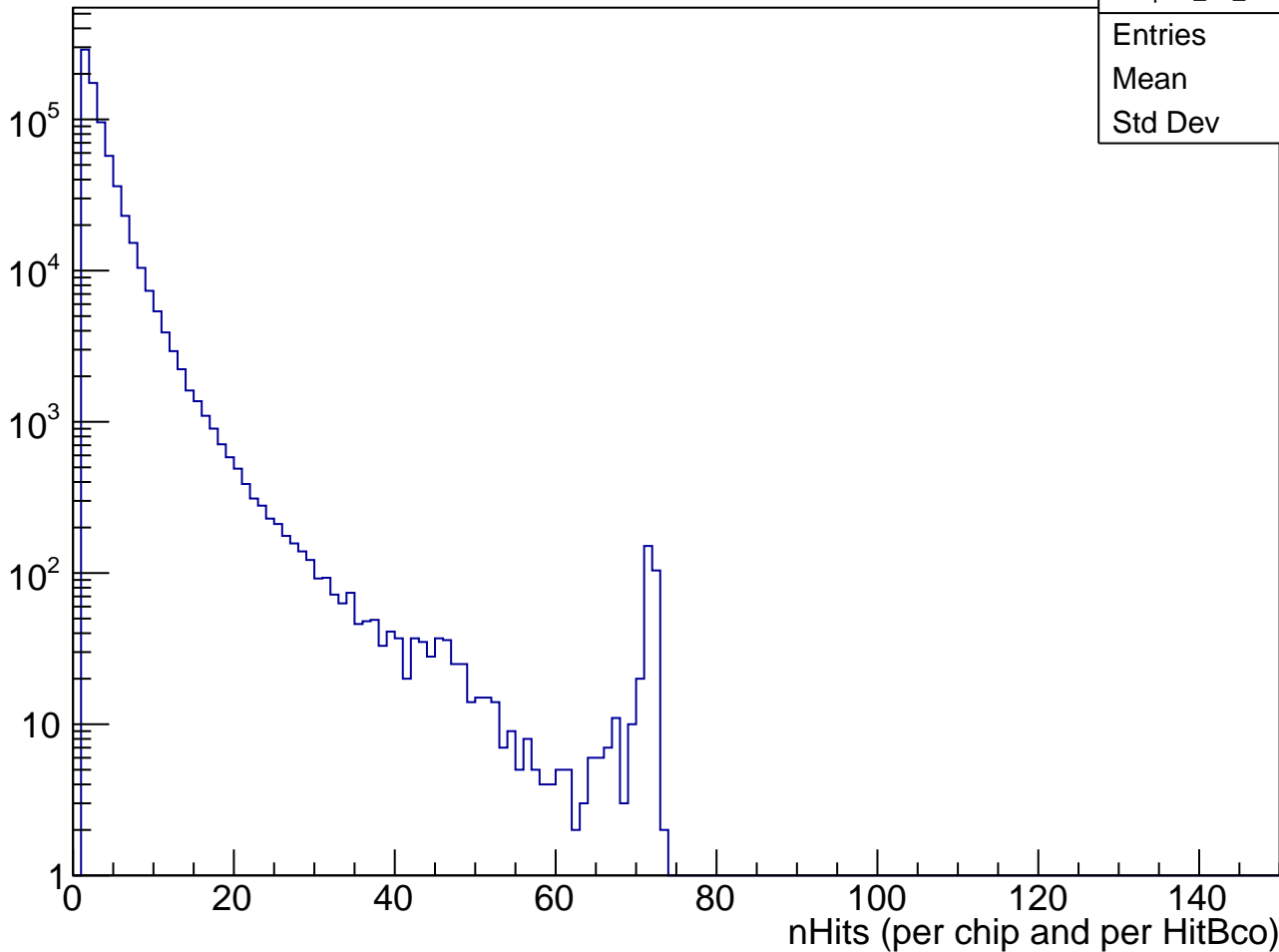
Mean 2.852

Std Dev 3.348

nHits (per chip and per HitBco)

nChipHit_F3_Fch12_Chip11

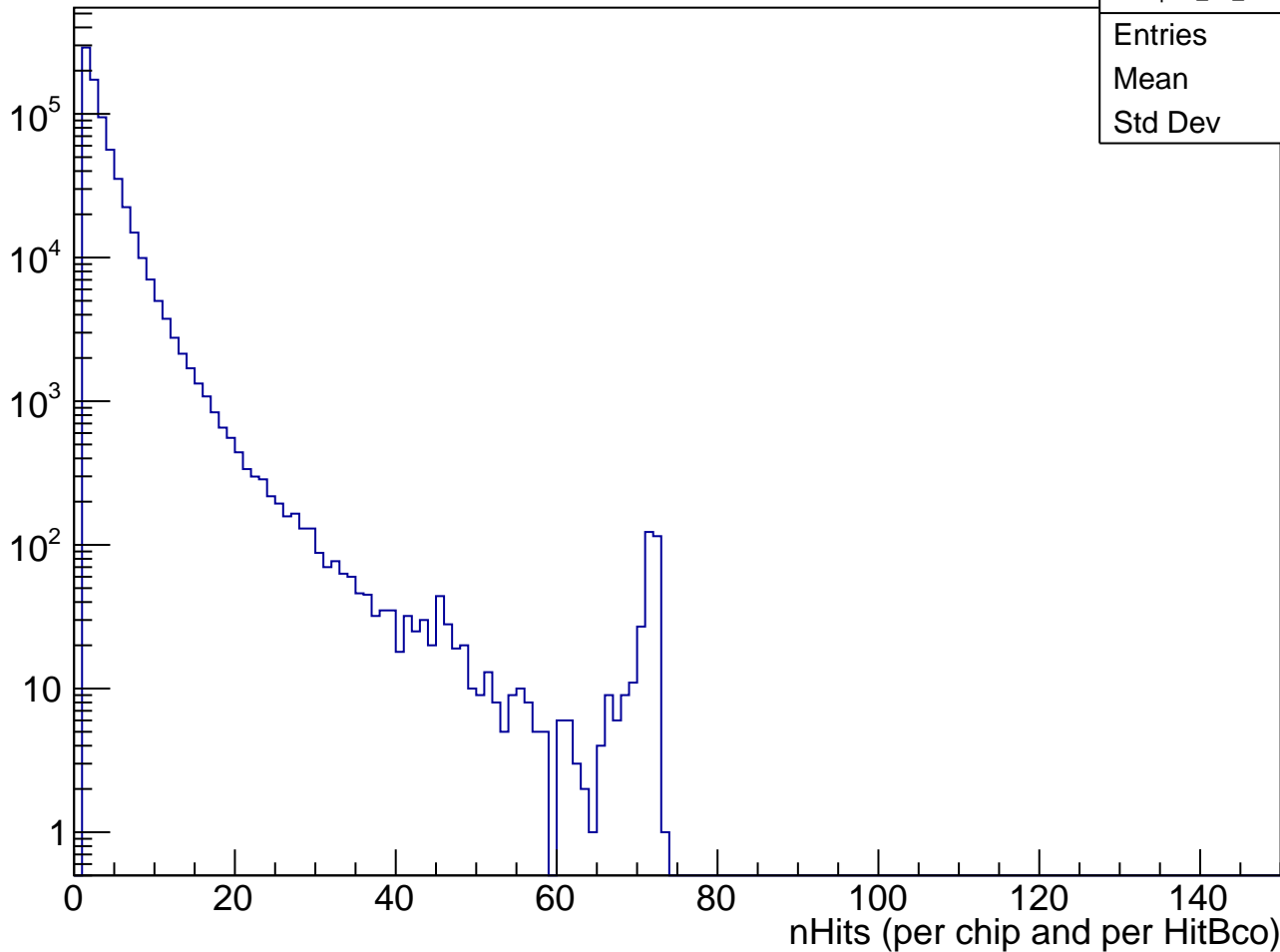
Entries



nChipHit_F3_Fch12_Chip11	
Entries	733384
Mean	2.877
Std Dev	3.383

nChipHit_F3_Fch12_Chip12

Entries



nChipHit_F3_Fch12_Chip12

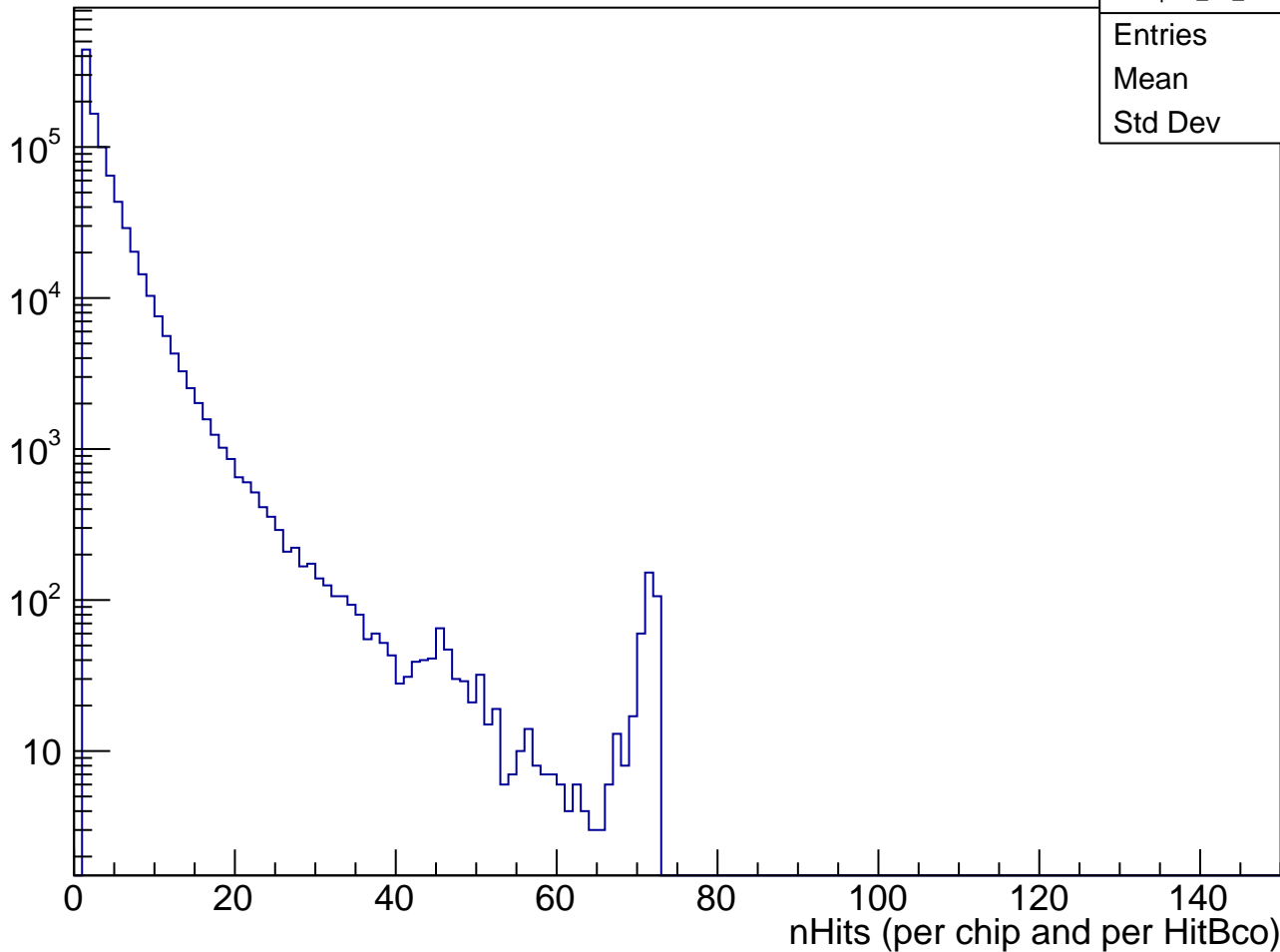
Entries 726828

Mean 2.84

Std Dev 3.316

nChipHit_F3_Fch12_Chip13

Entries



nChipHit_F3_Fch12_Chip13

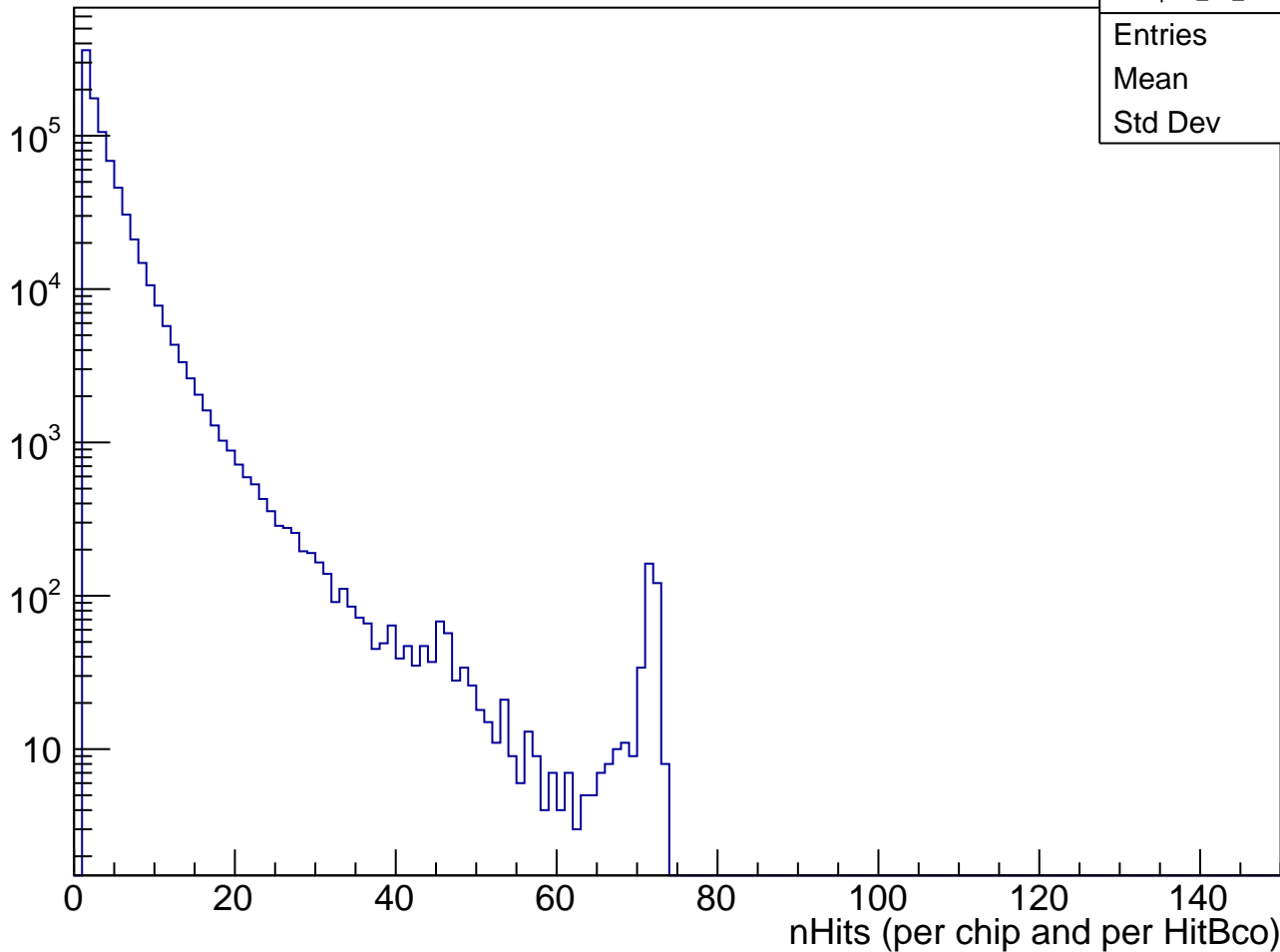
Entries 925287

Mean 2.825

Std Dev 3.514

nChipHit_F3_Fch12_Chip14

Entries



nChipHit_F3_Fch12_Chip14

Entries 870100

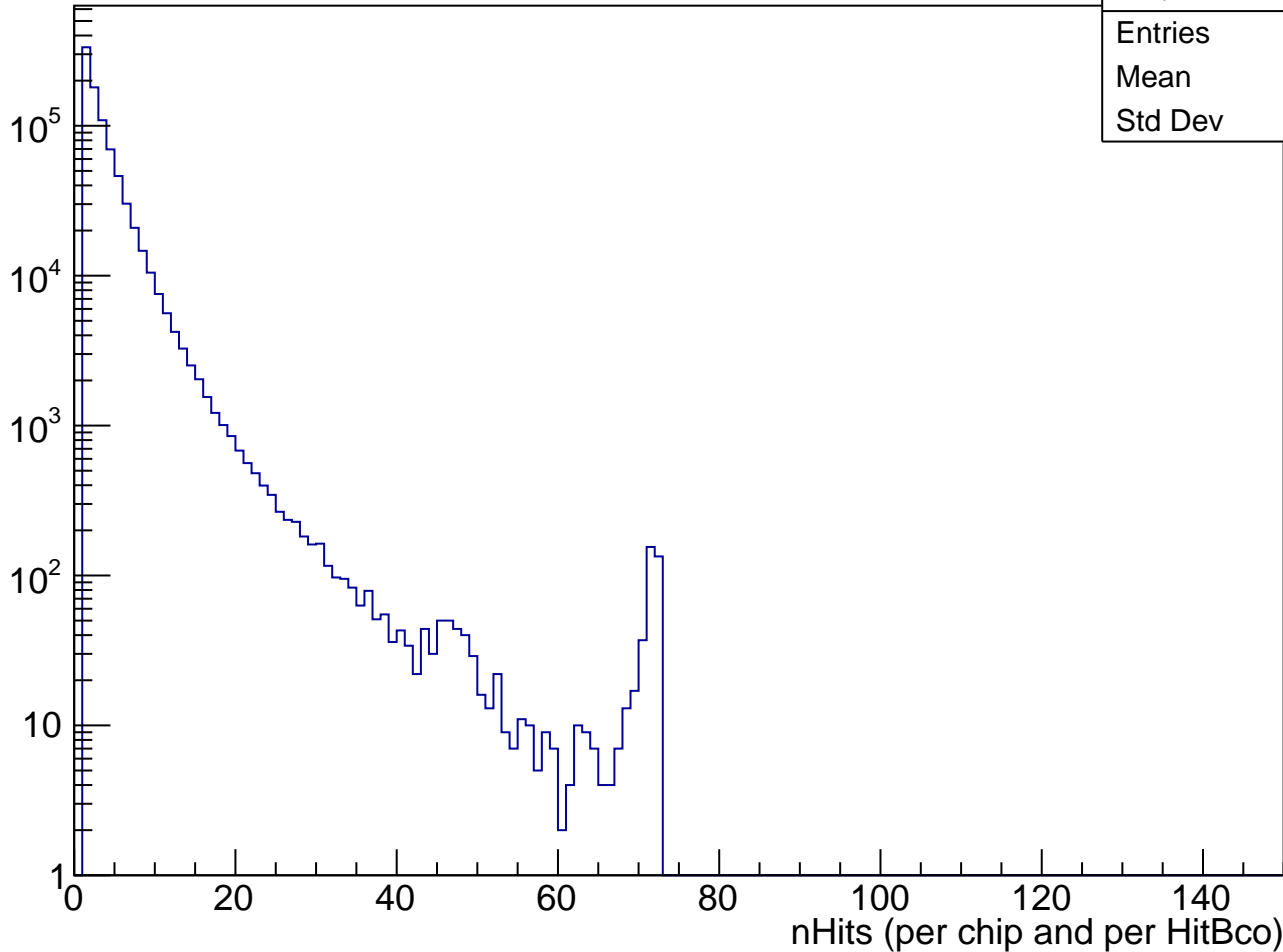
Mean 3.029

Std Dev 3.633

nHits (per chip and per HitBco)

nChipHit_F3_Fch12_Chip15

Entries



nChipHit_F3_Fch12_Chip15

Entries 850343

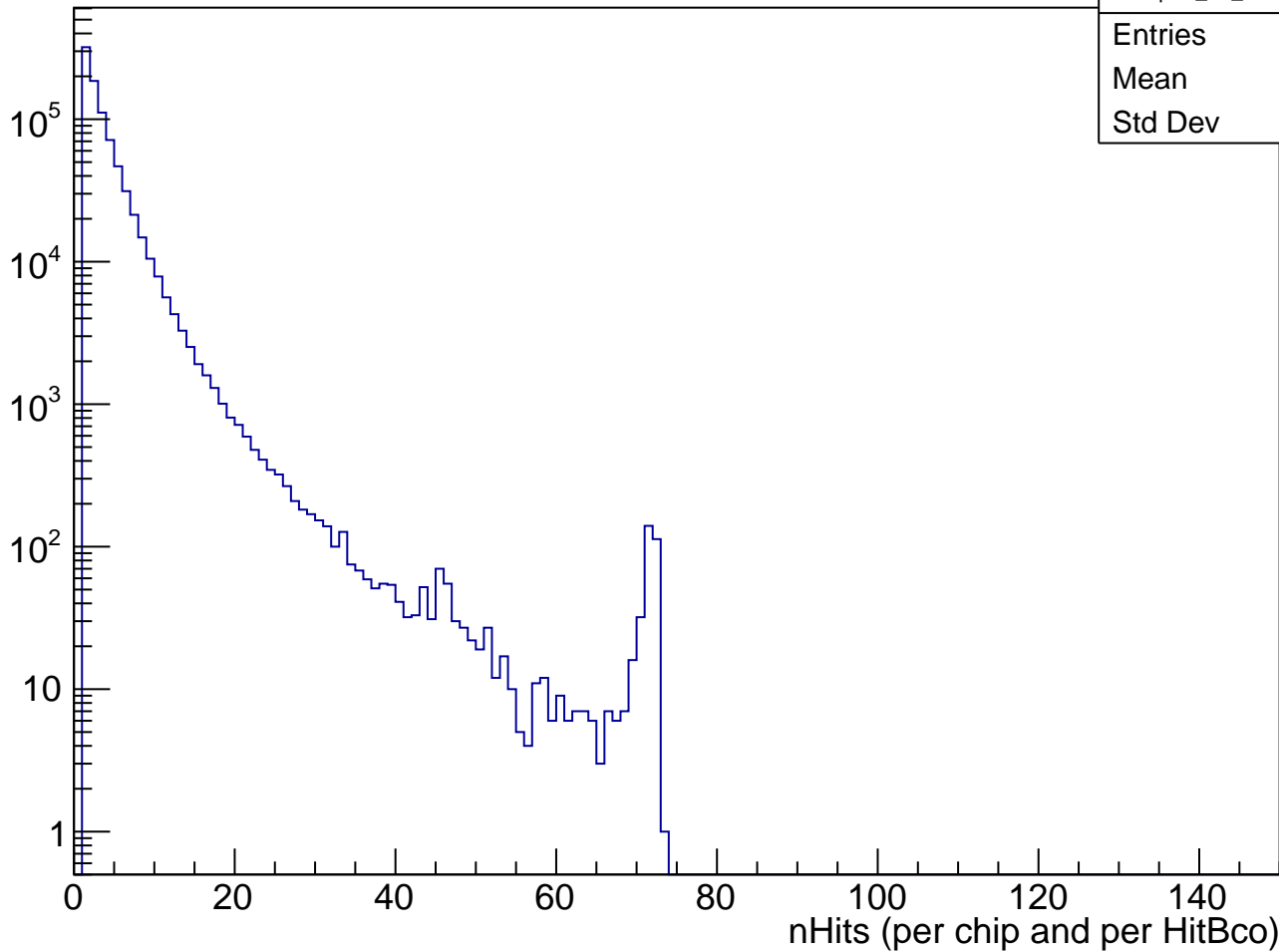
Mean 3.066

Std Dev 3.61

nHits (per chip and per HitBco)

nChipHit_F3_Fch12_Chip16

Entries



nChipHit_F3_Fch12_Chip16

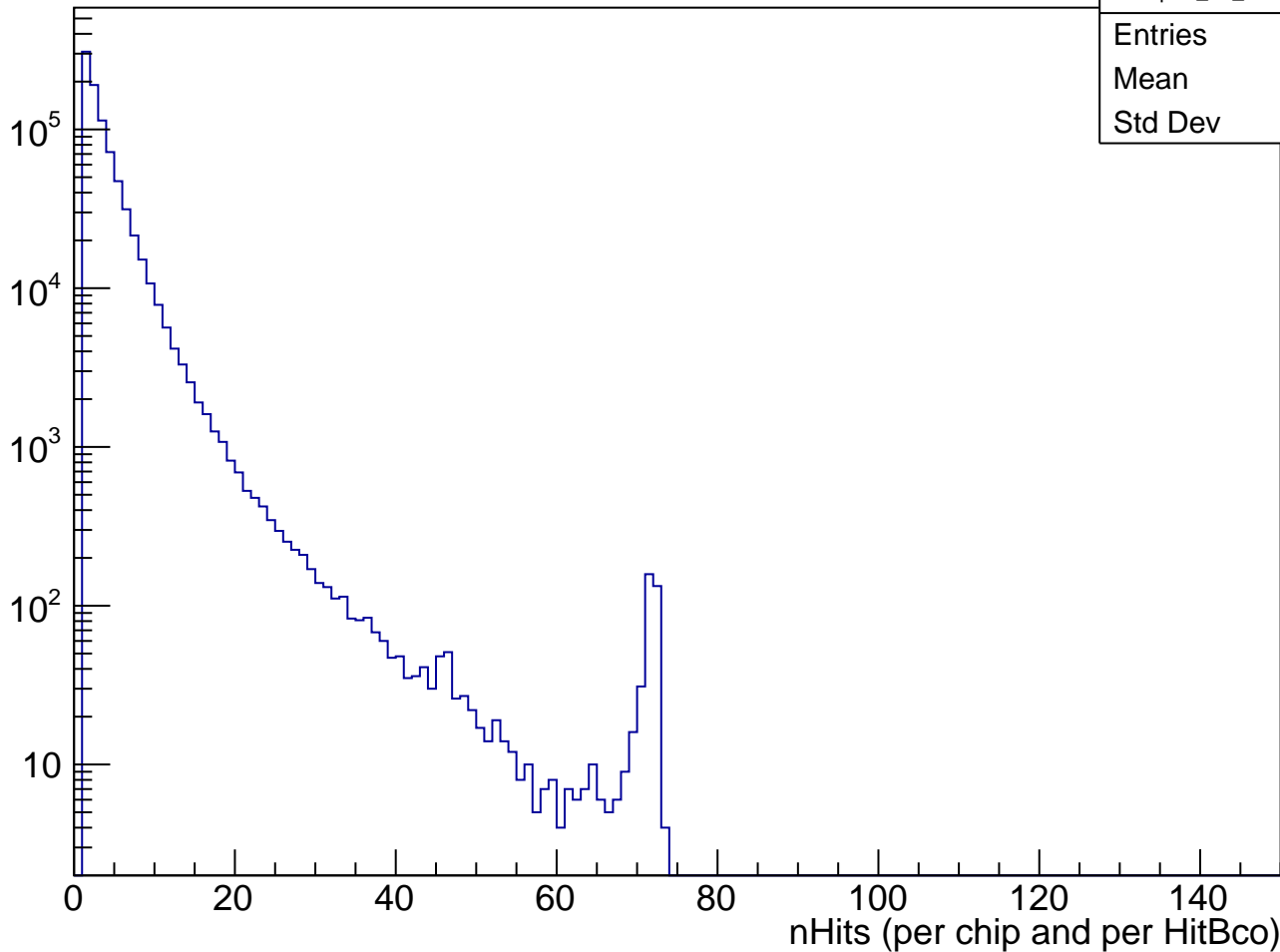
Entries 849075

Mean 3.108

Std Dev 3.6

nChipHit_F3_Fch12_Chip17

Entries



nChipHit_F3_Fch12_Chip17

Entries 846373

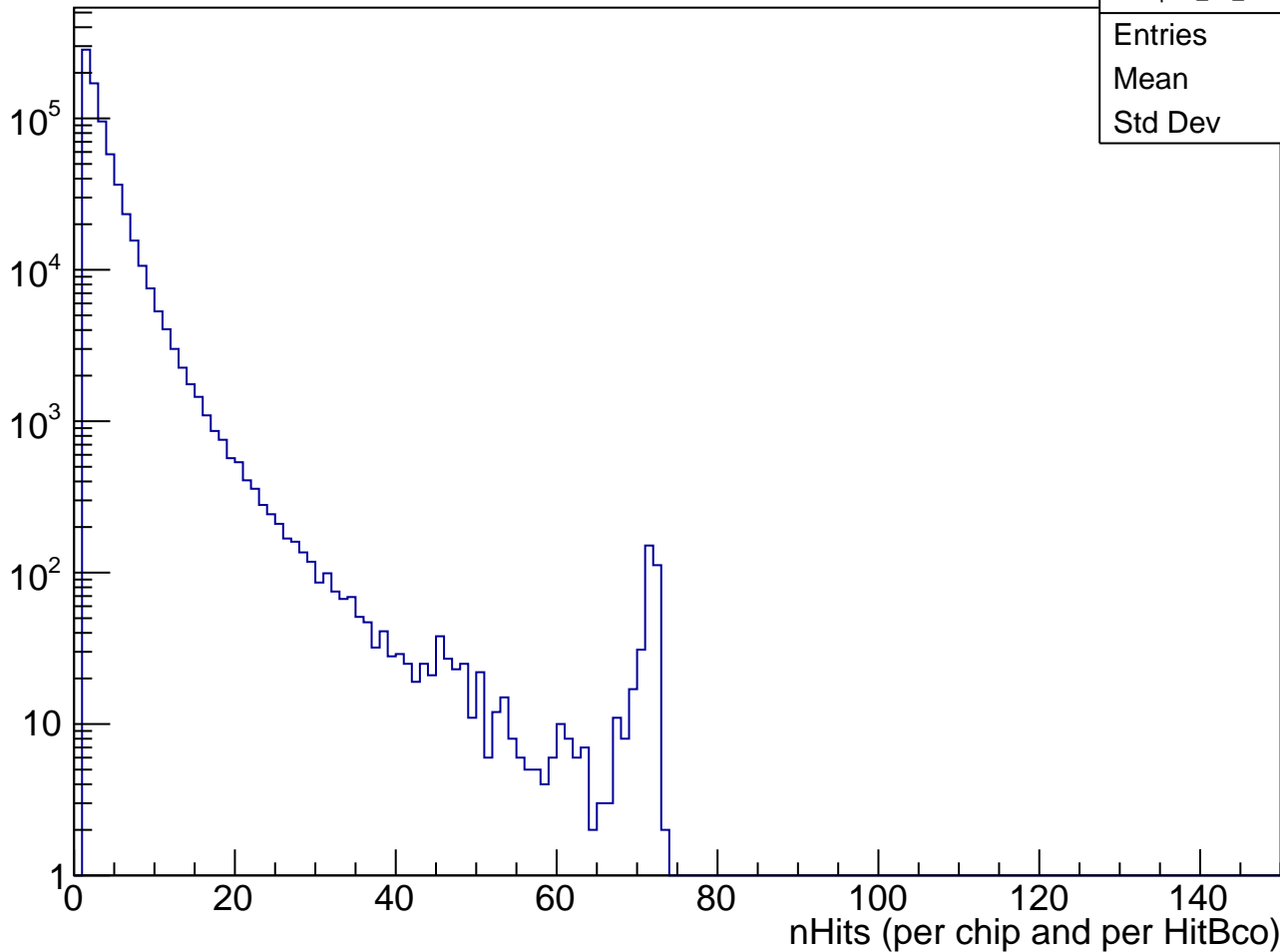
Mean 3.139

Std Dev 3.628

nHits (per chip and per HitBco)

nChipHit_F3_Fch12_Chip18

Entries



nChipHit_F3_Fch12_Chip18

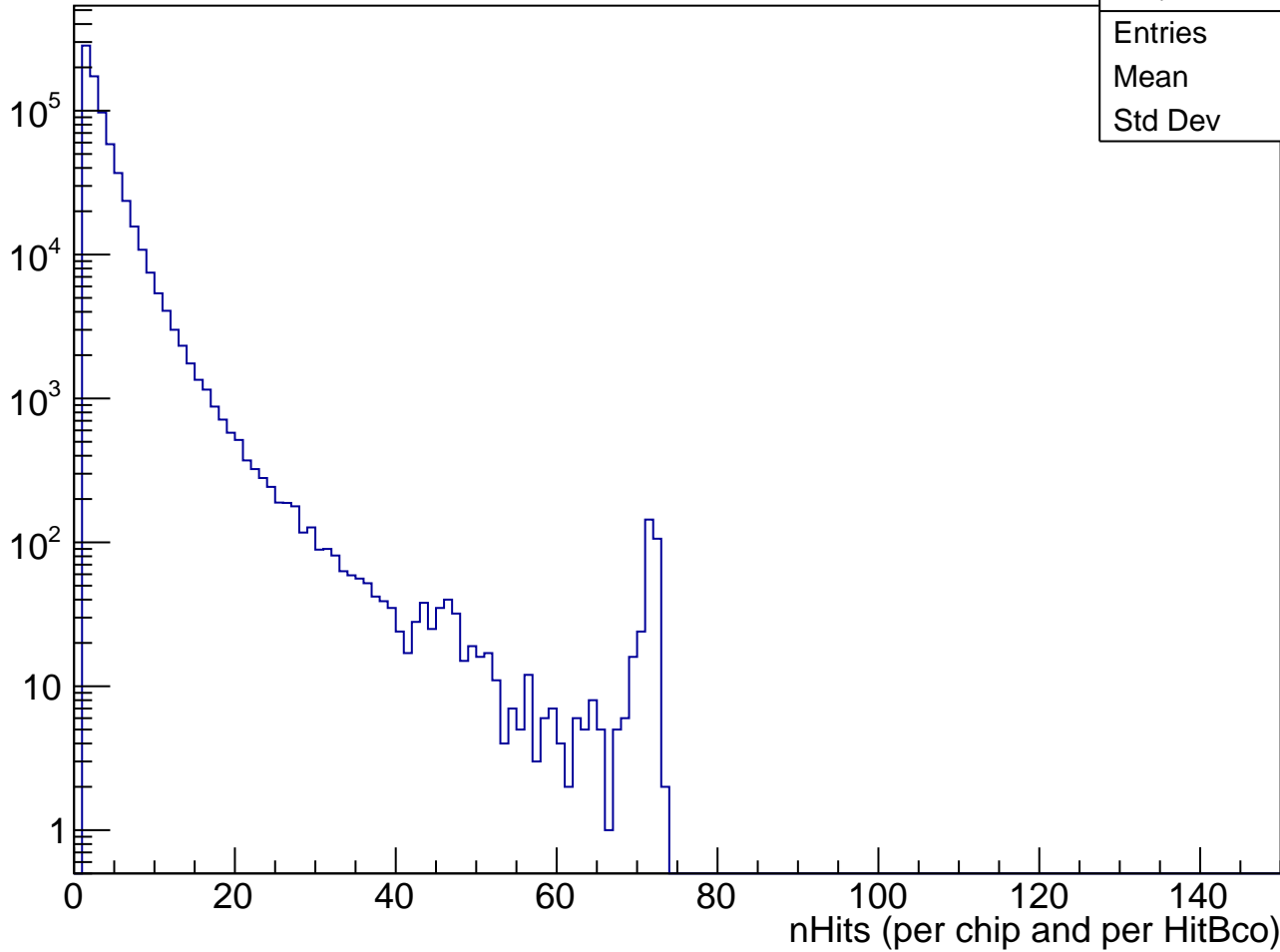
Entries 726799

Mean 2.911

Std Dev 3.426

nChipHit_F3_Fch12_Chip19

Entries



nChipHit_F3_Fch12_Chip19

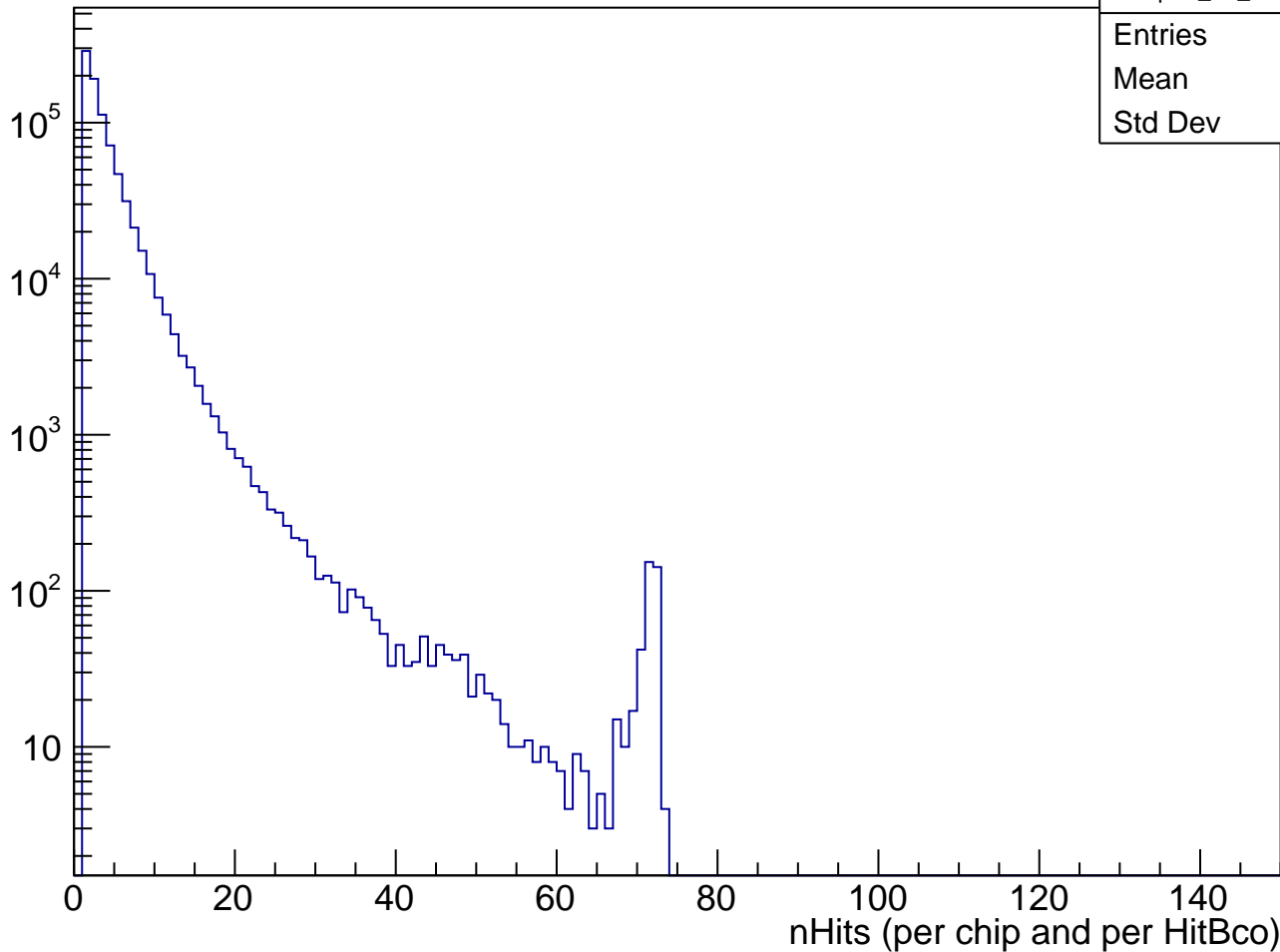
Entries 731899

Mean 2.912

Std Dev 3.394

nChipHit_F3_Fch12_Chip2

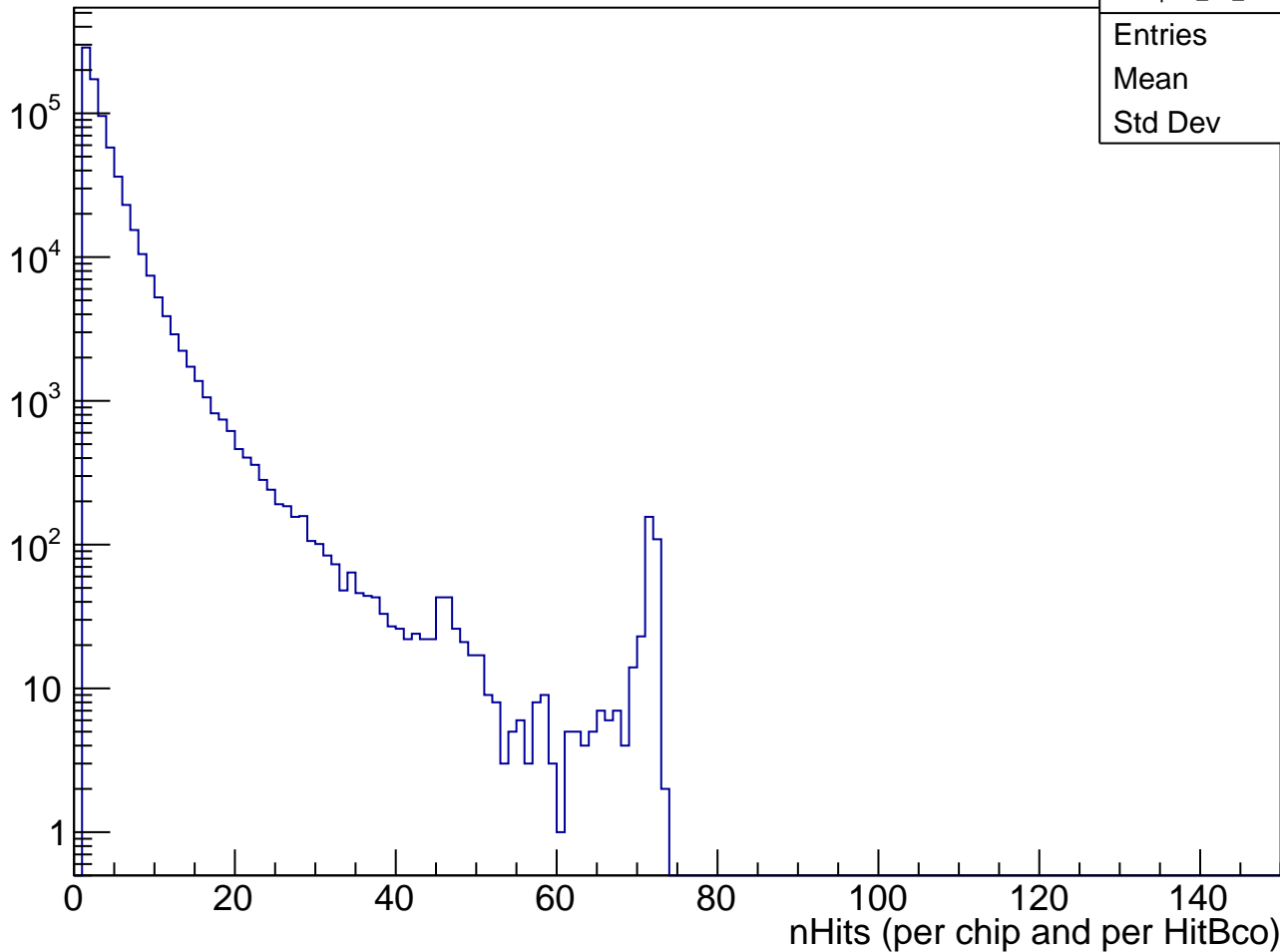
Entries



nChipHit_F3_Fch12_Chip2	
Entries	824515
Mean	3.198
Std Dev	3.692

nChipHit_F3_Fch12_Chip20

Entries



nChipHit_F3_Fch12_Chip20

Entries 730037

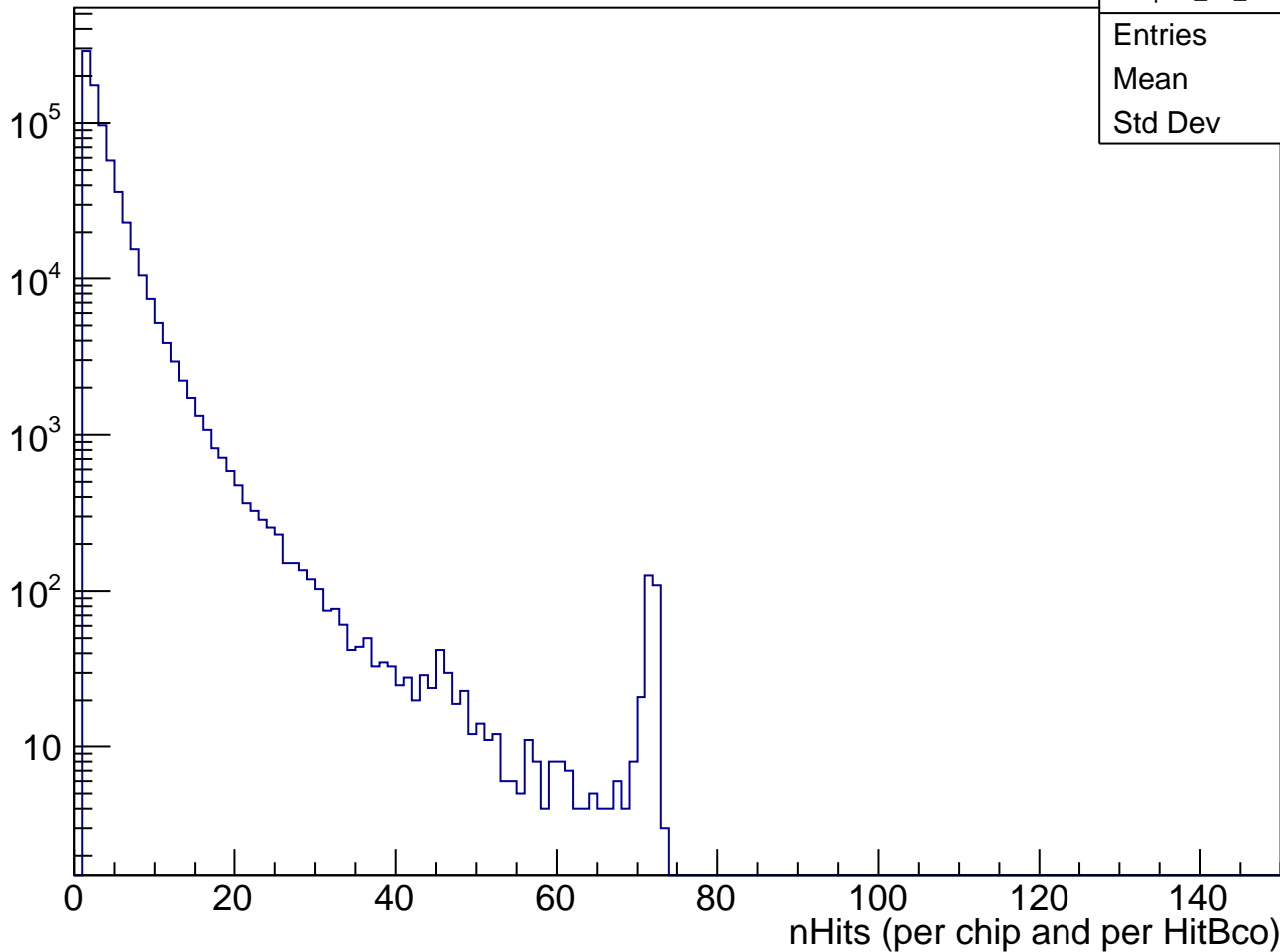
Mean 2.886

Std Dev 3.381

nHits (per chip and per HitBco)

nChipHit_F3_Fch12_Chip21

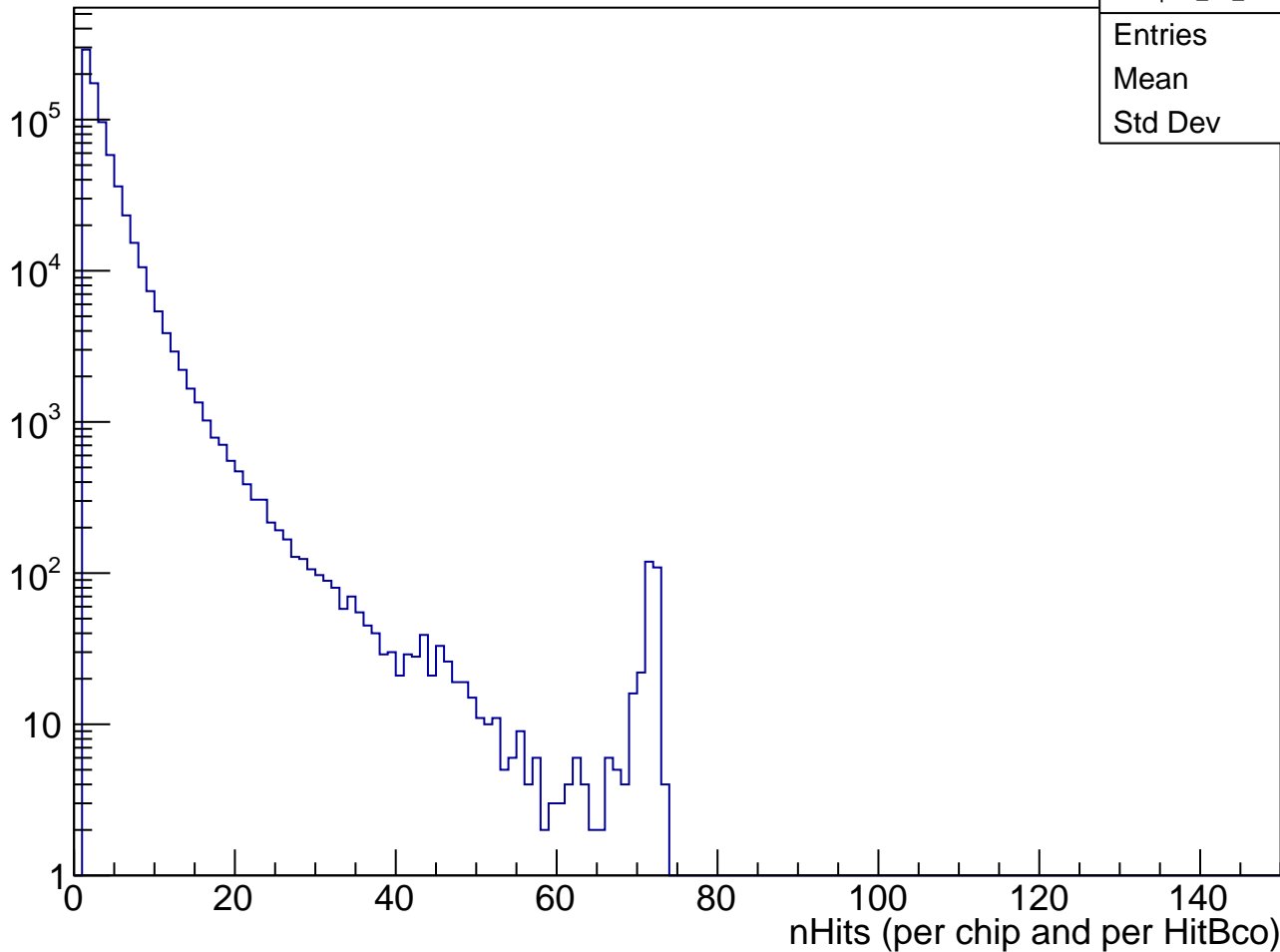
Entries



nChipHit_F3_Fch12_Chip21	
Entries	734114
Mean	2.871
Std Dev	3.33

nChipHit_F3_Fch12_Chip22

Entries



nChipHit_F3_Fch12_Chip22

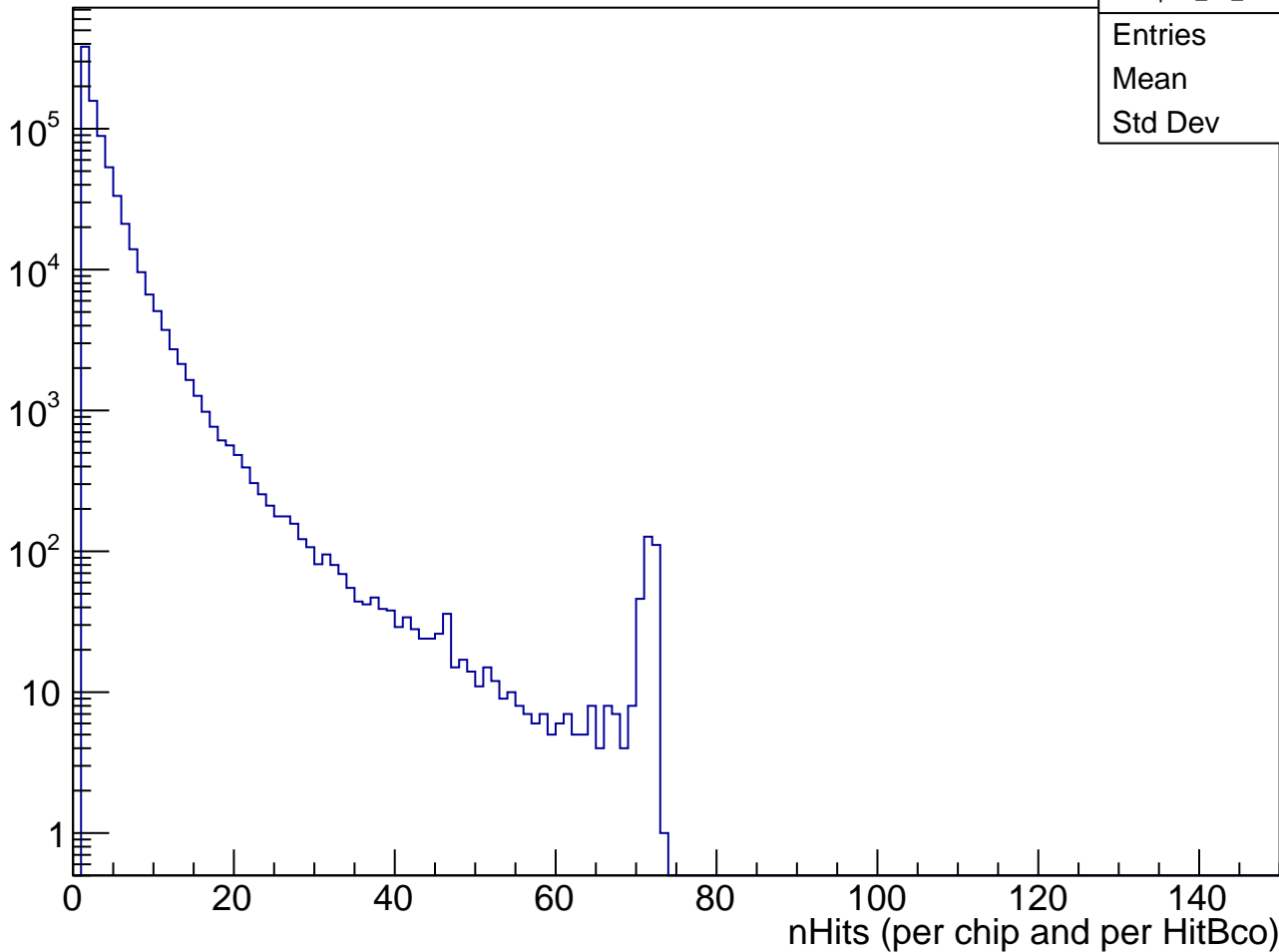
Entries 735469

Mean 2.863

Std Dev 3.304

nChipHit_F3_Fch12_Chip23

Entries



nChipHit_F3_Fch12_Chip23

Entries 789779

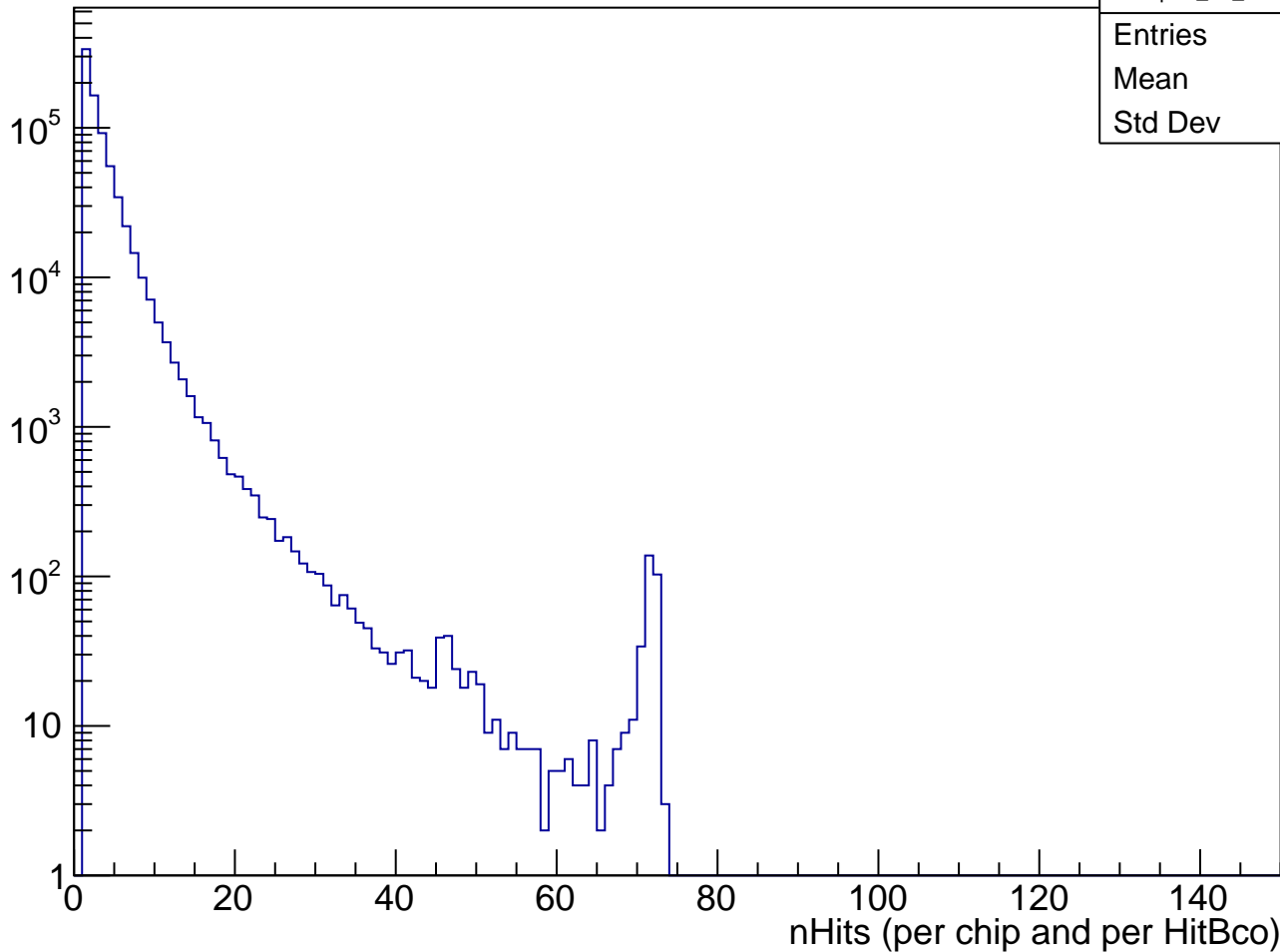
Mean 2.615

Std Dev 3.243

nHits (per chip and per HitBco)

nChipHit_F3_Fch12_Chip24

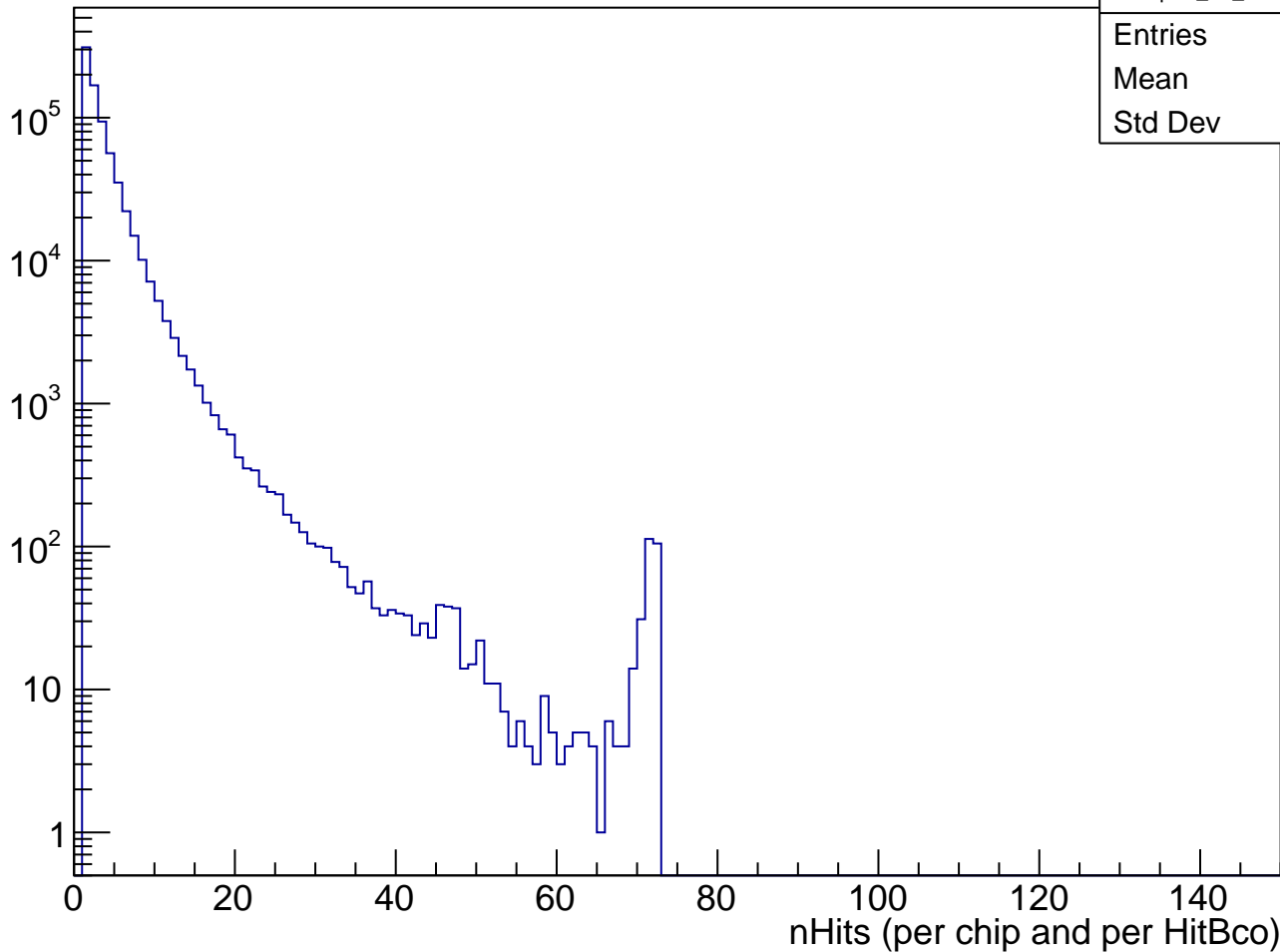
Entries



nChipHit_F3_Fch12_Chip24	
Entries	759108
Mean	2.725
Std Dev	3.283

nChipHit_F3_Fch12_Chip25

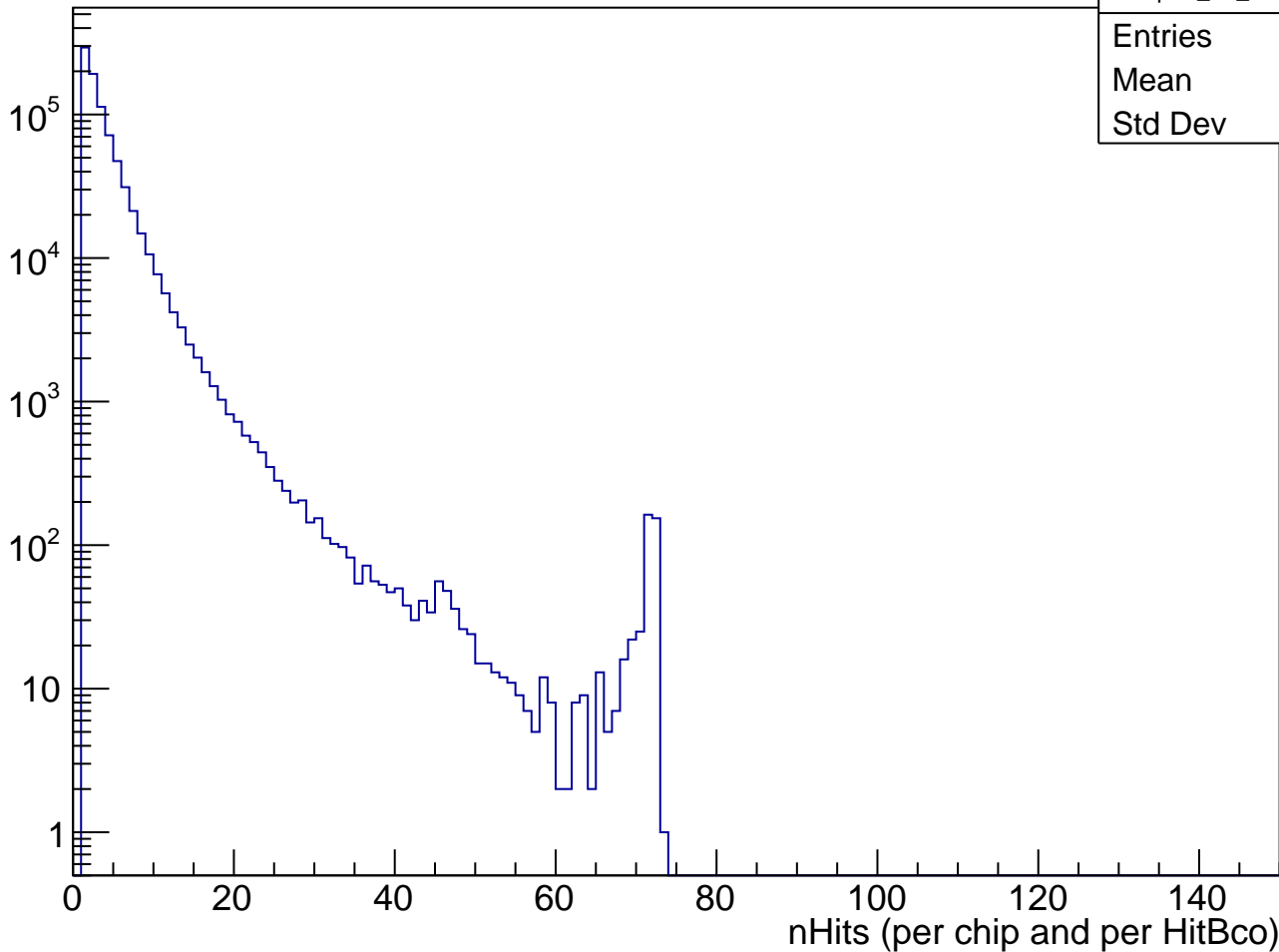
Entries



nChipHit_F3_Fch12_Chip25	
Entries	742749
Mean	2.803
Std Dev	3.311

nChipHit_F3_Fch12_Chip3

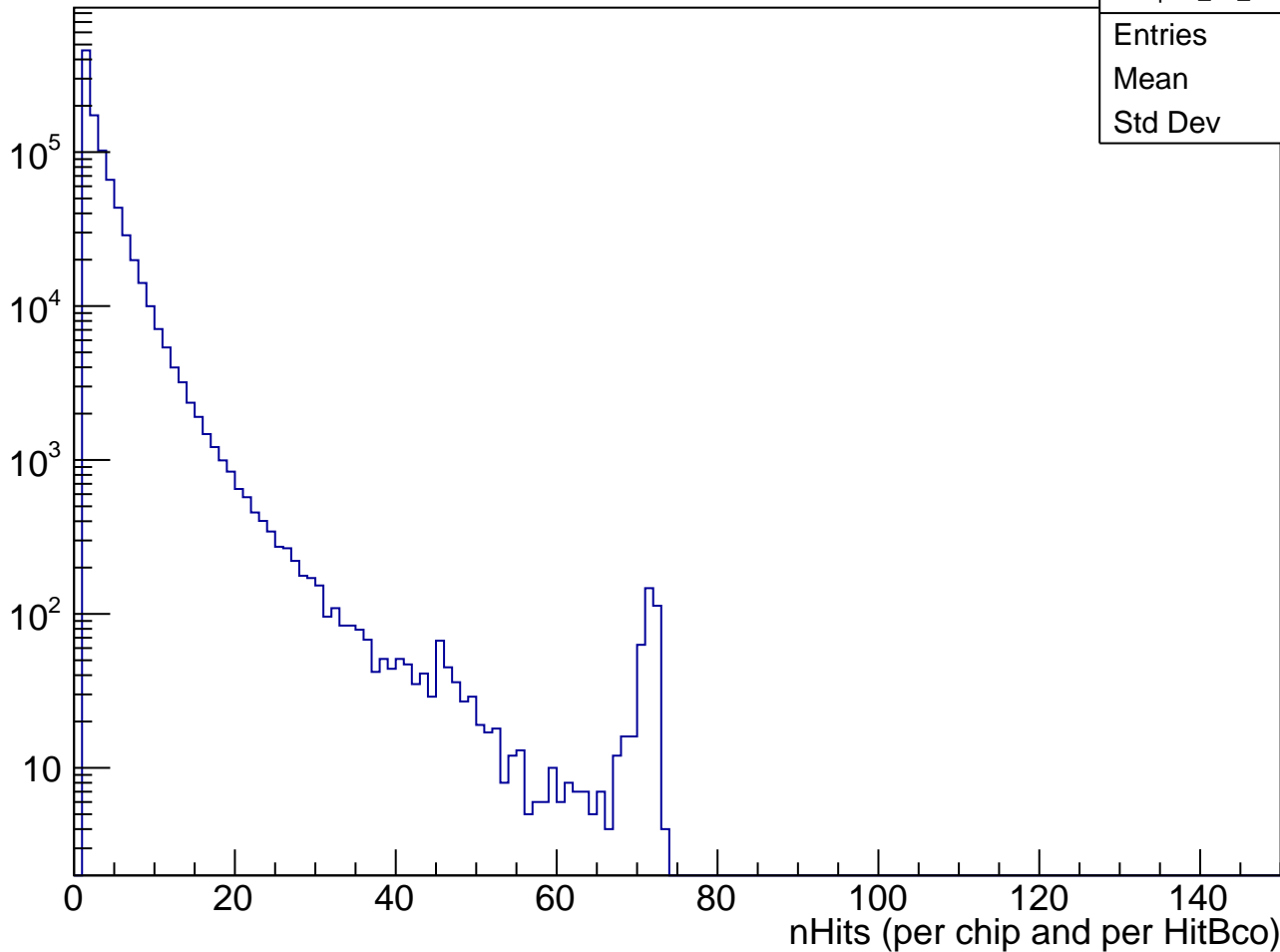
Entries



nChipHit_F3_Fch12_Chip3	
Entries	830271
Mean	3.173
Std Dev	3.658

nChipHit_F3_Fch12_Chip4

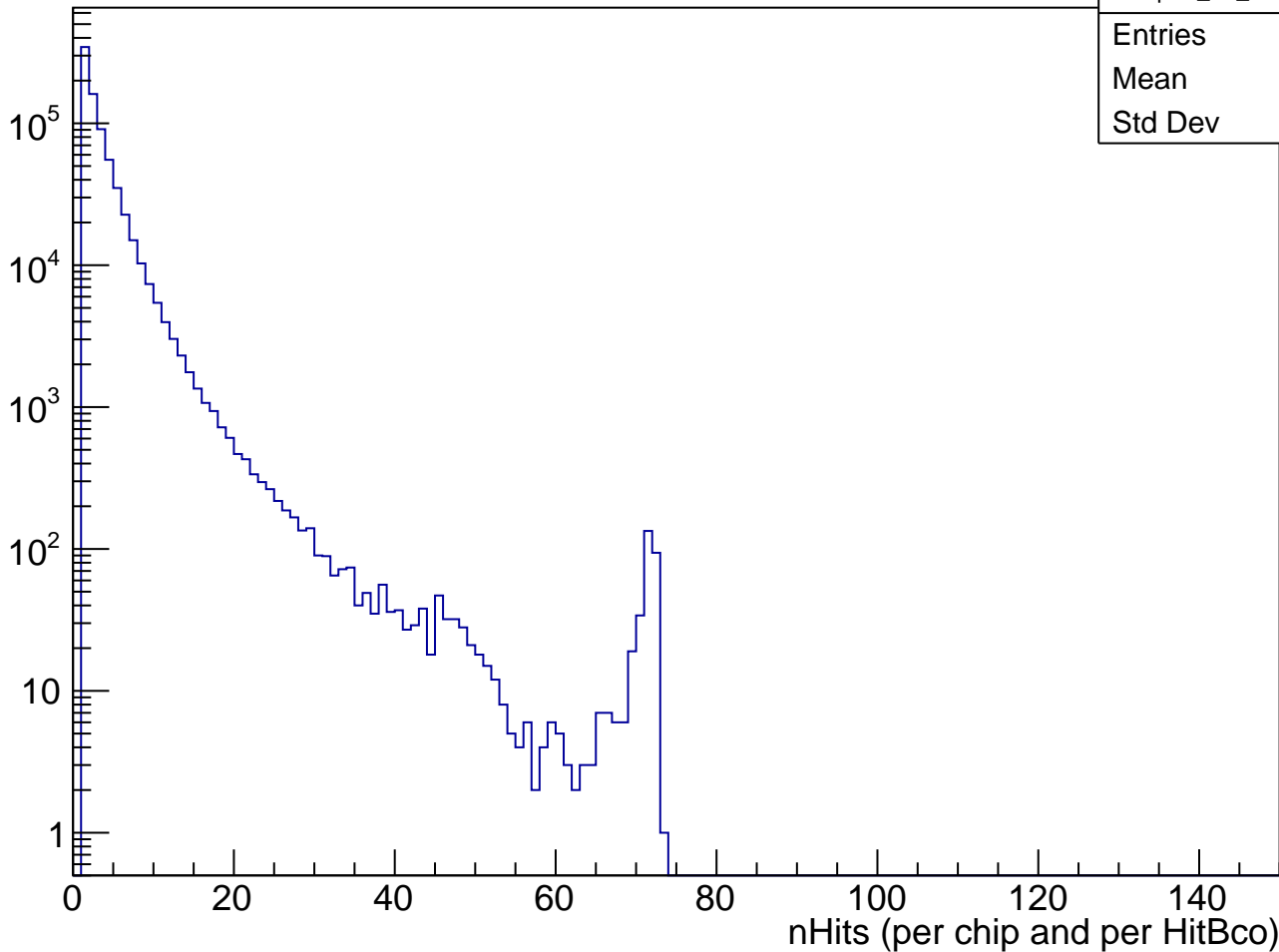
Entries



nChipHit_F3_Fch12_Chip4	
Entries	949992
Mean	2.769
Std Dev	3.462

nChipHit_F3_Fch12_Chip5

Entries

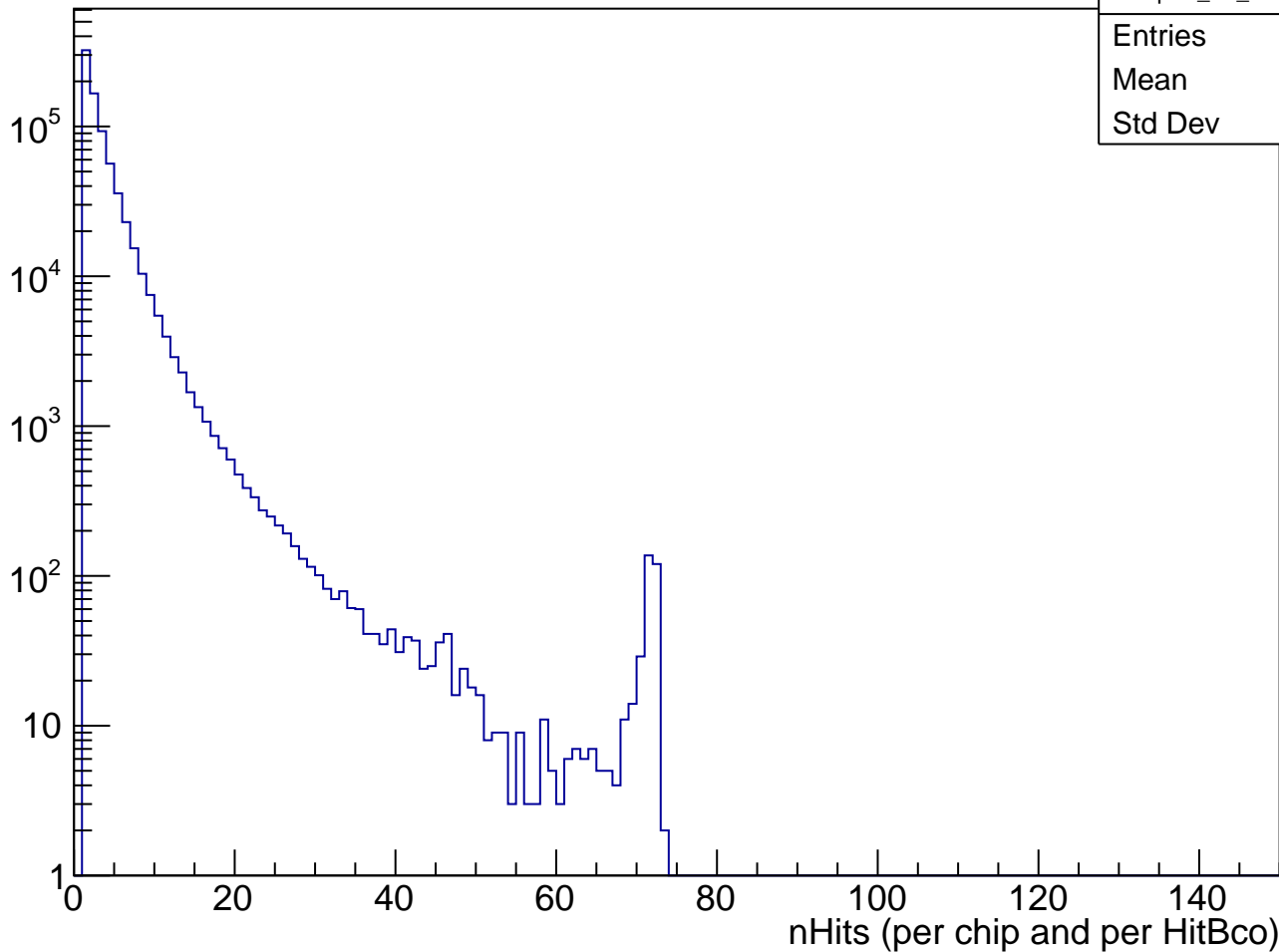


nChipHit_F3_Fch12_Chip5	
Entries	767559
Mean	2.756
Std Dev	3.352

nHits (per chip and per HitBco)

nChipHit_F3_Fch12_Chip6

Entries



nChipHit_F3_Fch12_Chip6

Entries 755245

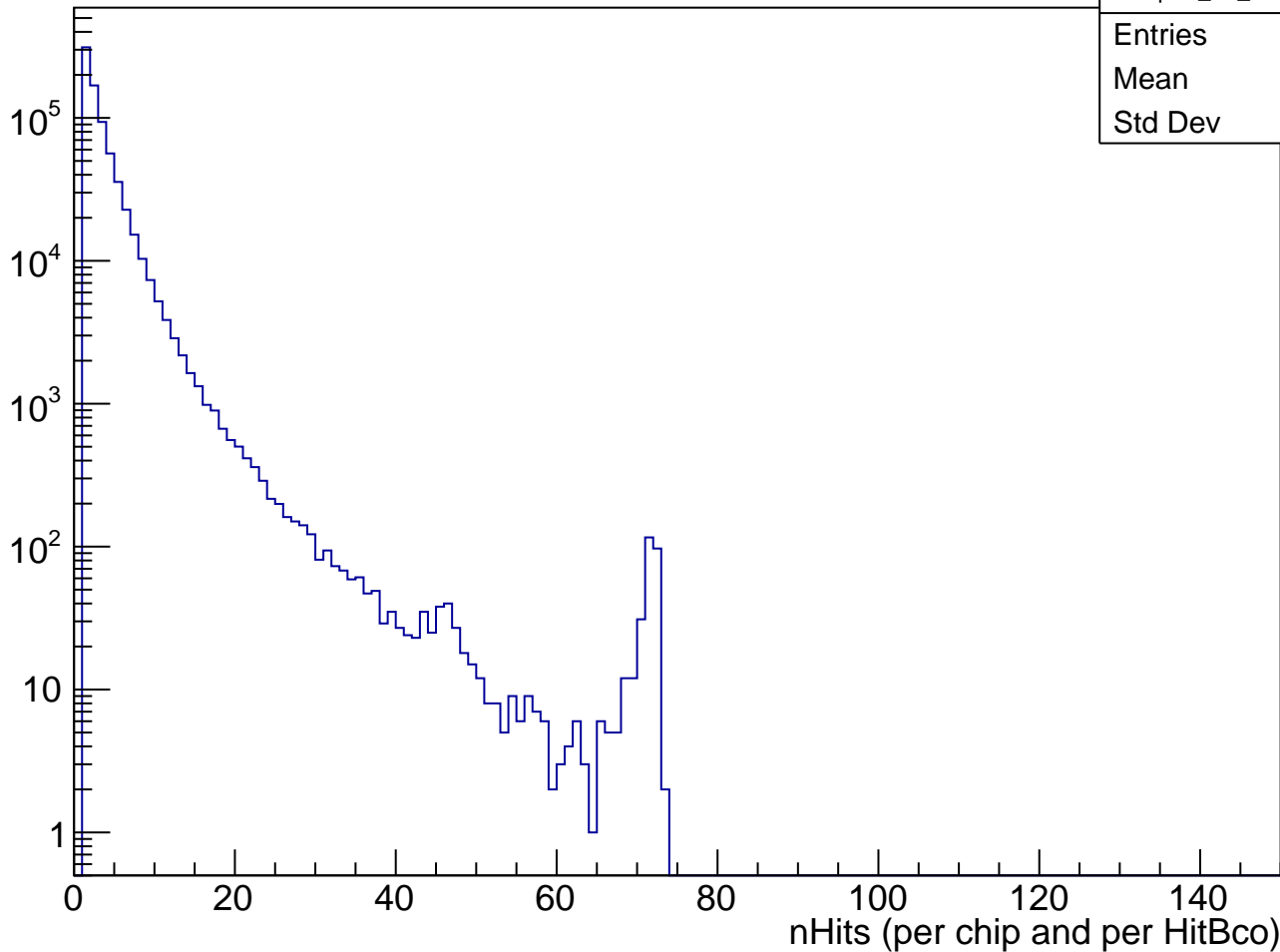
Mean 2.805

Std Dev 3.372

nHits (per chip and per HitBco)

nChipHit_F3_Fch12_Chip7

Entries

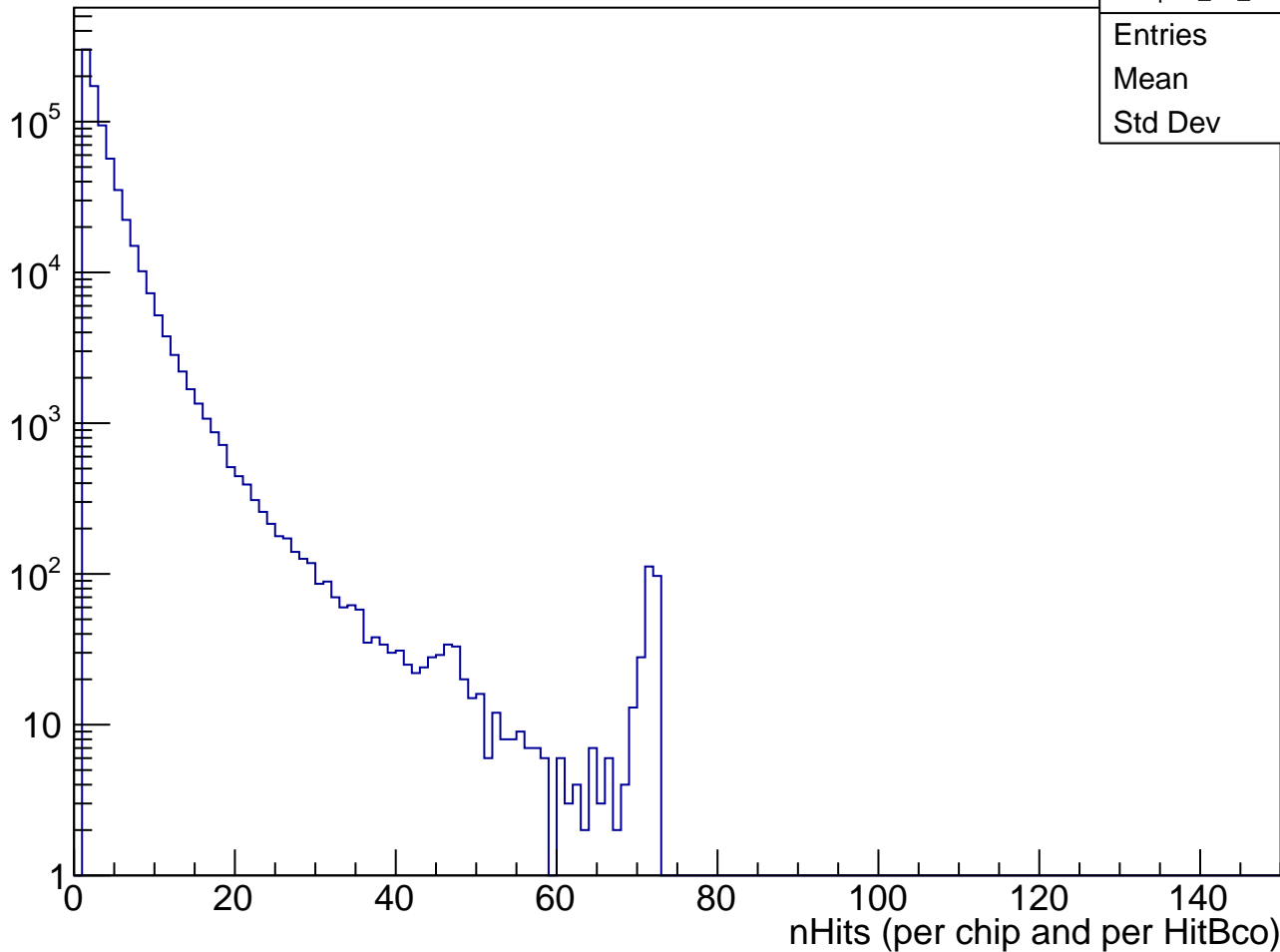


nChipHit_F3_Fch12_Chip7	
Entries	746085
Mean	2.81
Std Dev	3.312

nHits (per chip and per HitBco)

nChipHit_F3_Fch12_Chip8

Entries



nChipHit_F3_Fch12_Chip8

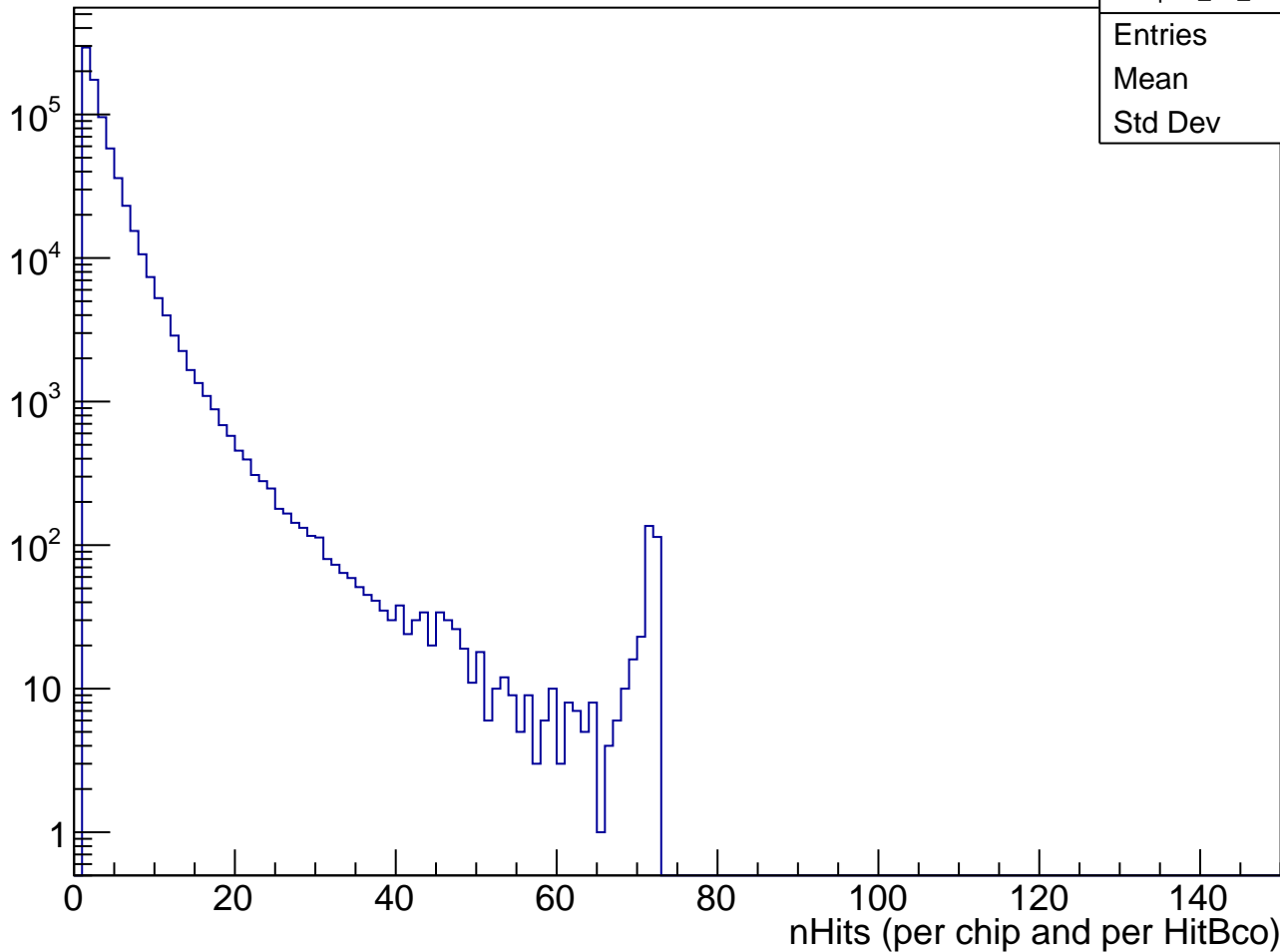
Entries 738205

Mean 2.82

Std Dev 3.282

nChipHit_F3_Fch12_Chip9

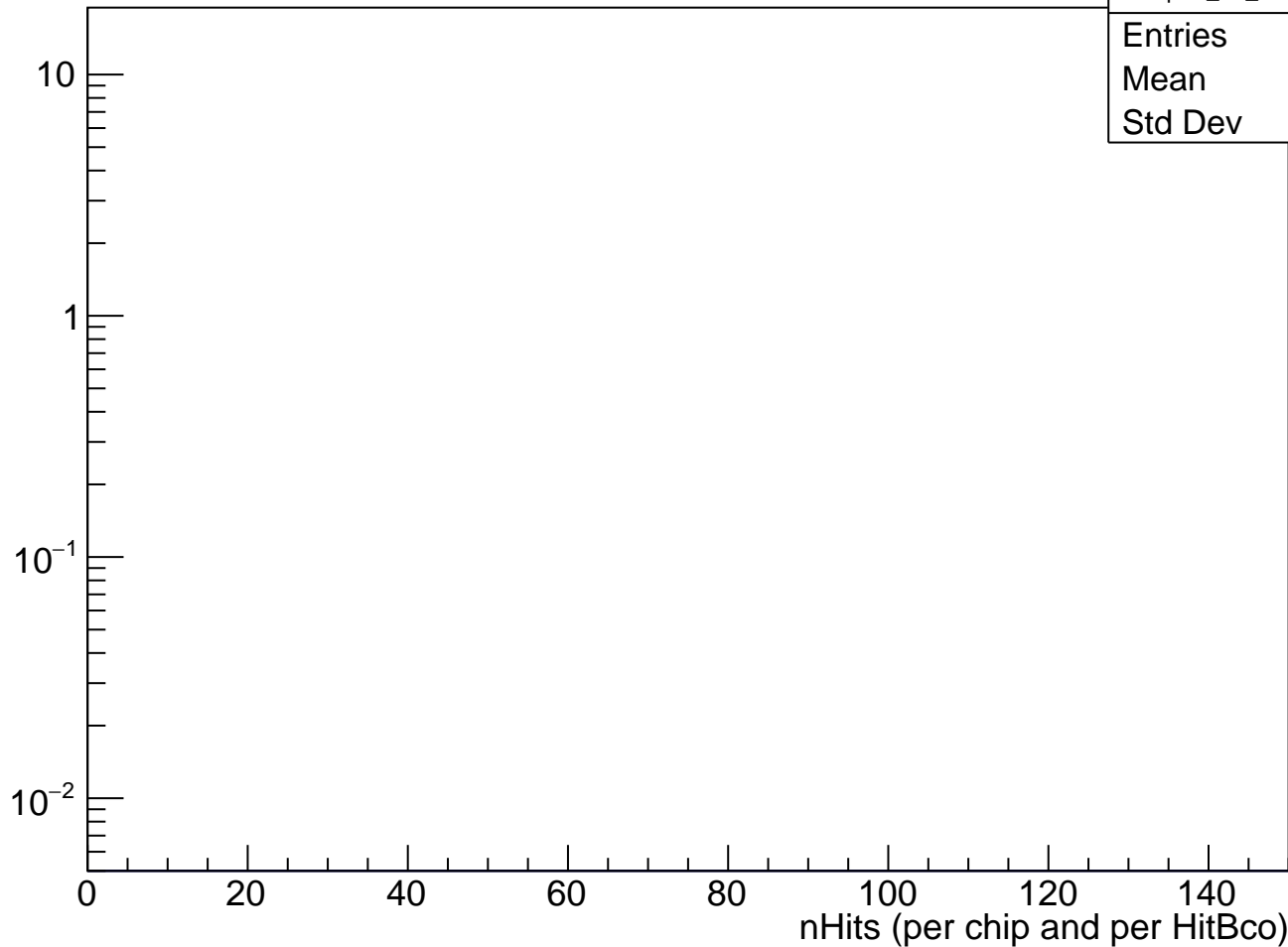
Entries



nChipHit_F3_Fch12_Chip9	
Entries	737802
Mean	2.866
Std Dev	3.358

nChipHit_F3_Fch13_Chip0

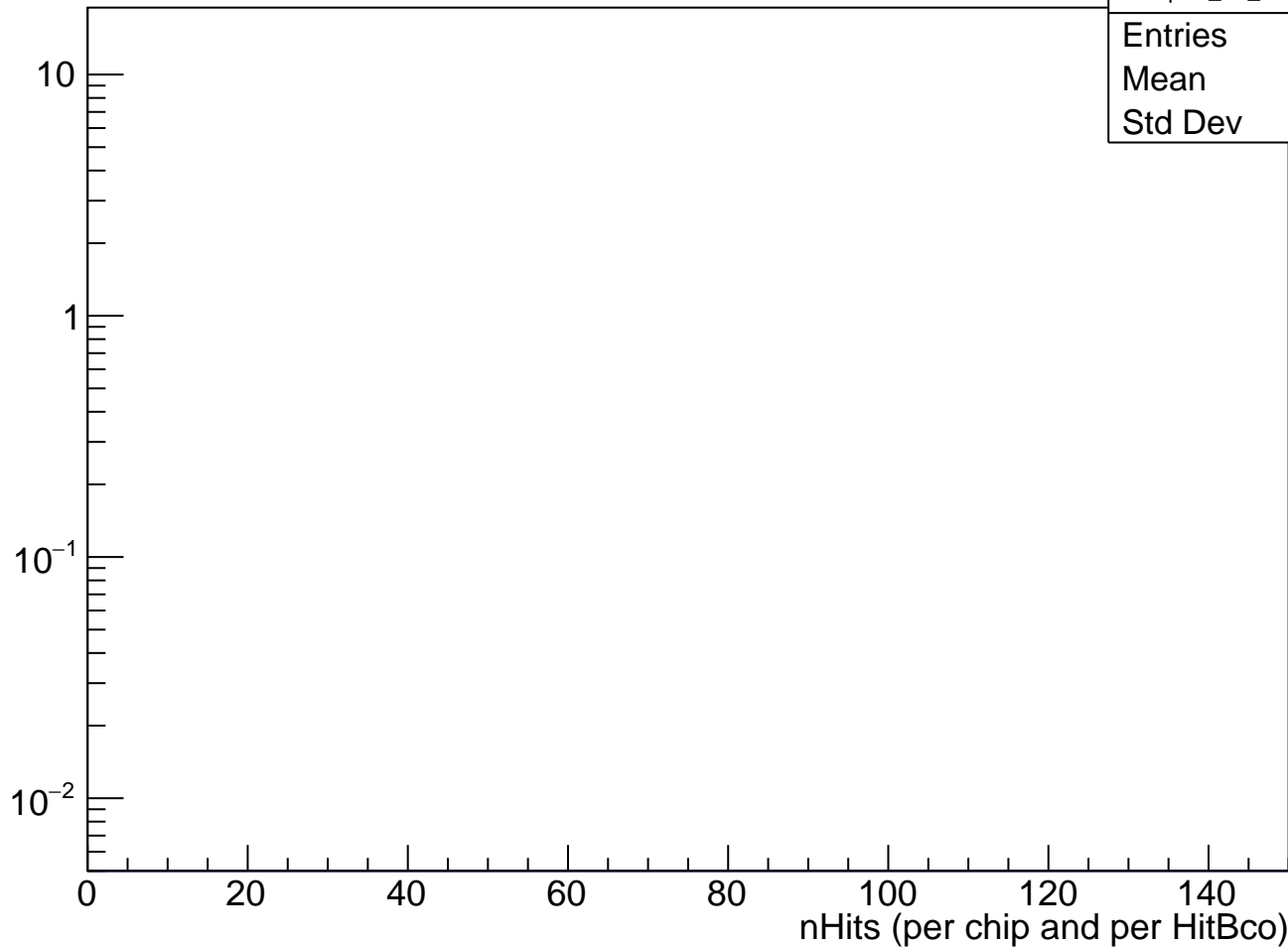
Entries



nChipHit_F3_Fch13_Chip0	
Entries	0
Mean	0
Std Dev	0

nChipHit_F3_Fch13_Chip1

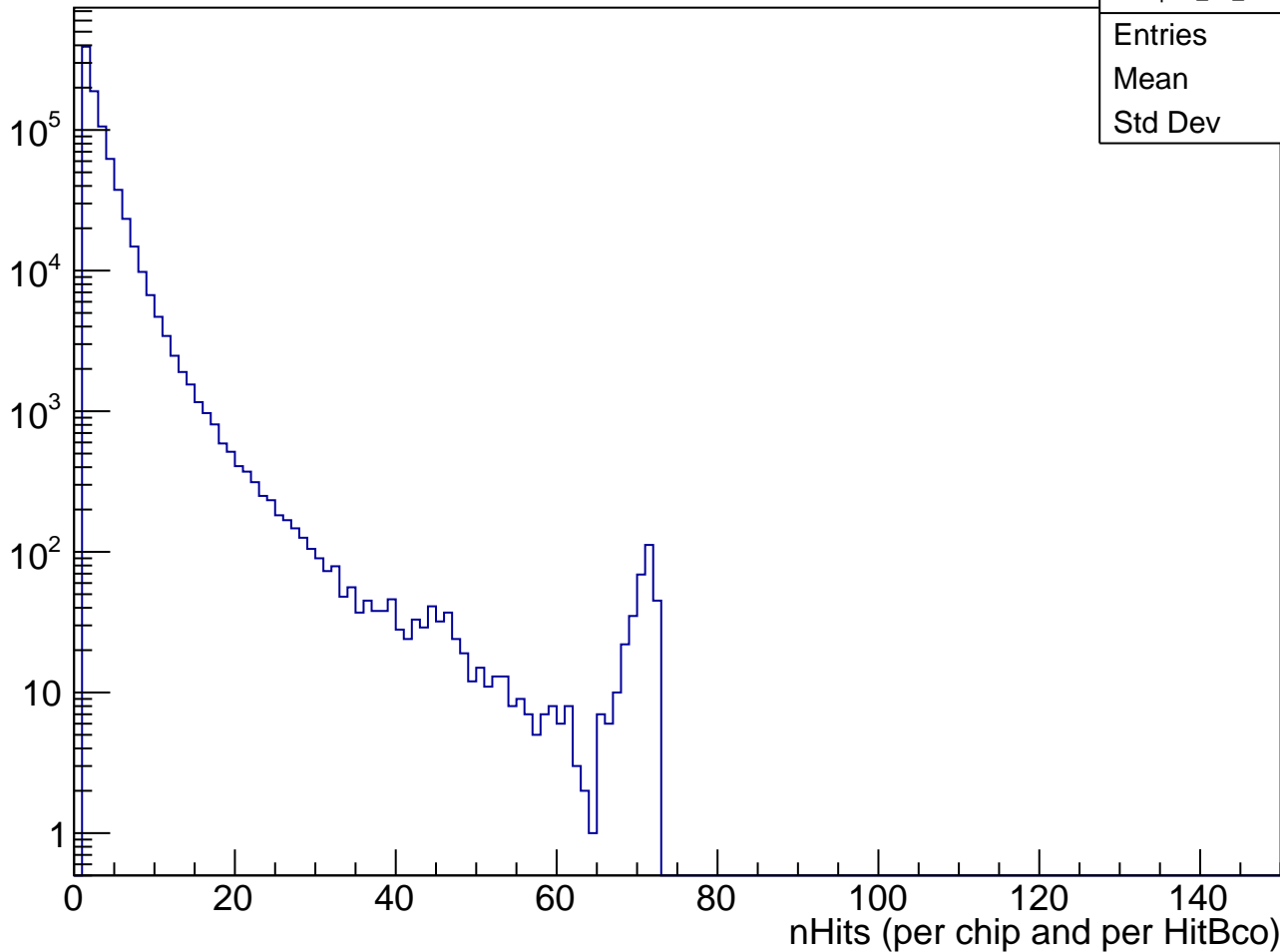
Entries



nChipHit_F3_Fch13_Chip1	
Entries	0
Mean	0
Std Dev	0

nChipHit_F3_Fch13_Chip10

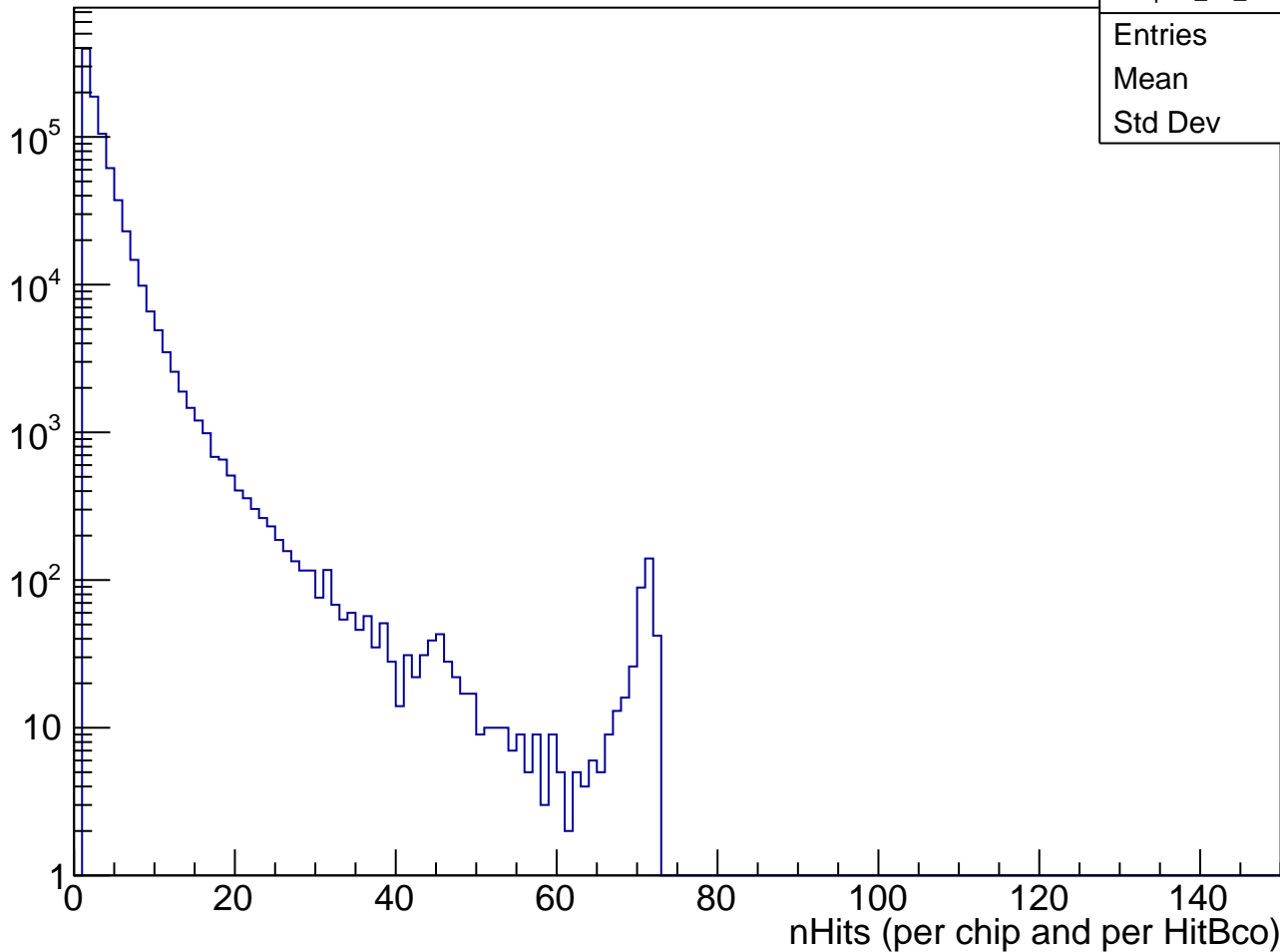
Entries



nChipHit_F3_Fch13_Chip10	
Entries	861809
Mean	2.605
Std Dev	3.085

nChipHit_F3_Fch13_Chip11

Entries

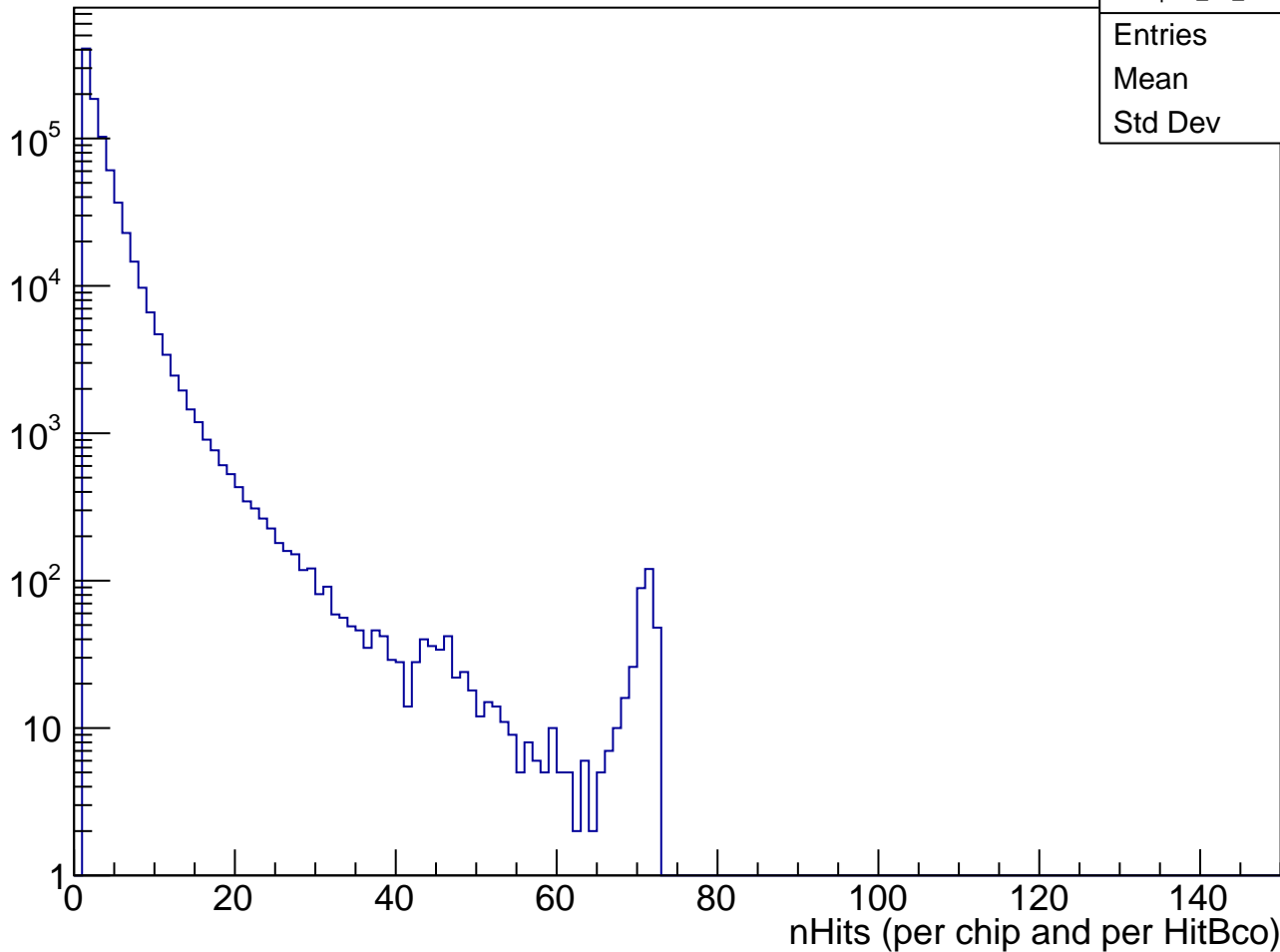


nChipHit_F3_Fch13_Chip11	
Entries	863332
Mean	2.596
Std Dev	3.109

nHits (per chip and per HitBco)

nChipHit_F3_Fch13_Chip12

Entries



nChipHit_F3_Fch13_Chip12

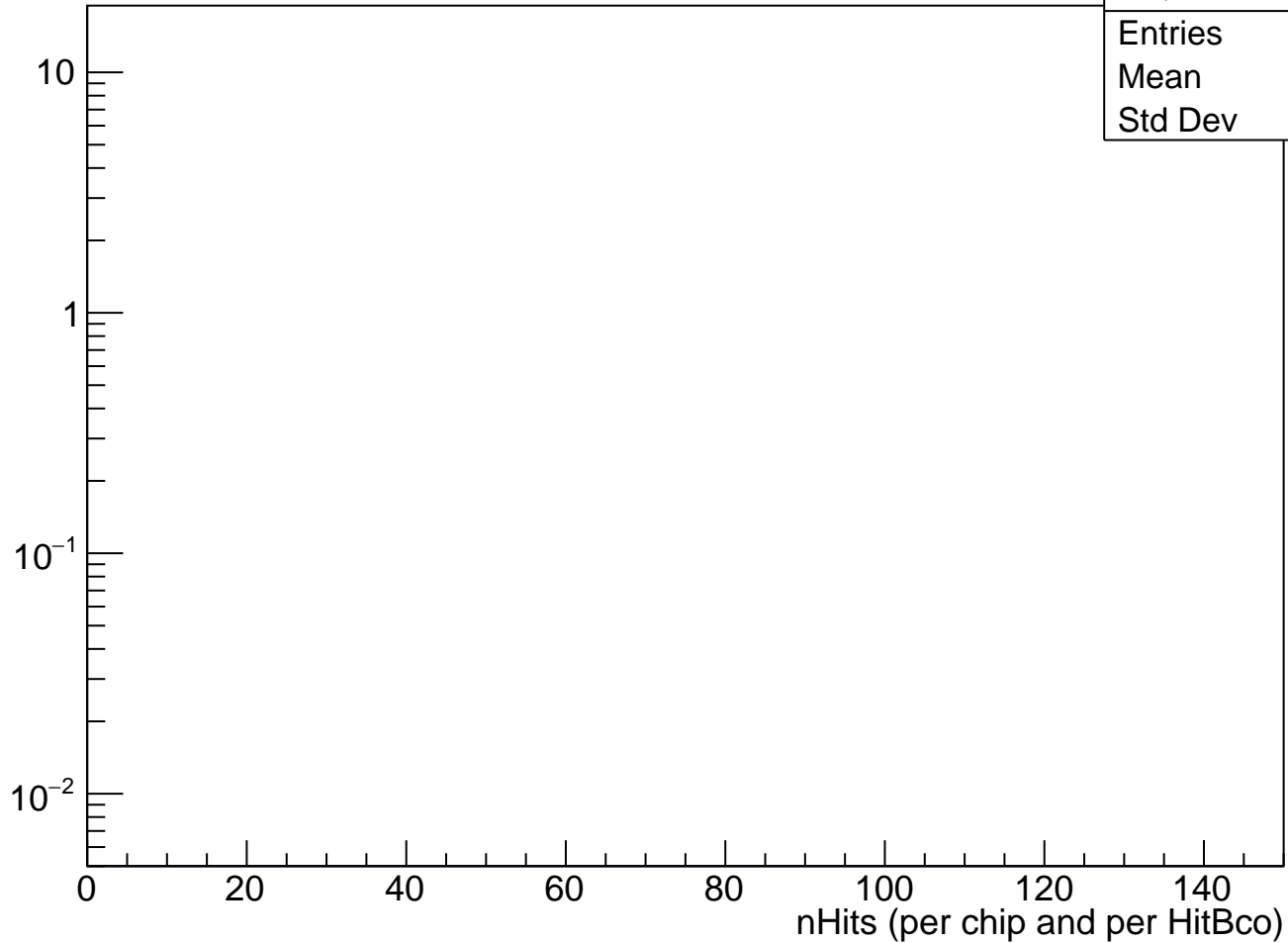
Entries 868255

Mean 2.567

Std Dev 3.087

nChipHit_F3_Fch13_Chip13

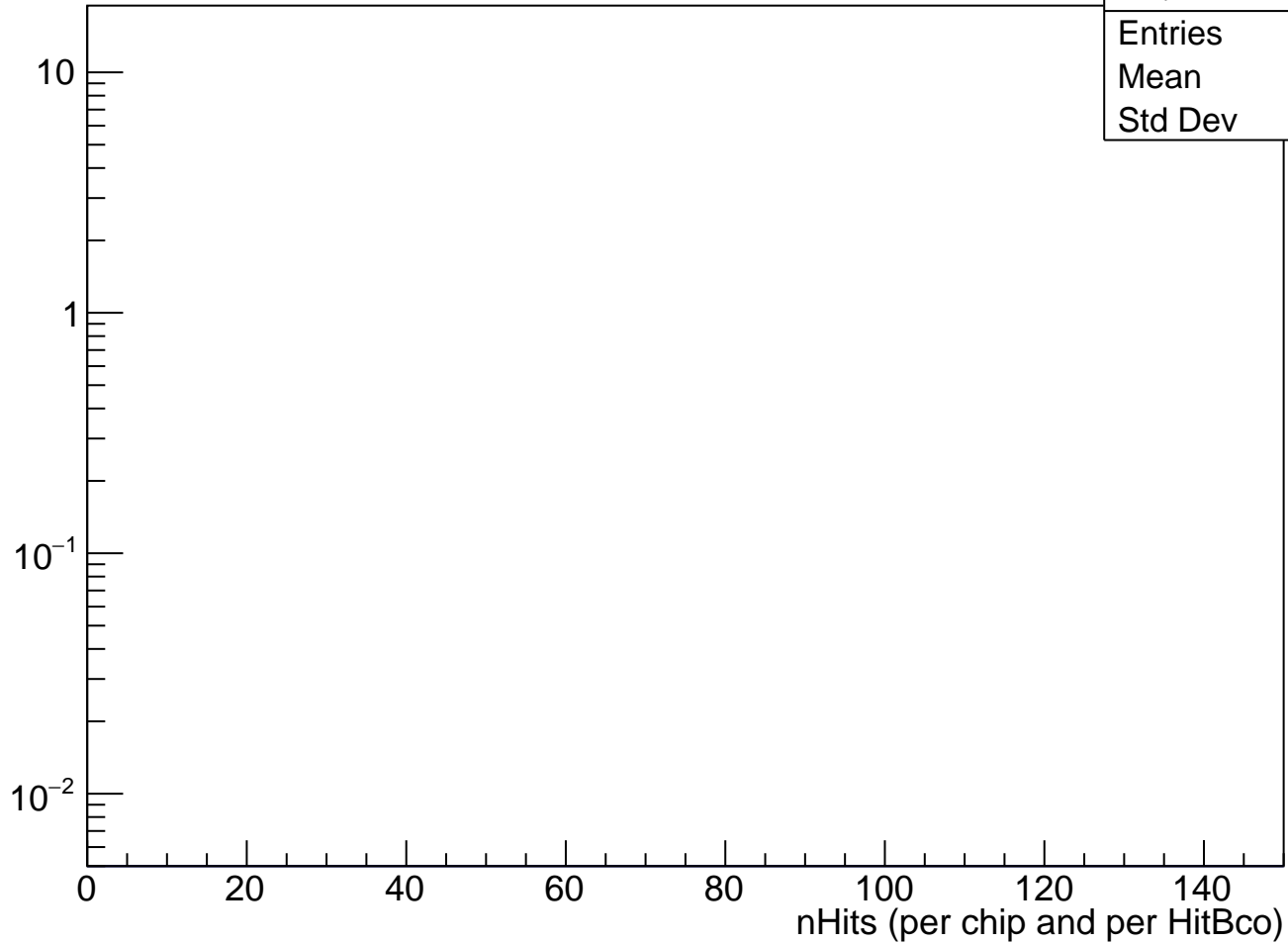
Entries



nChipHit_F3_Fch13_Chip13	
Entries	0
Mean	0
Std Dev	0

nChipHit_F3_Fch13_Chip14

Entries



nChipHit_F3_Fch13_Chip14

Entries 0

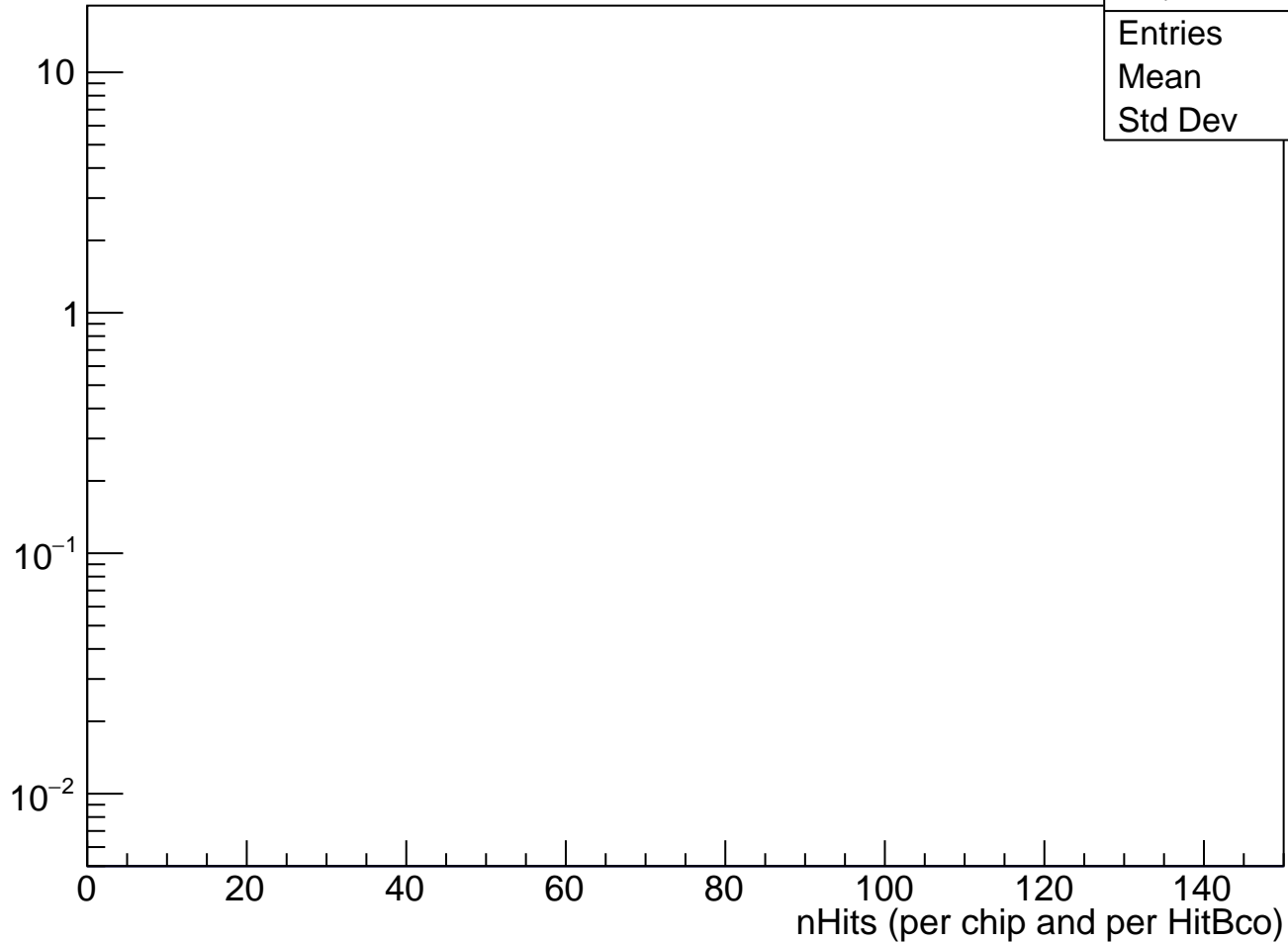
Mean 0

Std Dev 0

nHits (per chip and per HitBco)

nChipHit_F3_Fch13_Chip15

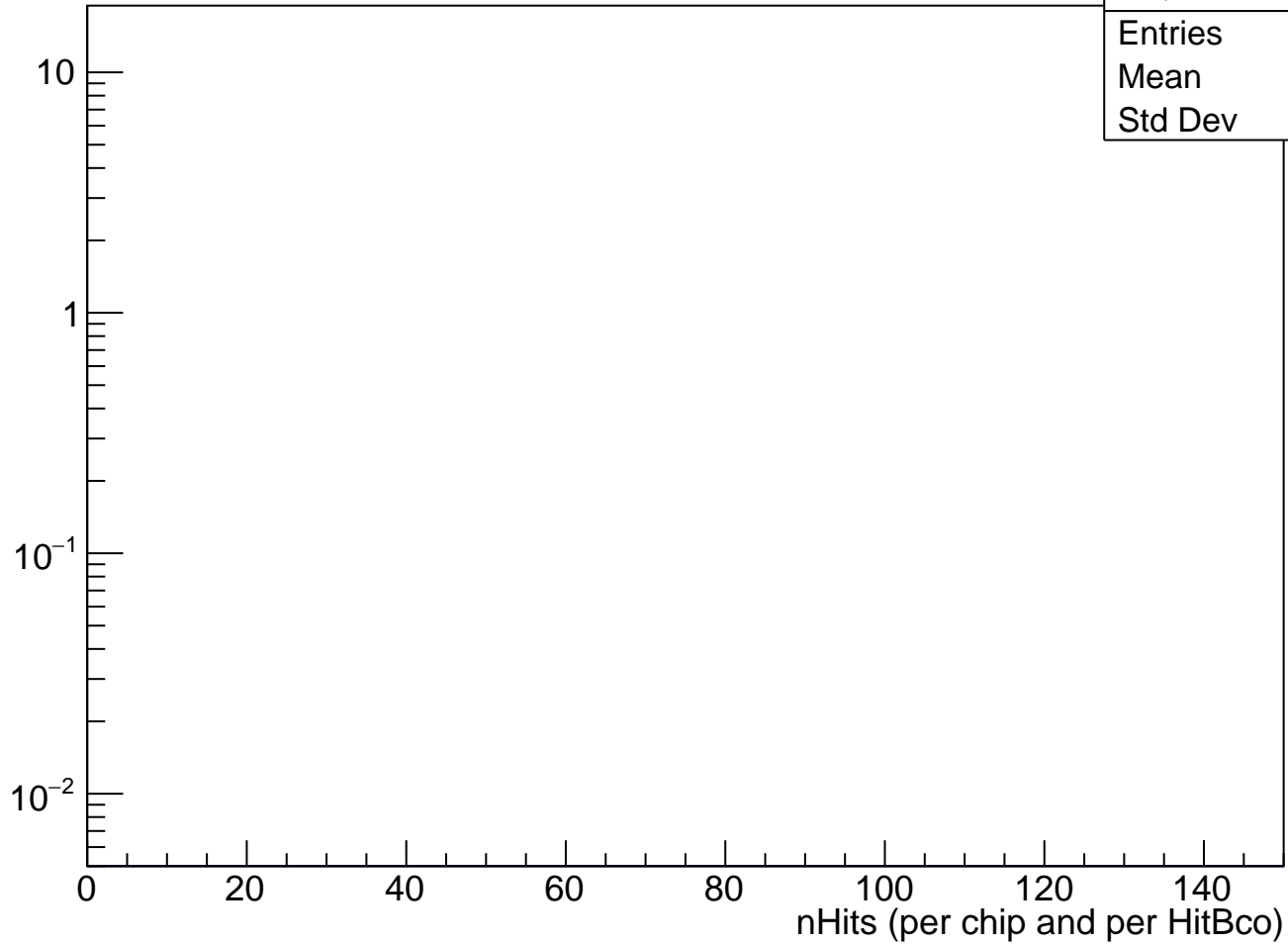
Entries



nChipHit_F3_Fch13_Chip15	
Entries	0
Mean	0
Std Dev	0

nChipHit_F3_Fch13_Chip16

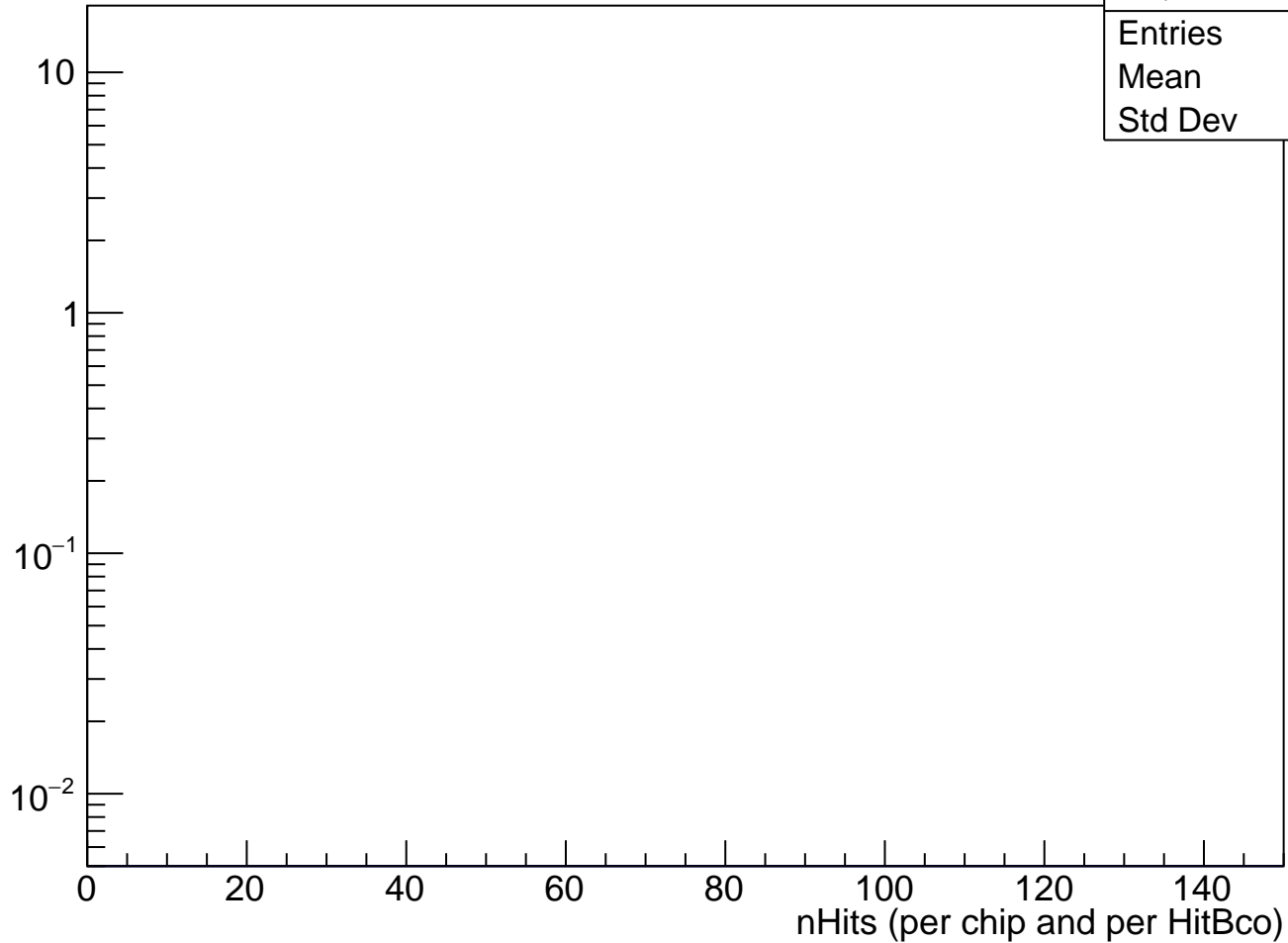
Entries



nChipHit_F3_Fch13_Chip16	
Entries	0
Mean	0
Std Dev	0

nChipHit_F3_Fch13_Chip17

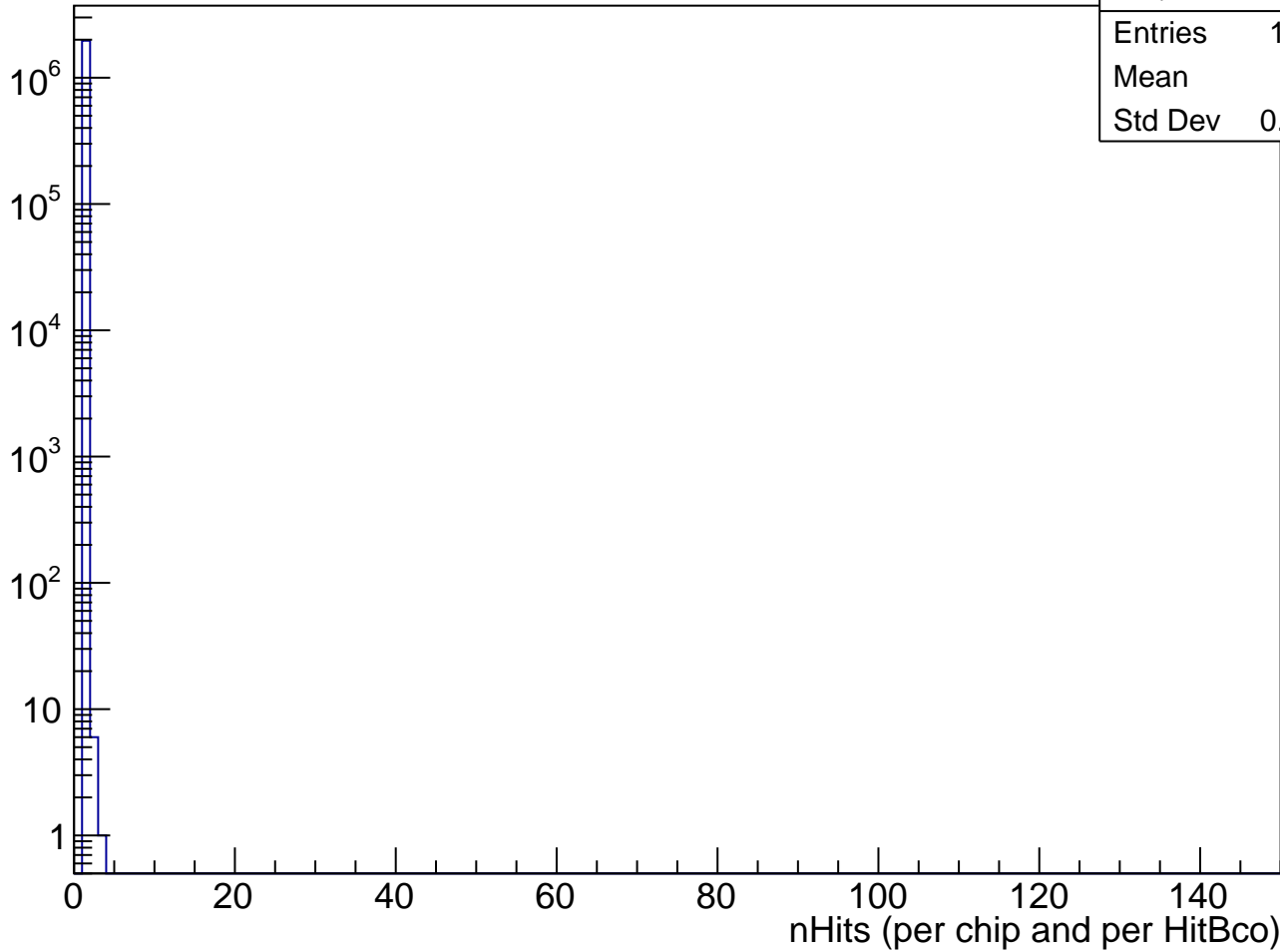
Entries



nChipHit_F3_Fch13_Chip17	
Entries	0
Mean	0
Std Dev	0

nChipHit_F3_Fch13_Chip18

Entries



nChipHit_F3_Fch13_Chip18

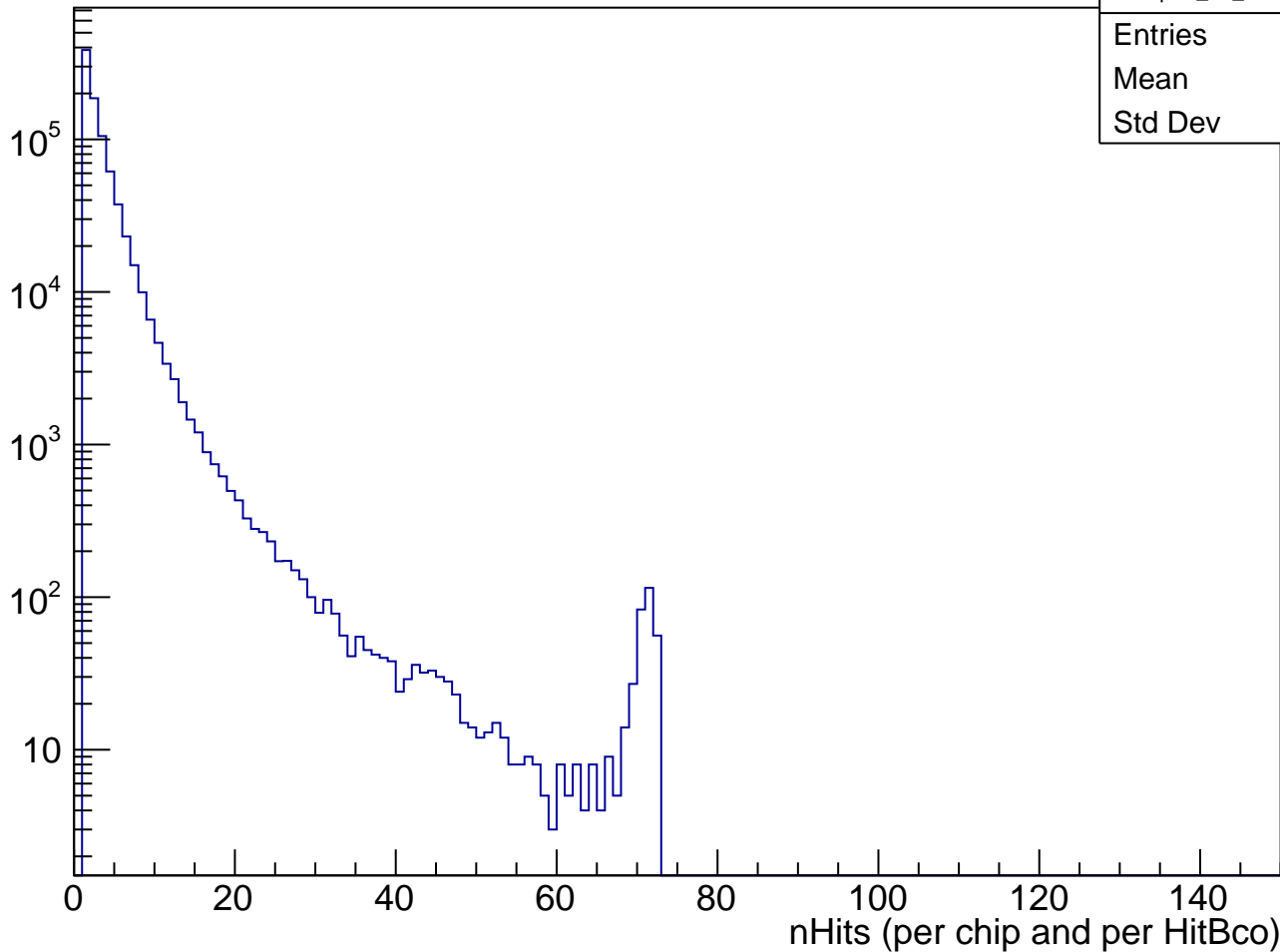
Entries 1963873

Mean 1

Std Dev 0.002257

nChipHit_F3_Fch13_Chip19

Entries



nChipHit_F3_Fch13_Chip19

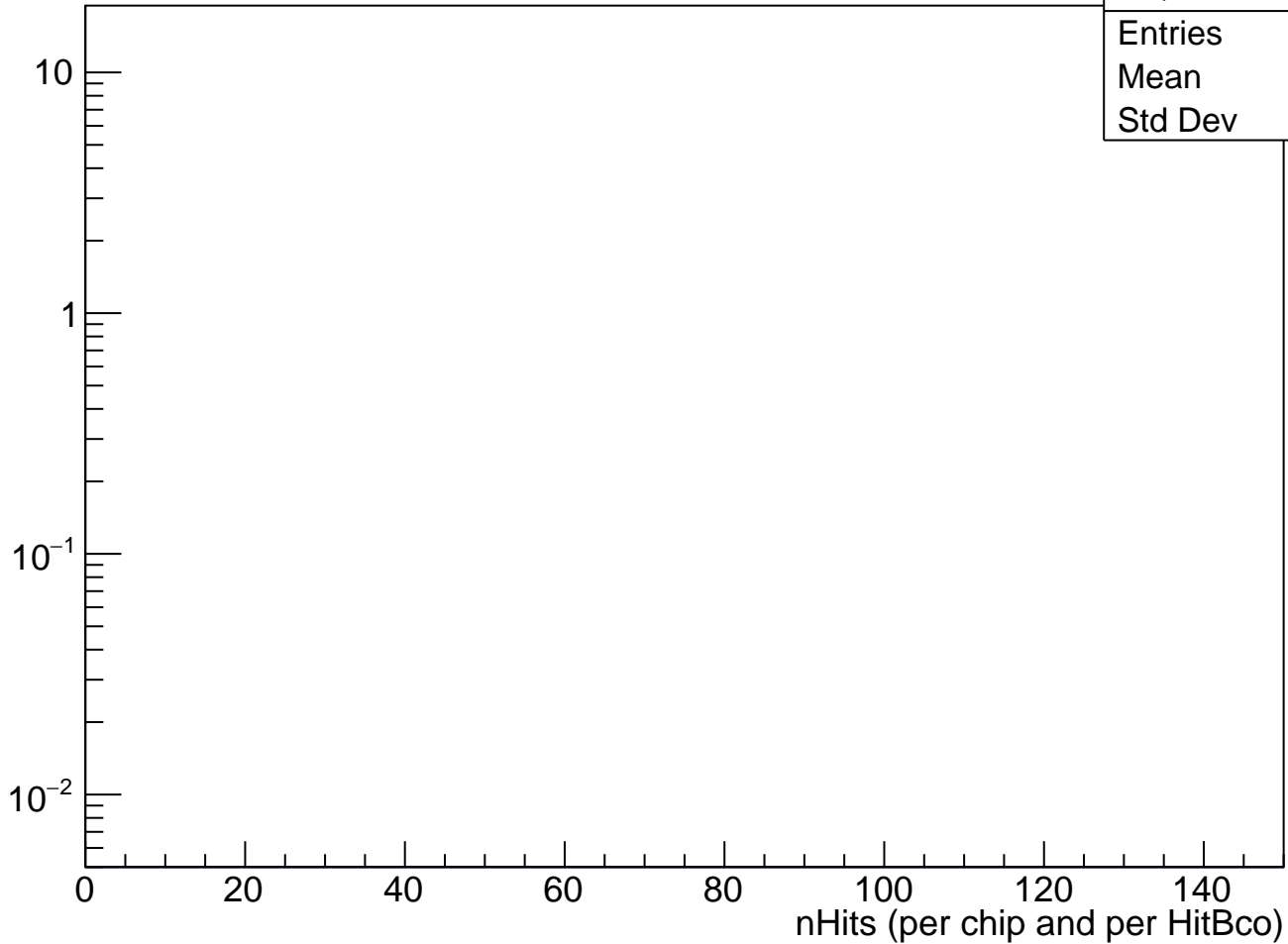
Entries 851763

Mean 2.616

Std Dev 3.106

nChipHit_F3_Fch13_Chip2

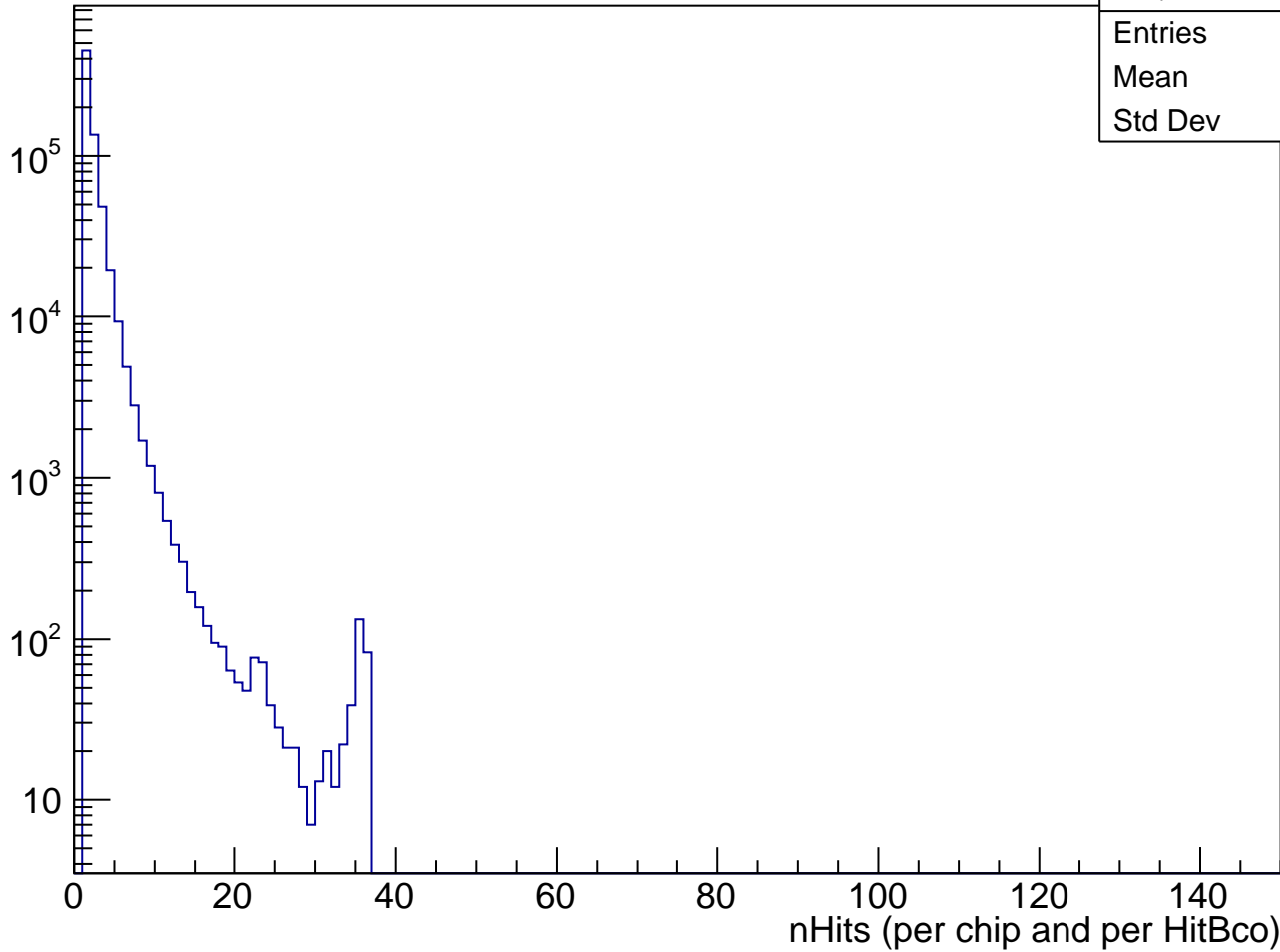
Entries



nChipHit_F3_Fch13_Chip2	
Entries	0
Mean	0
Std Dev	0

nChipHit_F3_Fch13_Chip20

Entries



nChipHit_F3_Fch13_Chip20

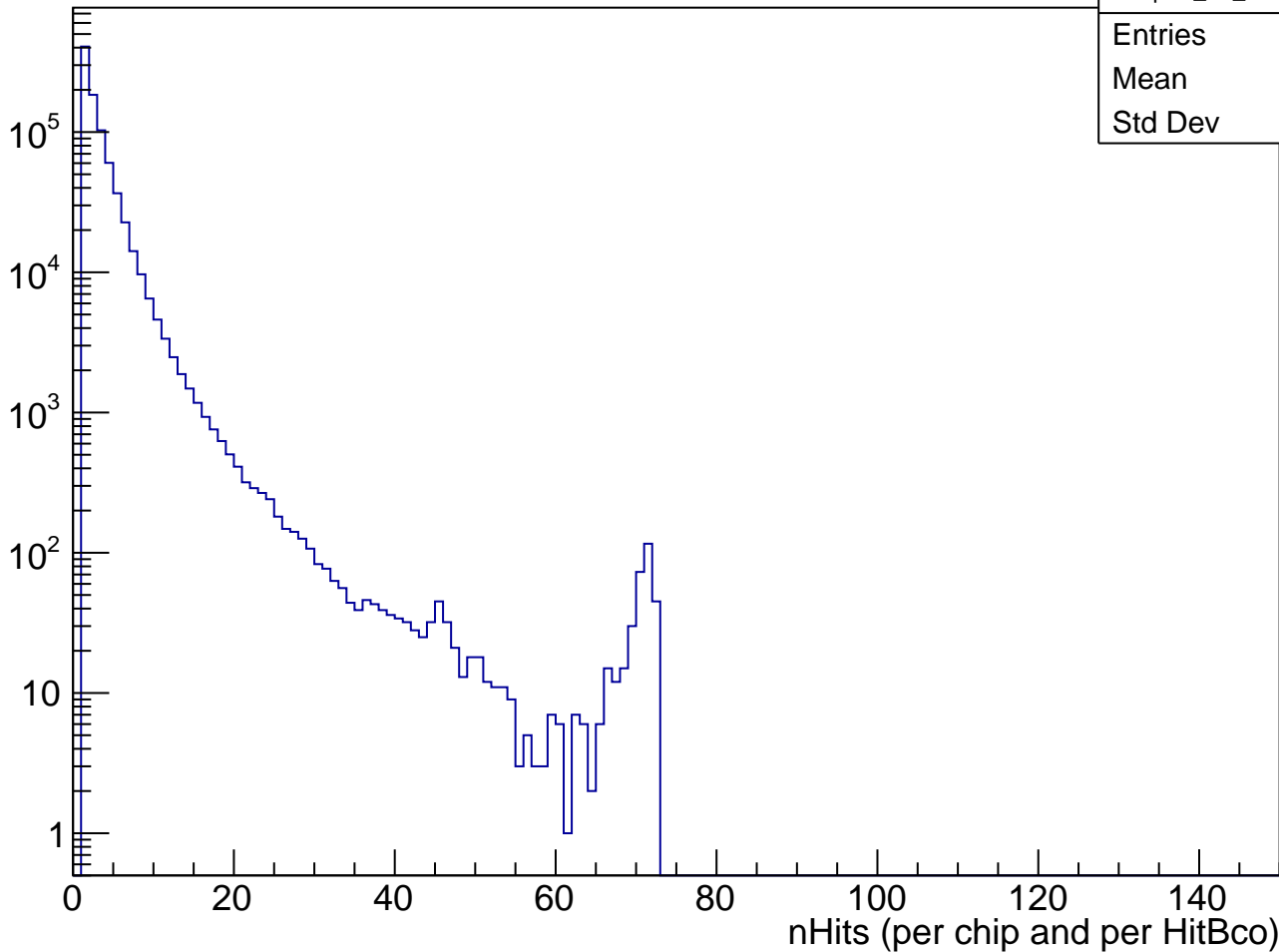
Entries 676378

Mean 1.651

Std Dev 1.569

nChipHit_F3_Fch13_Chip21

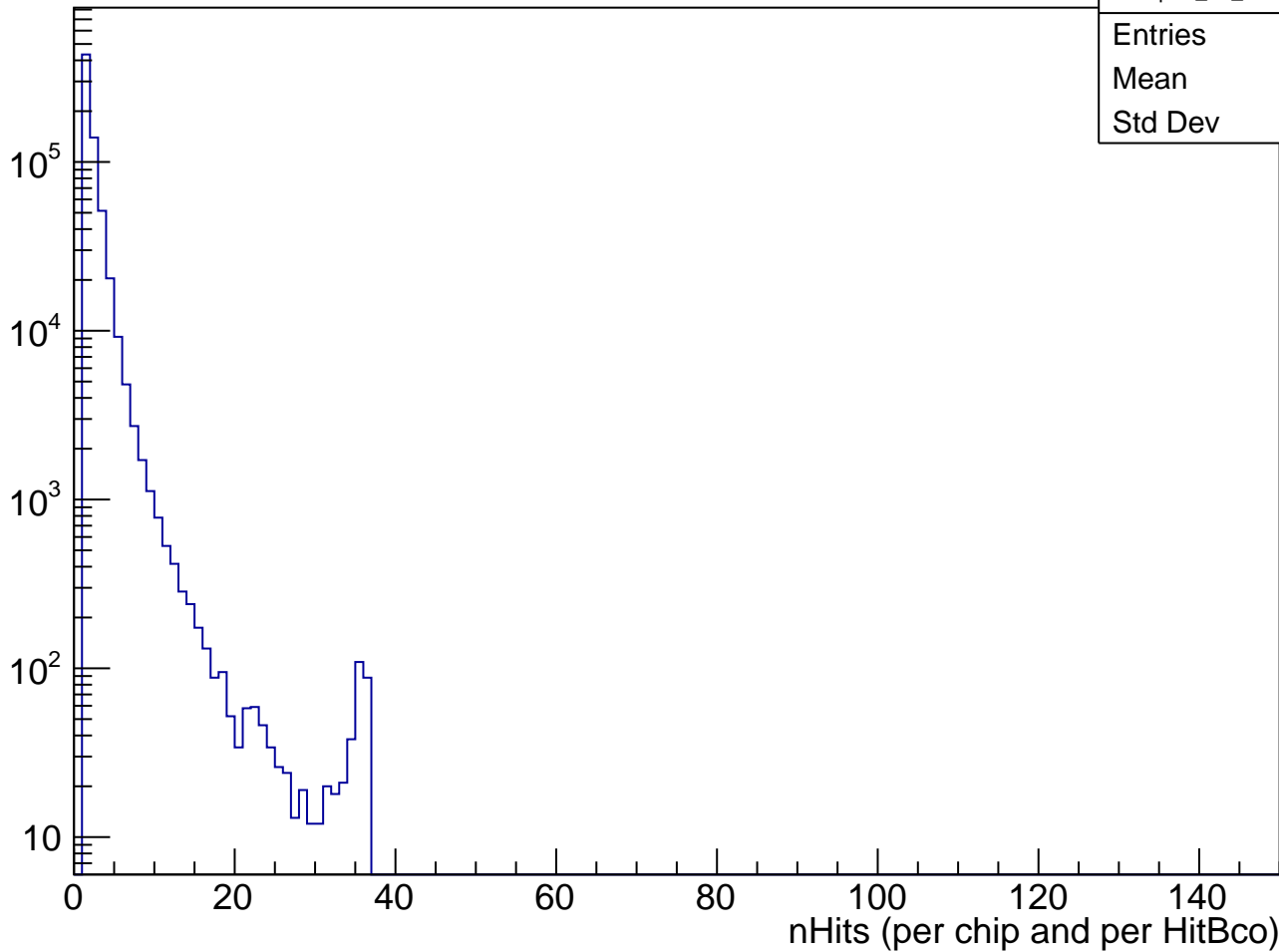
Entries



nChipHit_F3_Fch13_Chip21	
Entries	865414
Mean	2.558
Std Dev	3.061

nChipHit_F3_Fch13_Chip22

Entries



nChipHit_F3_Fch13_Chip22

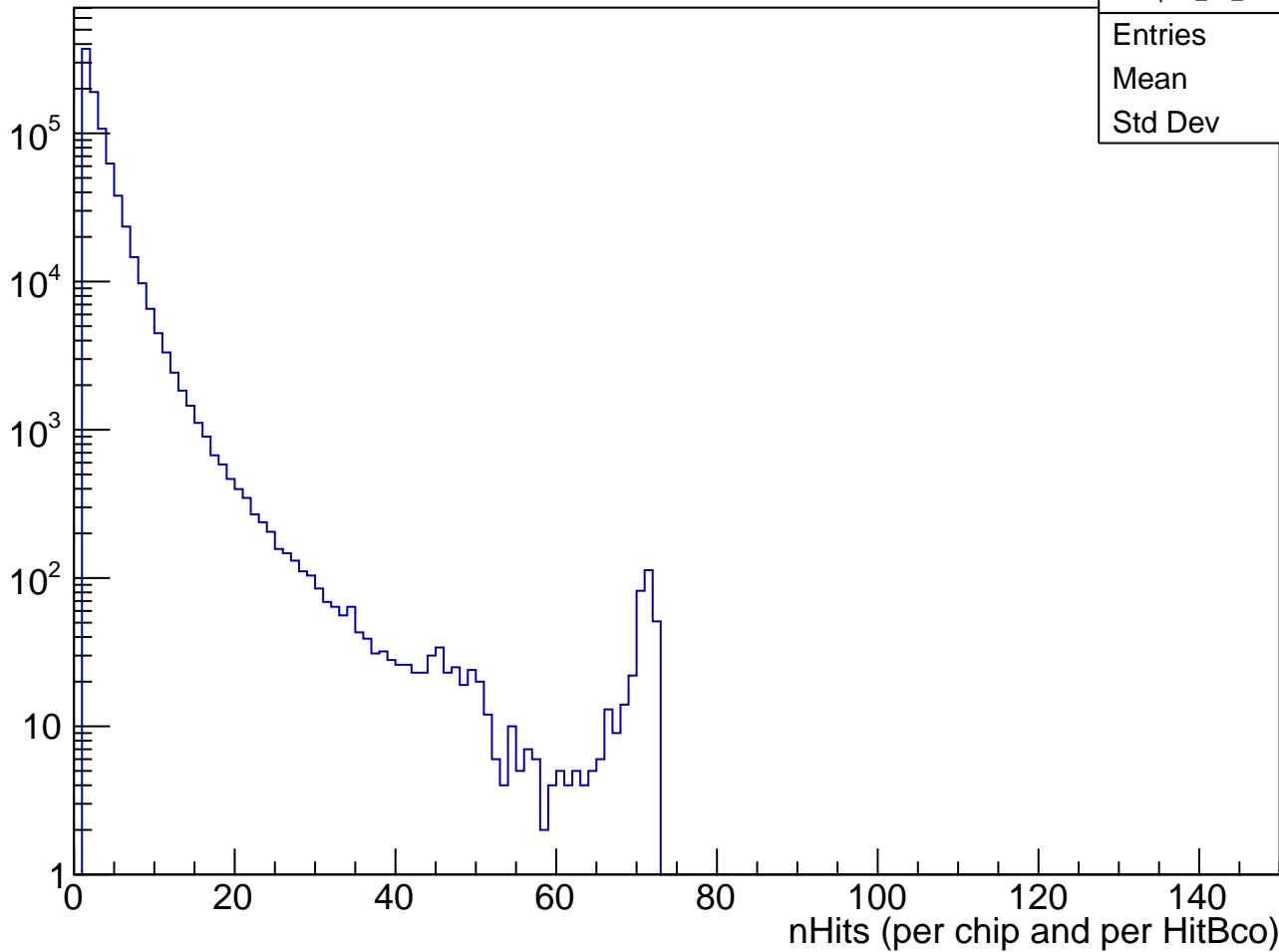
Entries 667726

Mean 1.676

Std Dev 1.562

nChipHit_F3_Fch13_Chip23

Entries



nChipHit_F3_Fch13_Chip23

Entries 844296

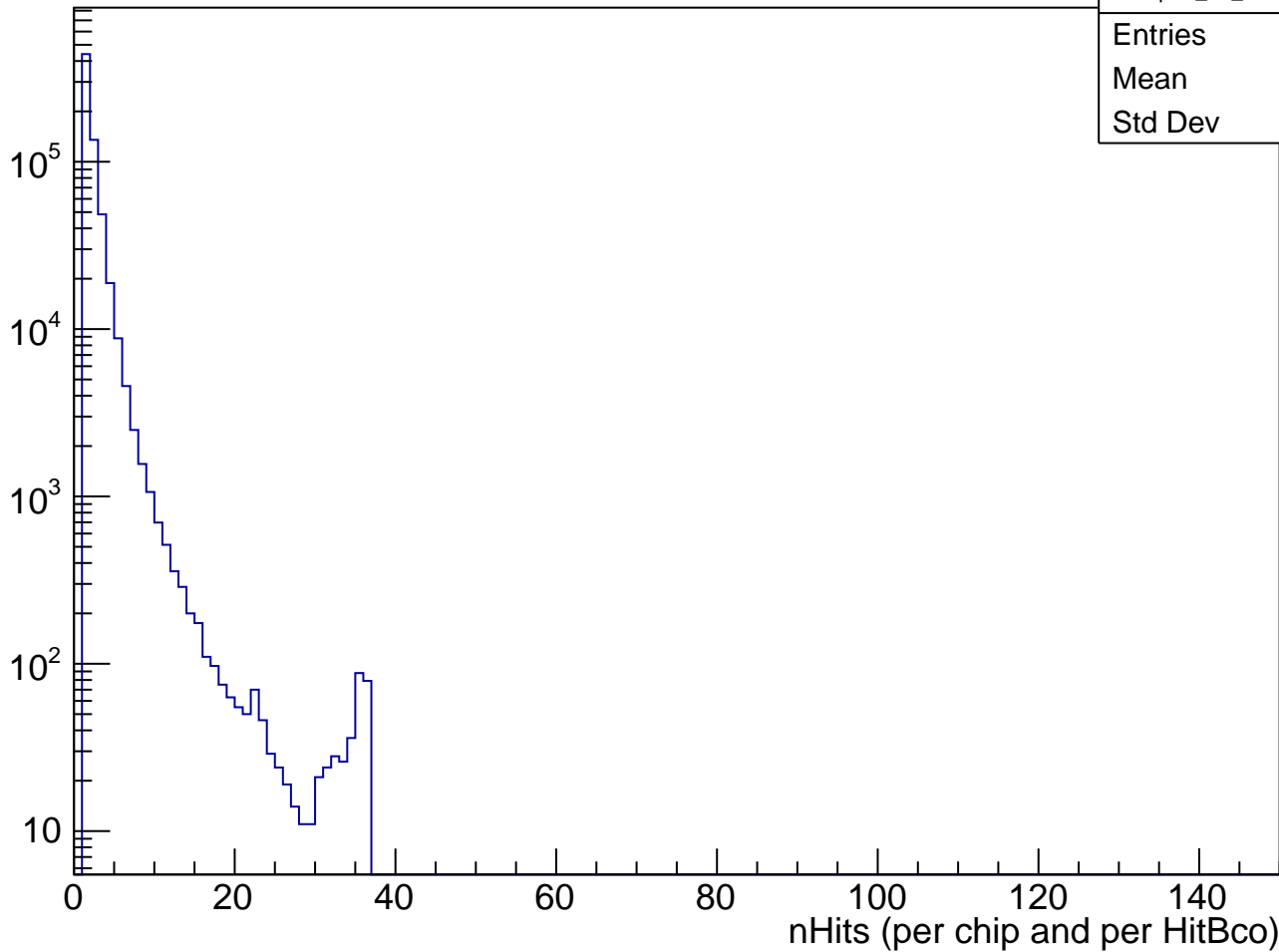
Mean 2.623

Std Dev 3.048

nHits (per chip and per HitBco)

nChipHit_F3_Fch13_Chip24

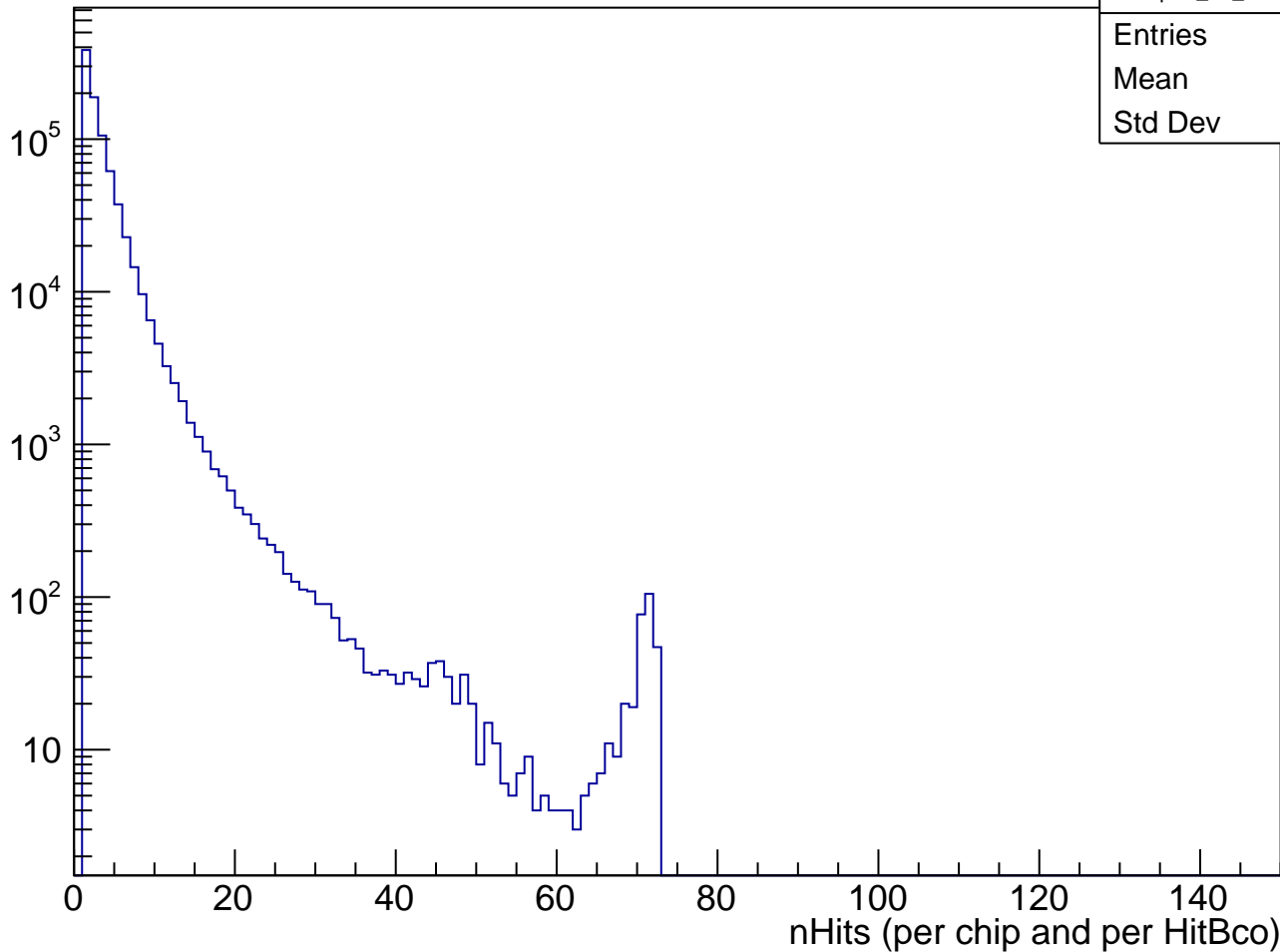
Entries



nChipHit_F3_Fch13_Chip24	
Entries	664316
Mean	1.644
Std Dev	1.53

nChipHit_F3_Fch13_Chip25

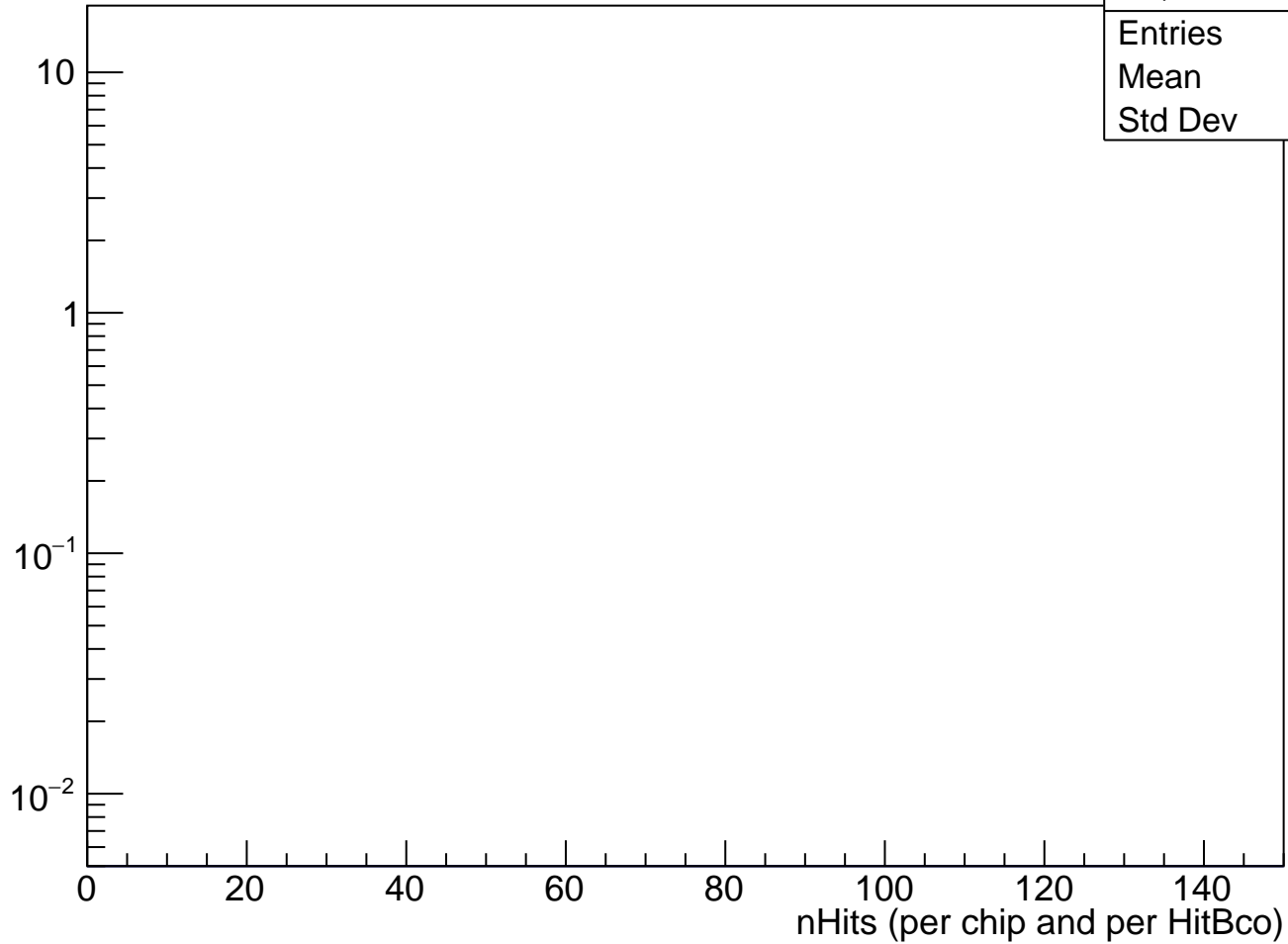
Entries



nChipHit_F3_Fch13_Chip25	
Entries	851076
Mean	2.599
Std Dev	3.051

nChipHit_F3_Fch13_Chip3

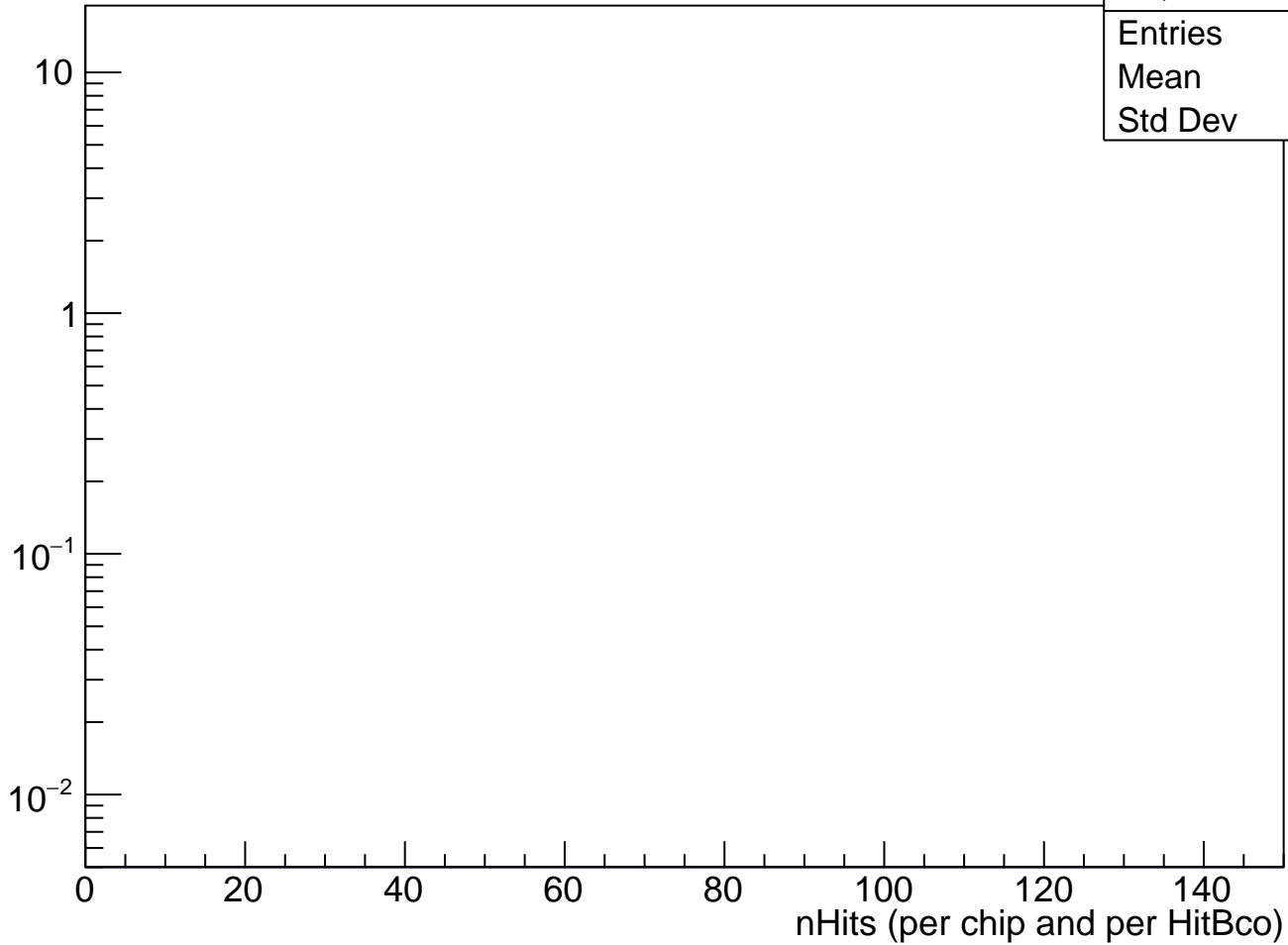
Entries



nChipHit_F3_Fch13_Chip3	
Entries	0
Mean	0
Std Dev	0

nChipHit_F3_Fch13_Chip4

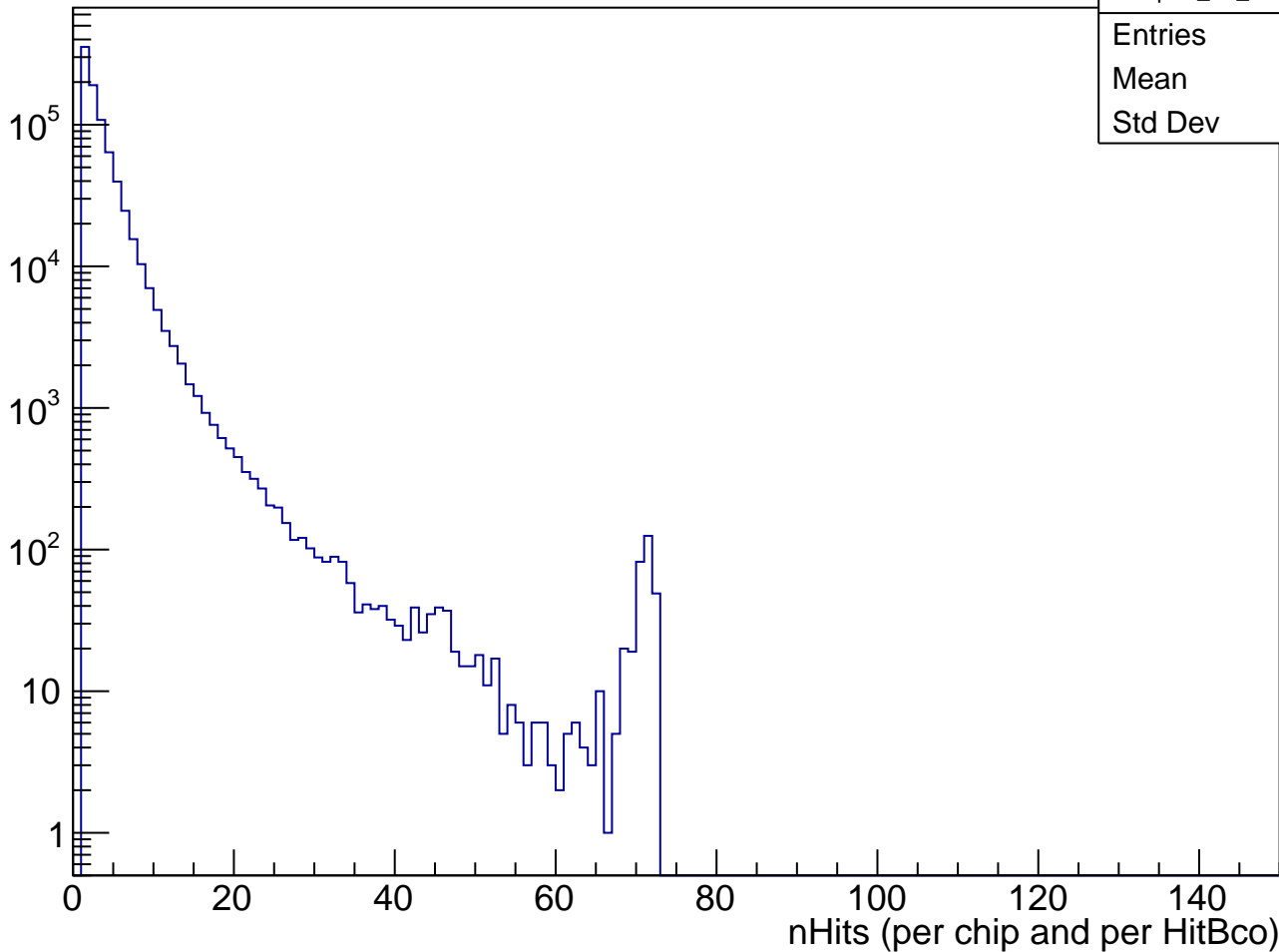
Entries



nChipHit_F3_Fch13_Chip4	
Entries	0
Mean	0
Std Dev	0

nChipHit_F3_Fch13_Chip5

Entries

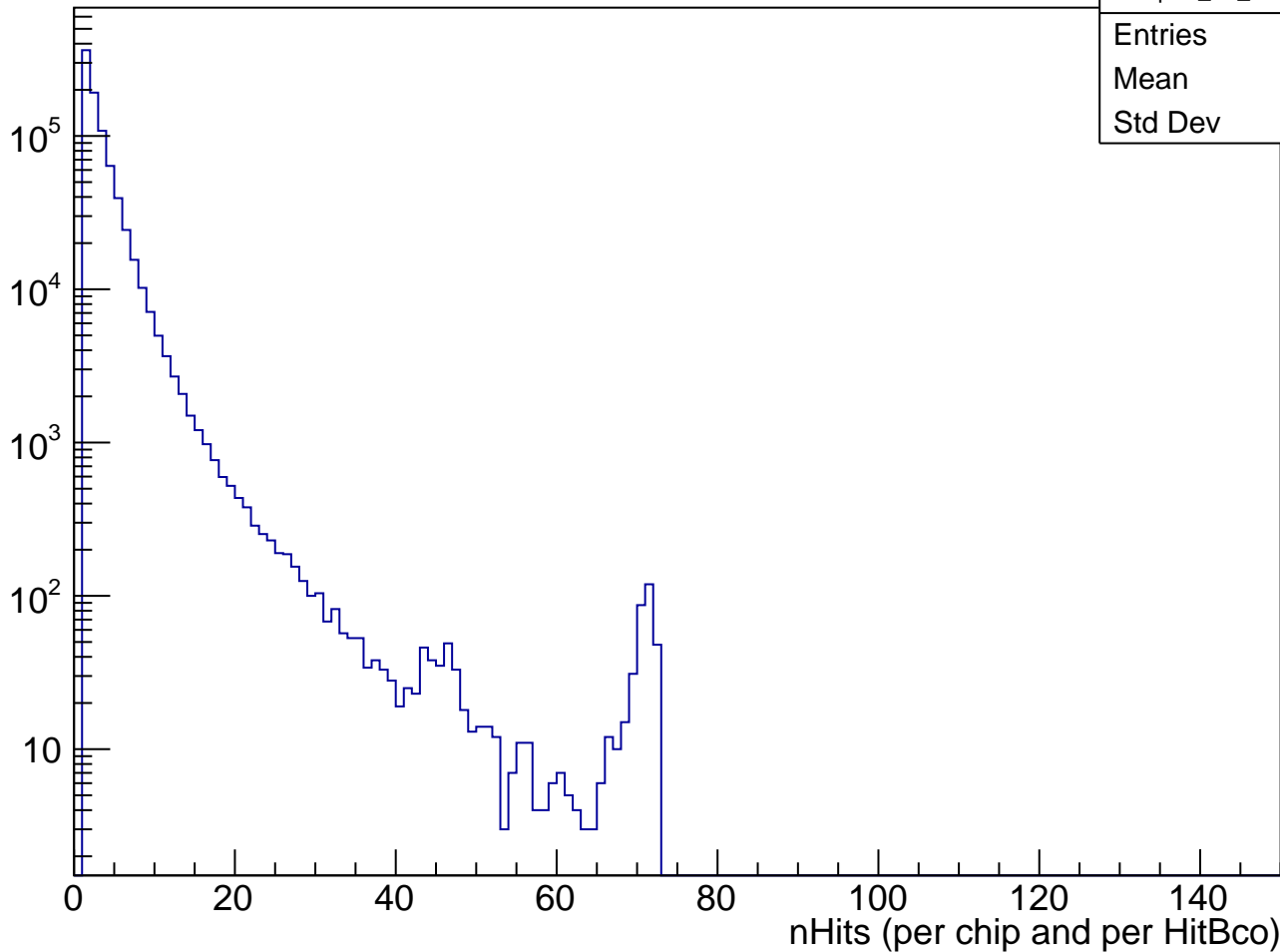


nChipHit_F3_Fch13_Chip5	
Entries	835832
Mean	2.707
Std Dev	3.135

nHits (per chip and per HitBco)

nChipHit_F3_Fch13_Chip6

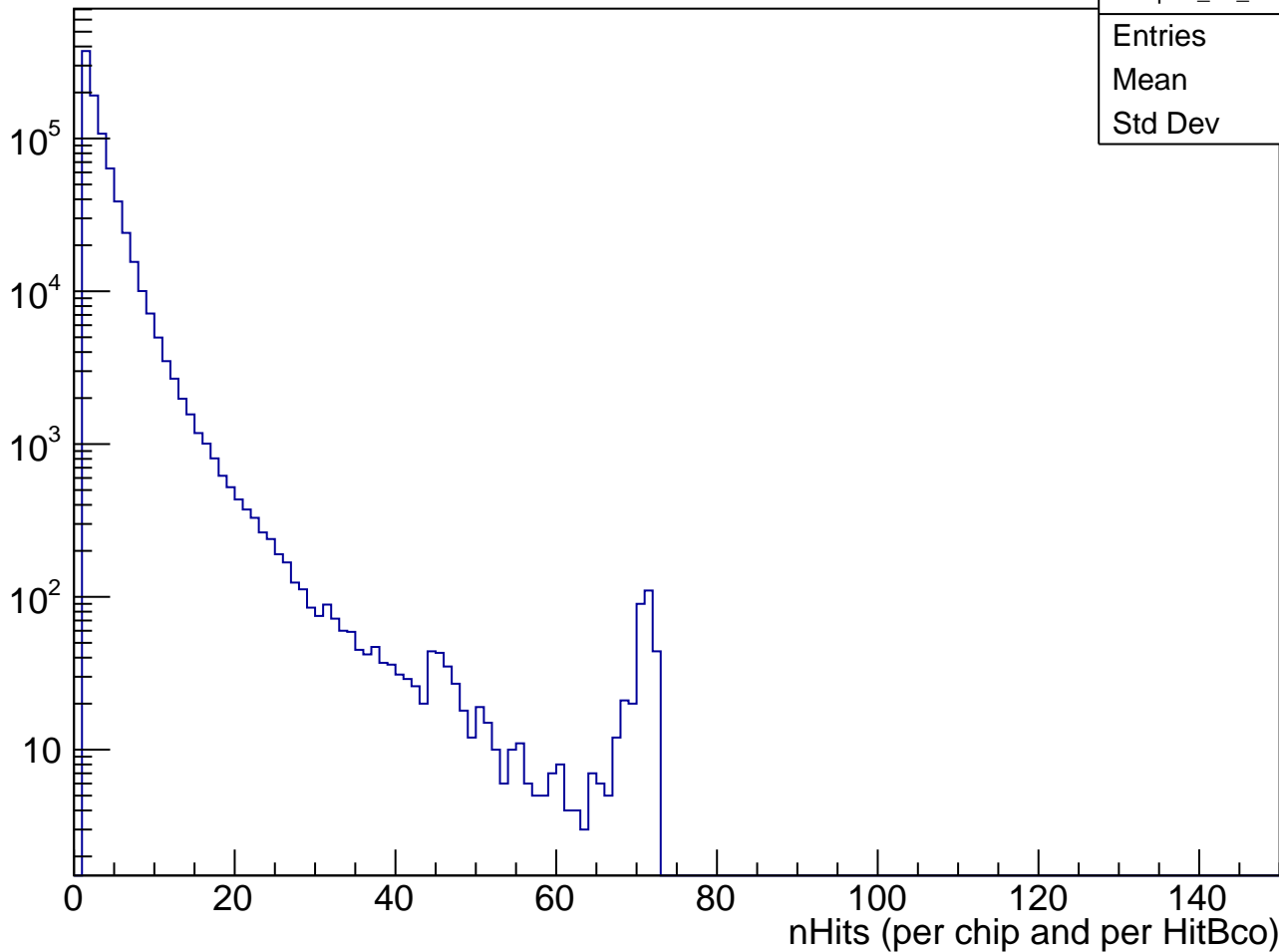
Entries



nChipHit_F3_Fch13_Chip6	
Entries	845426
Mean	2.692
Std Dev	3.15

nChipHit_F3_Fch13_Chip7

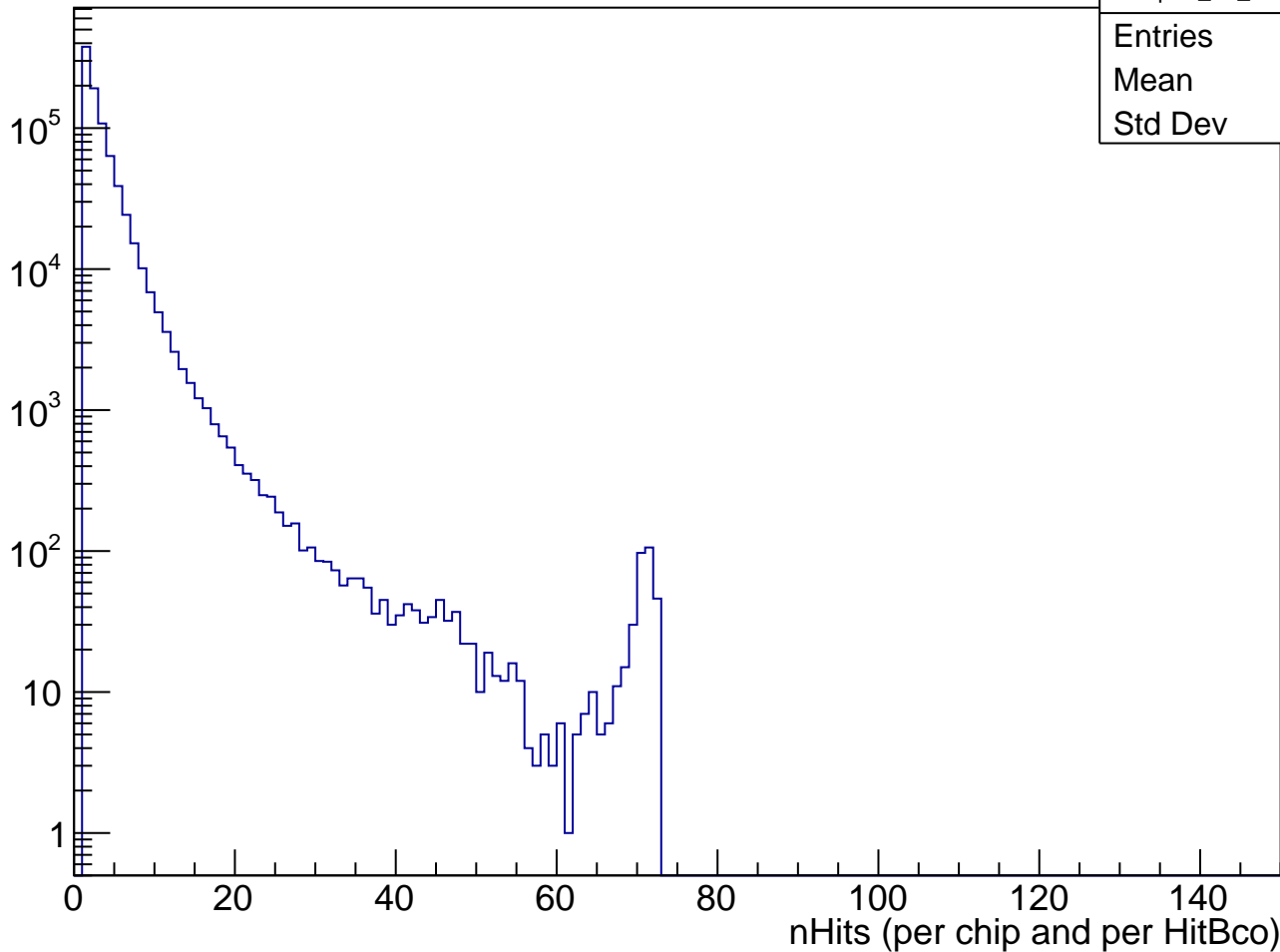
Entries



nChipHit_F3_Fch13_Chip7	
Entries	853826
Mean	2.664
Std Dev	3.121

nChipHit_F3_Fch13_Chip8

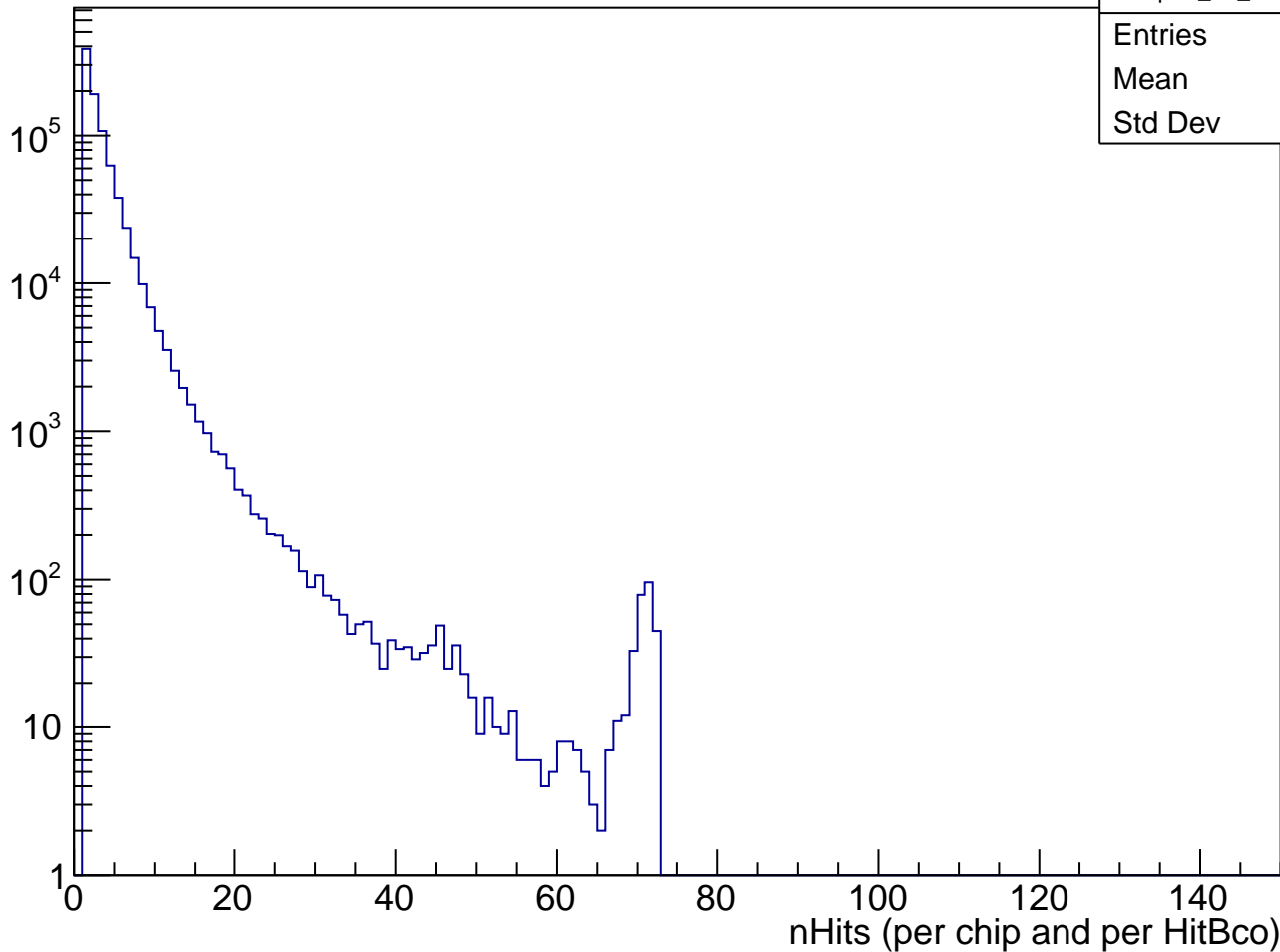
Entries



nChipHit_F3_Fch13_Chip8	
Entries	858041
Mean	2.658
Std Dev	3.147

nChipHit_F3_Fch13_Chip9

Entries

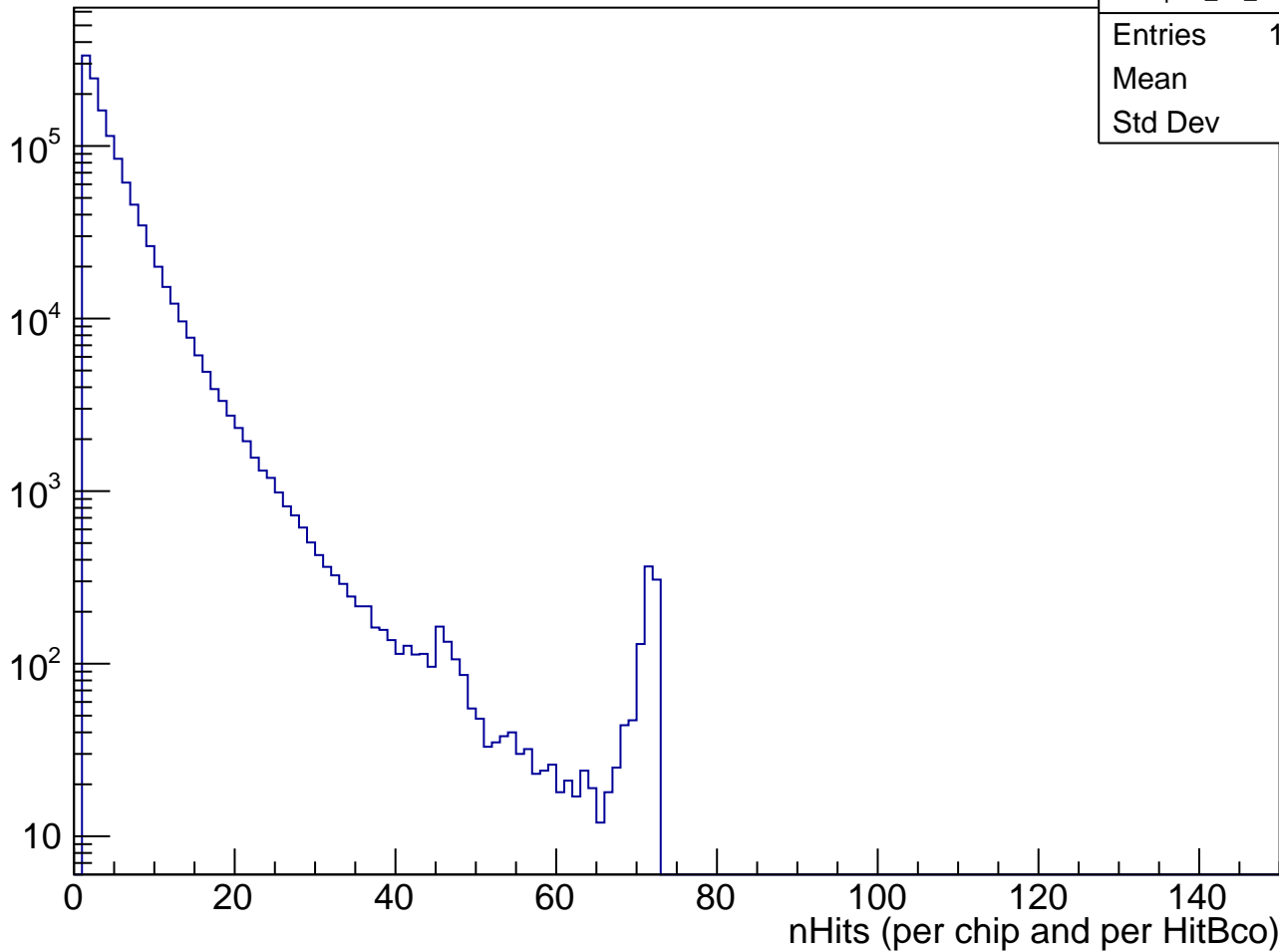


nChipHit_F3_Fch13_Chip9	
Entries	860402
Mean	2.625
Std Dev	3.086

nHits (per chip and per HitBco)

nChipHit_F3_Fch1_Chip0

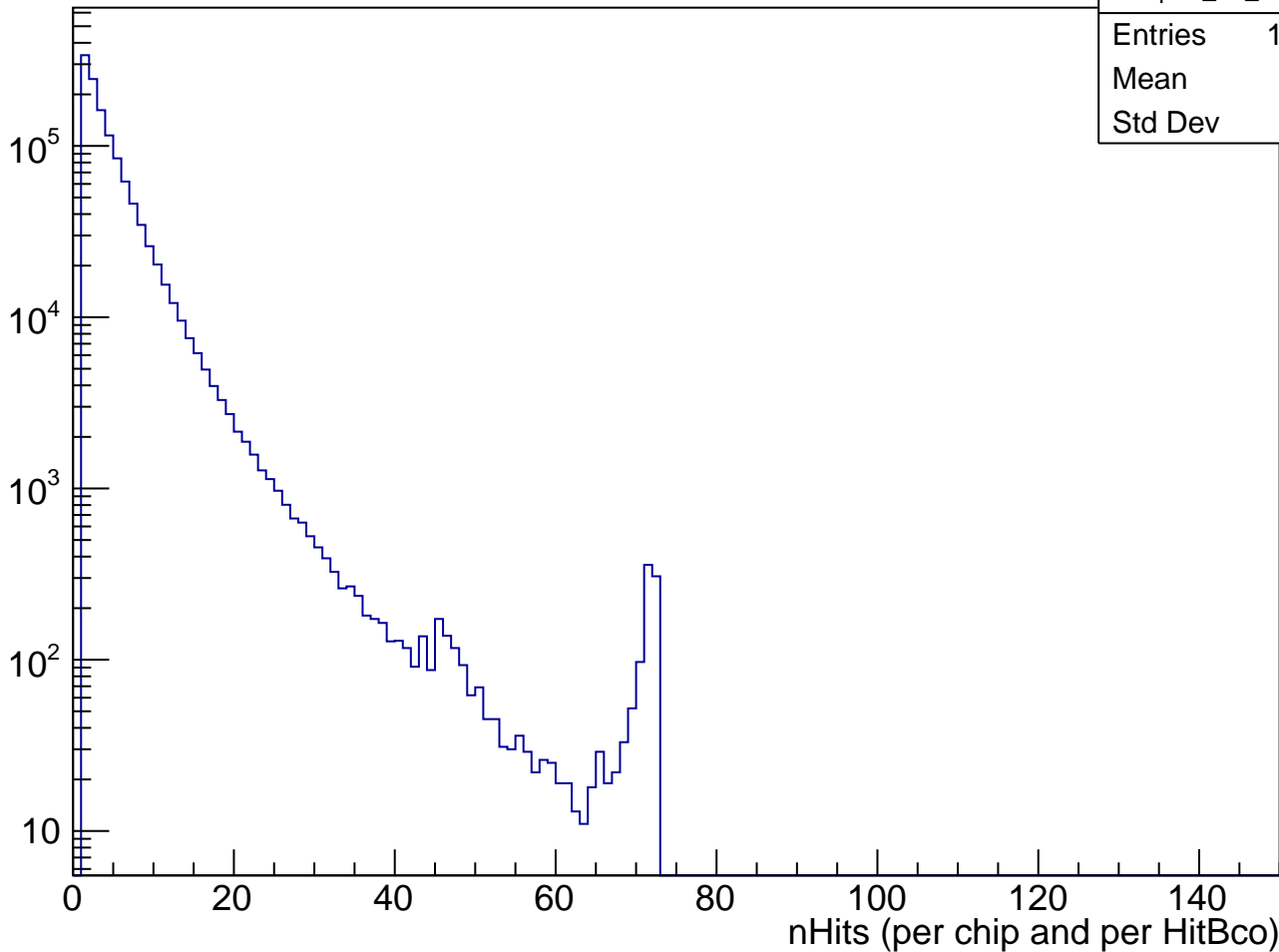
Entries



nChipHit_F3_Fch1_Chip0	
Entries	1210830
Mean	4.12
Std Dev	4.736

nChipHit_F3_Fch1_Chip1

Entries

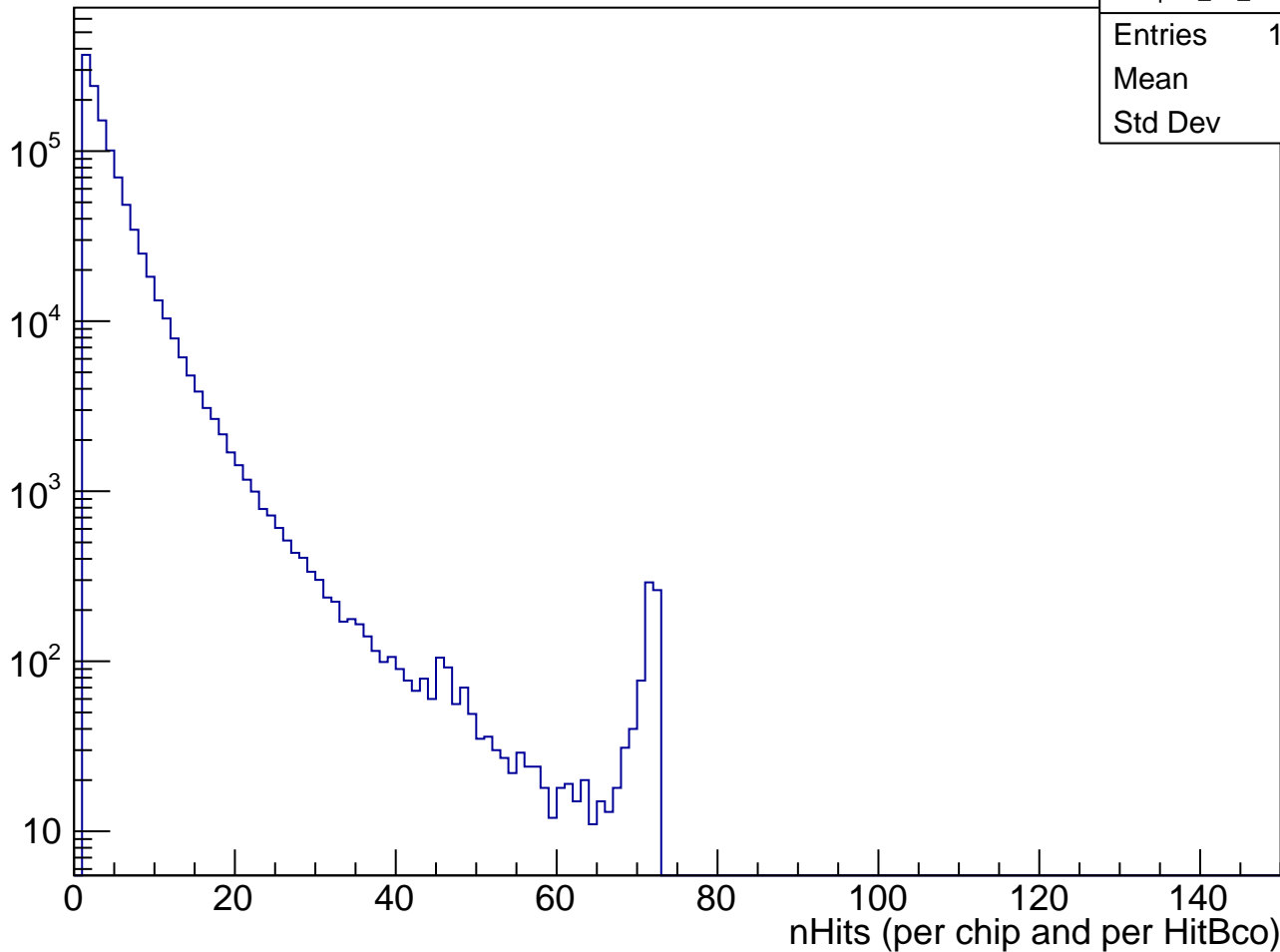


nChipHit_F3_Fch1_Chip1	
Entries	1217375
Mean	4.105
Std Dev	4.712

nHits (per chip and per HitBco)

nChipHit_F3_Fch1_Chip10

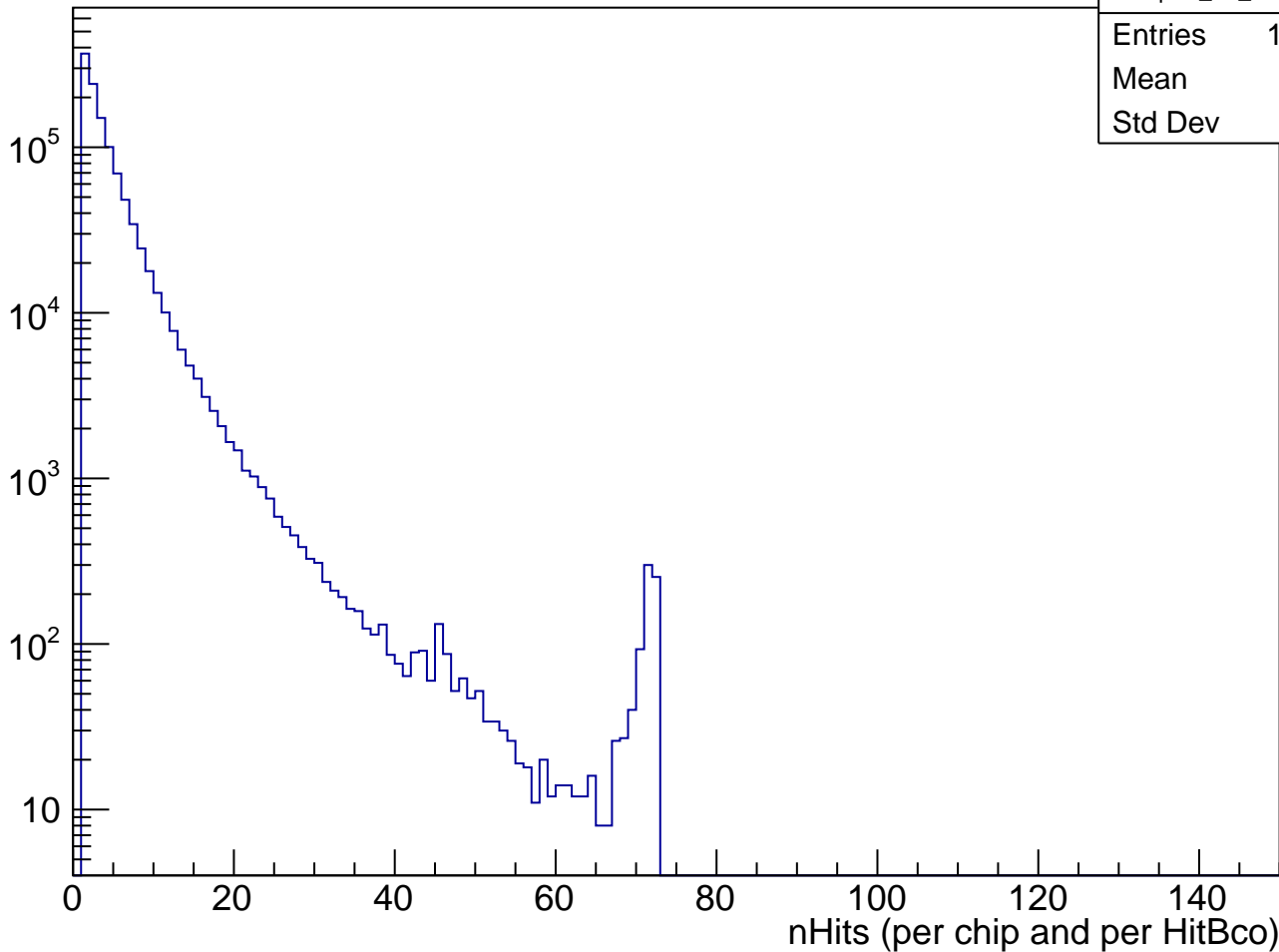
Entries



nChipHit_F3_Fch1_Chip10	
Entries	1125079
Mean	3.556
Std Dev	4.22

nChipHit_F3_Fch1_Chip11

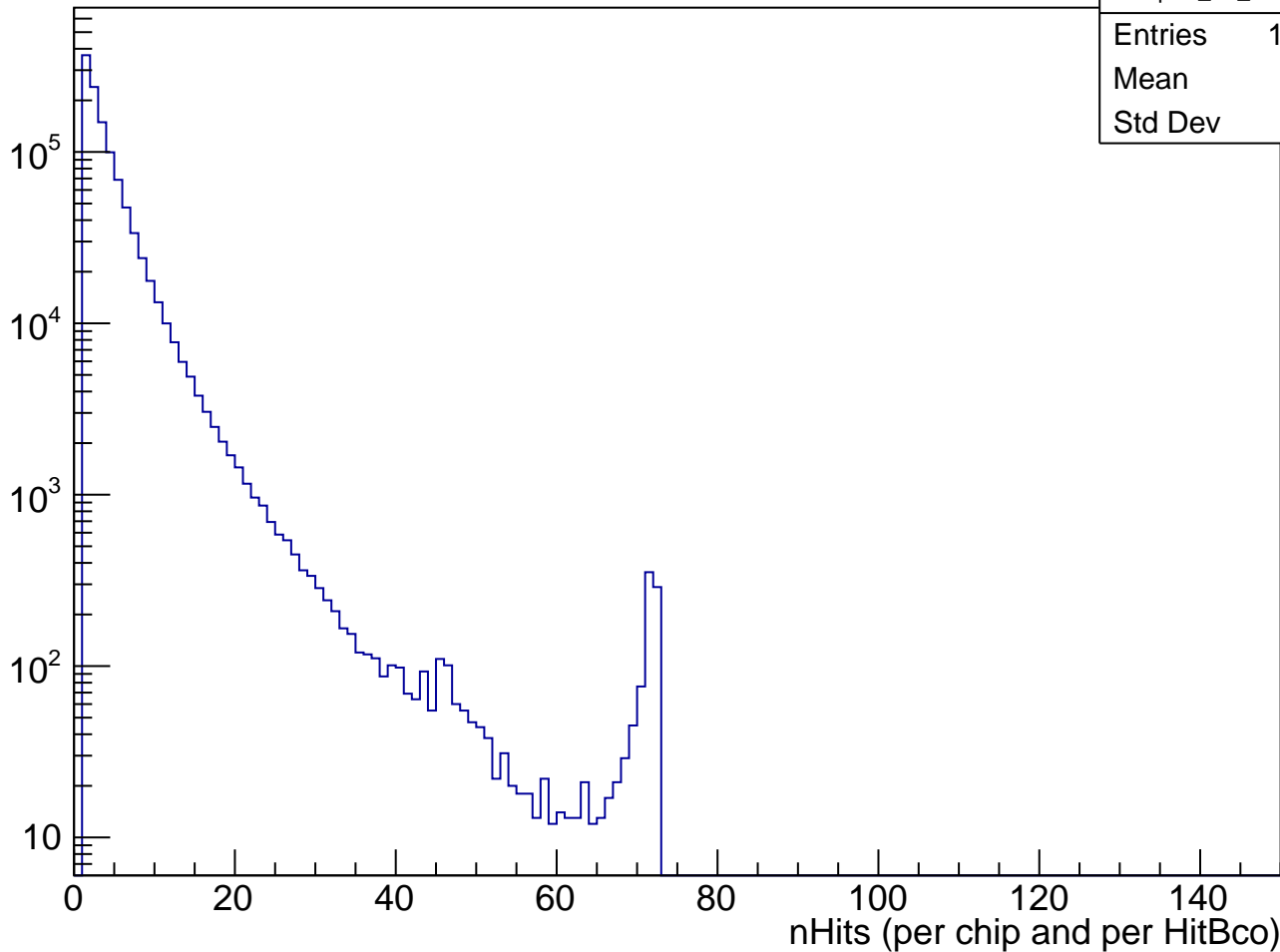
Entries



nChipHit_F3_Fch1_Chip11	
Entries	1120345
Mean	3.549
Std Dev	4.223

nChipHit_F3_Fch1_Chip12

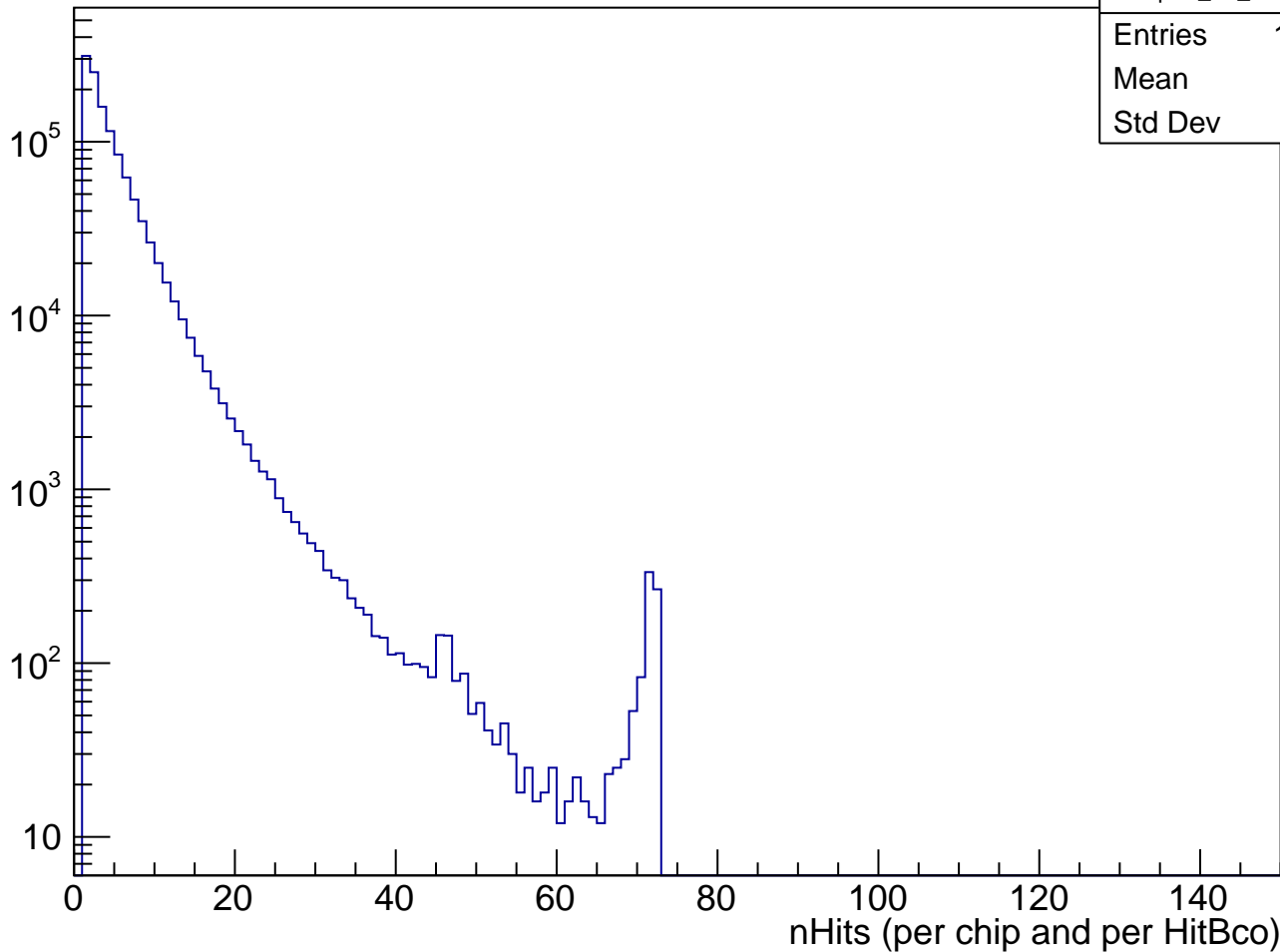
Entries



nChipHit_F3_Fch1_Chip12	
Entries	1112168
Mean	3.542
Std Dev	4.249

nChipHit_F3_Fch1_Chip13

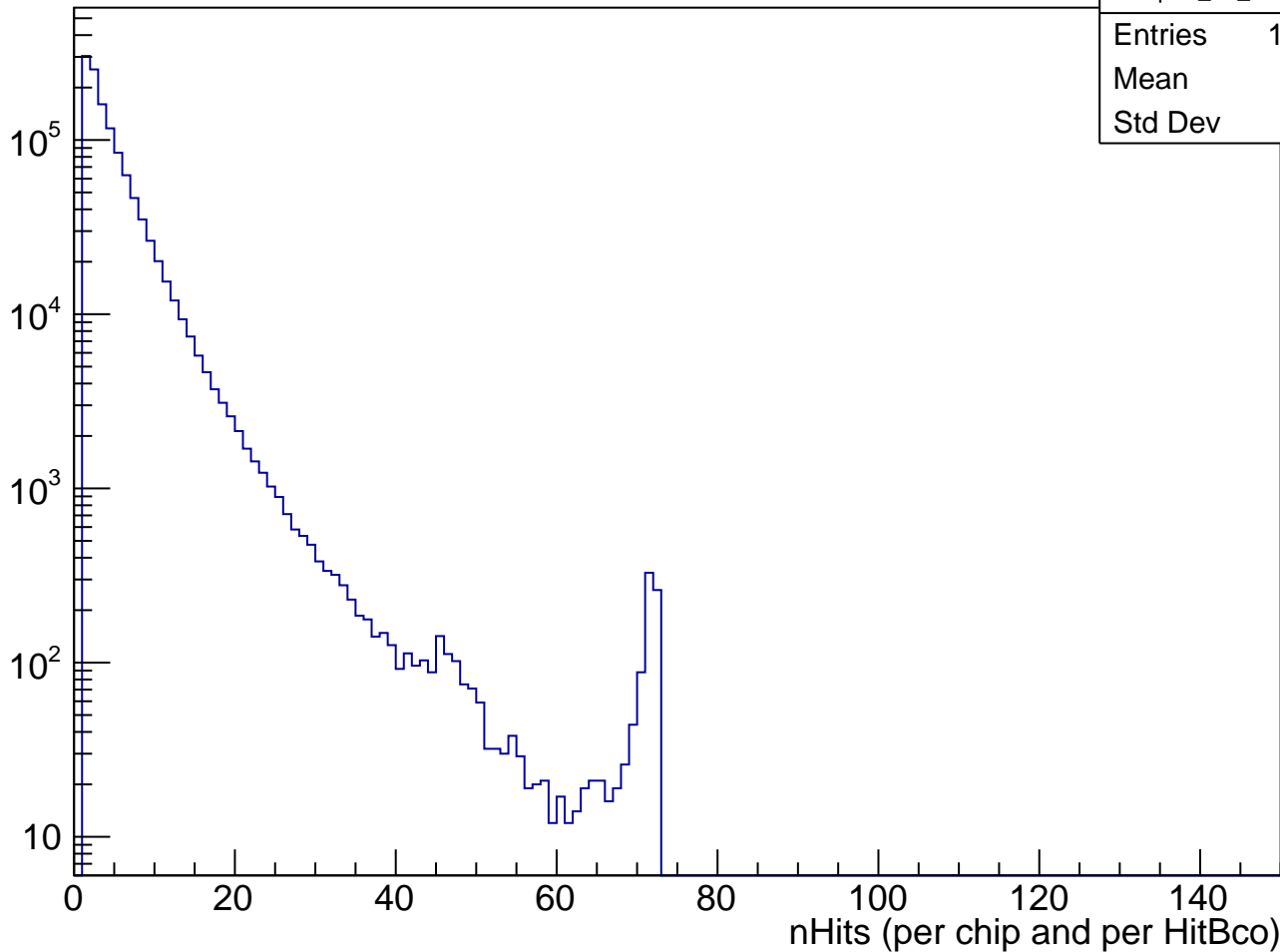
Entries



nChipHit_F3_Fch1_Chip13	
Entries	1192741
Mean	4.134
Std Dev	4.625

nChipHit_F3_Fch1_Chip14

Entries



nChipHit_F3_Fch1_Chip14

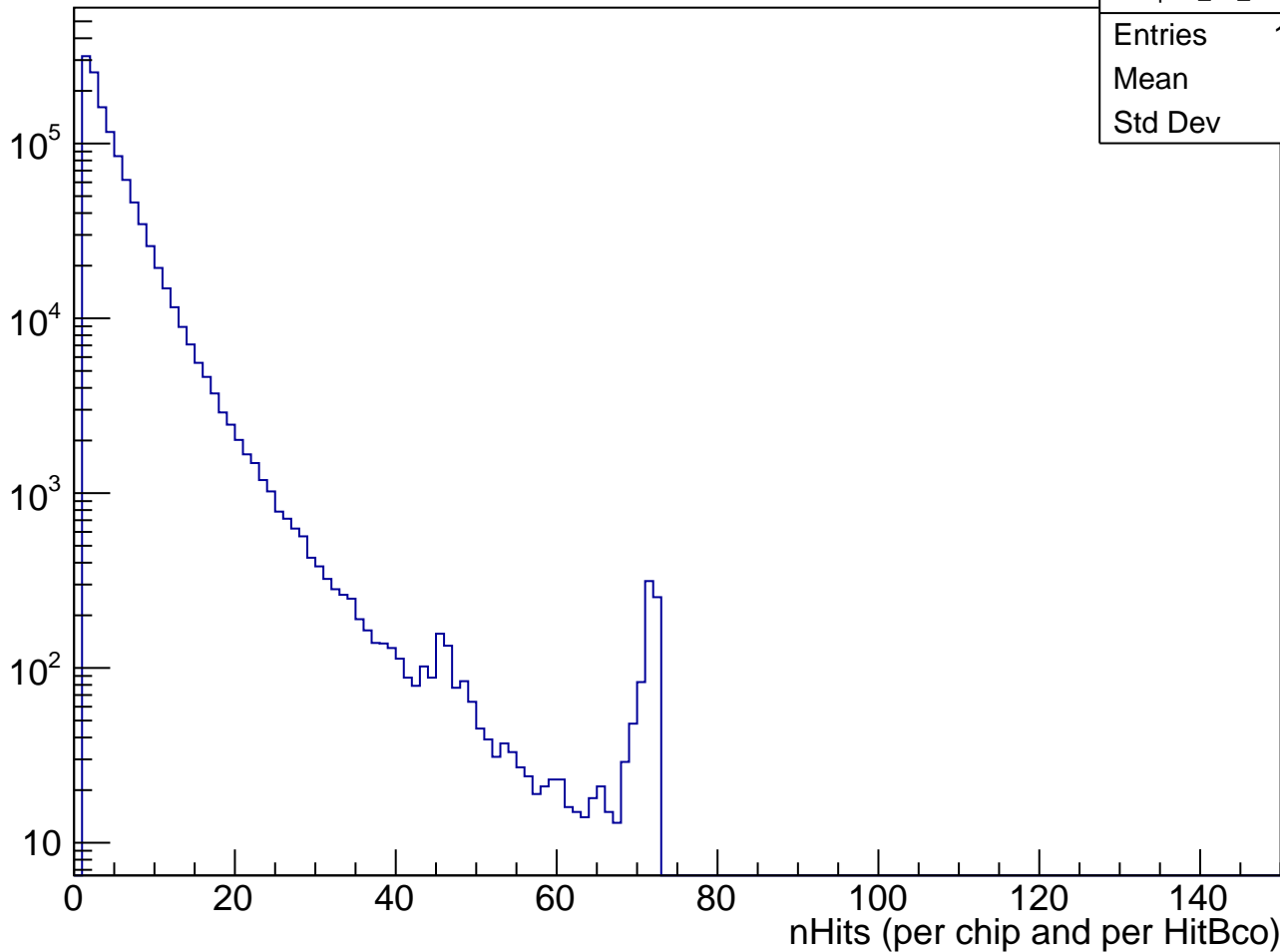
Entries 1189305

Mean 4.136

Std Dev 4.587

nChipHit_F3_Fch1_Chip15

Entries

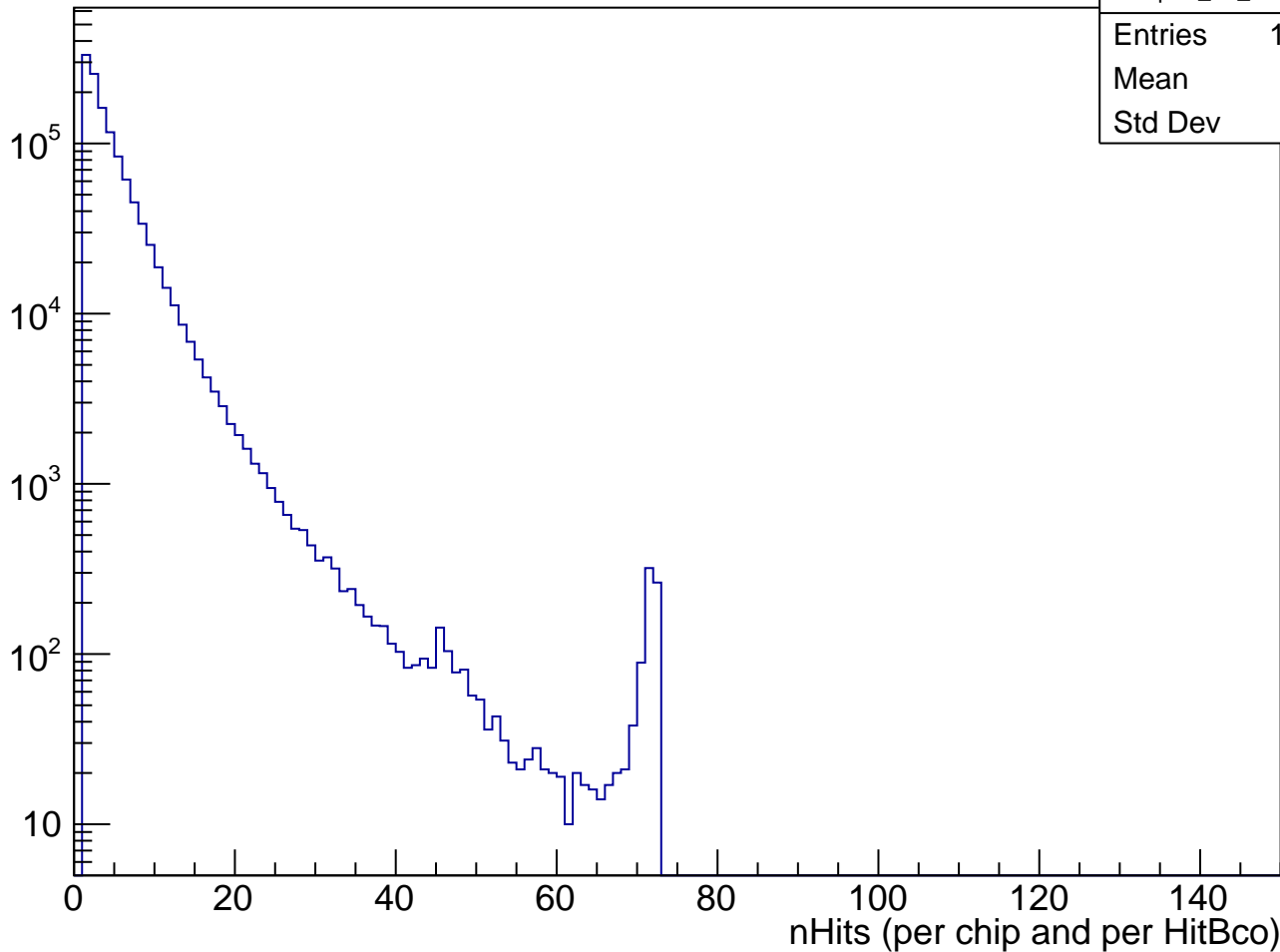


nChipHit_F3_Fch1_Chip15	
Entries	1198001
Mean	4.067
Std Dev	4.537

nHits (per chip and per HitBco)

nChipHit_F3_Fch1_Chip16

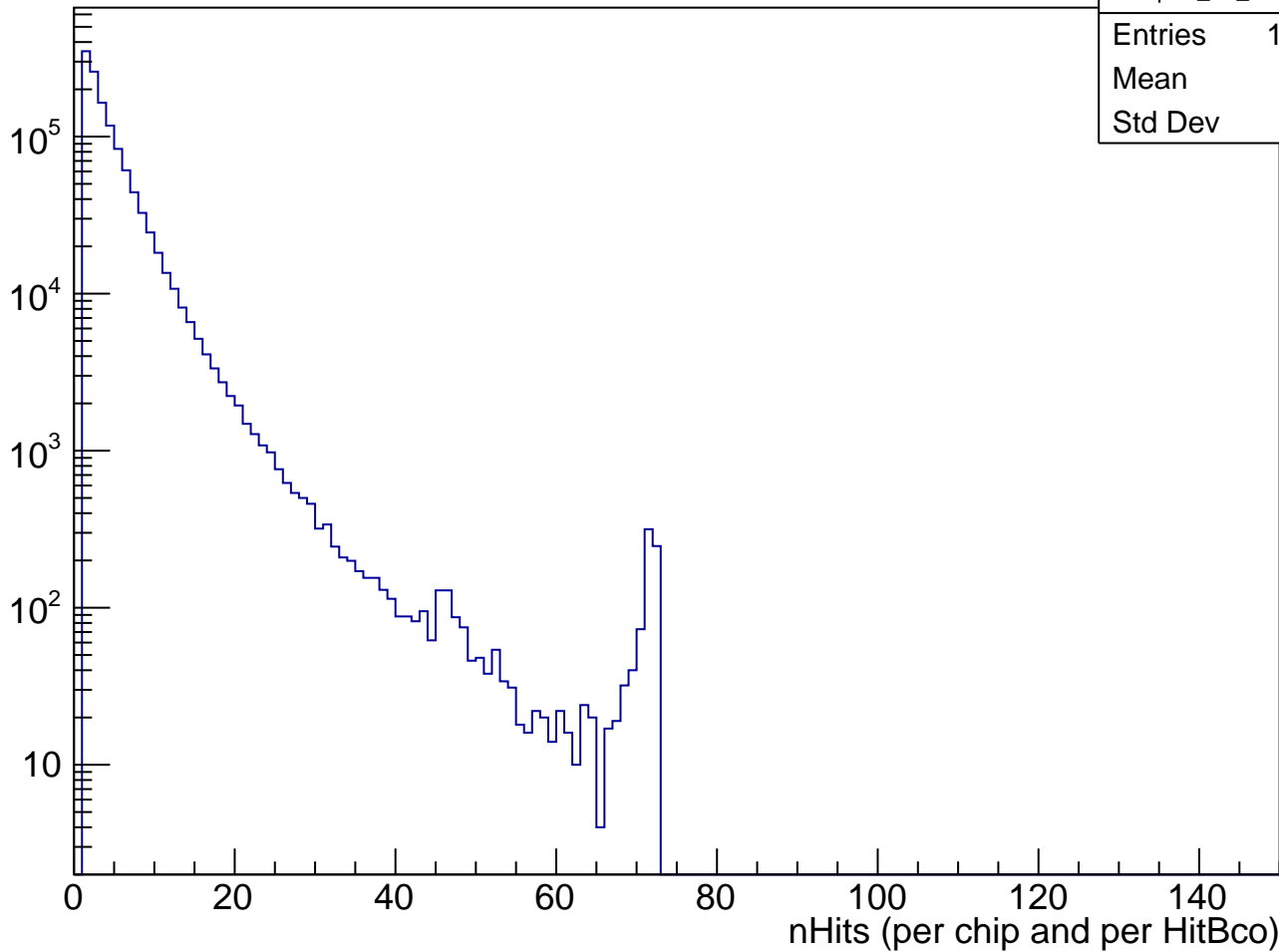
Entries



nChipHit_F3_Fch1_Chip16	
Entries	1207550
Mean	3.98
Std Dev	4.473

nChipHit_F3_Fch1_Chip17

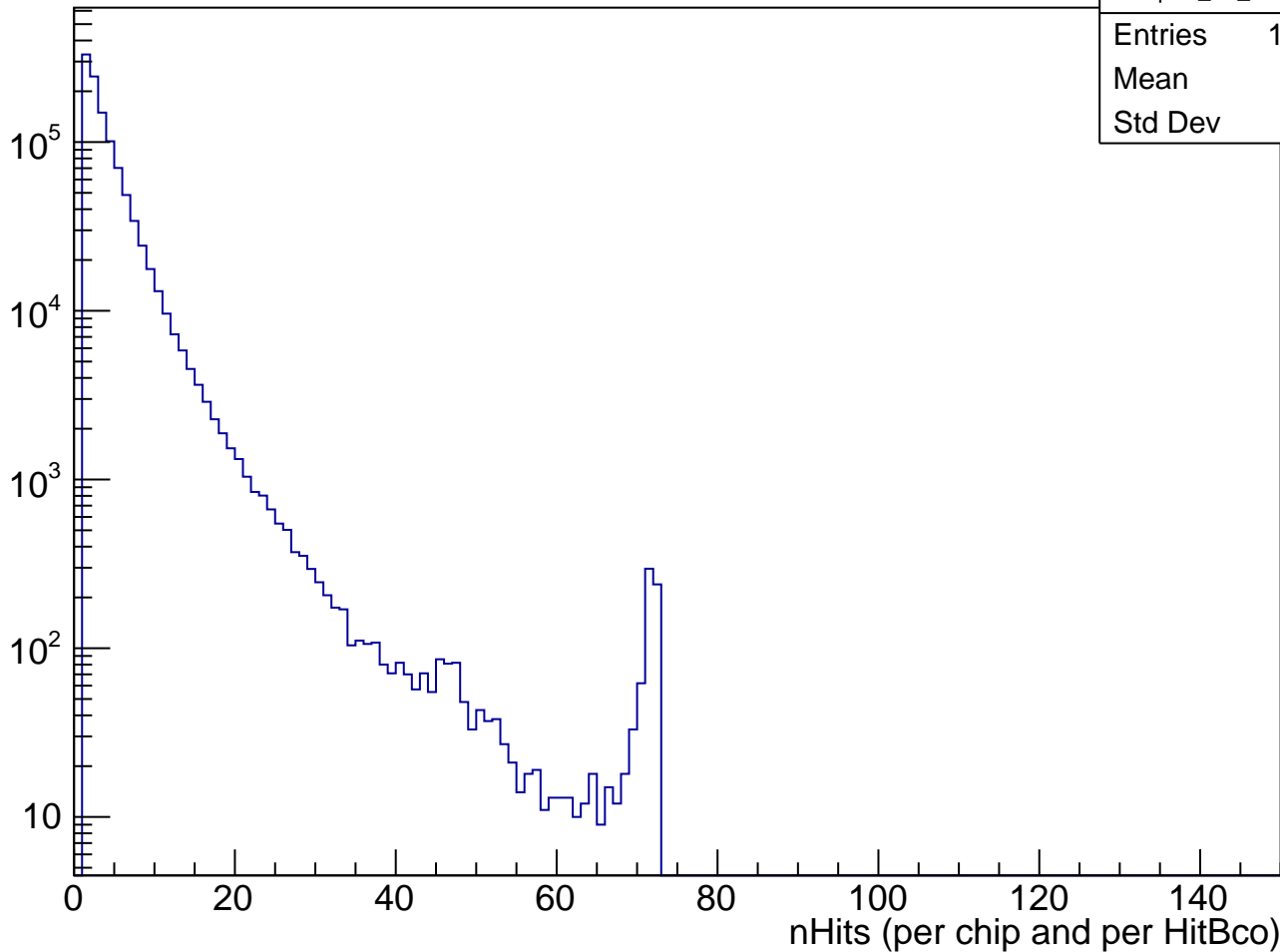
Entries



nChipHit_F3_Fch1_Chip17	
Entries	1224935
Mean	3.886
Std Dev	4.385

nChipHit_F3_Fch1_Chip18

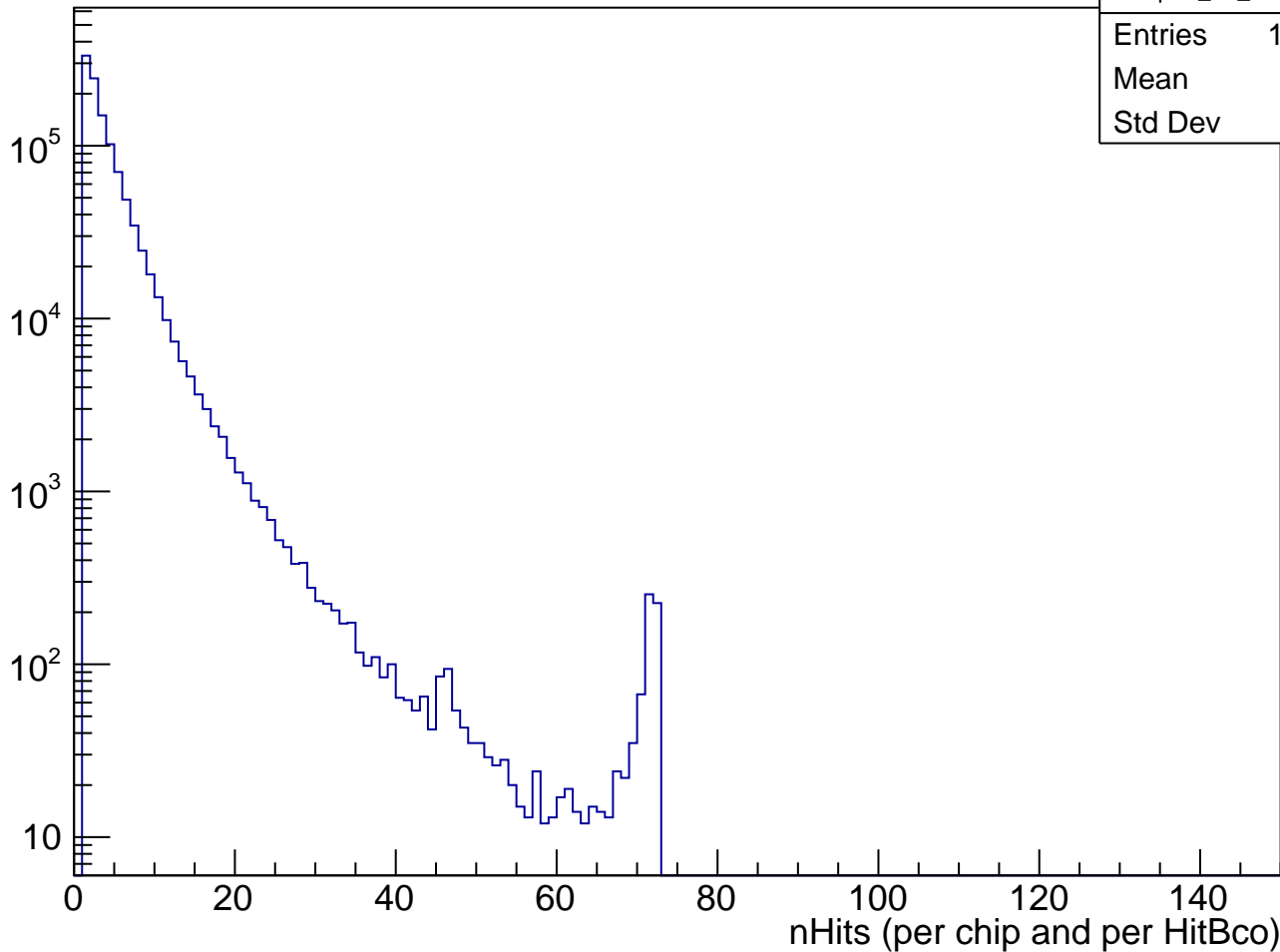
Entries



nChipHit_F3_Fch1_Chip18	
Entries	1082918
Mean	3.573
Std Dev	4.108

nChipHit_F3_Fch1_Chip19

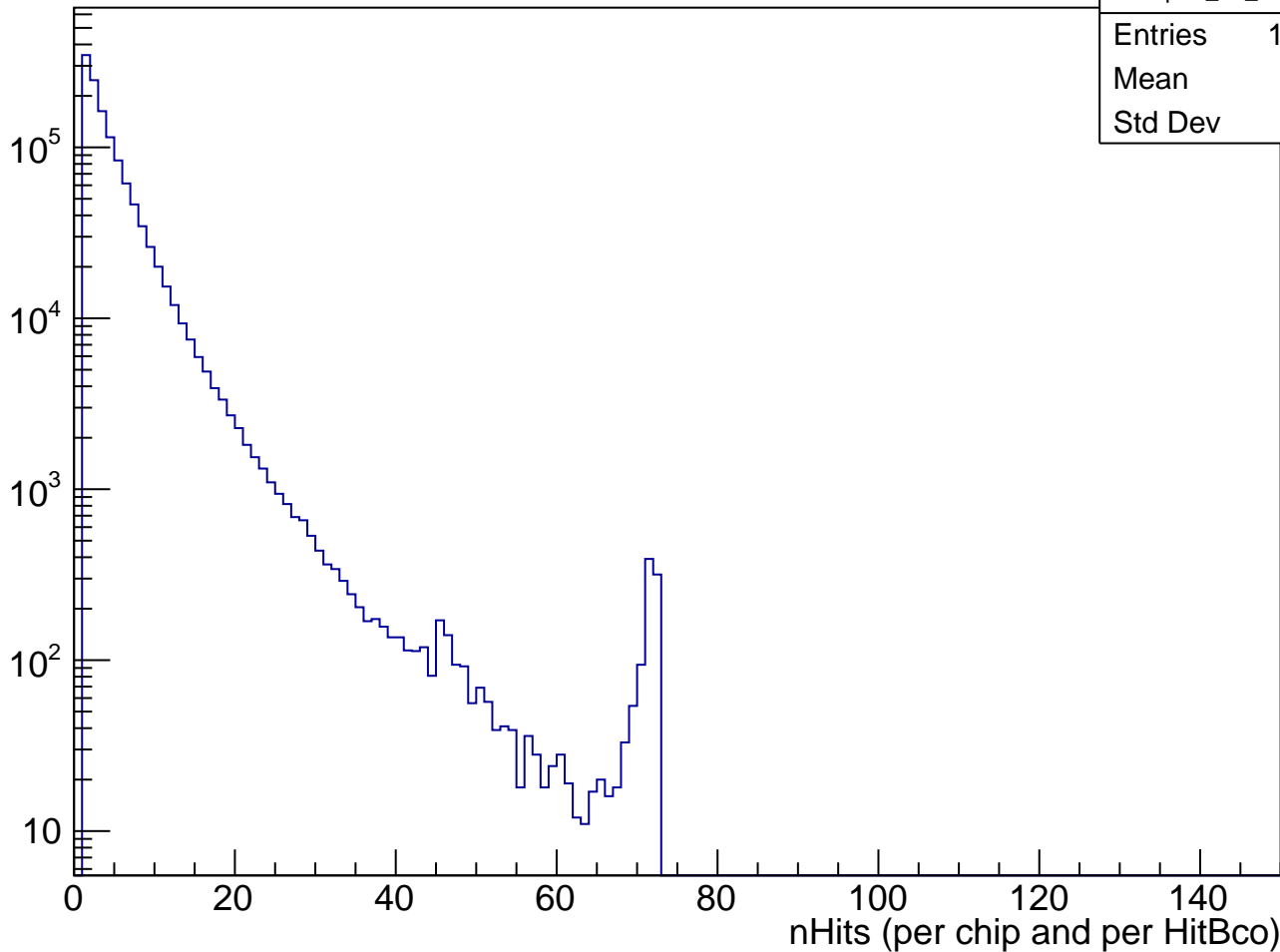
Entries



nChipHit_F3_Fch1_Chip19	
Entries	1089227
Mean	3.583
Std Dev	4.1

nChipHit_F3_Fch1_Chip2

Entries



nChipHit_F3_Fch1_Chip2

Entries 1225122

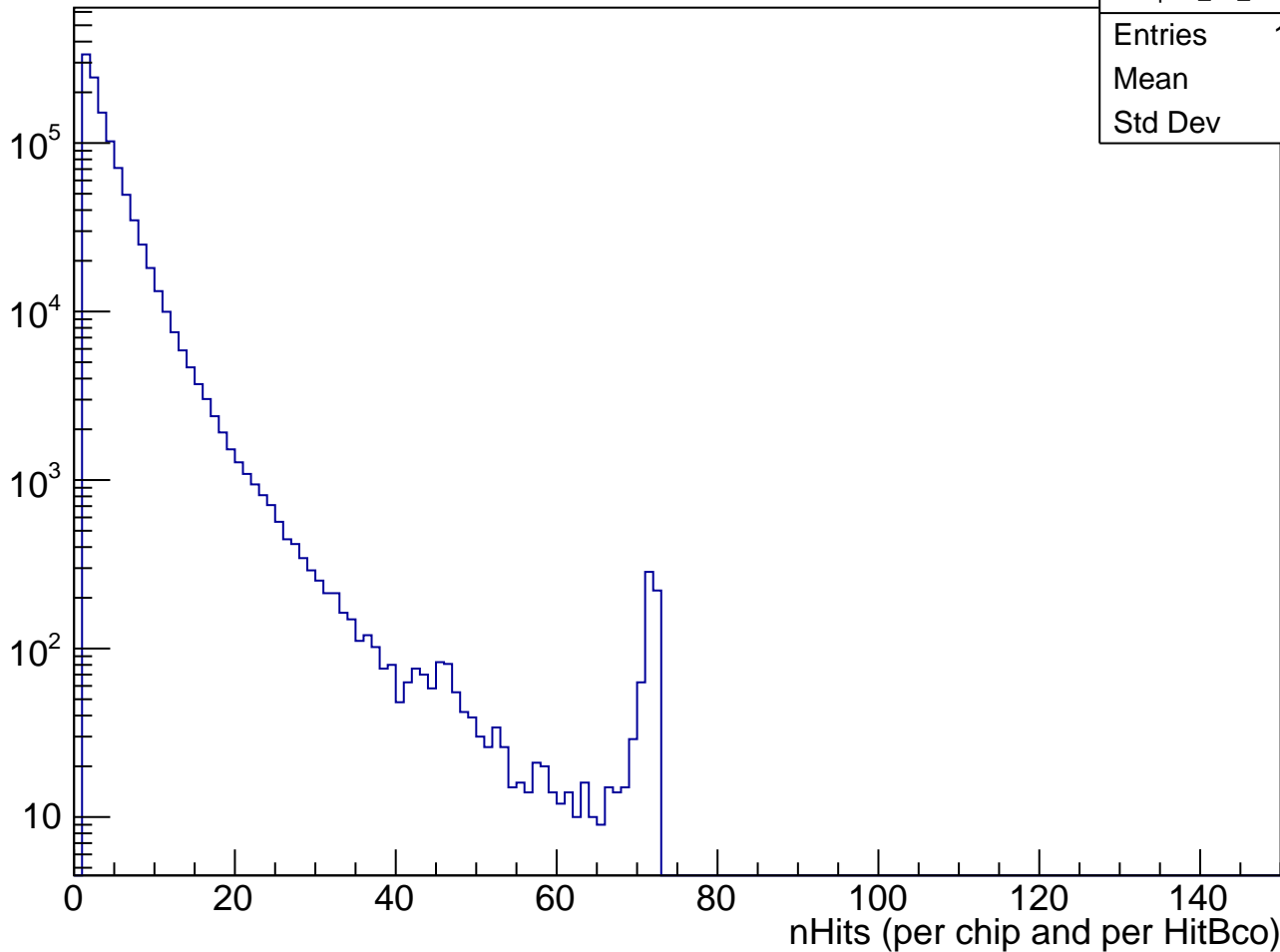
Mean 4.075

Std Dev 4.704

nHits (per chip and per HitBco)

nChipHit_F3_Fch1_Chip20

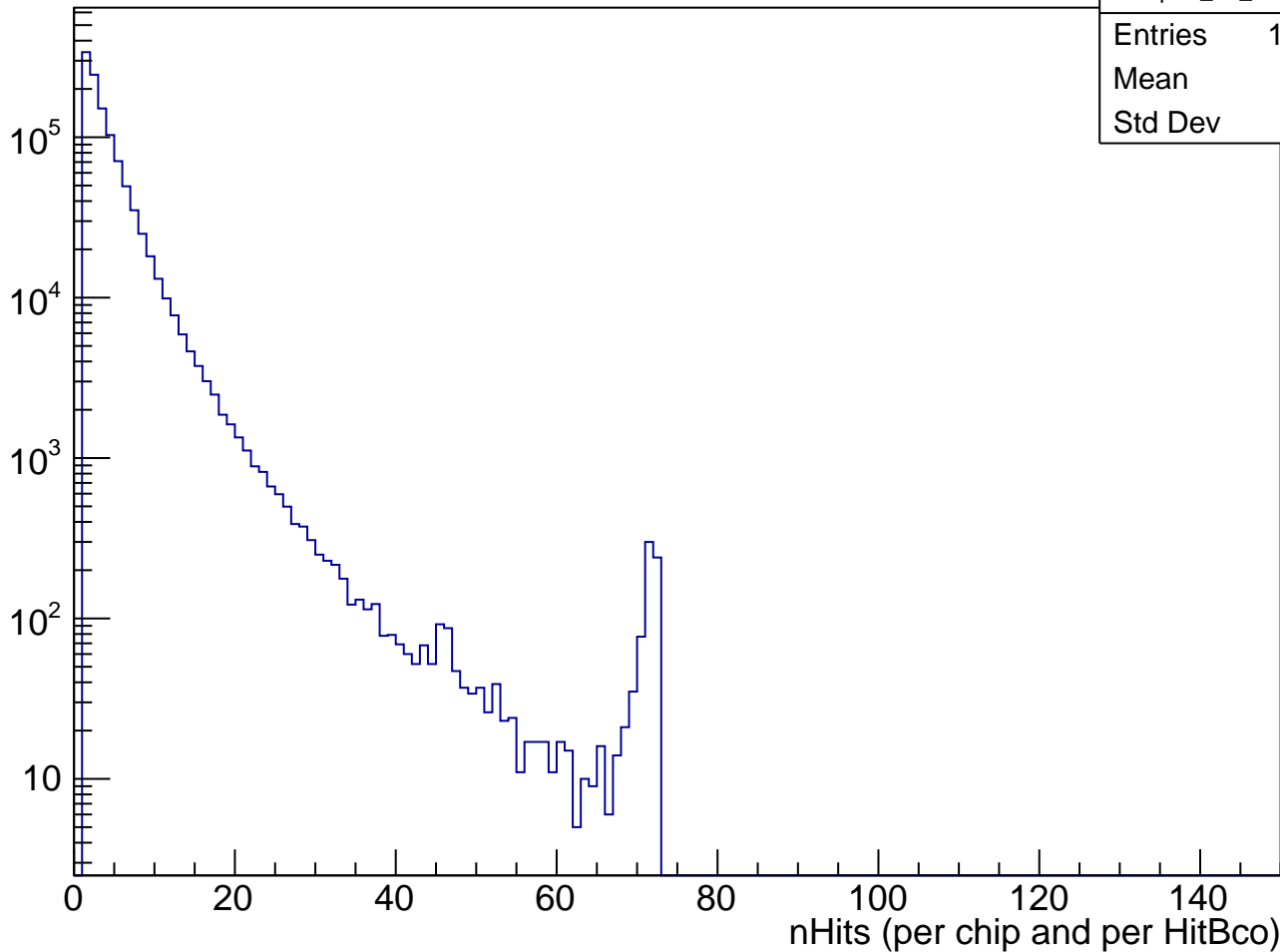
Entries



nChipHit_F3_Fch1_Chip20	
Entries	1096661
Mean	3.581
Std Dev	4.088

nChipHit_F3_Fch1_Chip21

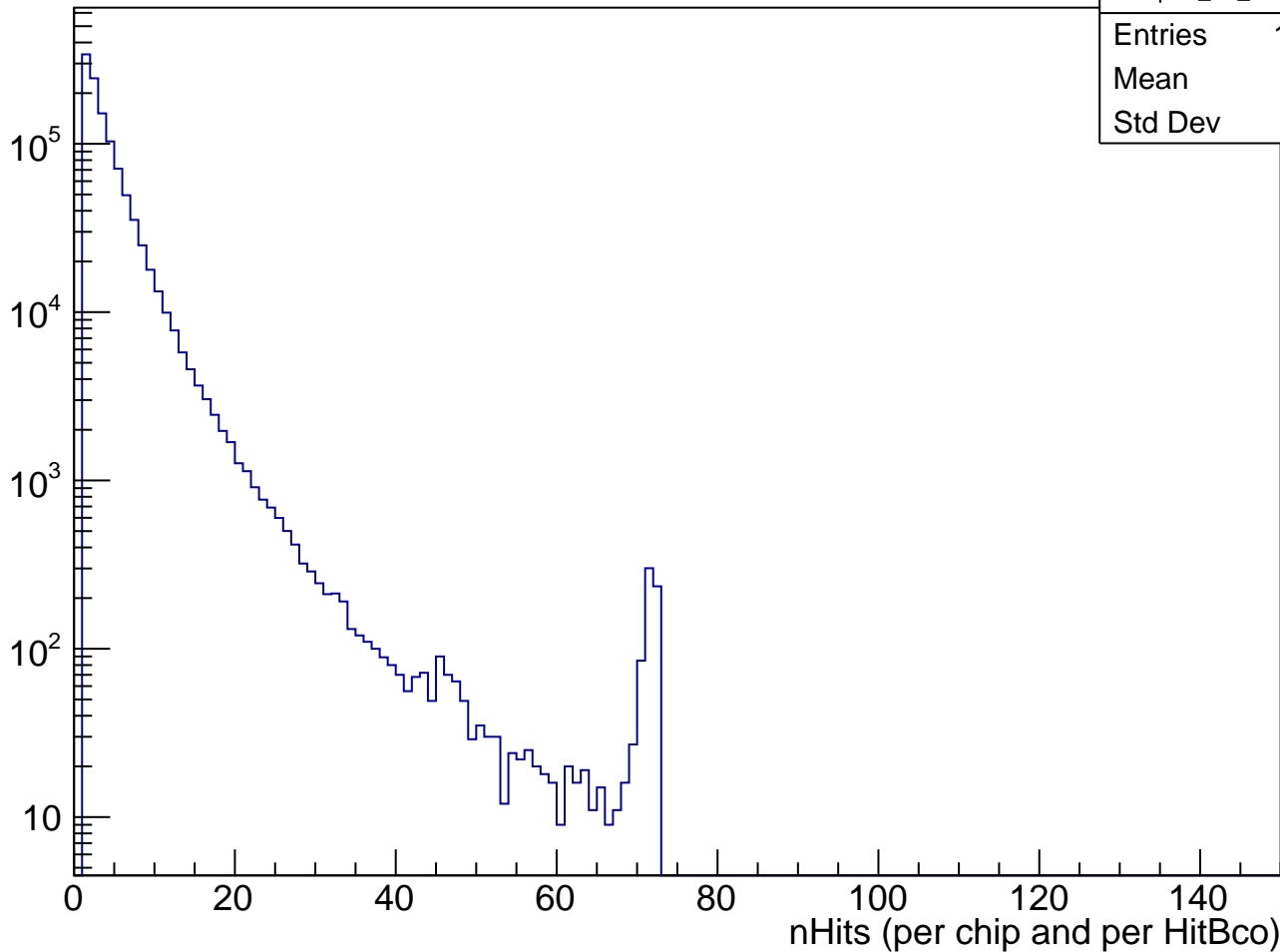
Entries



nChipHit_F3_Fch1_Chip21	
Entries	1101402
Mean	3.583
Std Dev	4.119

nChipHit_F3_Fch1_Chip22

Entries

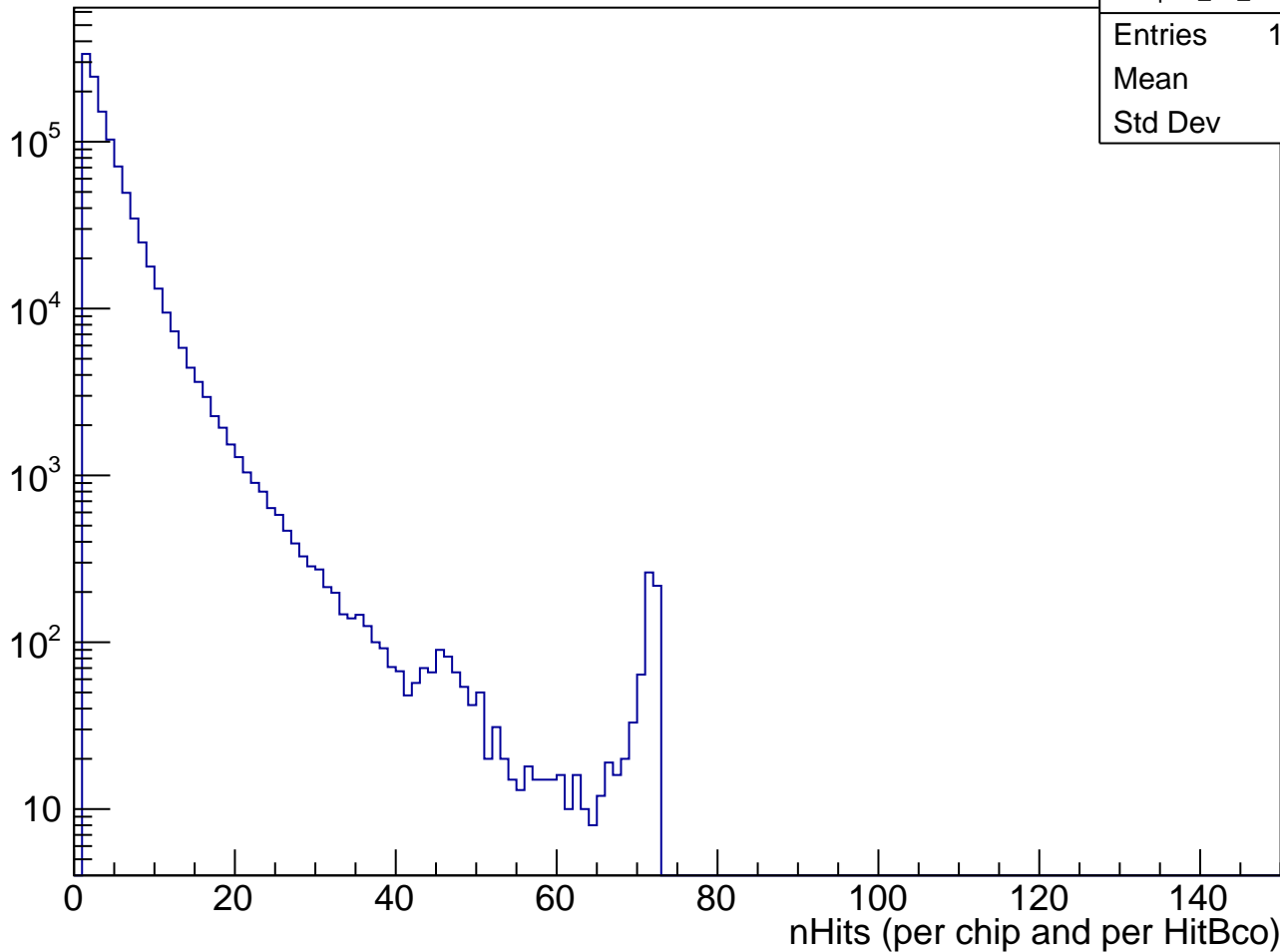


nChipHit_F3_Fch1_Chip22	
Entries	1103091
Mean	3.581
Std Dev	4.121

nHits (per chip and per HitBco)

nChipHit_F3_Fch1_Chip23

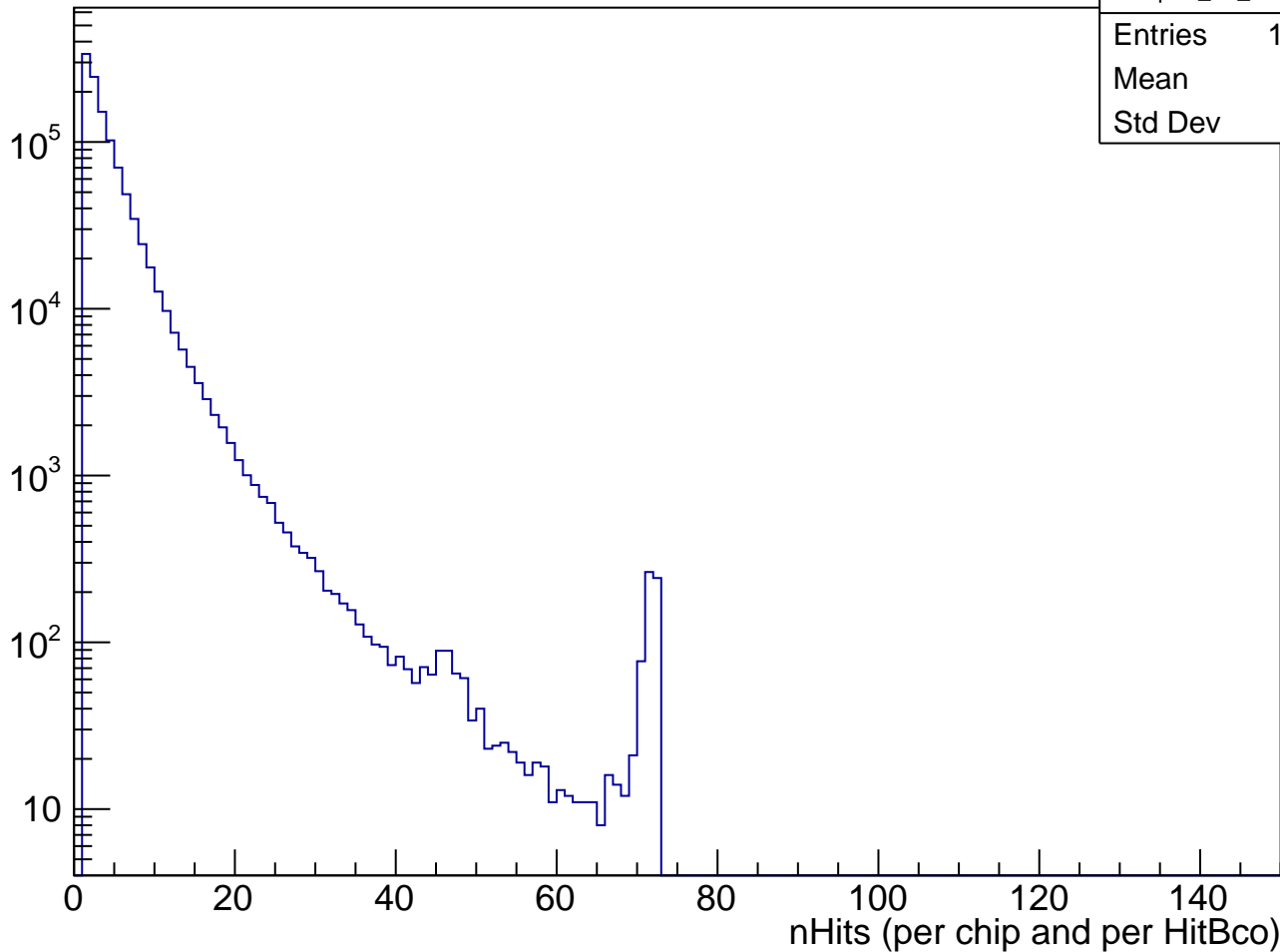
Entries



nChipHit_F3_Fch1_Chip23	
Entries	1096068
Mean	3.566
Std Dev	4.068

nChipHit_F3_Fch1_Chip24

Entries



nChipHit_F3_Fch1_Chip24

Entries 1094422

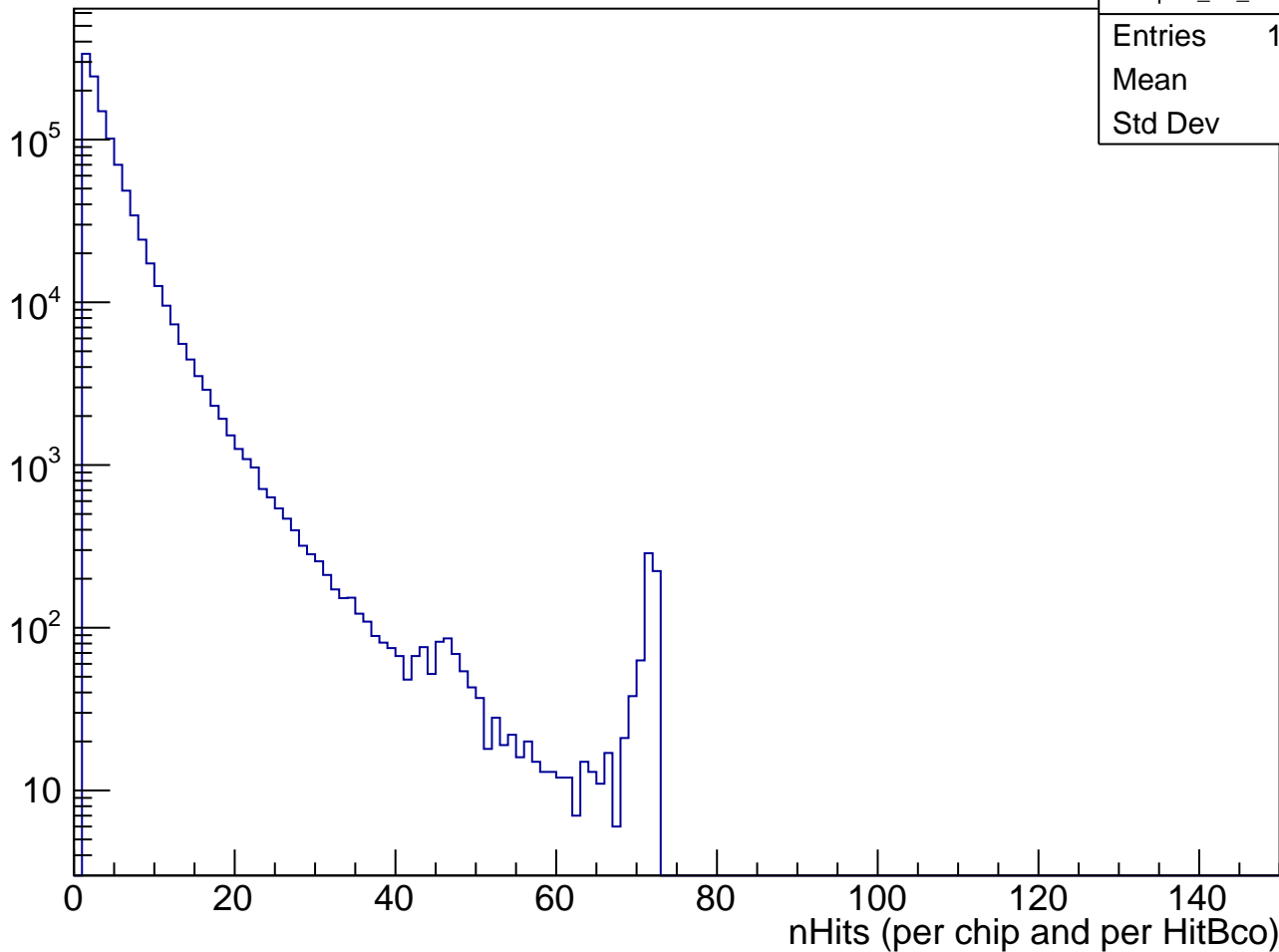
Mean 3.552

Std Dev 4.076

nHits (per chip and per HitBco)

nChipHit_F3_Fch1_Chip25

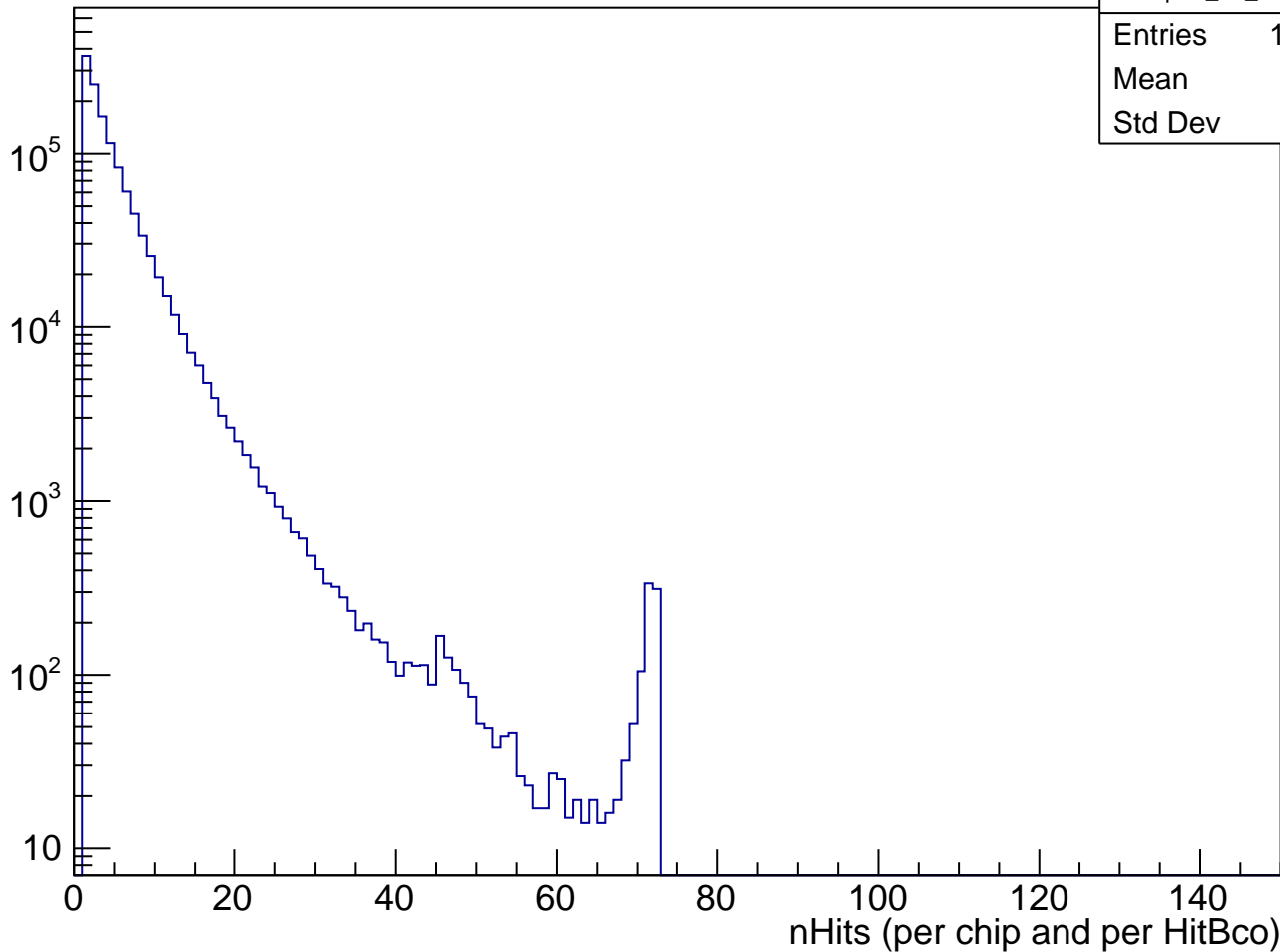
Entries



nChipHit_F3_Fch1_Chip25	
Entries	1087512
Mean	3.547
Std Dev	4.071

nChipHit_F3_Fch1_Chip3

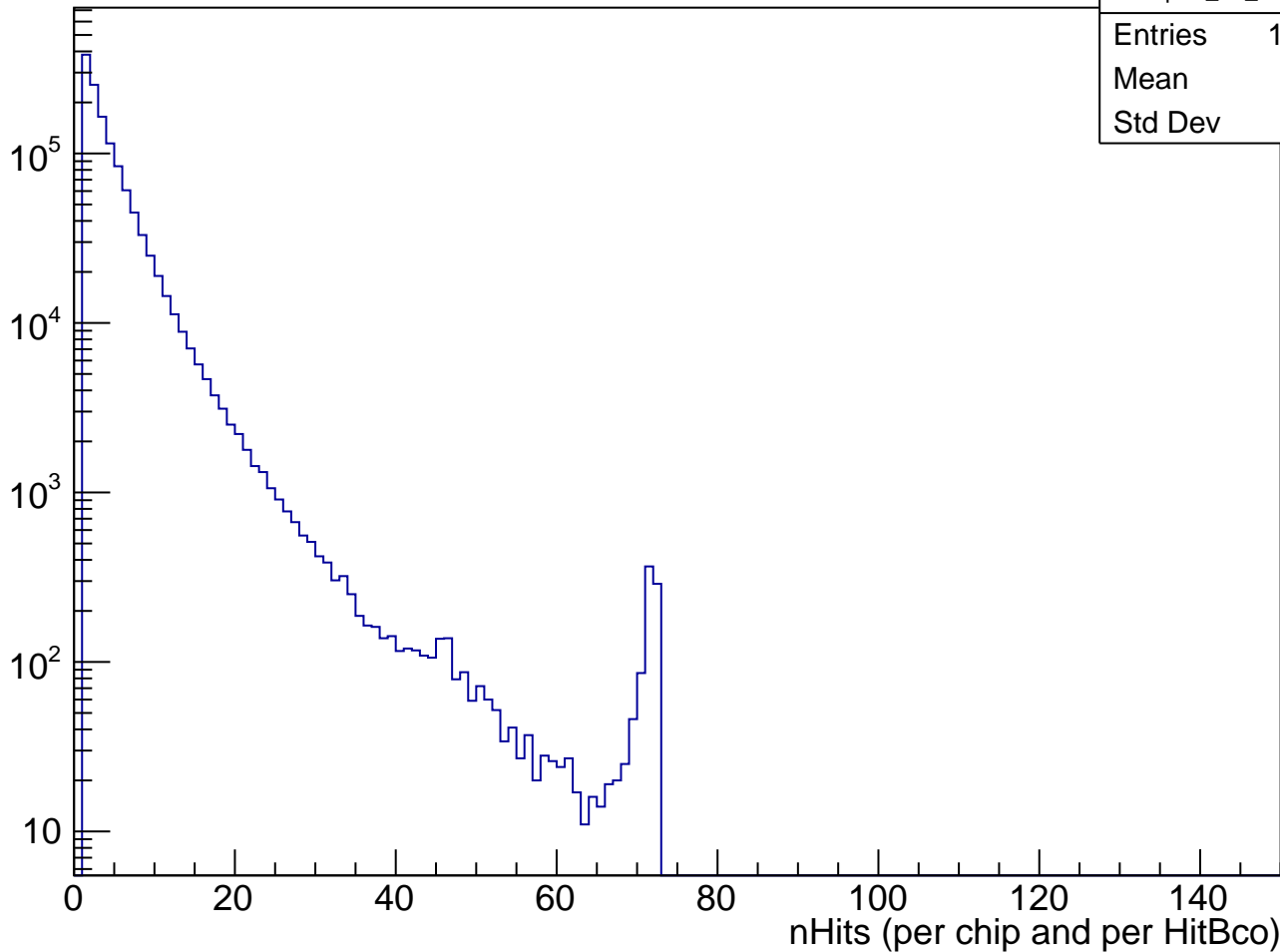
Entries



nChipHit_F3_Fch1_Chip3	
Entries	1239400
Mean	3.99
Std Dev	4.625

nChipHit_F3_Fch1_Chip4

Entries



nChipHit_F3_Fch1_Chip4

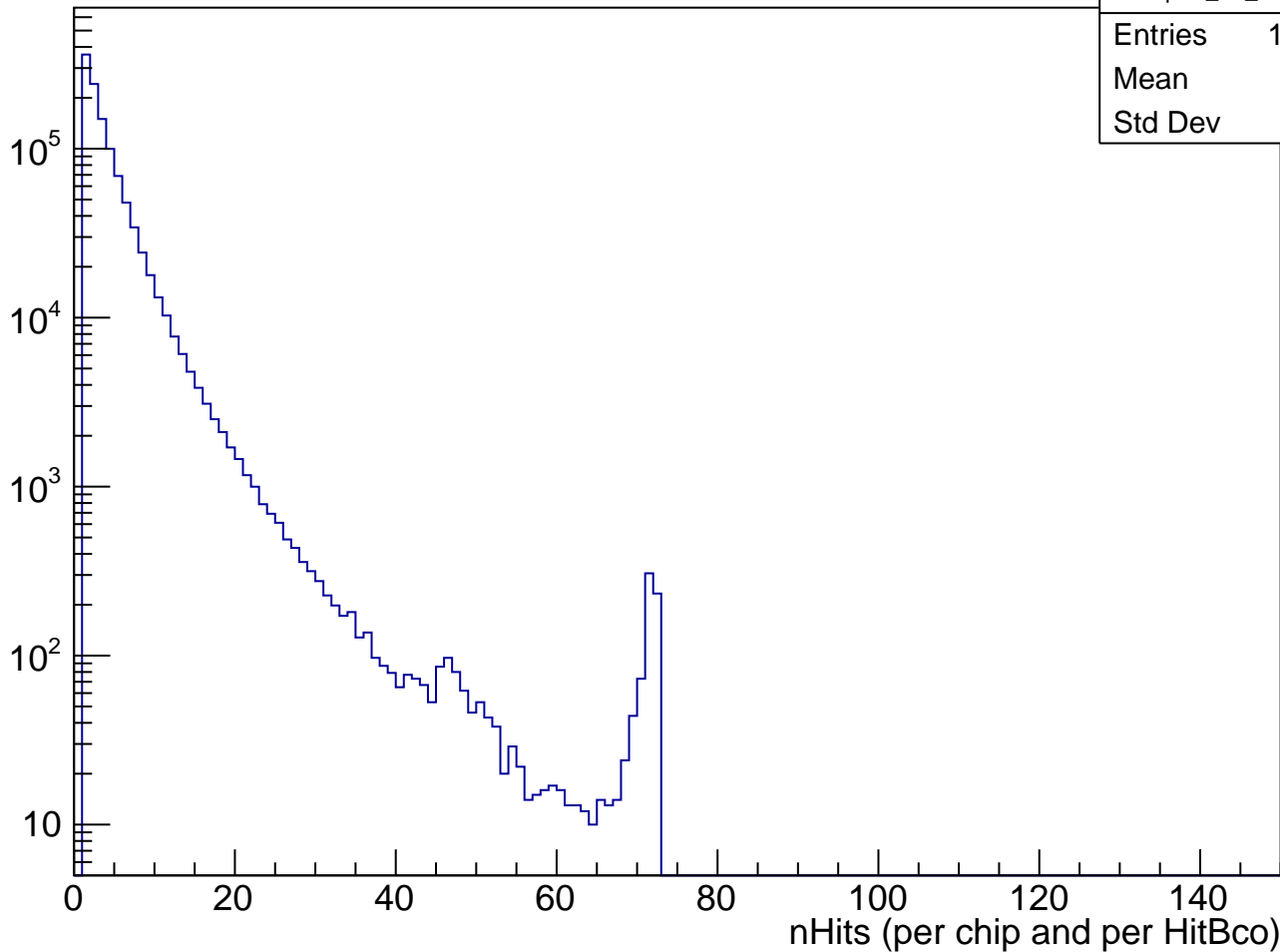
Entries 1259964

Mean 3.915

Std Dev 4.581

nChipHit_F3_Fch1_Chip5

Entries



nChipHit_F3_Fch1_Chip5

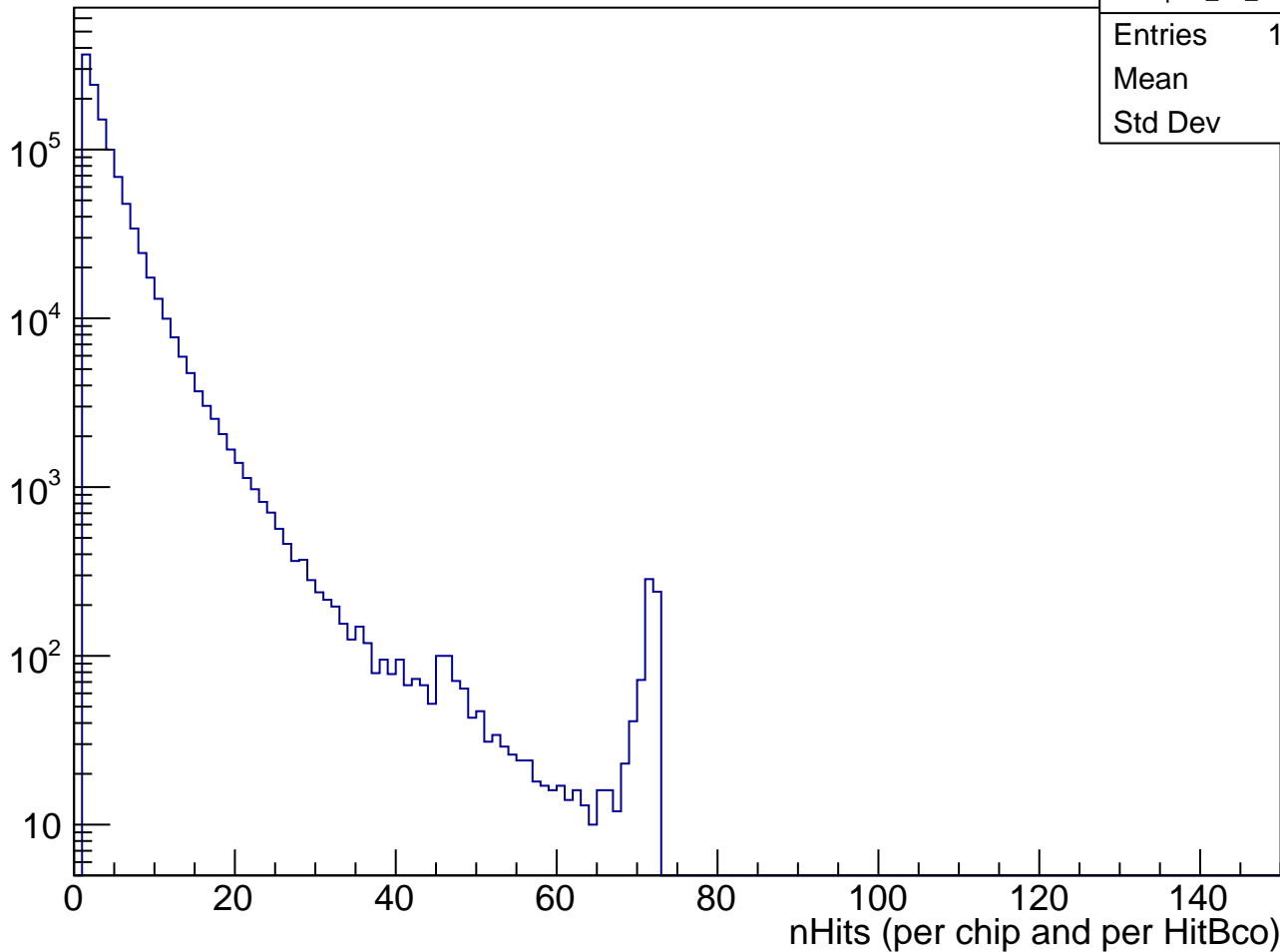
Entries 1110947

Mean 3.552

Std Dev 4.186

nChipHit_F3_Fch1_Chip6

Entries



nChipHit_F3_Fch1_Chip6

Entries 1115012

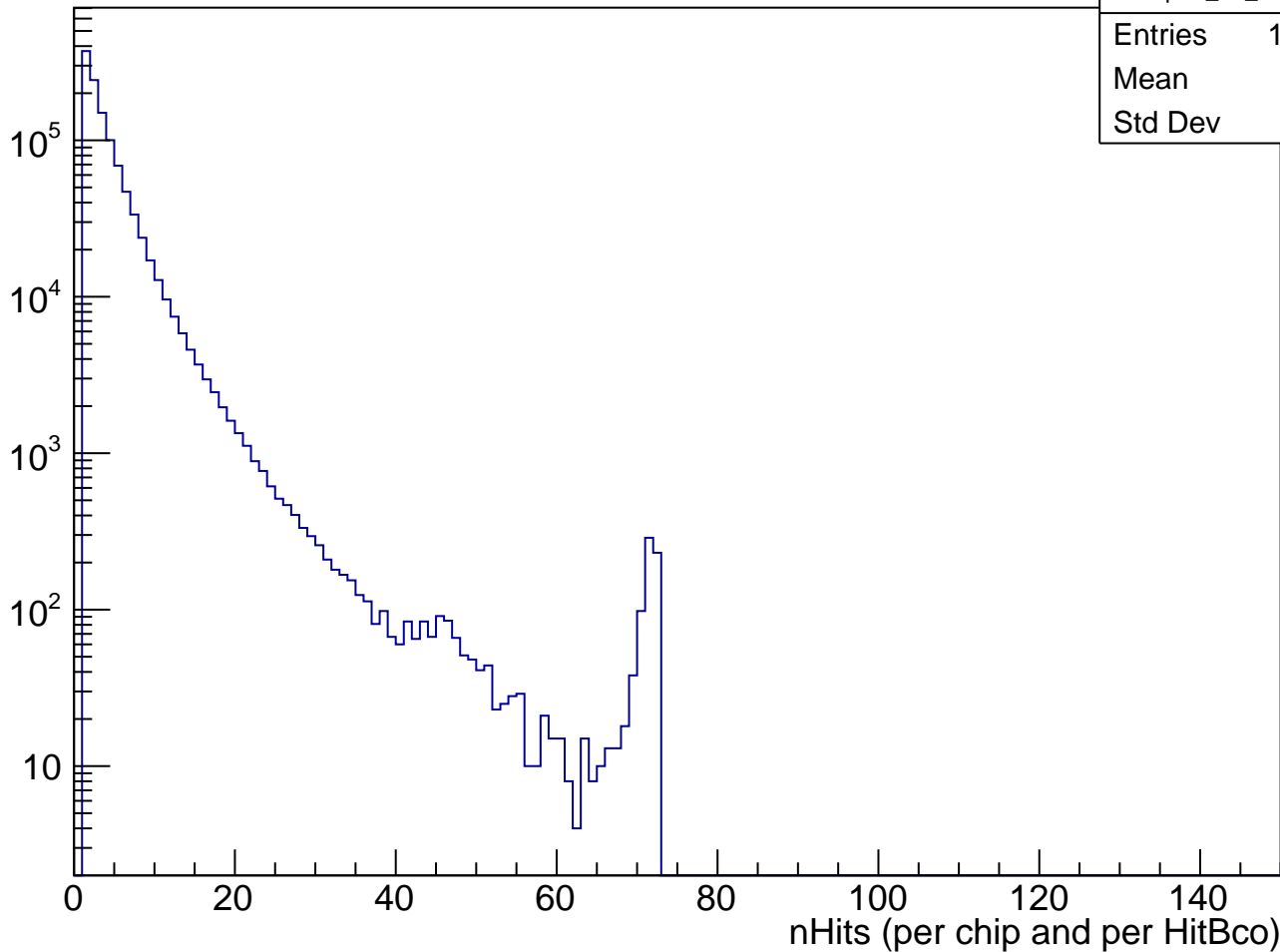
Mean 3.52

Std Dev 4.146

nHits (per chip and per HitBco)

nChipHit_F3_Fch1_Chip7

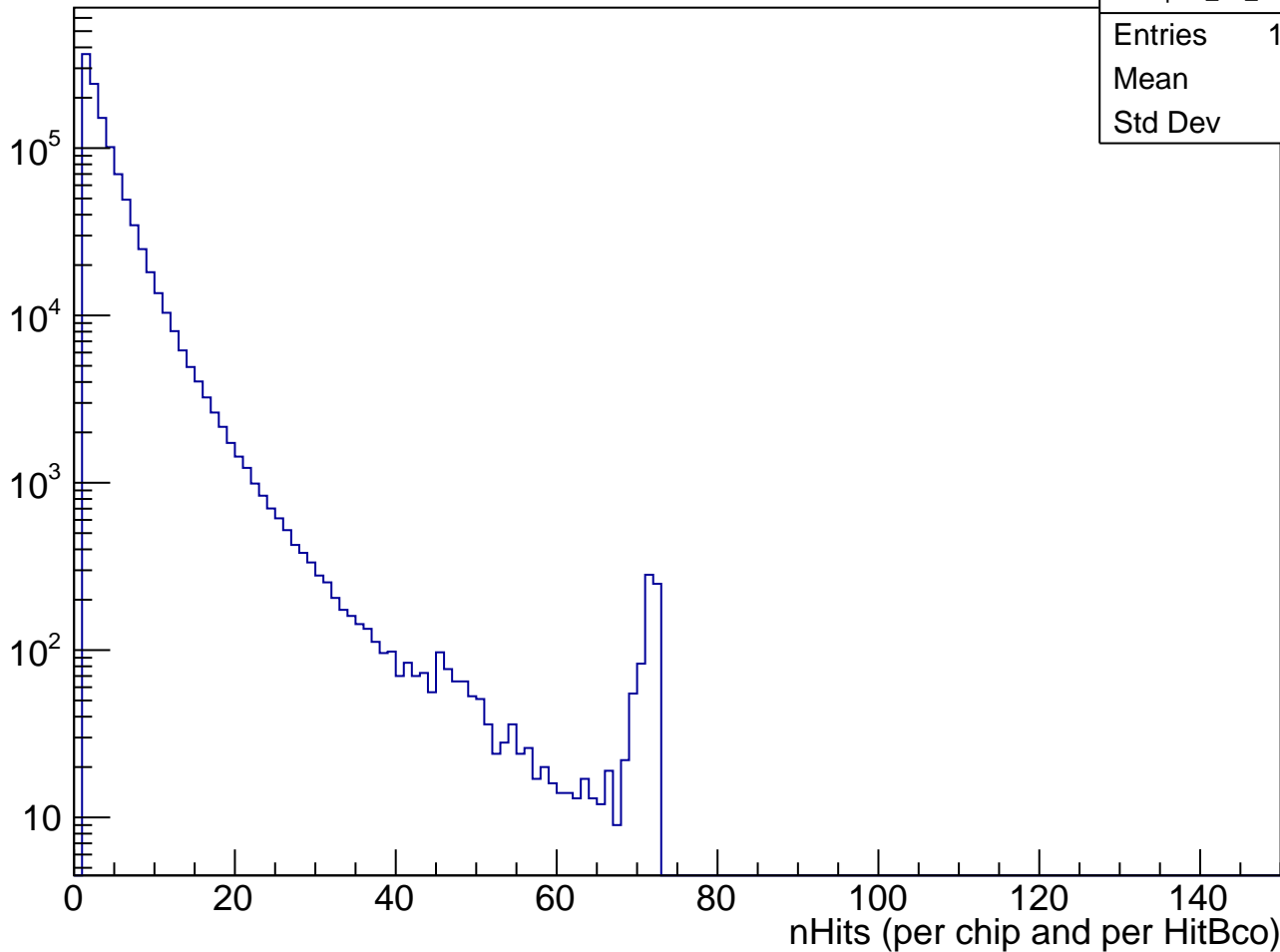
Entries



nChipHit_F3_Fch1_Chip7	
Entries	1117662
Mean	3.479
Std Dev	4.097

nChipHit_F3_Fch1_Chip8

Entries



nChipHit_F3_Fch1_Chip8

Entries 1124167

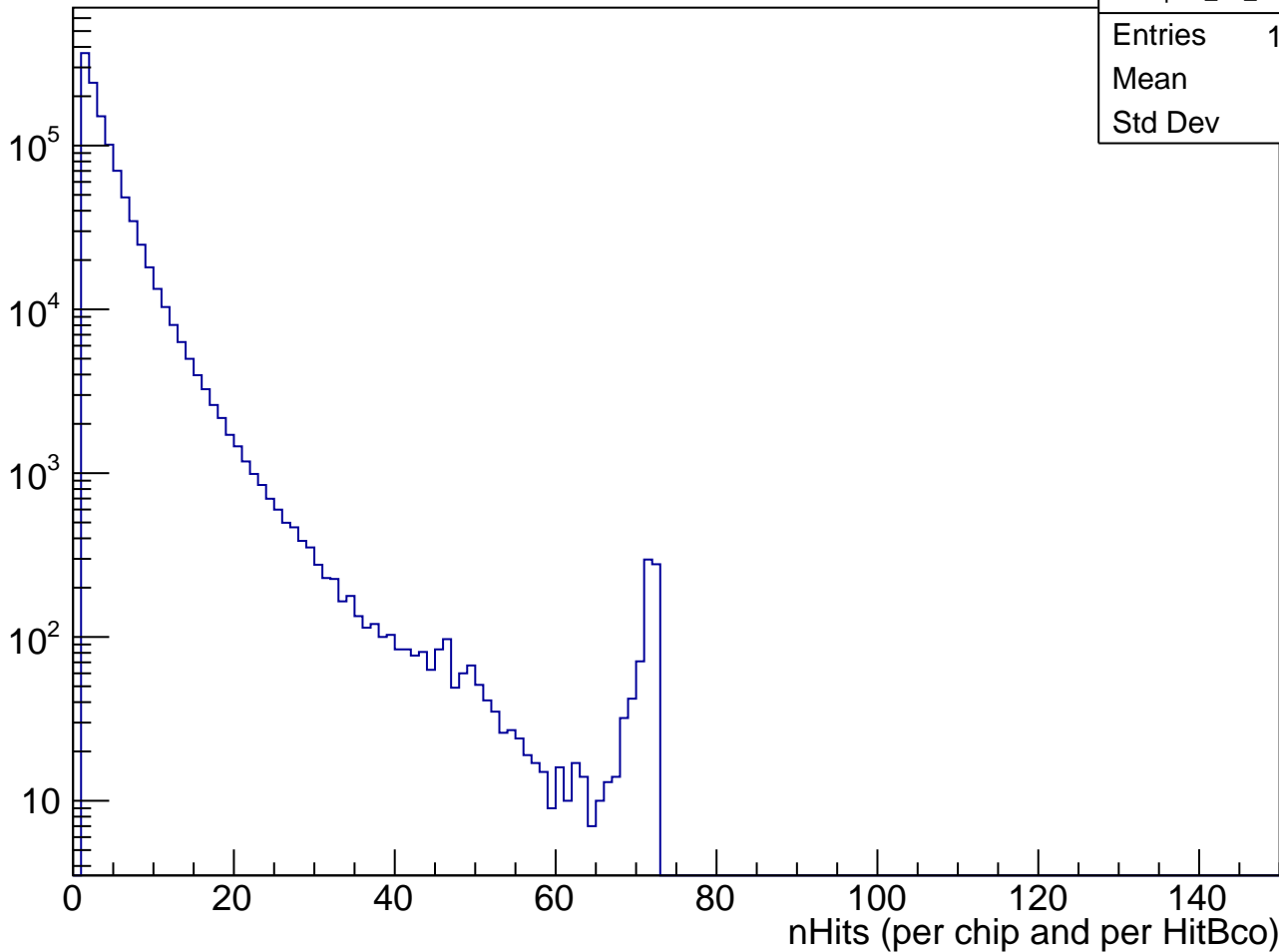
Mean 3.57

Std Dev 4.211

nHits (per chip and per HitBco)

nChipHit_F3_Fch1_Chip9

Entries



nChipHit_F3_Fch1_Chip9

Entries 1124422

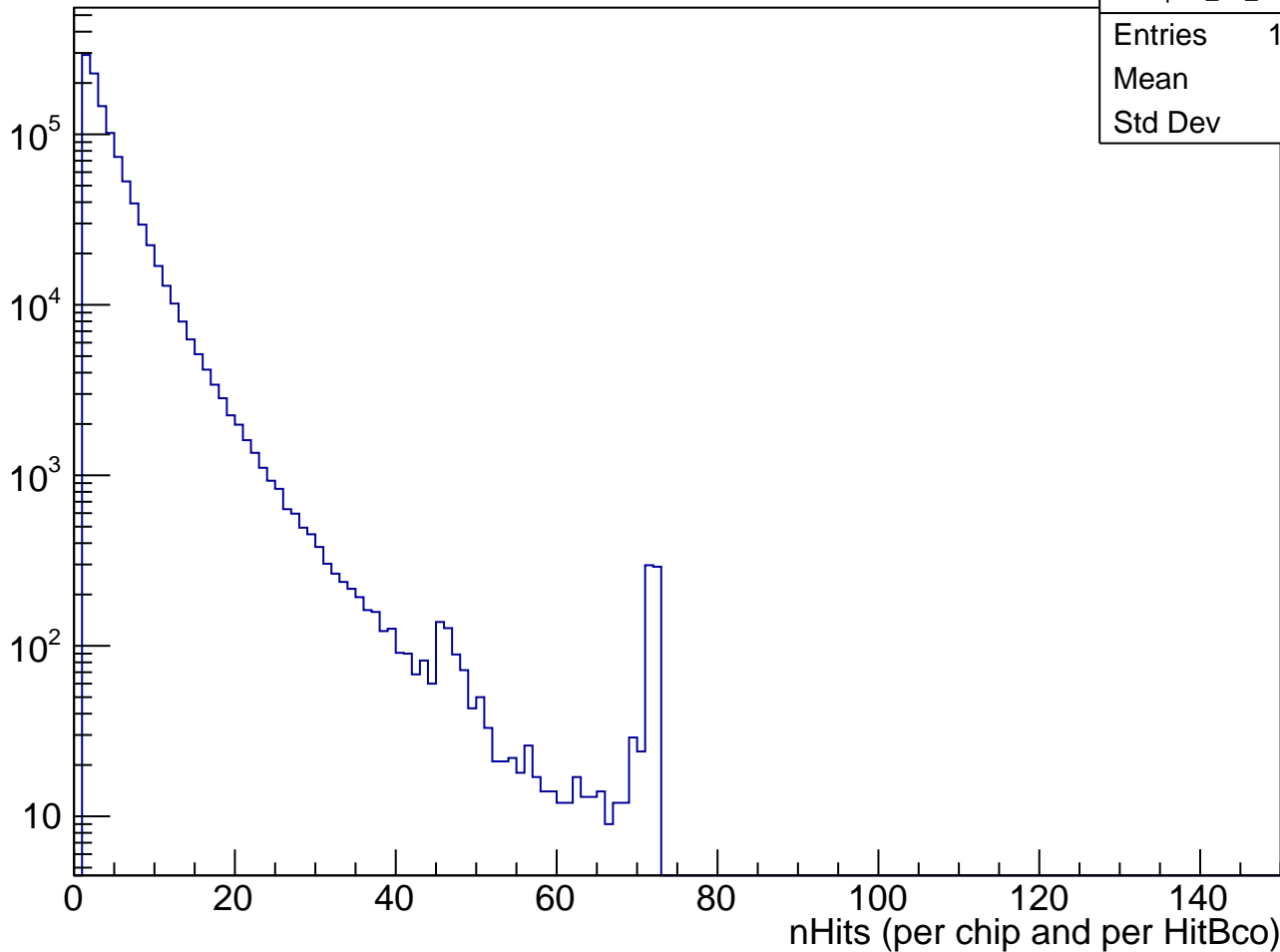
Mean 3.564

Std Dev 4.223

nHits (per chip and per HitBco)

nChipHit_F3_Fch2_Chip0

Entries



nChipHit_F3_Fch2_Chip0

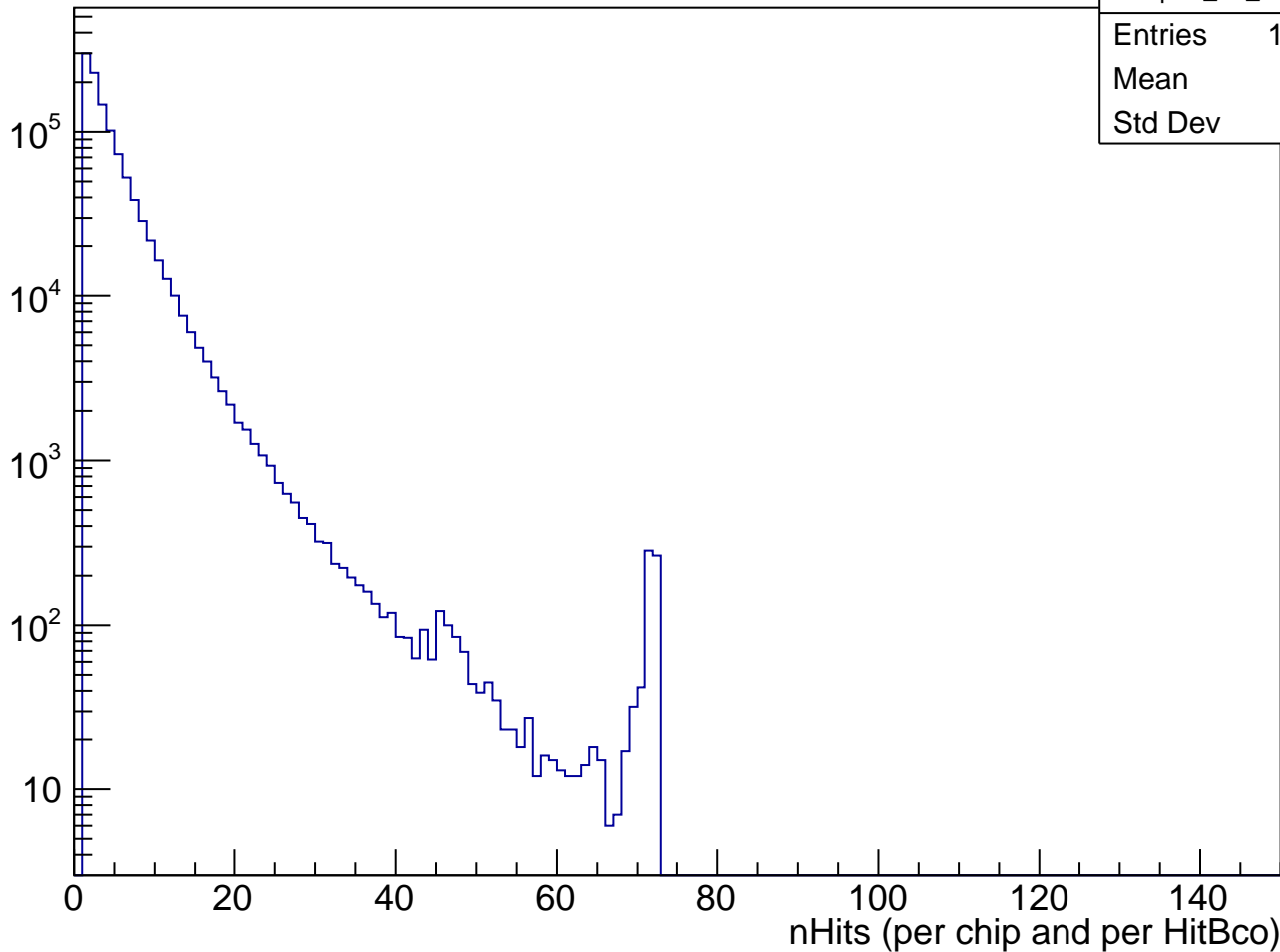
Entries 1071607

Mean 4.031

Std Dev 4.563

nChipHit_F3_Fch2_Chip1

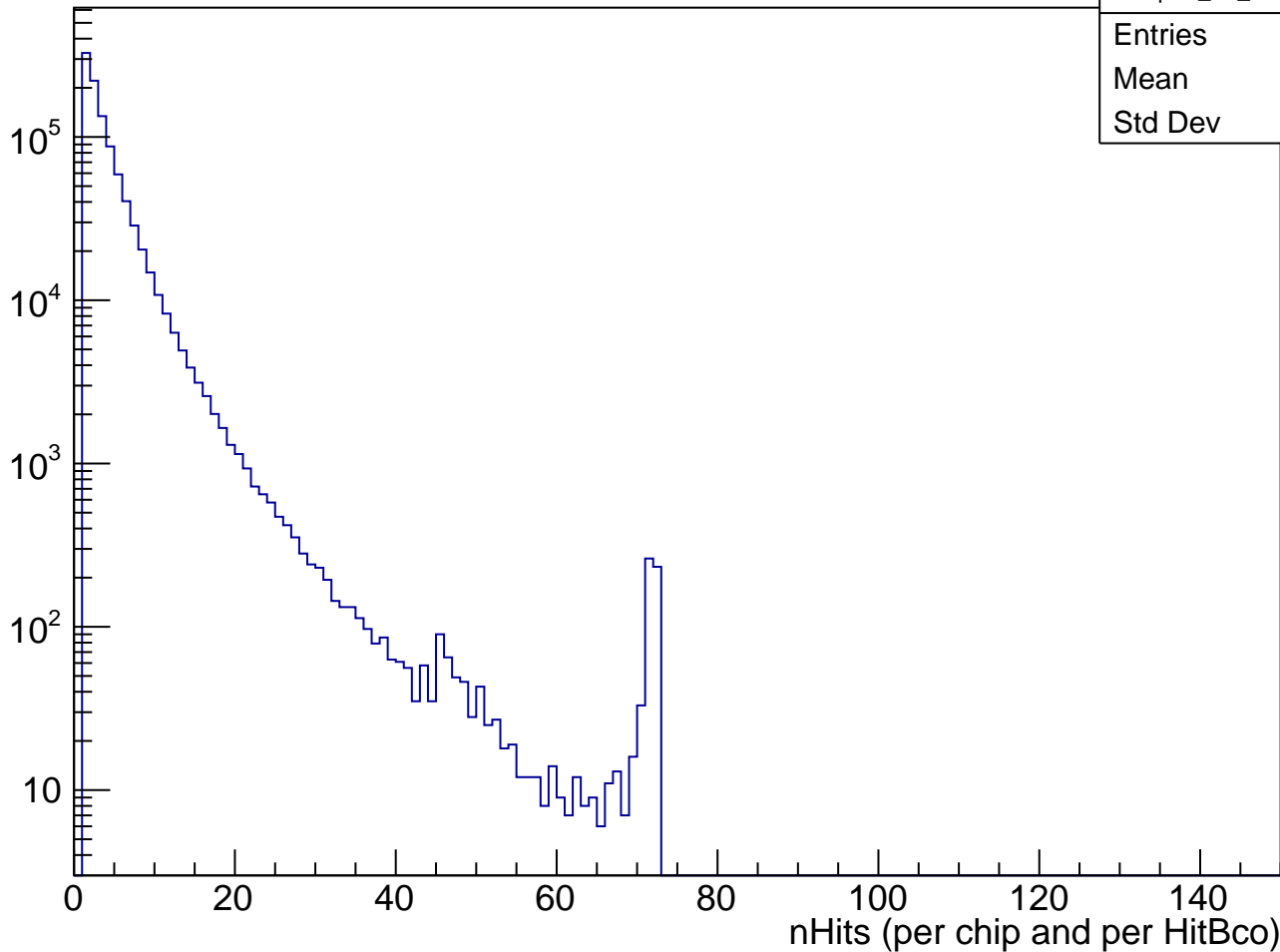
Entries



nChipHit_F3_Fch2_Chip1	
Entries	1073626
Mean	3.958
Std Dev	4.481

nChipHit_F3_Fch2_Chip10

Entries

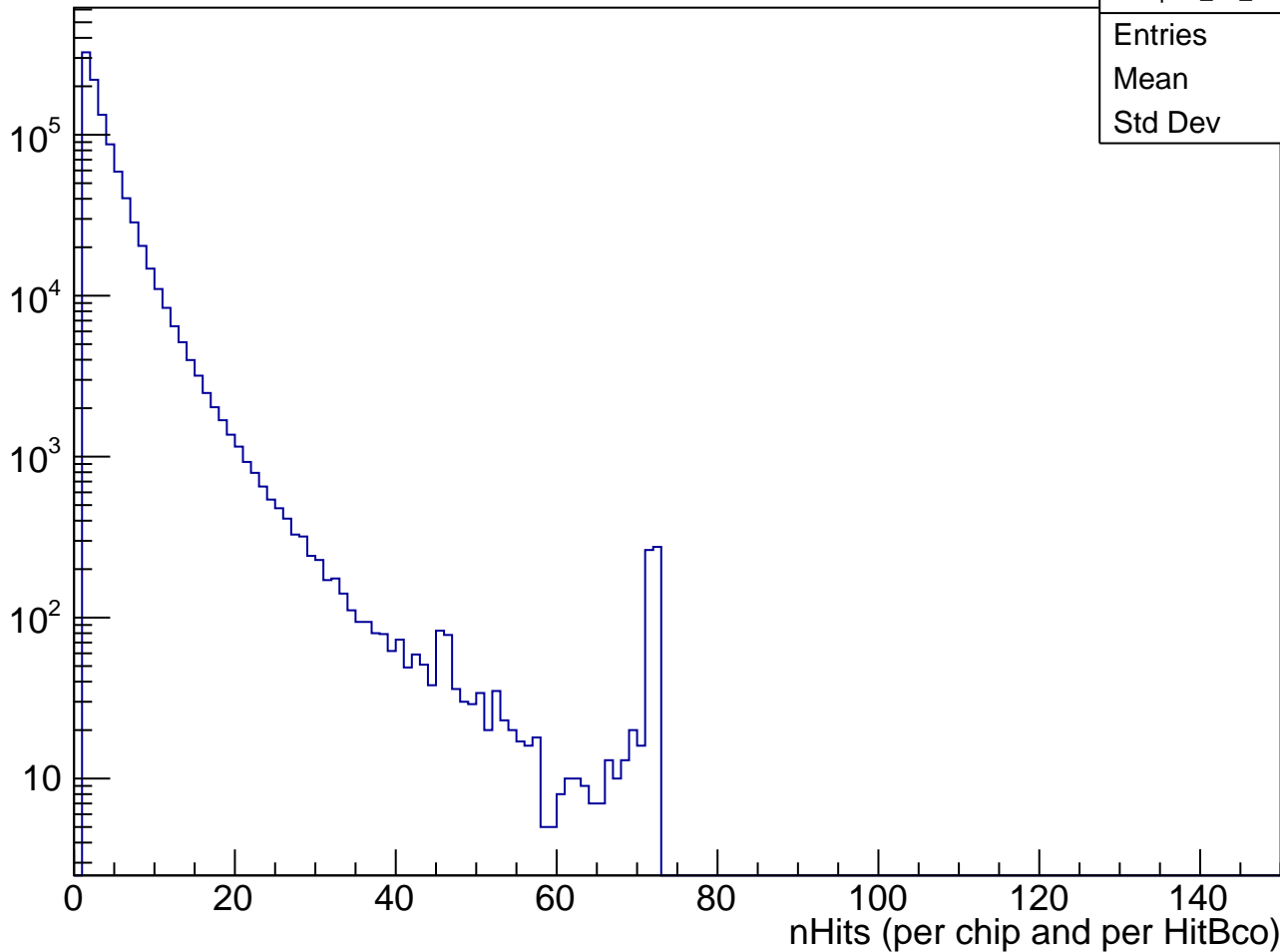


nChipHit_F3_Fch2_Chip10	
Entries	986199
Mean	3.427
Std Dev	4.011

nHits (per chip and per HitBco)

nChipHit_F3_Fch2_Chip11

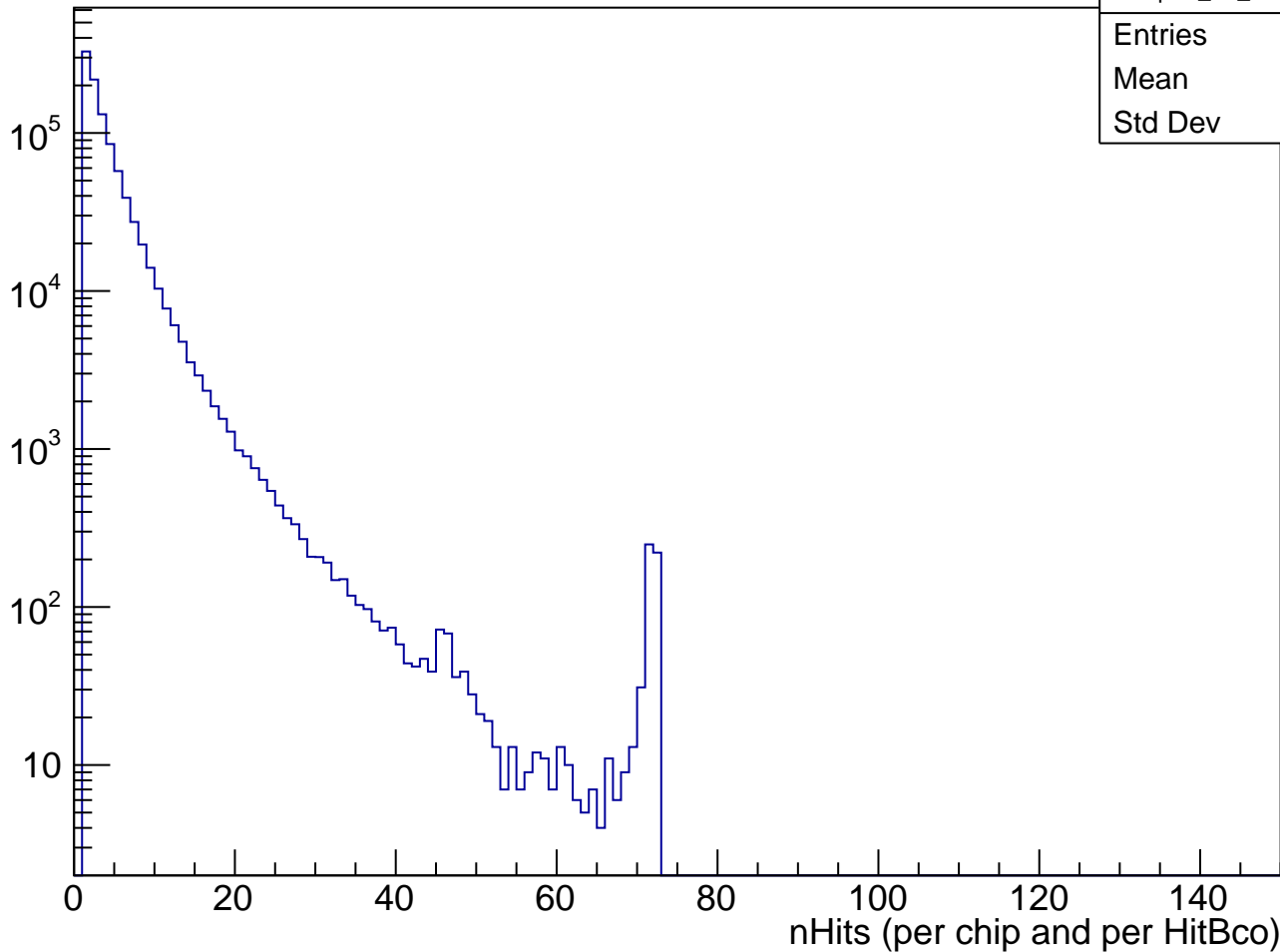
Entries



nChipHit_F3_Fch2_Chip11	
Entries	982310
Mean	3.443
Std Dev	4.045

nChipHit_F3_Fch2_Chip12

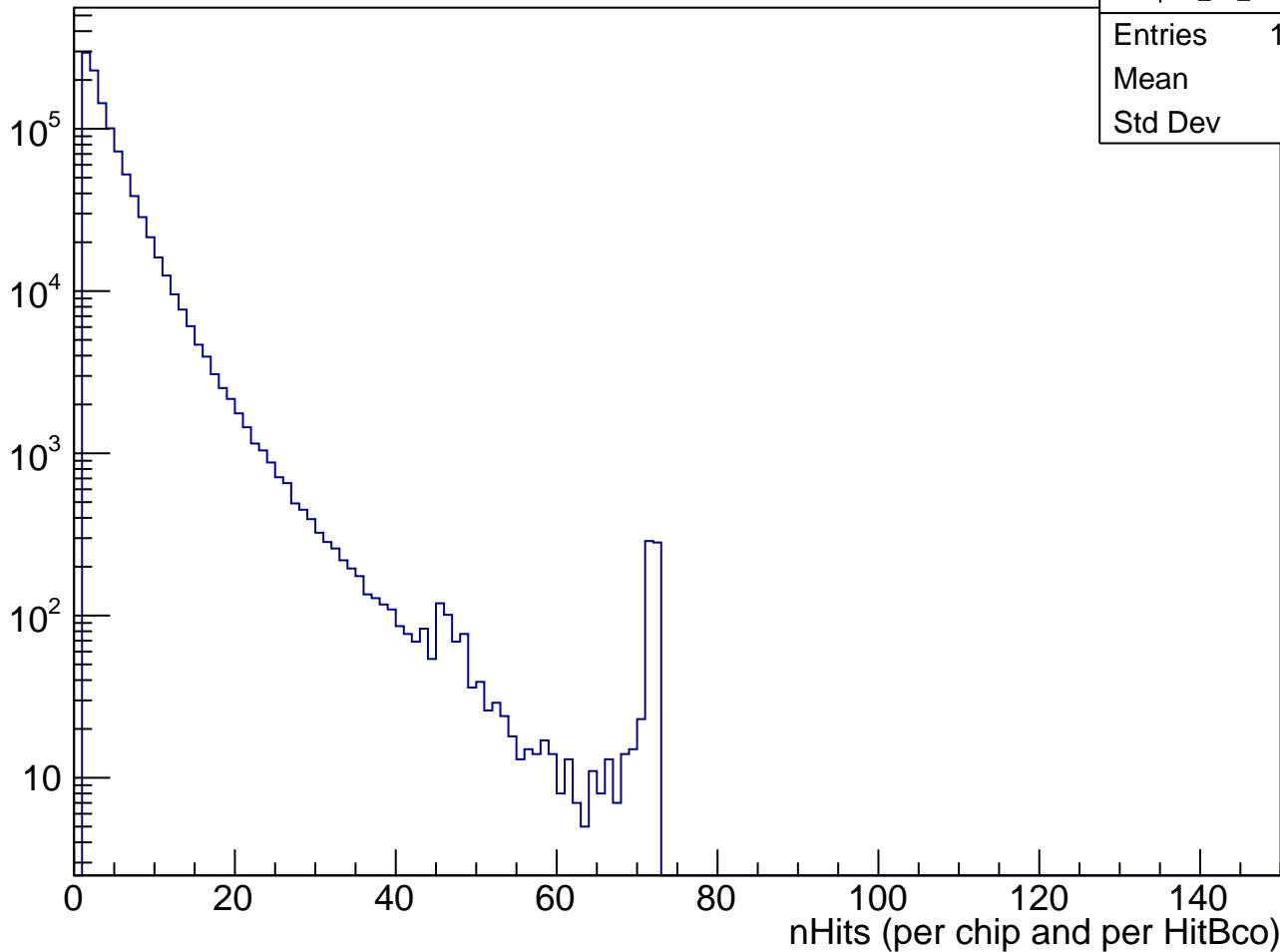
Entries



nChipHit_F3_Fch2_Chip12	
Entries	969813
Mean	3.369
Std Dev	3.925

nChipHit_F3_Fch2_Chip13

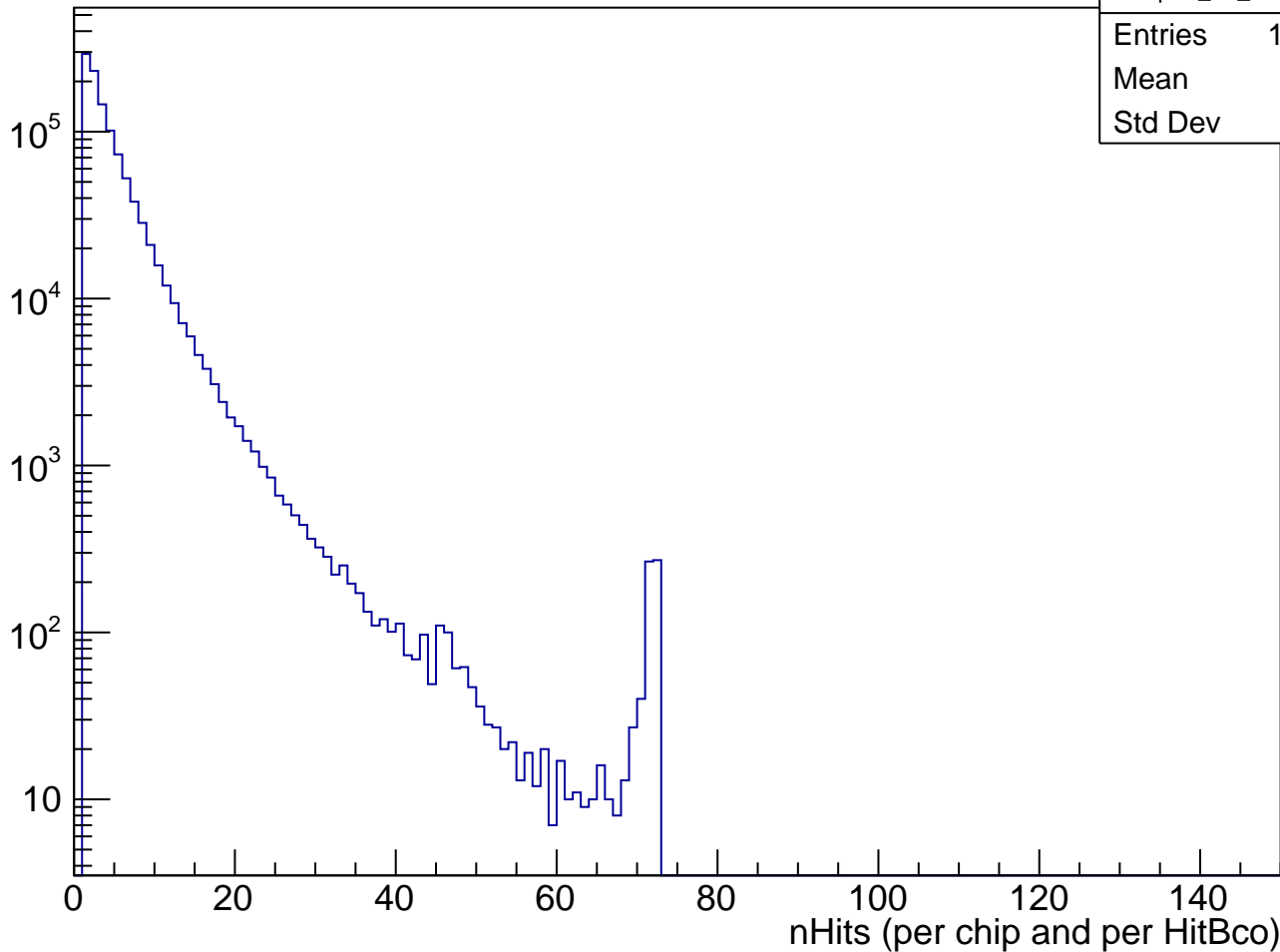
Entries



nChipHit_F3_Fch2_Chip13	
Entries	1062740
Mean	3.945
Std Dev	4.436

nChipHit_F3_Fch2_Chip14

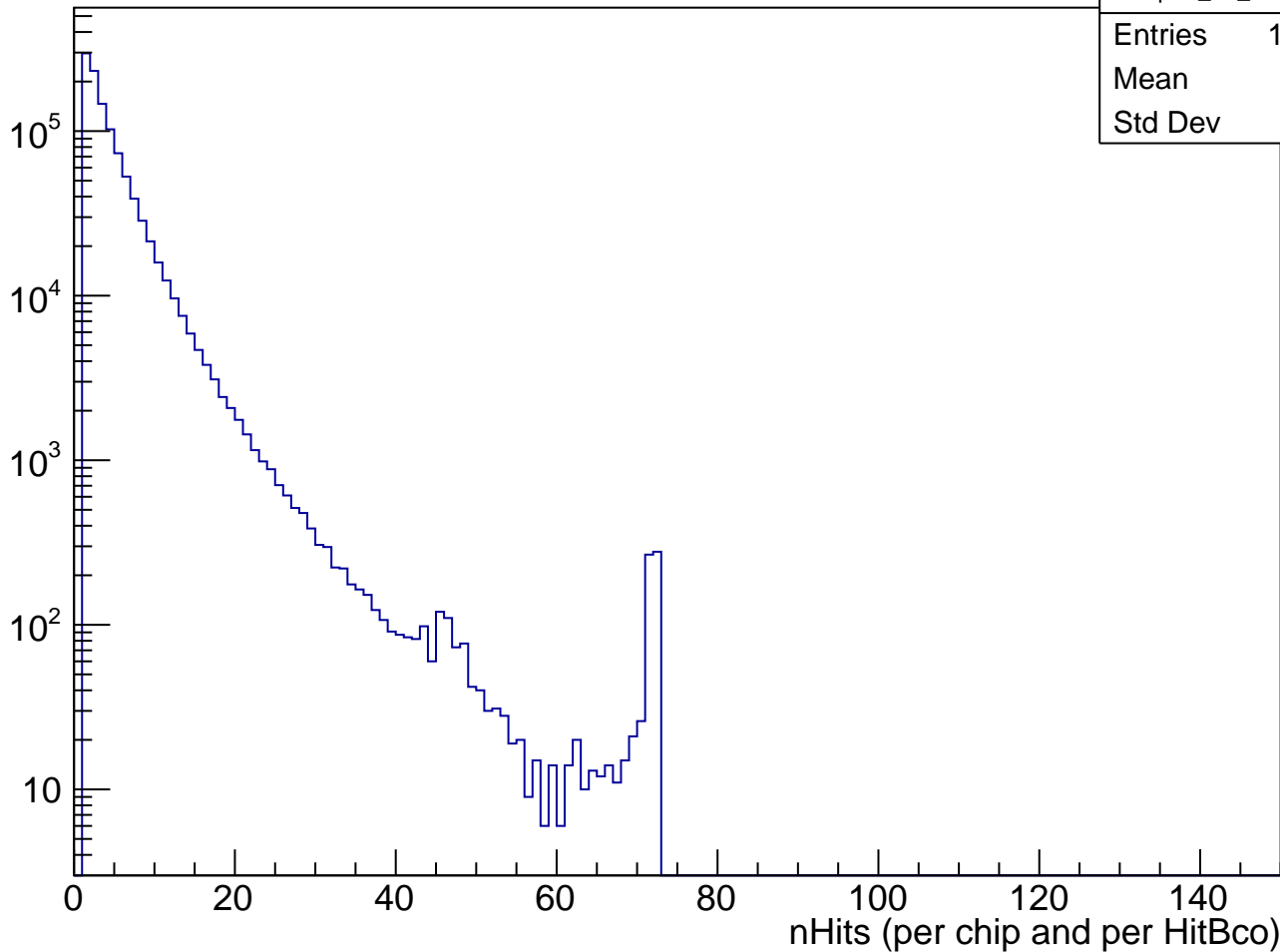
Entries



nChipHit_F3_Fch2_Chip14	
Entries	1062169
Mean	3.919
Std Dev	4.399

nChipHit_F3_Fch2_Chip15

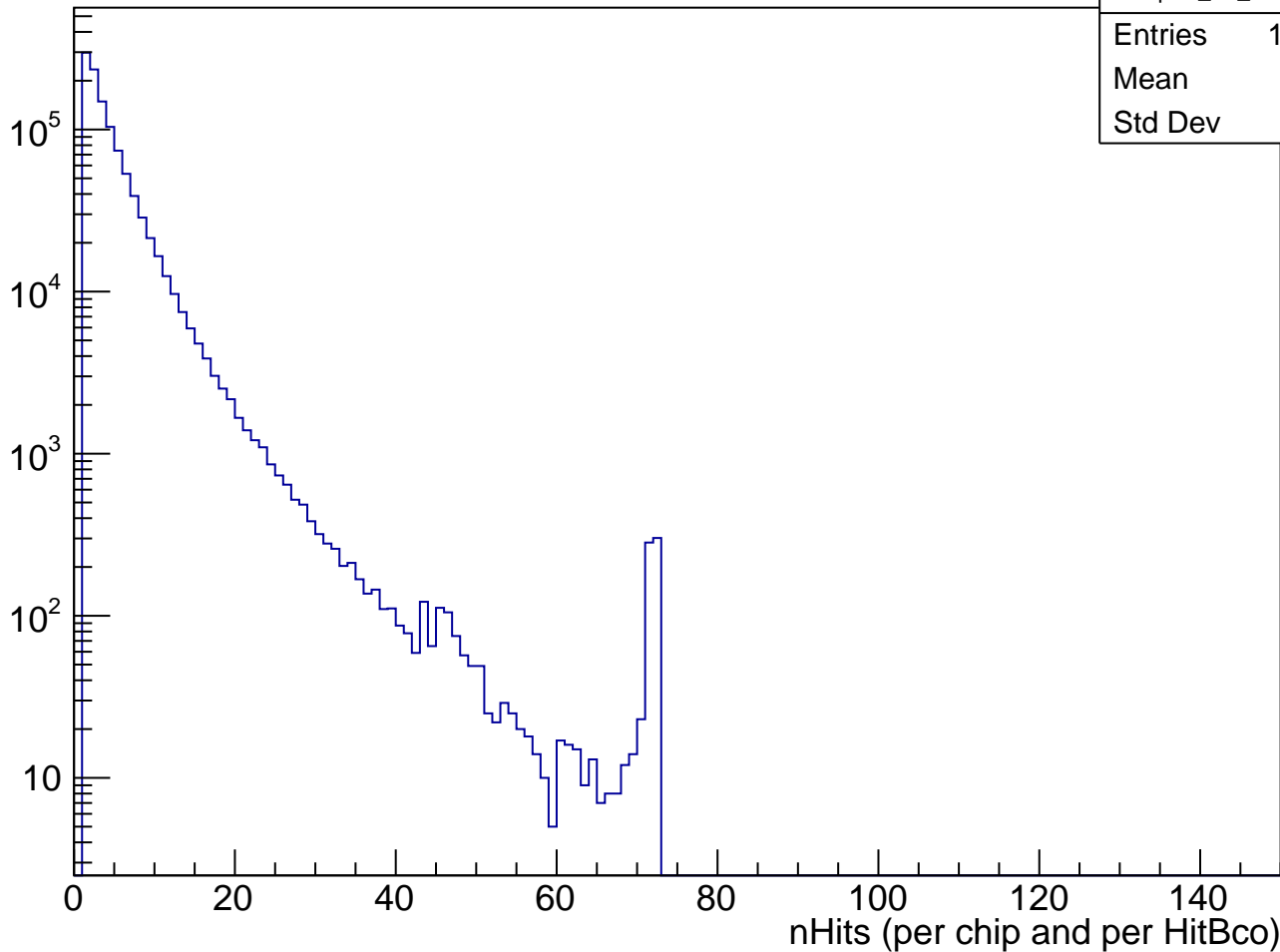
Entries



nChipHit_F3_Fch2_Chip15	
Entries	1072946
Mean	3.925
Std Dev	4.411

nChipHit_F3_Fch2_Chip16

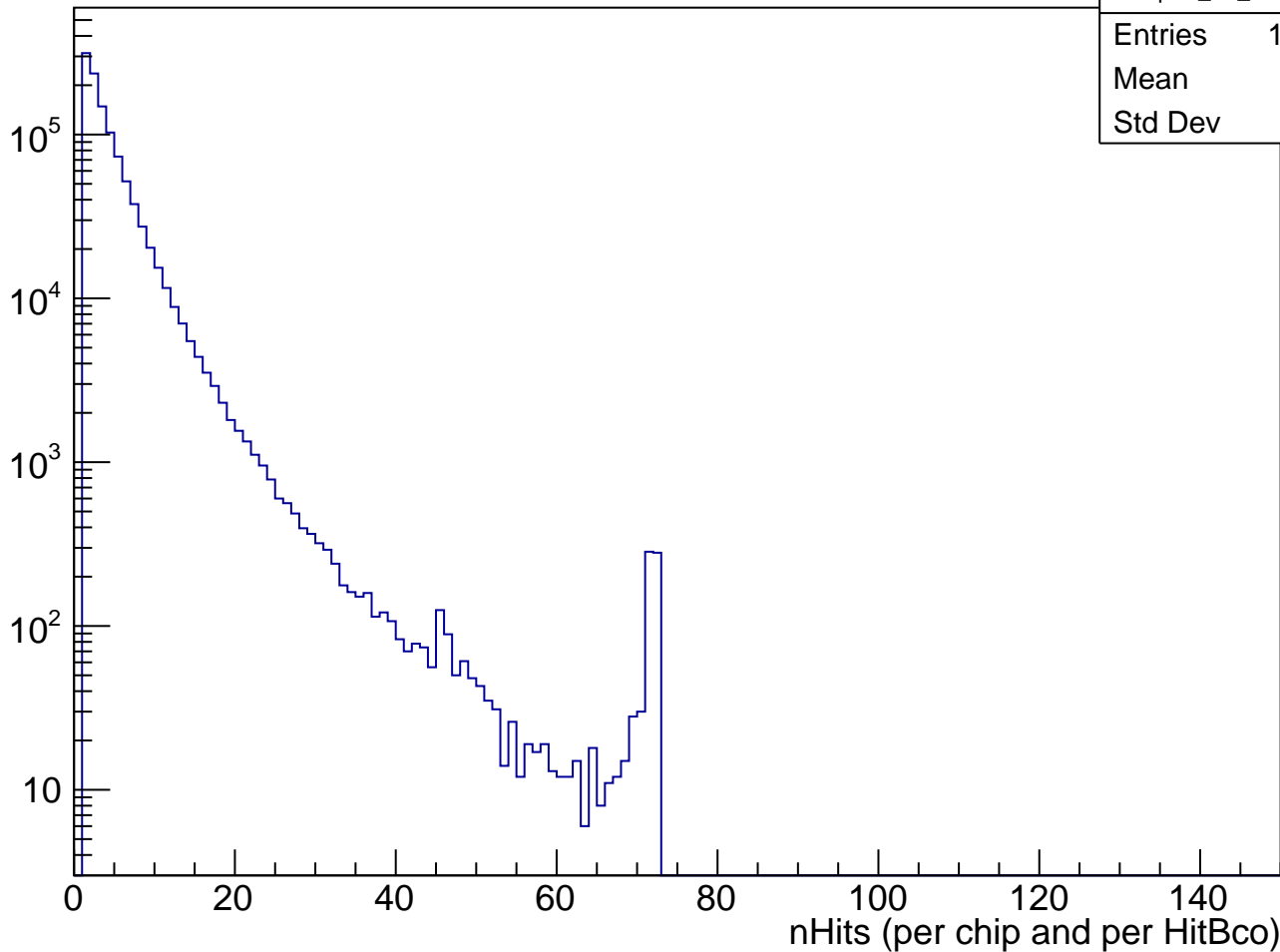
Entries



nChipHit_F3_Fch2_Chip16	
Entries	1082798
Mean	3.93
Std Dev	4.423

nChipHit_F3_Fch2_Chip17

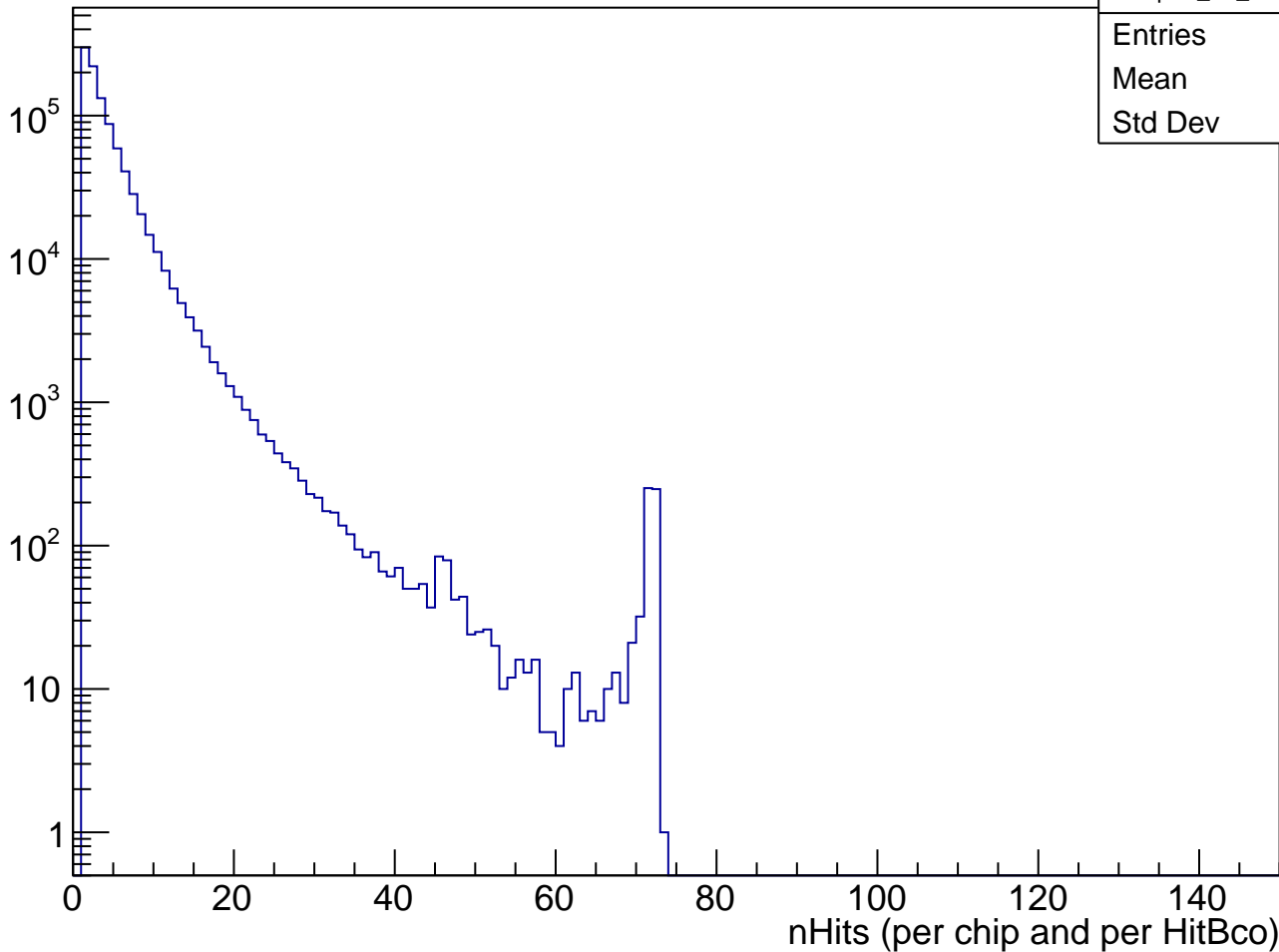
Entries



nChipHit_F3_Fch2_Chip17	
Entries	1086828
Mean	3.805
Std Dev	4.319

nChipHit_F3_Fch2_Chip18

Entries

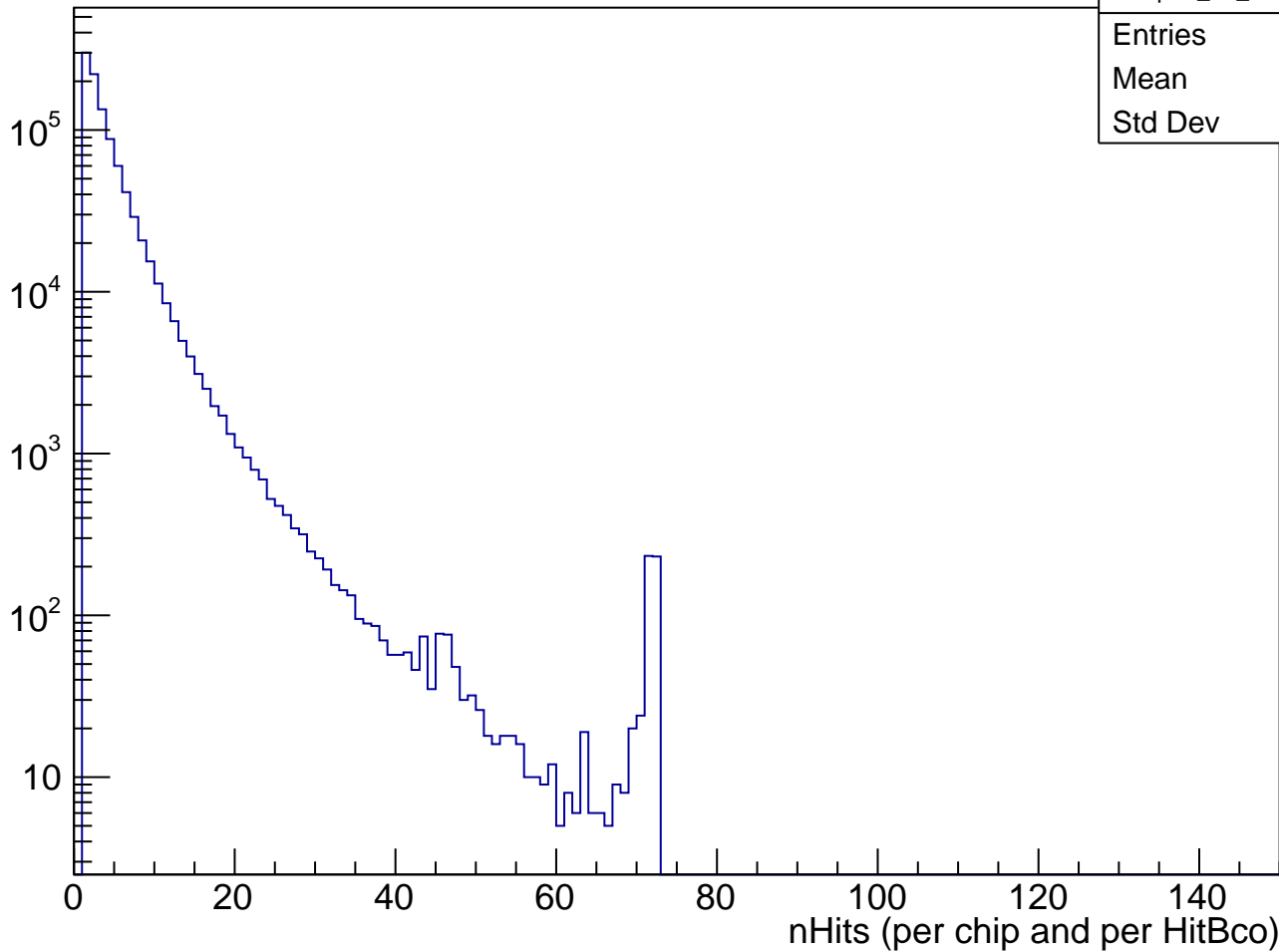


nChipHit_F3_Fch2_Chip18	
Entries	956292
Mean	3.489
Std Dev	4.018

nHits (per chip and per HitBco)

nChipHit_F3_Fch2_Chip19

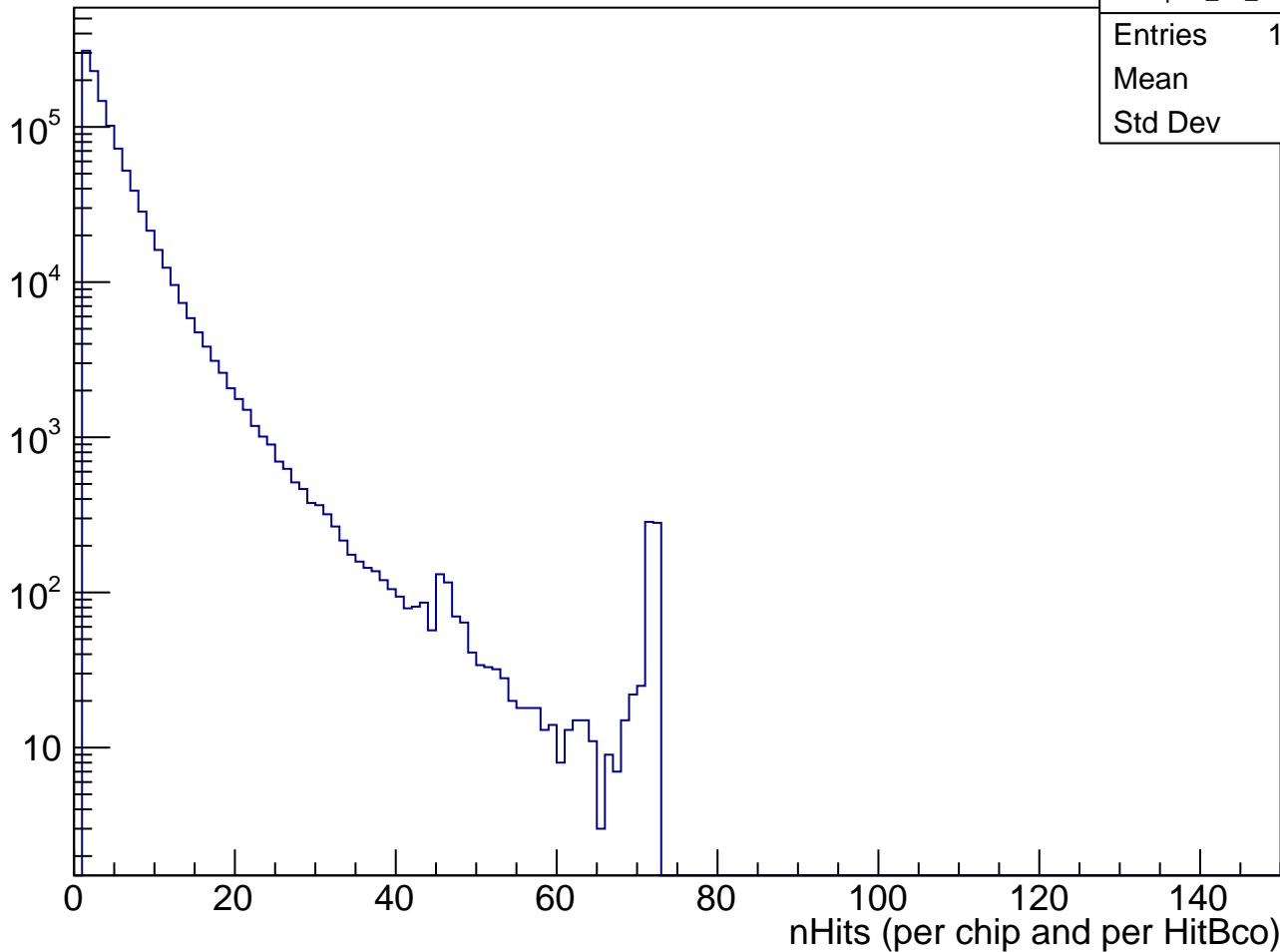
Entries



nChipHit_F3_Fch2_Chip19	
Entries	965407
Mean	3.507
Std Dev	4.011

nChipHit_F3_Fch2_Chip2

Entries



nChipHit_F3_Fch2_Chip2

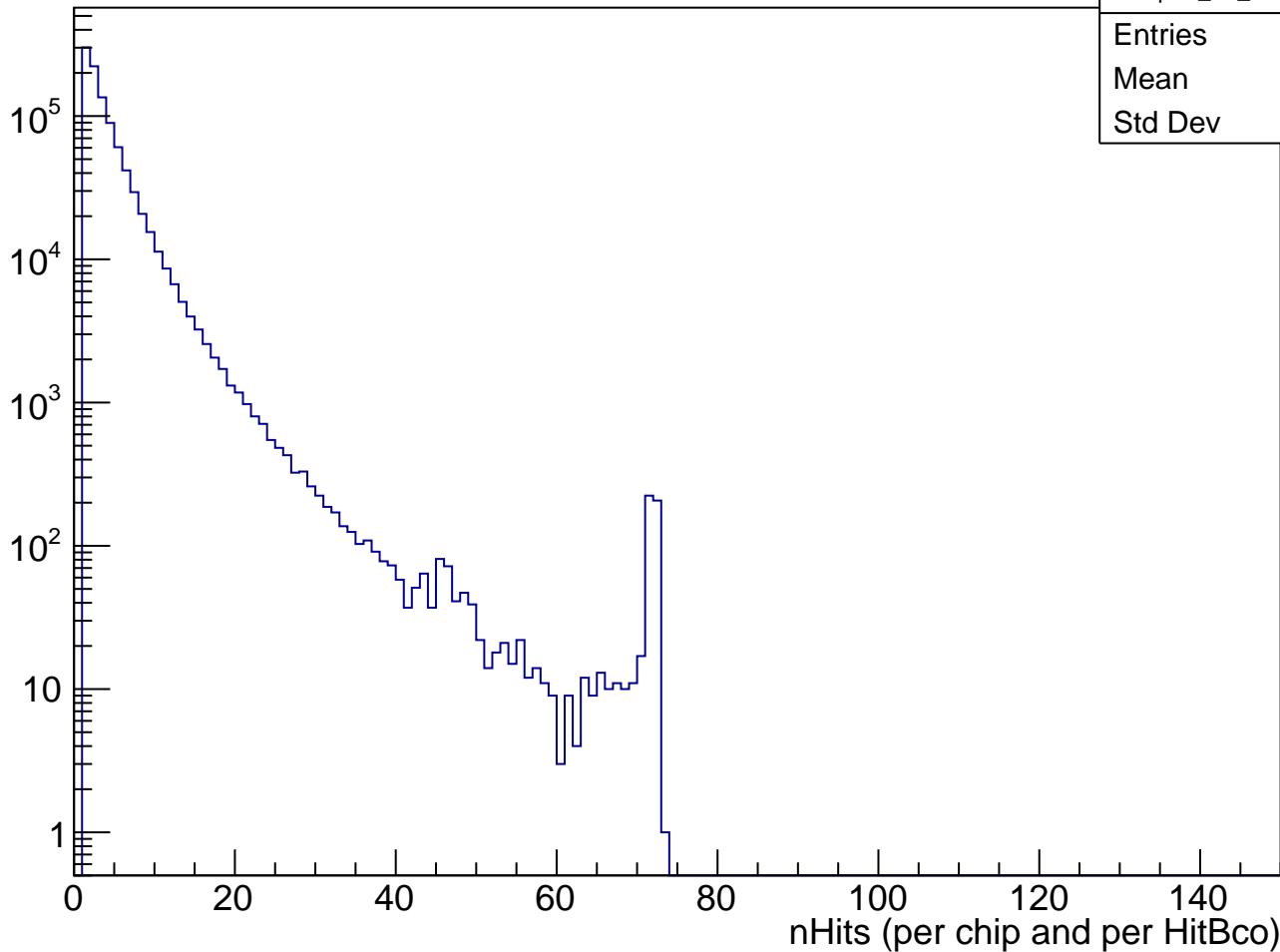
Entries 1081658

Mean 3.901

Std Dev 4.428

nChipHit_F3_Fch2_Chip20

Entries

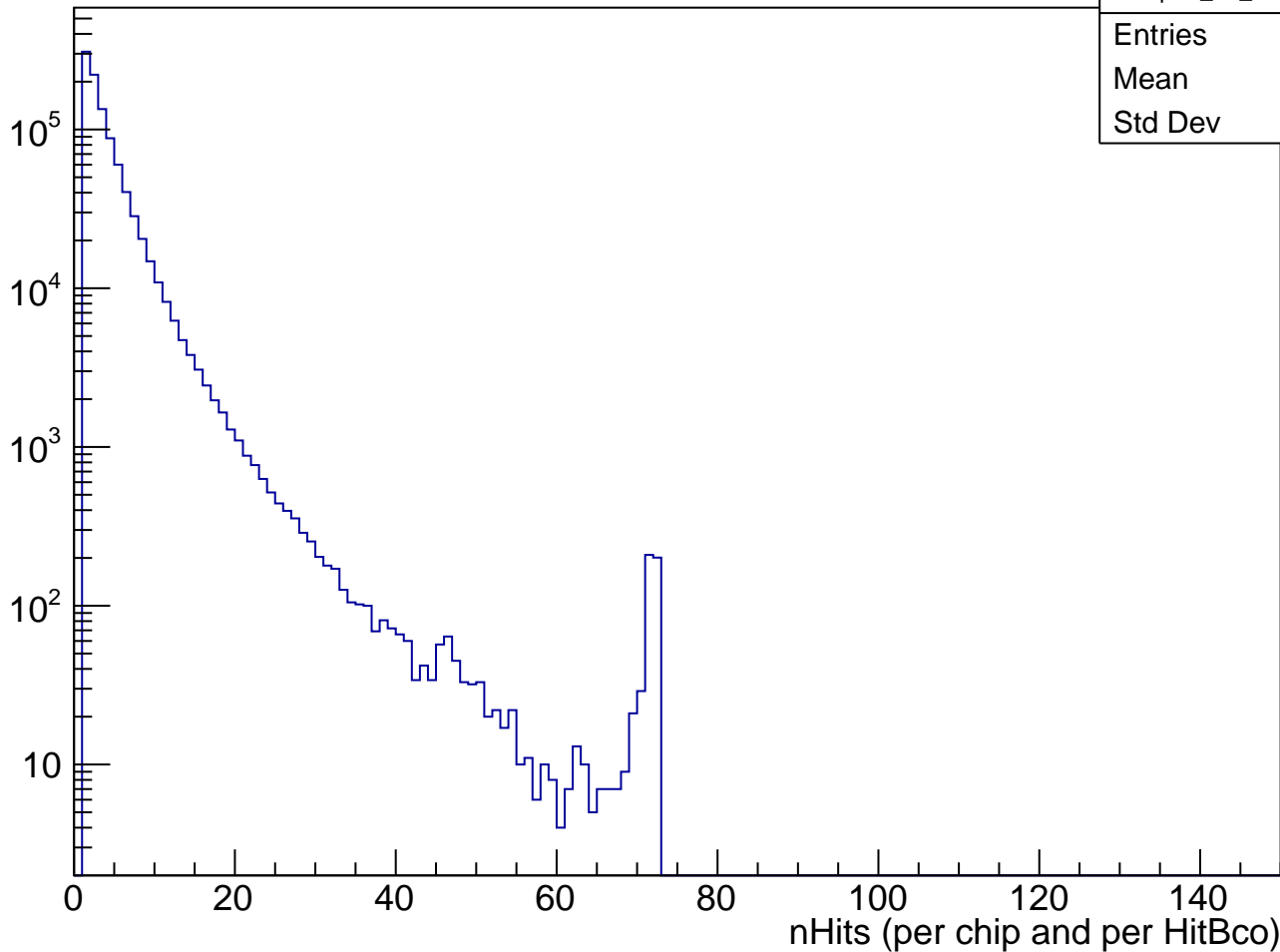


nChipHit_F3_Fch2_Chip20	
Entries	971870
Mean	3.518
Std Dev	4.004

nHits (per chip and per HitBco)

nChipHit_F3_Fch2_Chip21

Entries

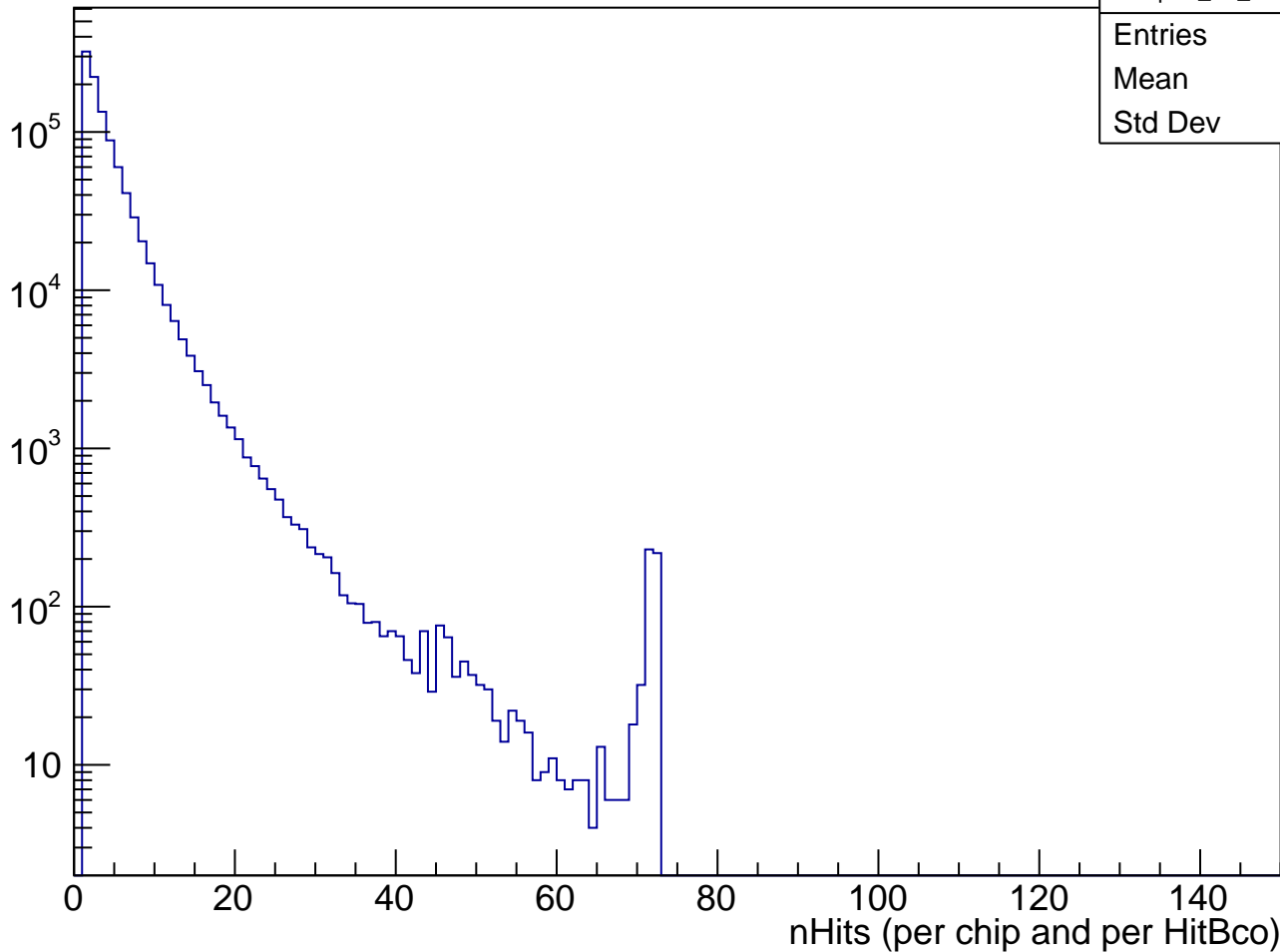


nChipHit_F3_Fch2_Chip21	
Entries	968938
Mean	3.45
Std Dev	3.929

nHits (per chip and per HitBco)

nChipHit_F3_Fch2_Chip22

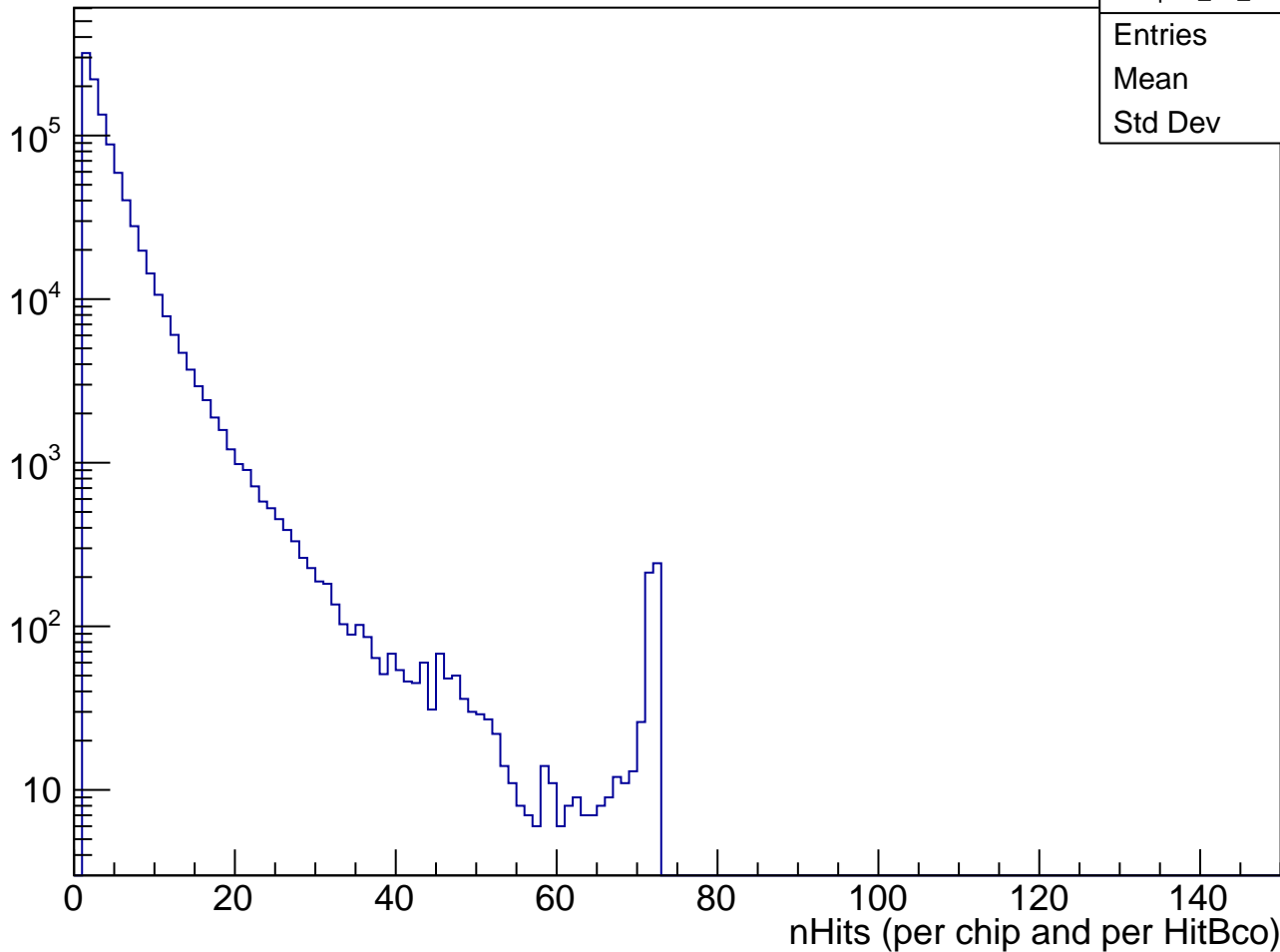
Entries



nChipHit_F3_Fch2_Chip22	
Entries	985725
Mean	3.428
Std Dev	3.951

nChipHit_F3_Fch2_Chip23

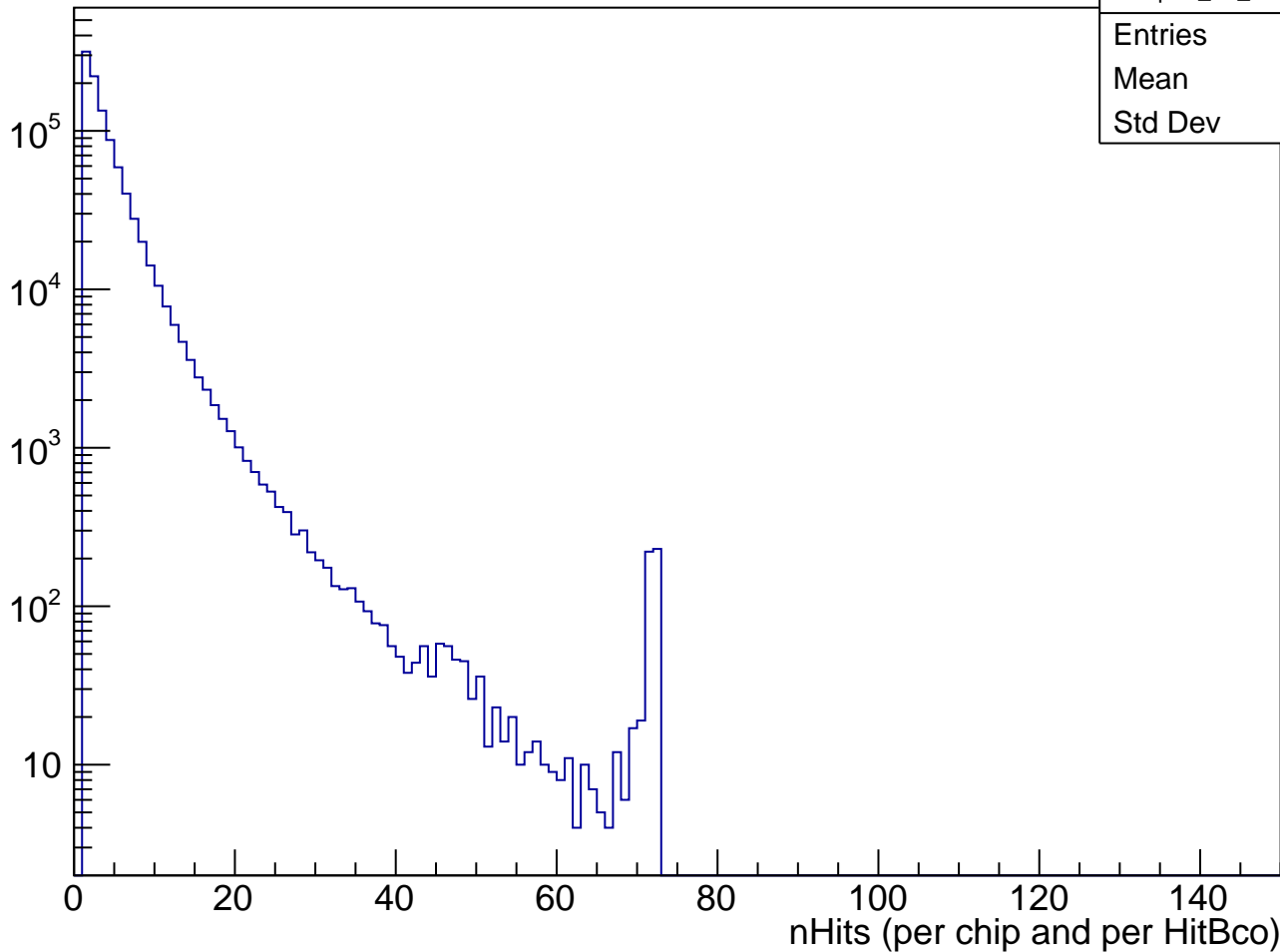
Entries



nChipHit_F3_Fch2_Chip23	
Entries	974368
Mean	3.397
Std Dev	3.897

nChipHit_F3_Fch2_Chip24

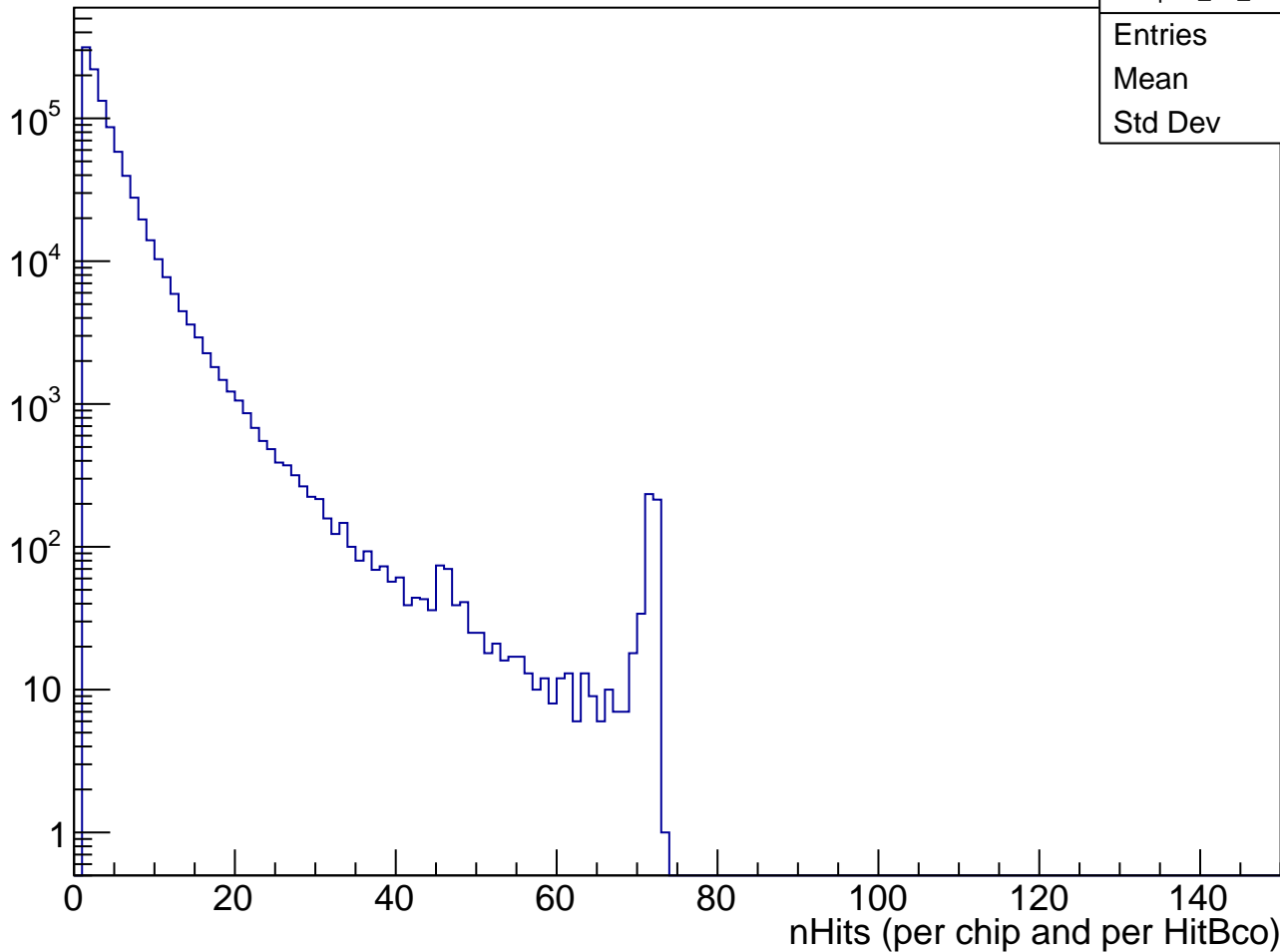
Entries



nChipHit_F3_Fch2_Chip24	
Entries	970255
Mean	3.395
Std Dev	3.893

nChipHit_F3_Fch2_Chip25

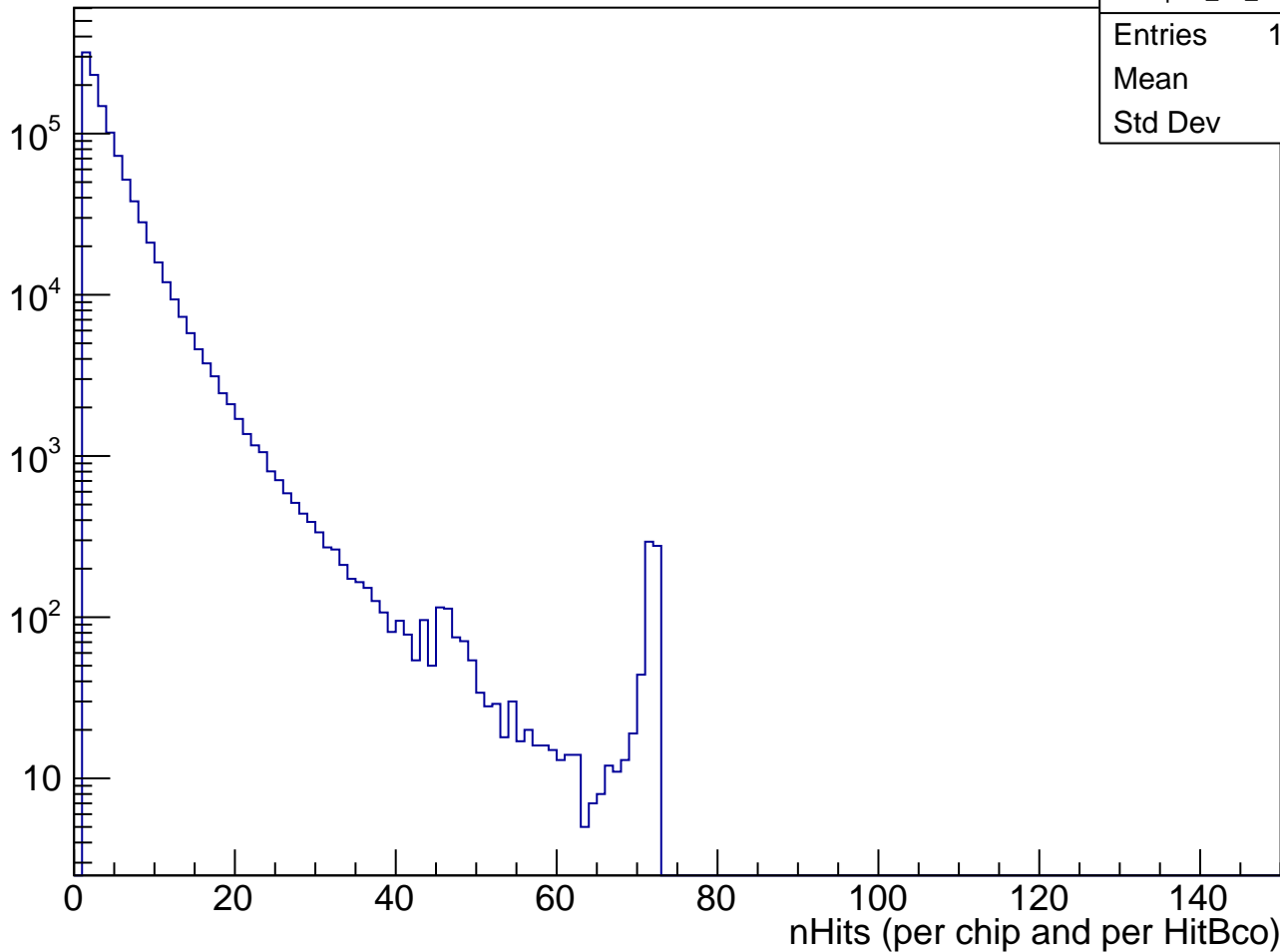
Entries



nChipHit_F3_Fch2_Chip25	
Entries	963005
Mean	3.389
Std Dev	3.901

nChipHit_F3_Fch2_Chip3

Entries



nChipHit_F3_Fch2_Chip3

Entries 1090209

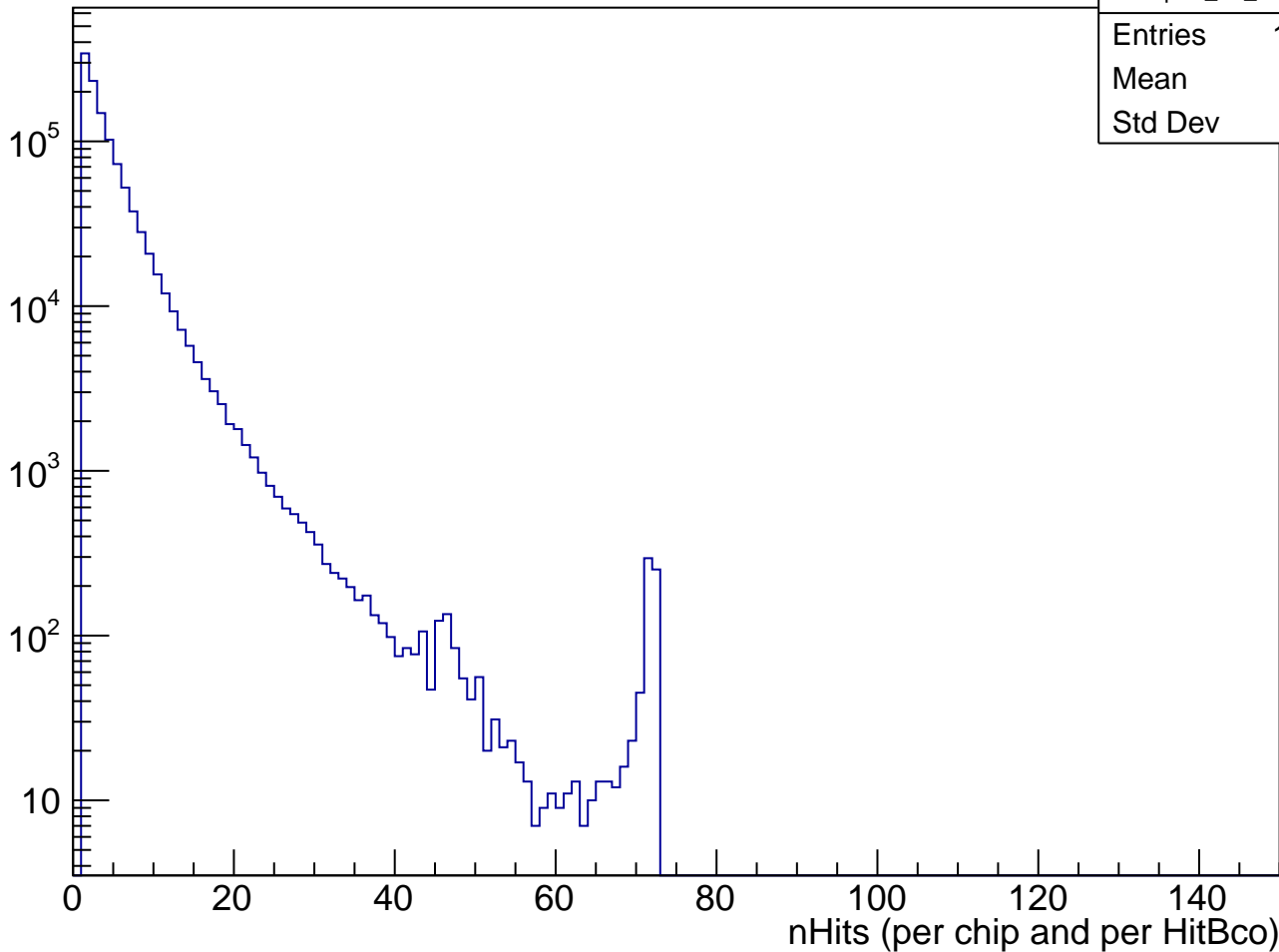
Mean 3.848

Std Dev 4.391

nHits (per chip and per HitBco)

nChipHit_F3_Fch2_Chip4

Entries



nChipHit_F3_Fch2_Chip4

Entries 1115461

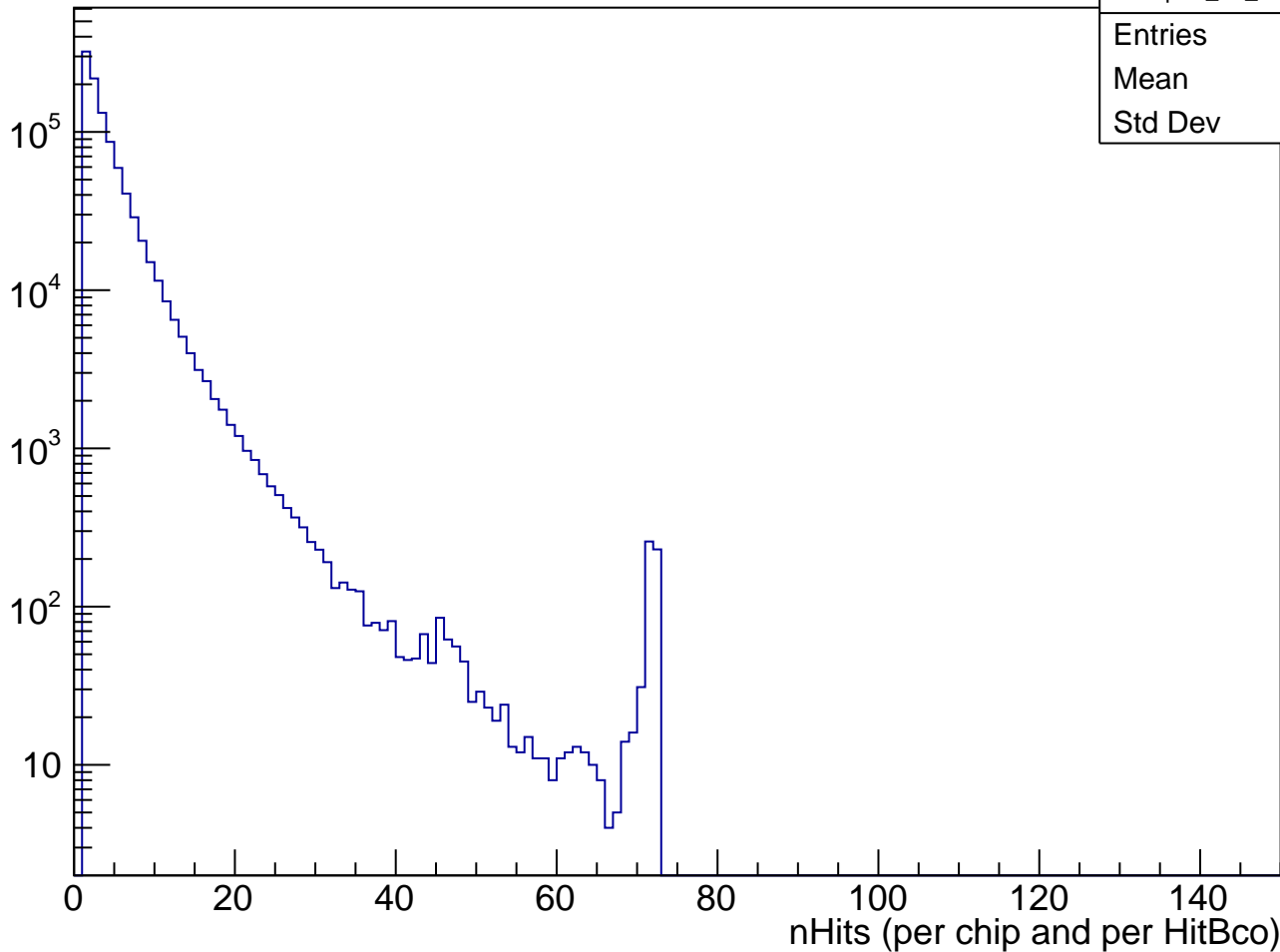
Mean 3.784

Std Dev 4.362

nHits (per chip and per HitBco)

nChipHit_F3_Fch2_Chip5

Entries



nChipHit_F3_Fch2_Chip5

Entries 978198

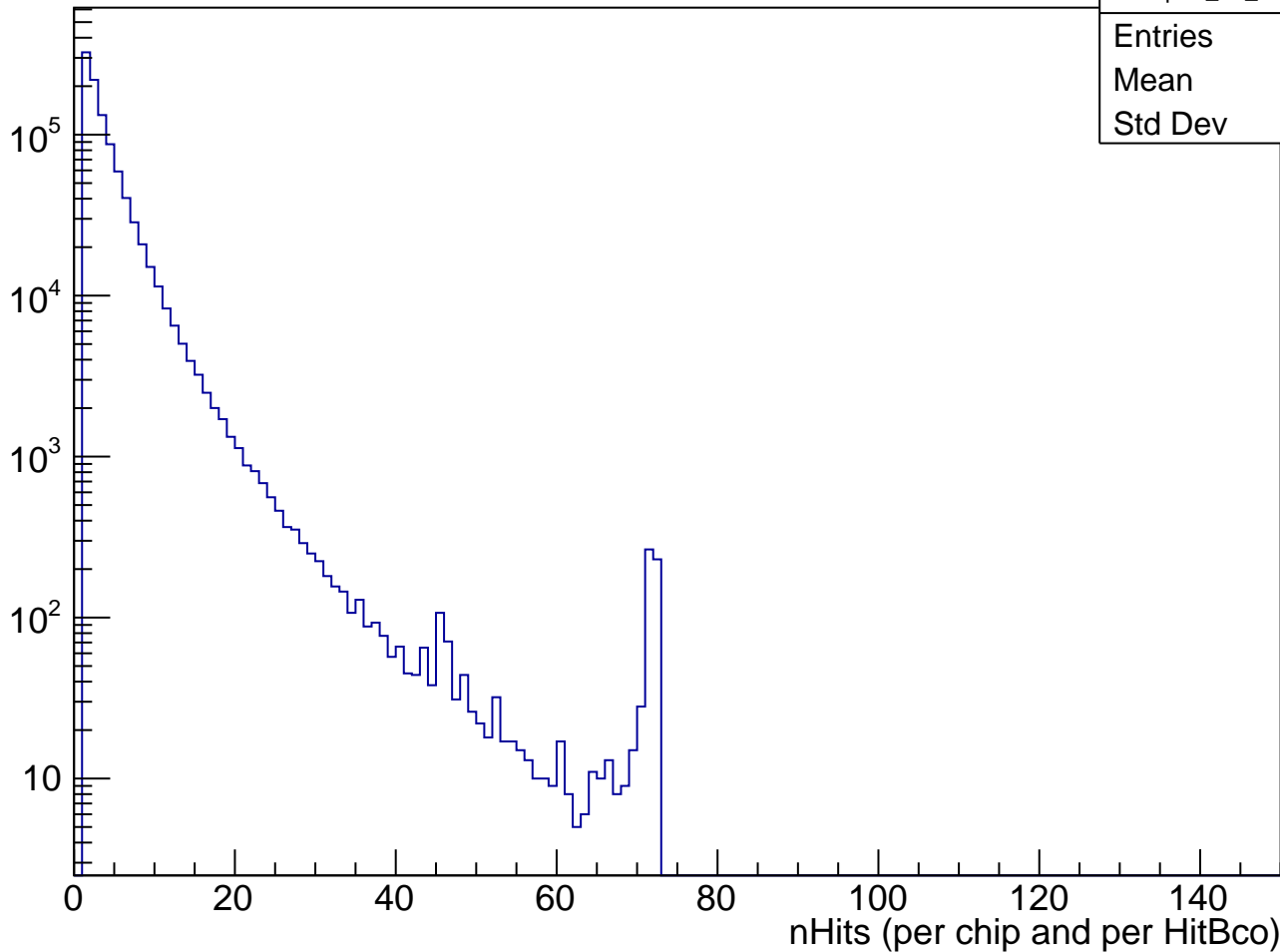
Mean 3.469

Std Dev 4.05

nHits (per chip and per HitBco)

nChipHit_F3_Fch2_Chip6

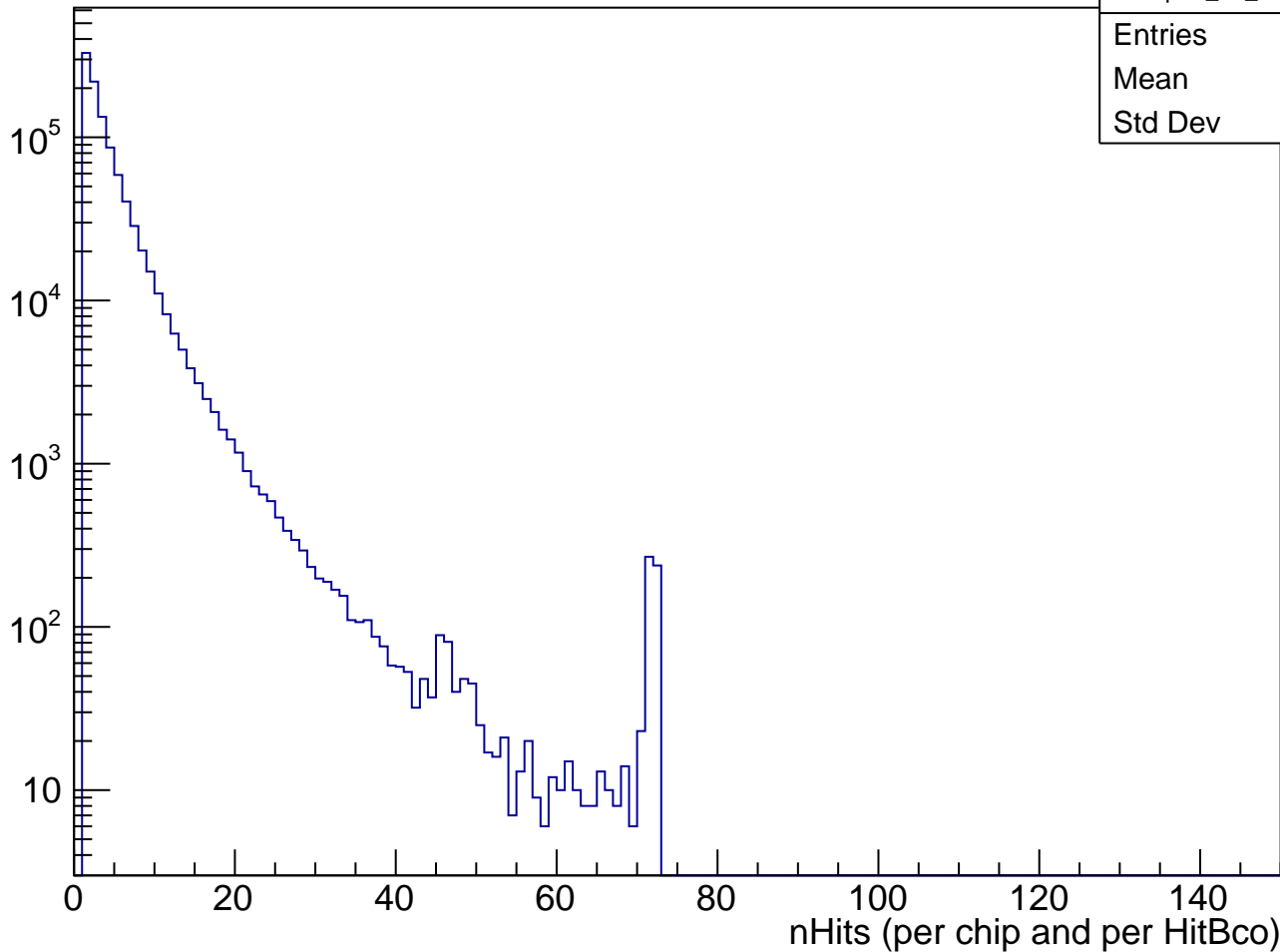
Entries



nChipHit_F3_Fch2_Chip6	
Entries	981331
Mean	3.447
Std Dev	4.02

nChipHit_F3_Fch2_Chip7

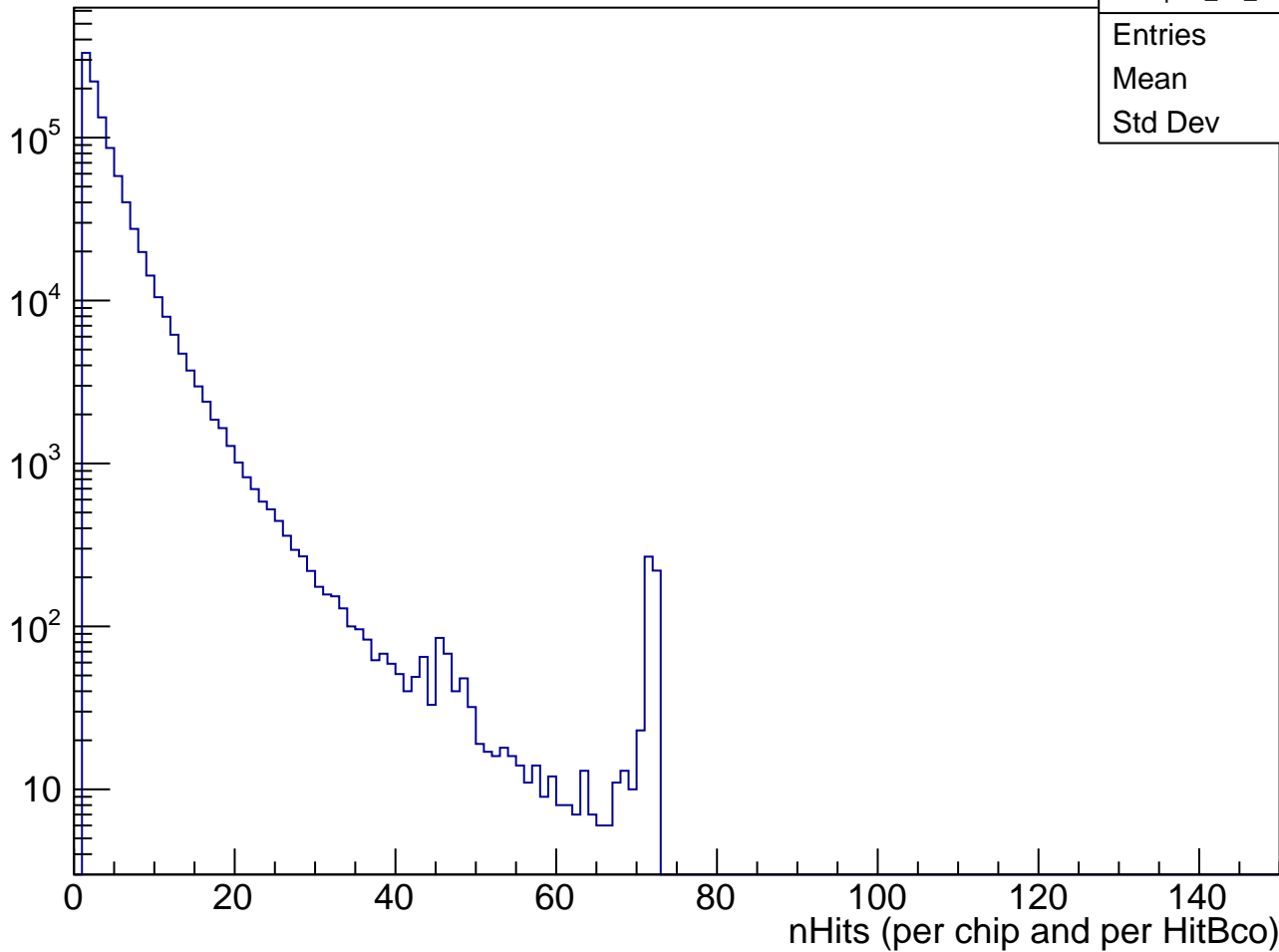
Entries



nChipHit_F3_Fch2_Chip7	
Entries	984490
Mean	3.425
Std Dev	4.007

nChipHit_F3_Fch2_Chip8

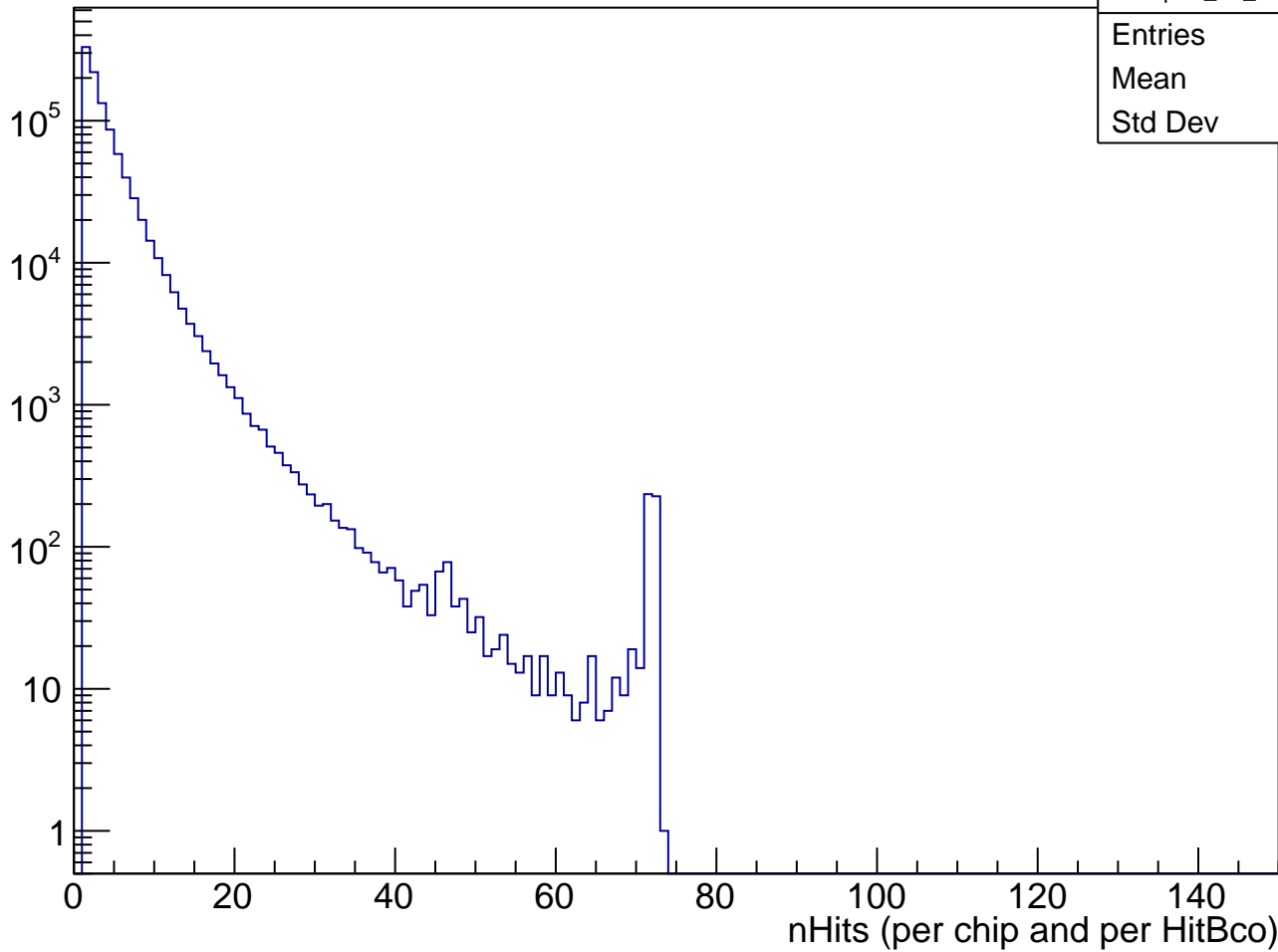
Entries



nChipHit_F3_Fch2_Chip8	
Entries	982166
Mean	3.367
Std Dev	3.914

nChipHit_F3_Fch2_Chip9

Entries



nChipHit_F3_Fch2_Chip9

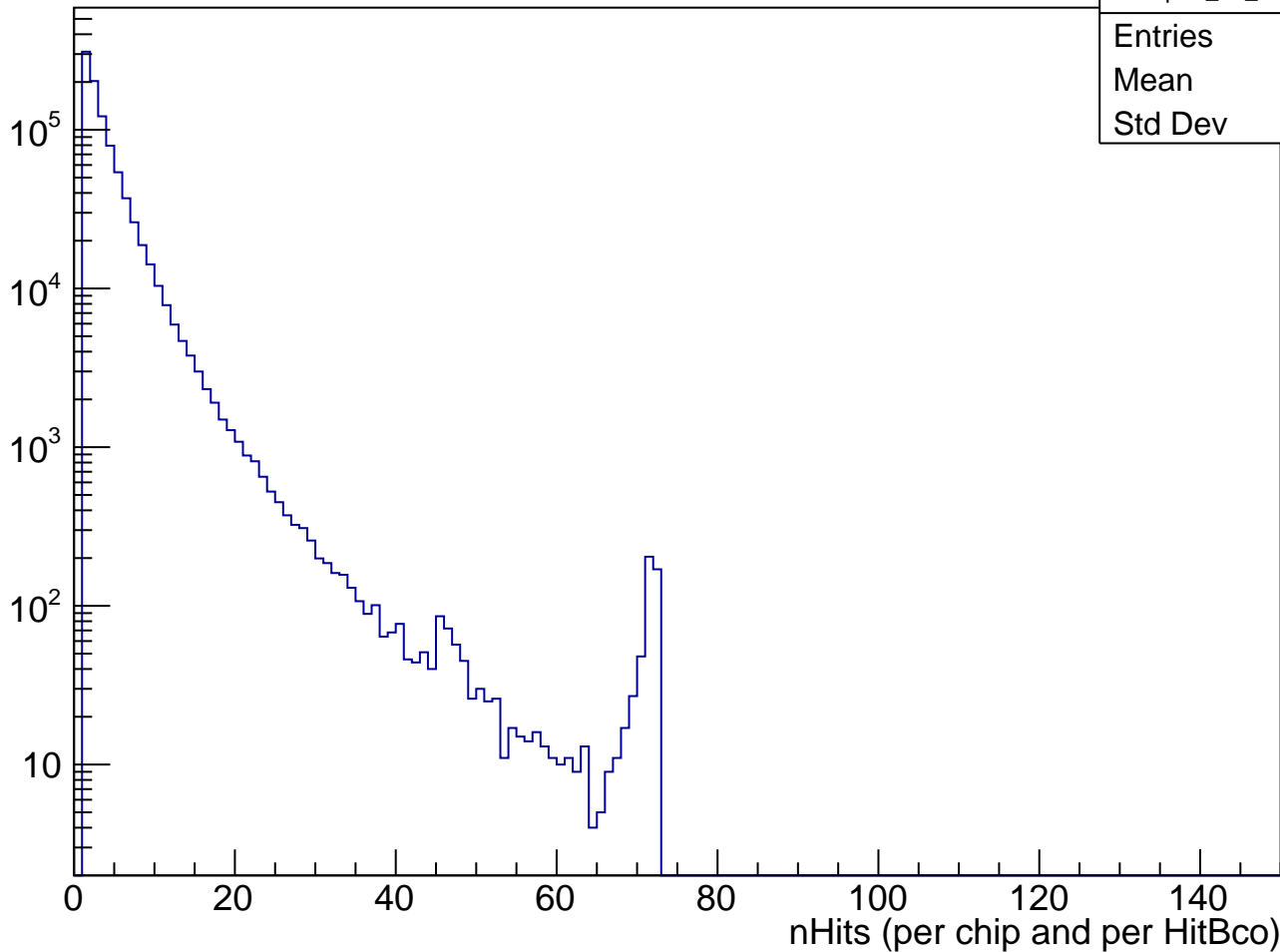
Entries 982529

Mean 3.392

Std Dev 3.945

nChipHit_F3_Fch3_Chip0

Entries



nChipHit_F3_Fch3_Chip0

Entries 915531

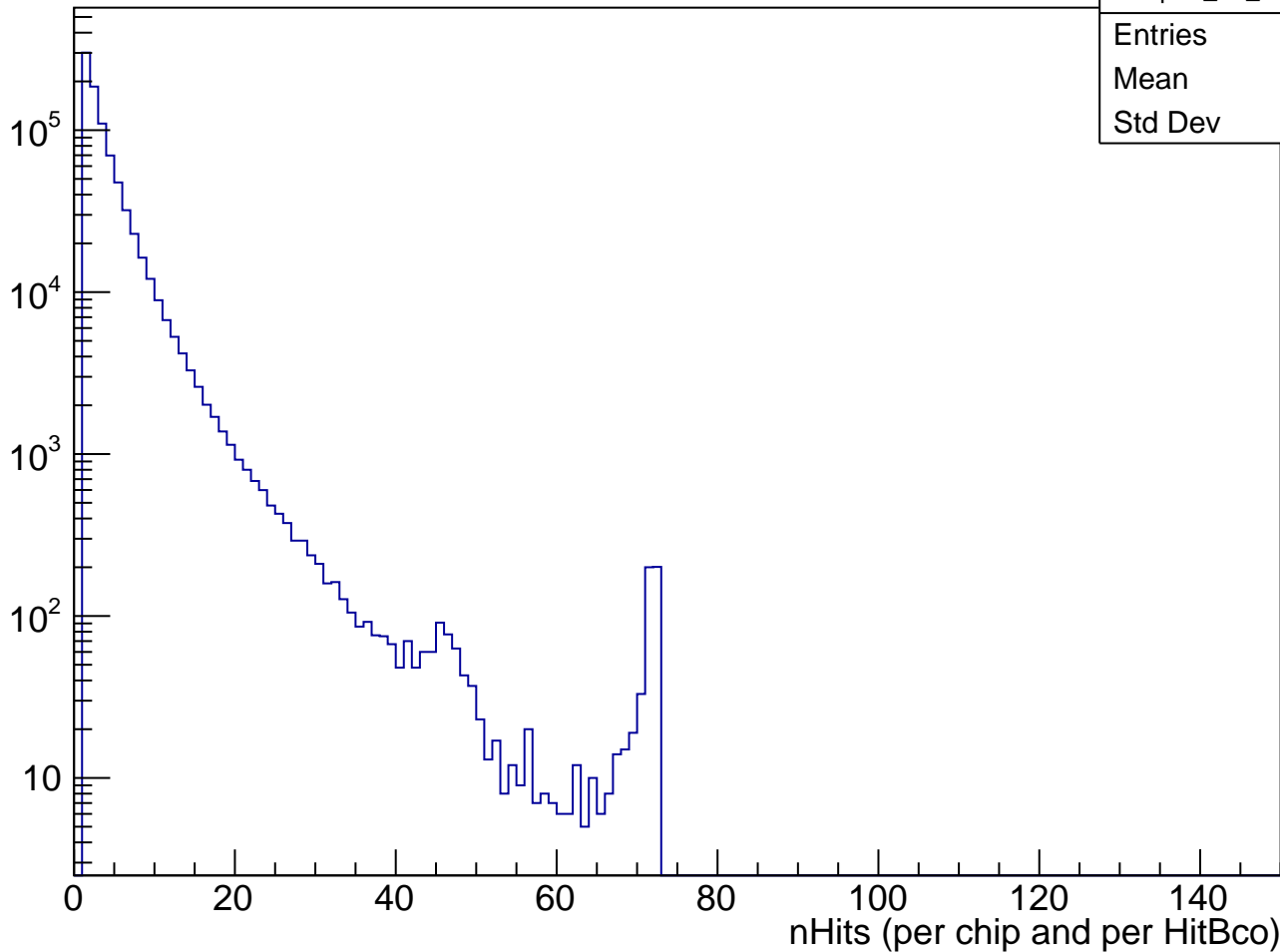
Mean 3.427

Std Dev 4.038

nHits (per chip and per HitBco)

nChipHit_F3_Fch3_Chip1

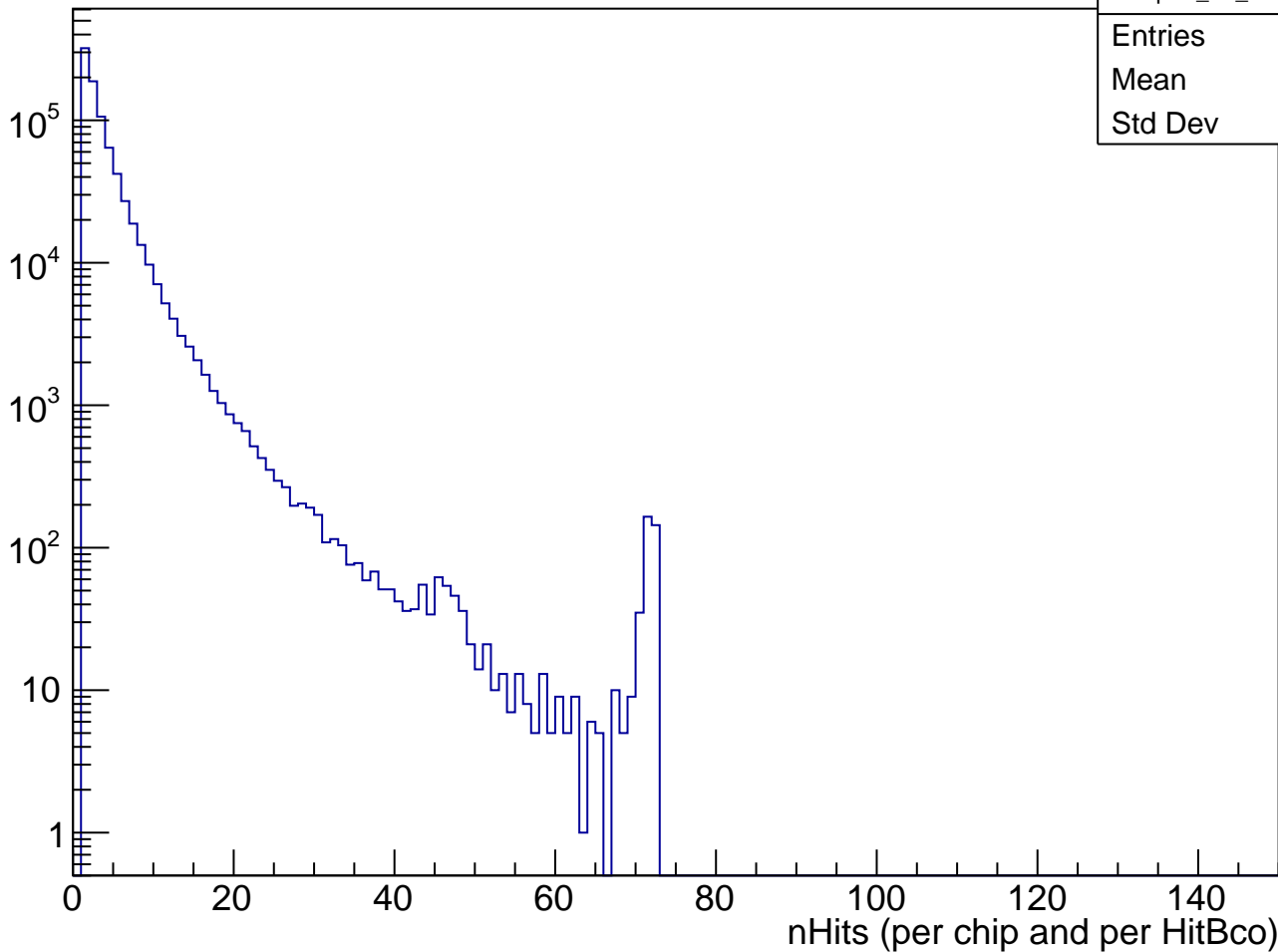
Entries



nChipHit_F3_Fch3_Chip1	
Entries	842335
Mean	3.34
Std Dev	4.049

nChipHit_F3_Fch3_Chip10

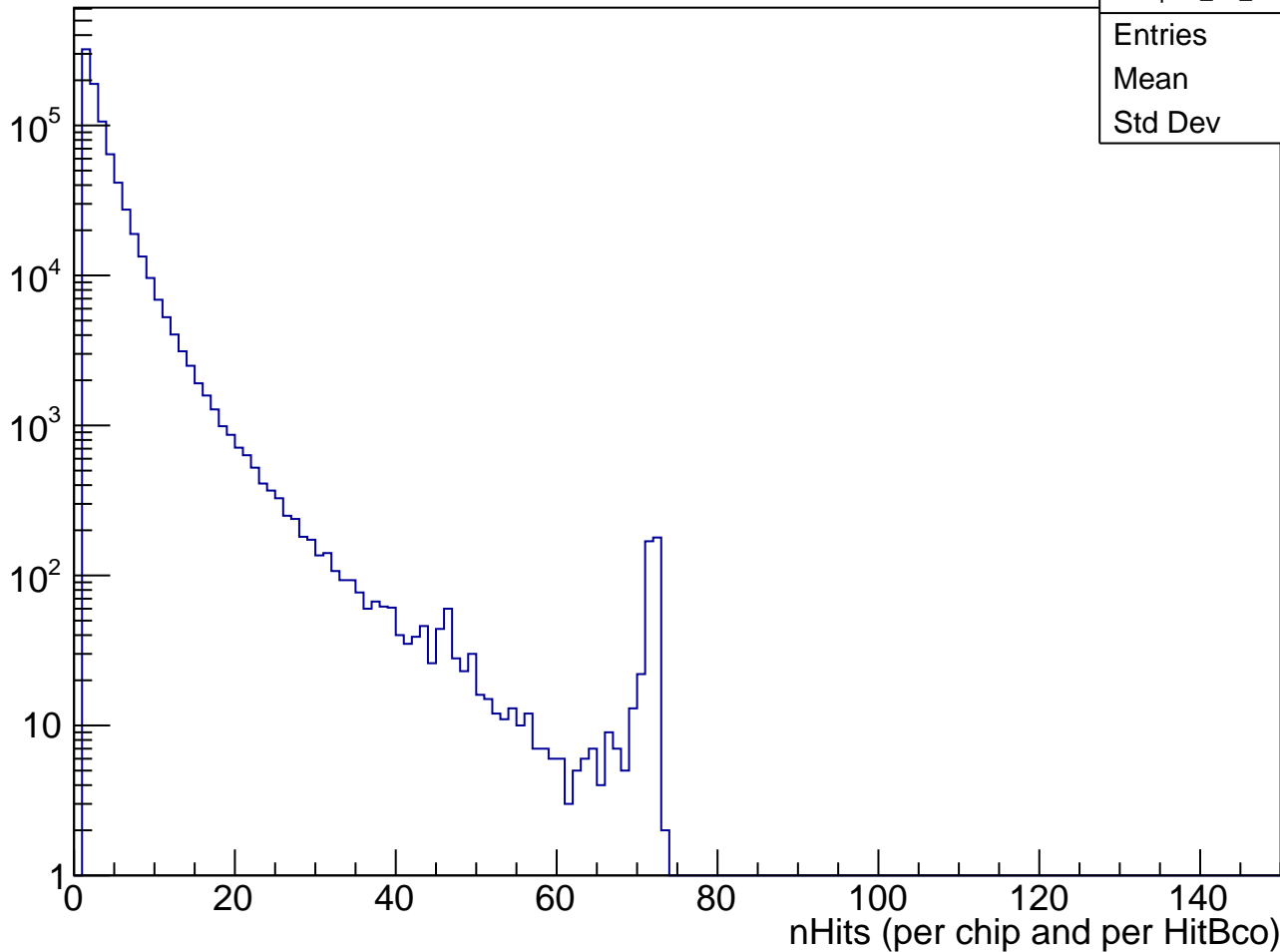
Entries



nChipHit_F3_Fch3_Chip10	
Entries	824524
Mean	3.042
Std Dev	3.667

nChipHit_F3_Fch3_Chip11

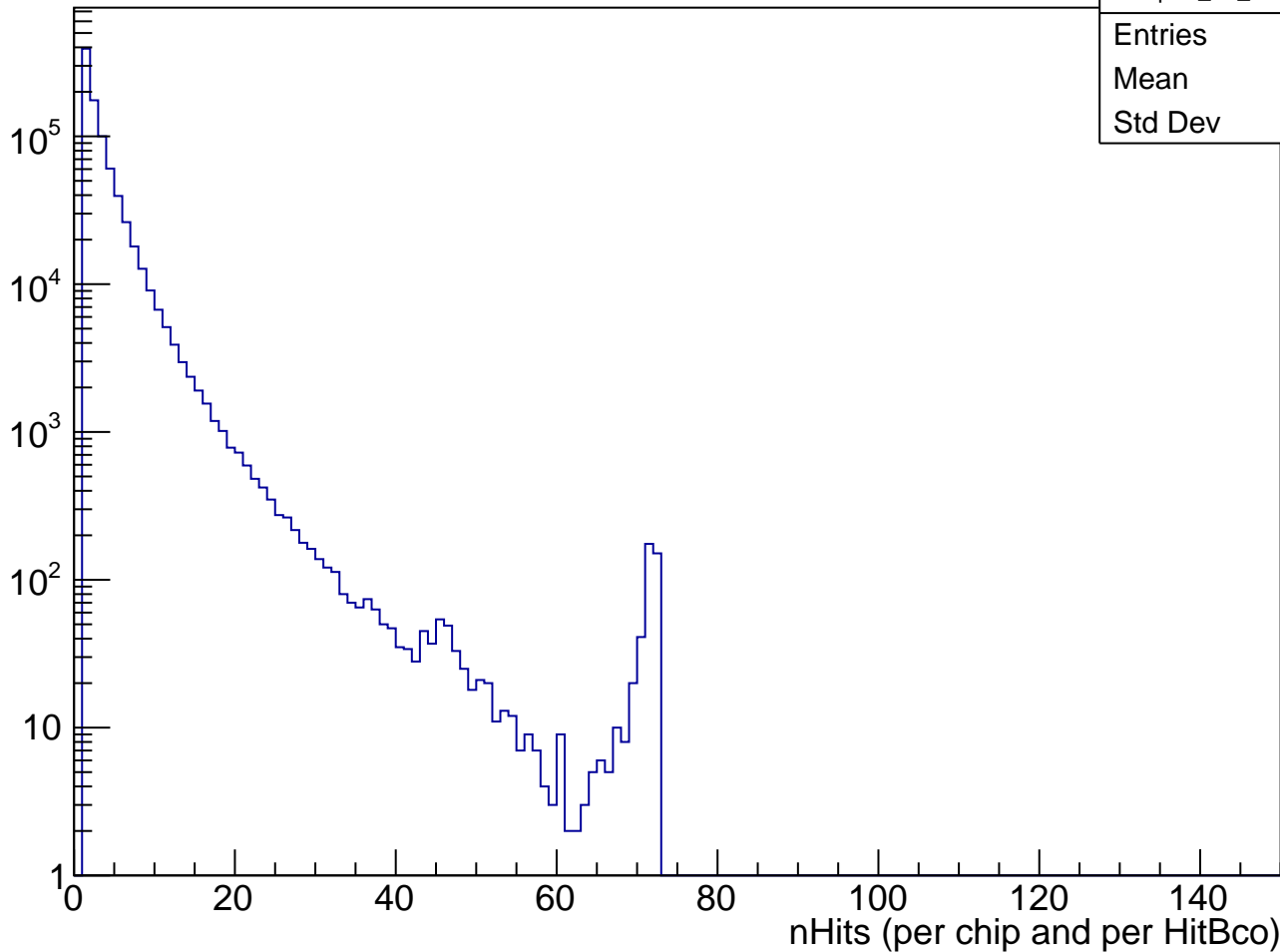
Entries



nChipHit_F3_Fch3_Chip11	
Entries	826303
Mean	3.033
Std Dev	3.67

nChipHit_F3_Fch3_Chip12

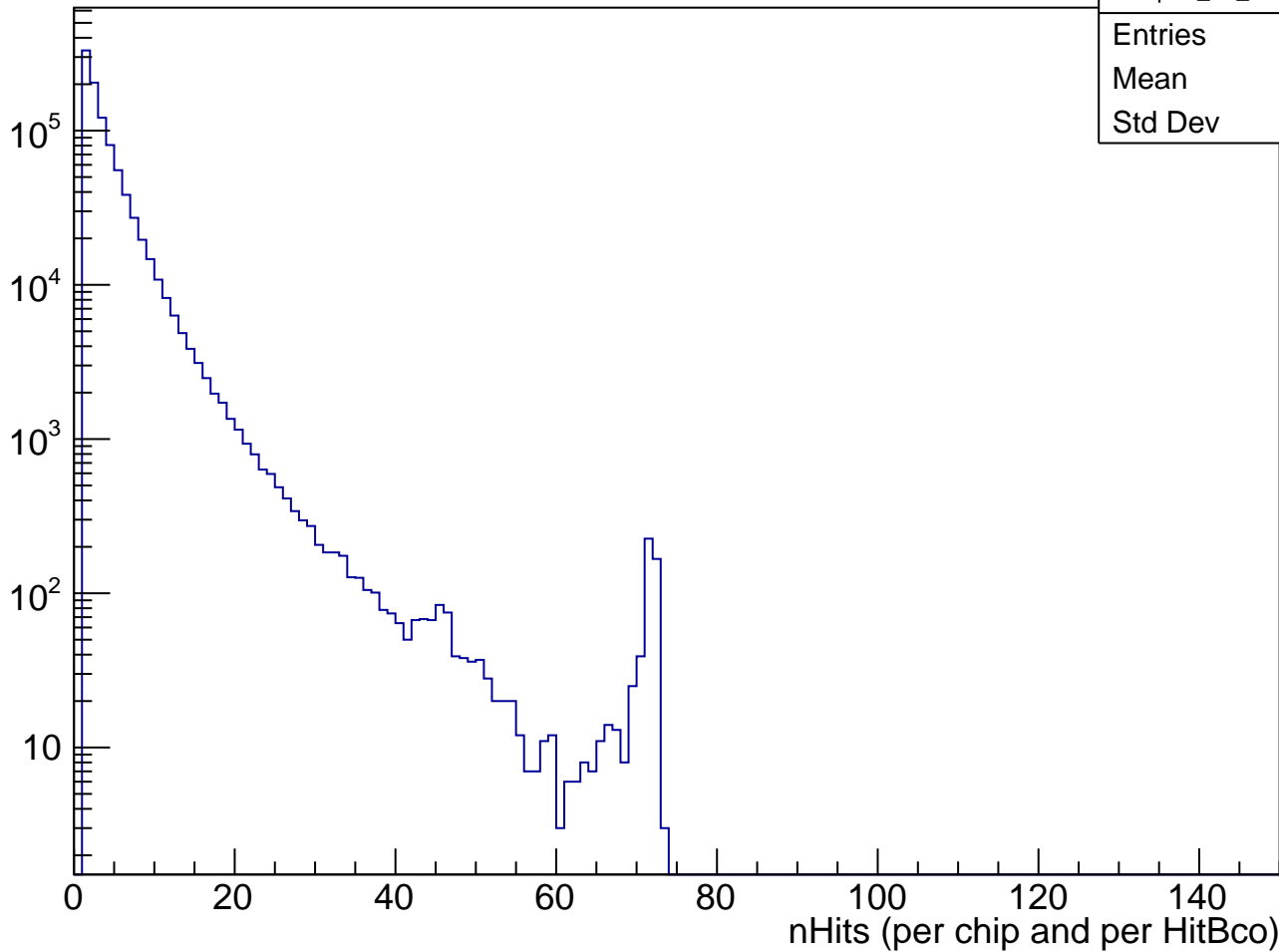
Entries



nChipHit_F3_Fch3_Chip12	
Entries	866164
Mean	2.843
Std Dev	3.567

nChipHit_F3_Fch3_Chip13

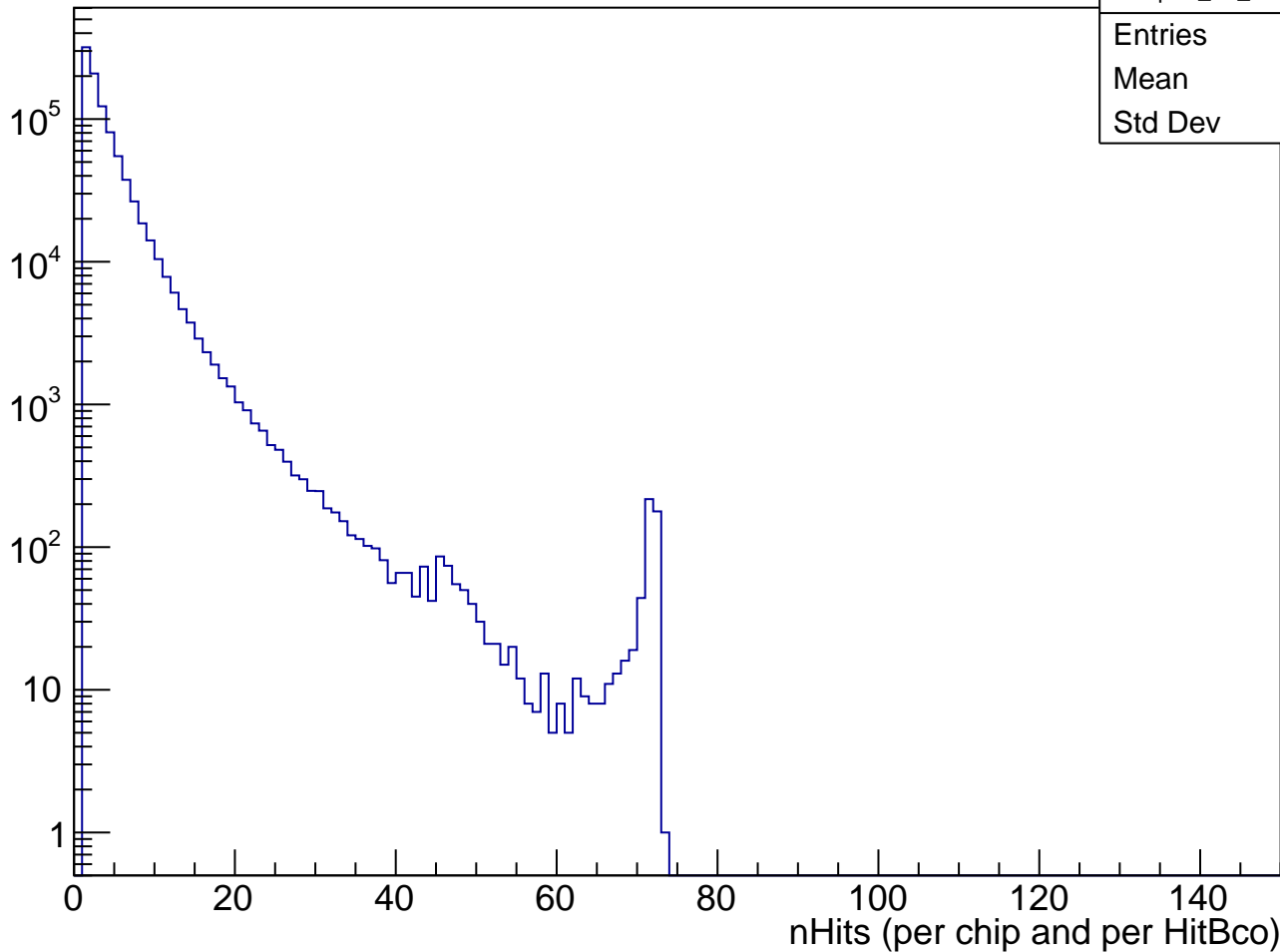
Entries



nChipHit_F3_Fch3_Chip13	
Entries	946315
Mean	3.421
Std Dev	4.057

nChipHit_F3_Fch3_Chip14

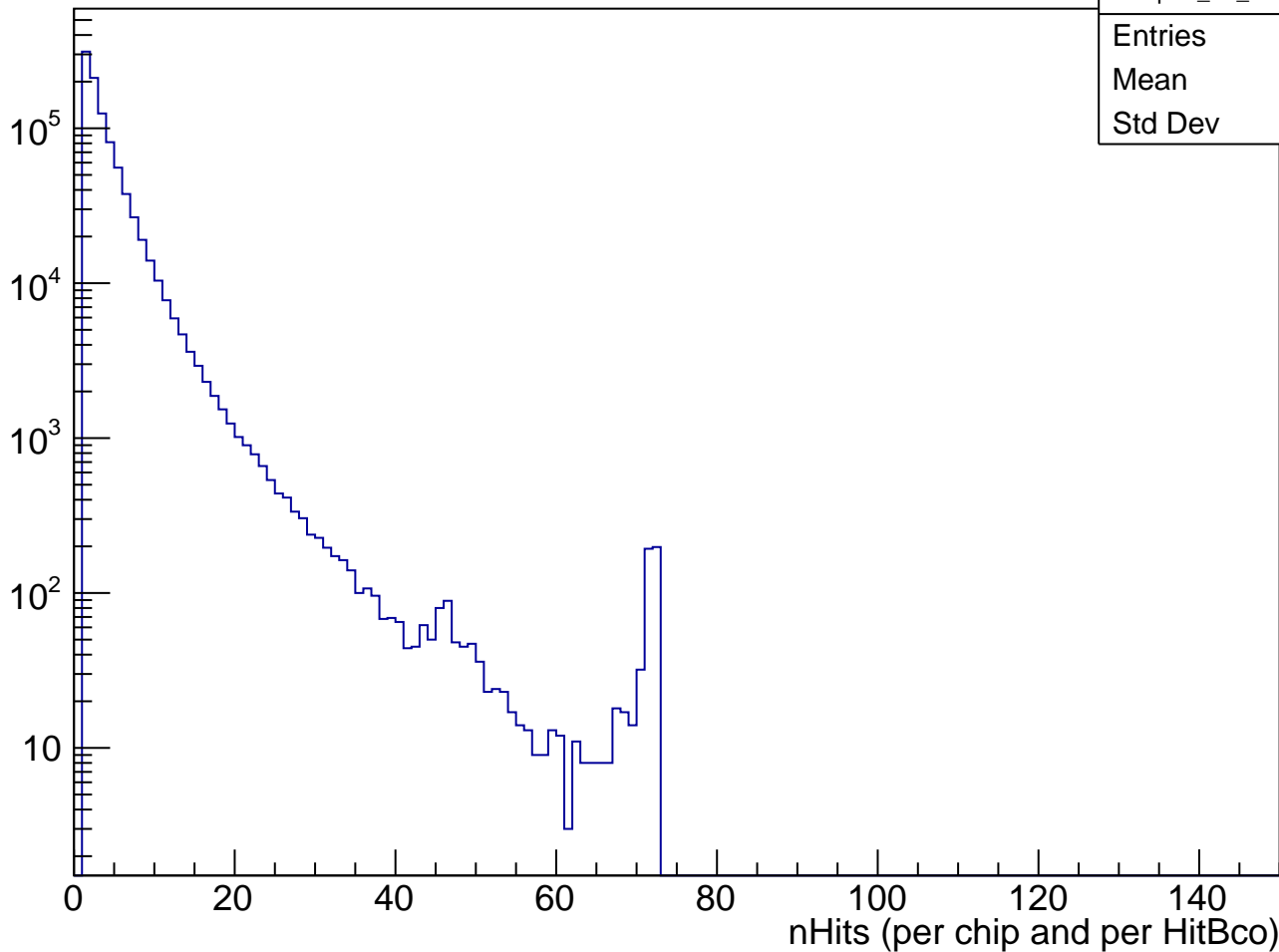
Entries



nChipHit_F3_Fch3_Chip14	
Entries	933522
Mean	3.404
Std Dev	4.023

nChipHit_F3_Fch3_Chip15

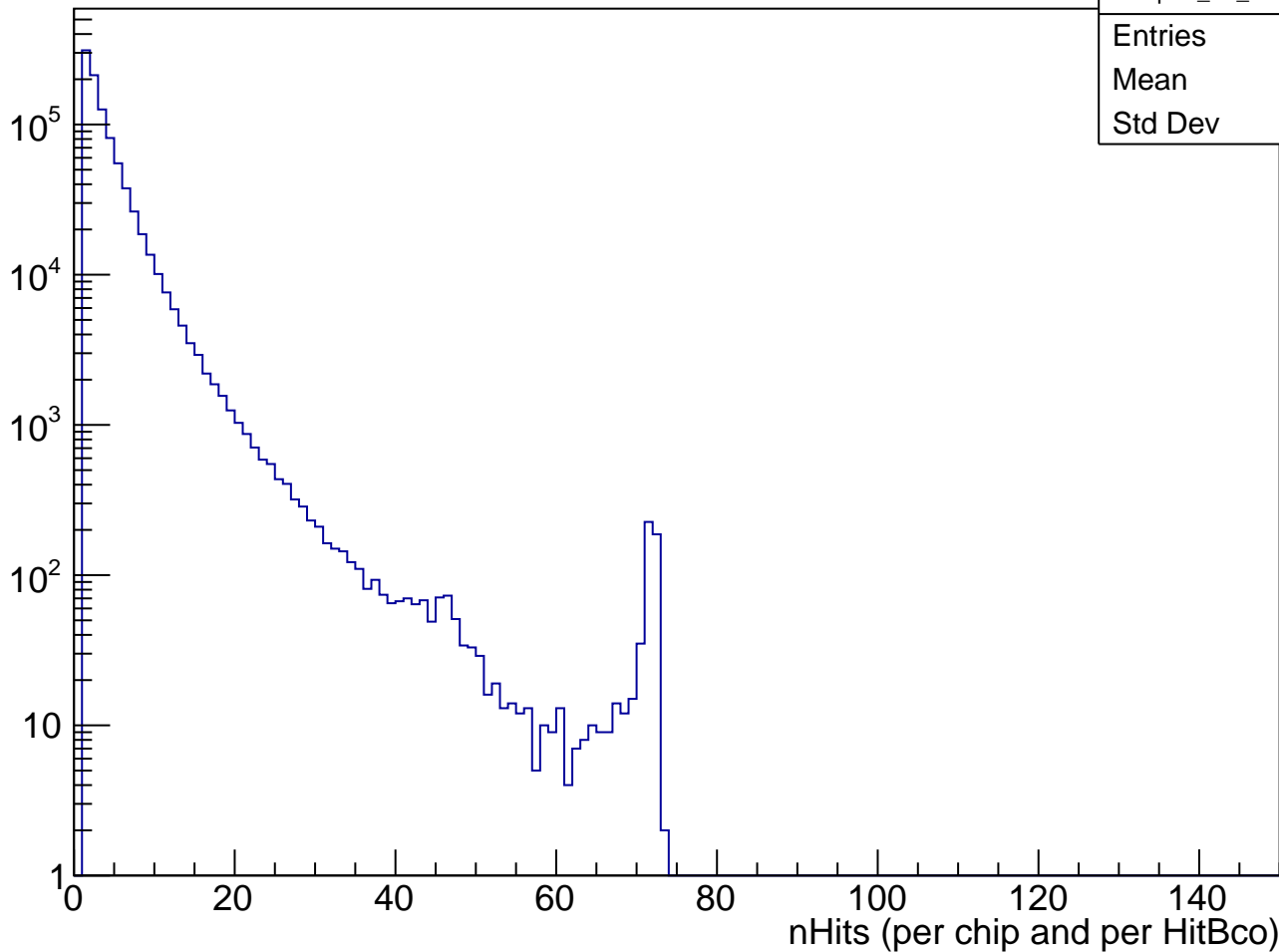
Entries



nChipHit_F3_Fch3_Chip15	
Entries	933376
Mean	3.414
Std Dev	4.012

nChipHit_F3_Fch3_Chip16

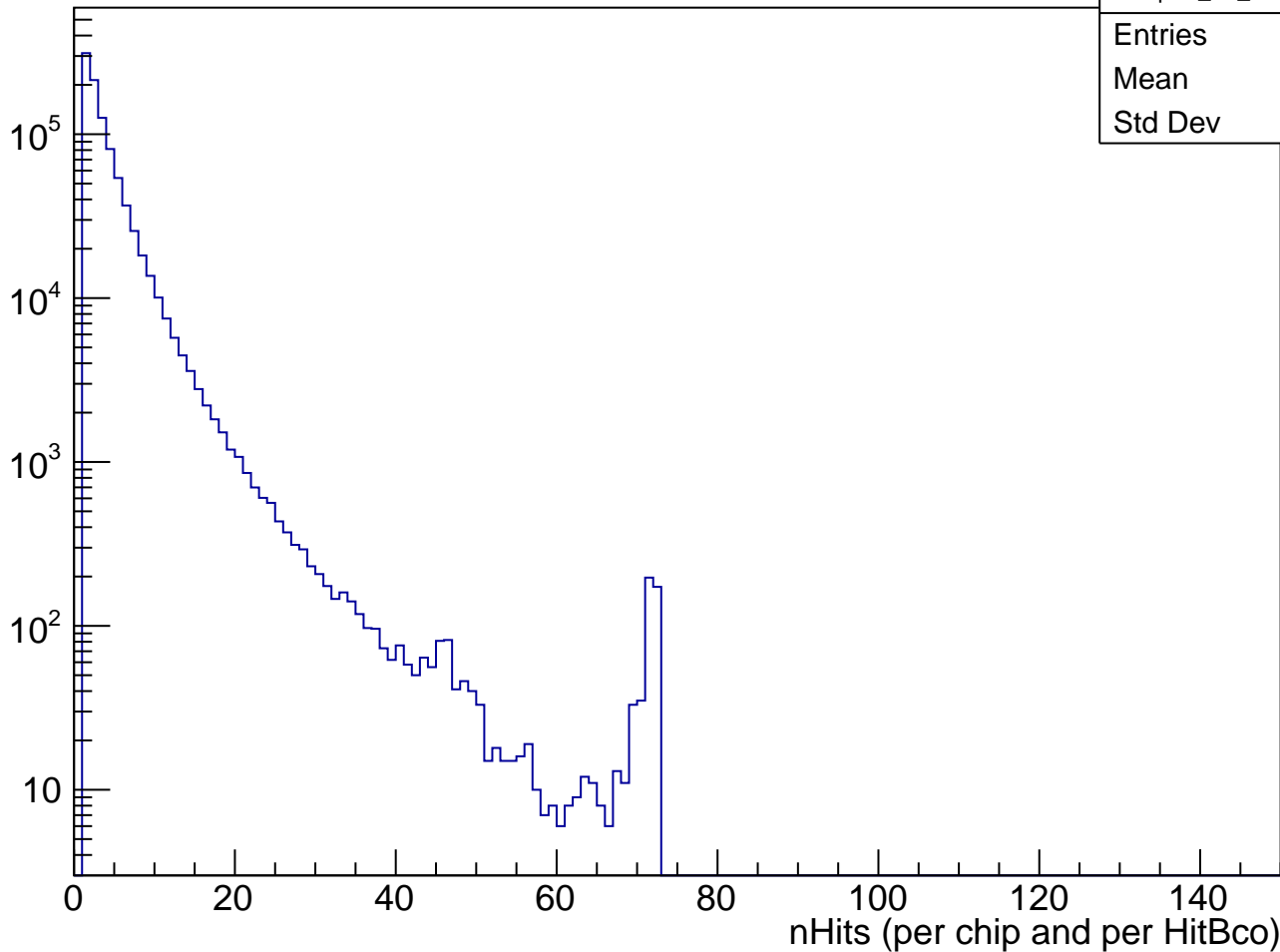
Entries



nChipHit_F3_Fch3_Chip16	
Entries	932469
Mean	3.391
Std Dev	3.974

nChipHit_F3_Fch3_Chip17

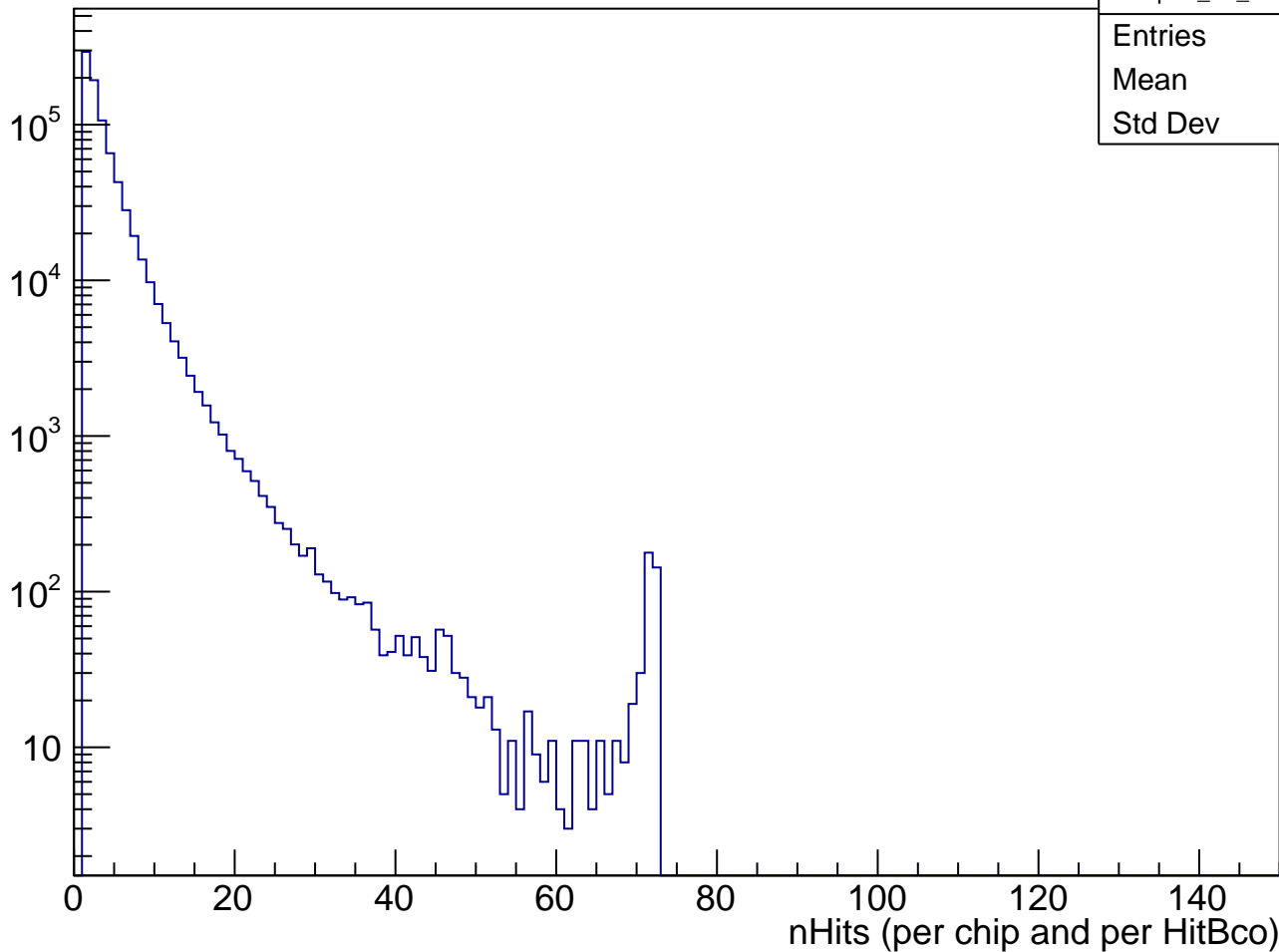
Entries



nChipHit_F3_Fch3_Chip17	
Entries	930613
Mean	3.374
Std Dev	3.965

nChipHit_F3_Fch3_Chip18

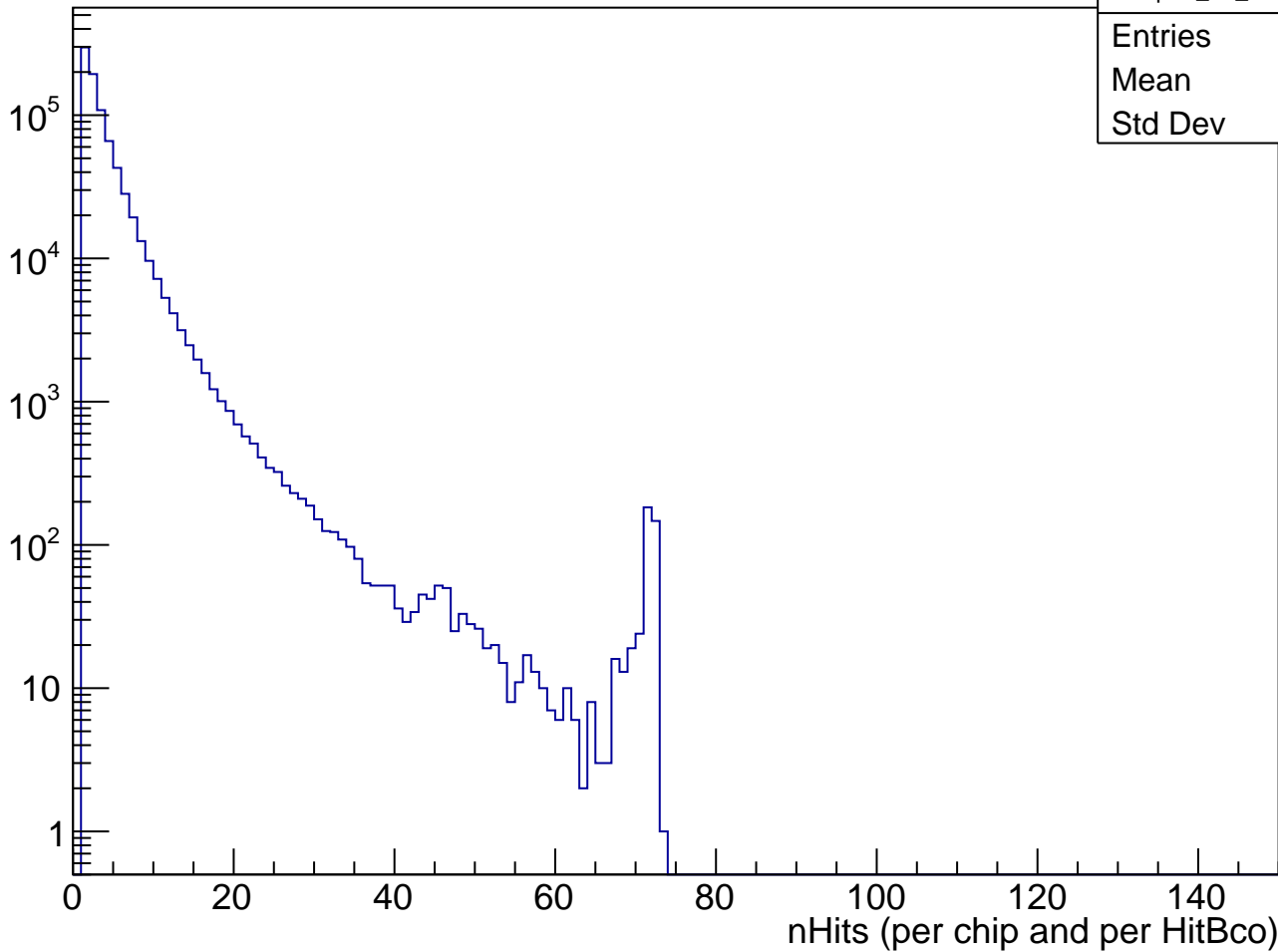
Entries



nChipHit_F3_Fch3_Chip18	
Entries	806760
Mean	3.106
Std Dev	3.683

nChipHit_F3_Fch3_Chip19

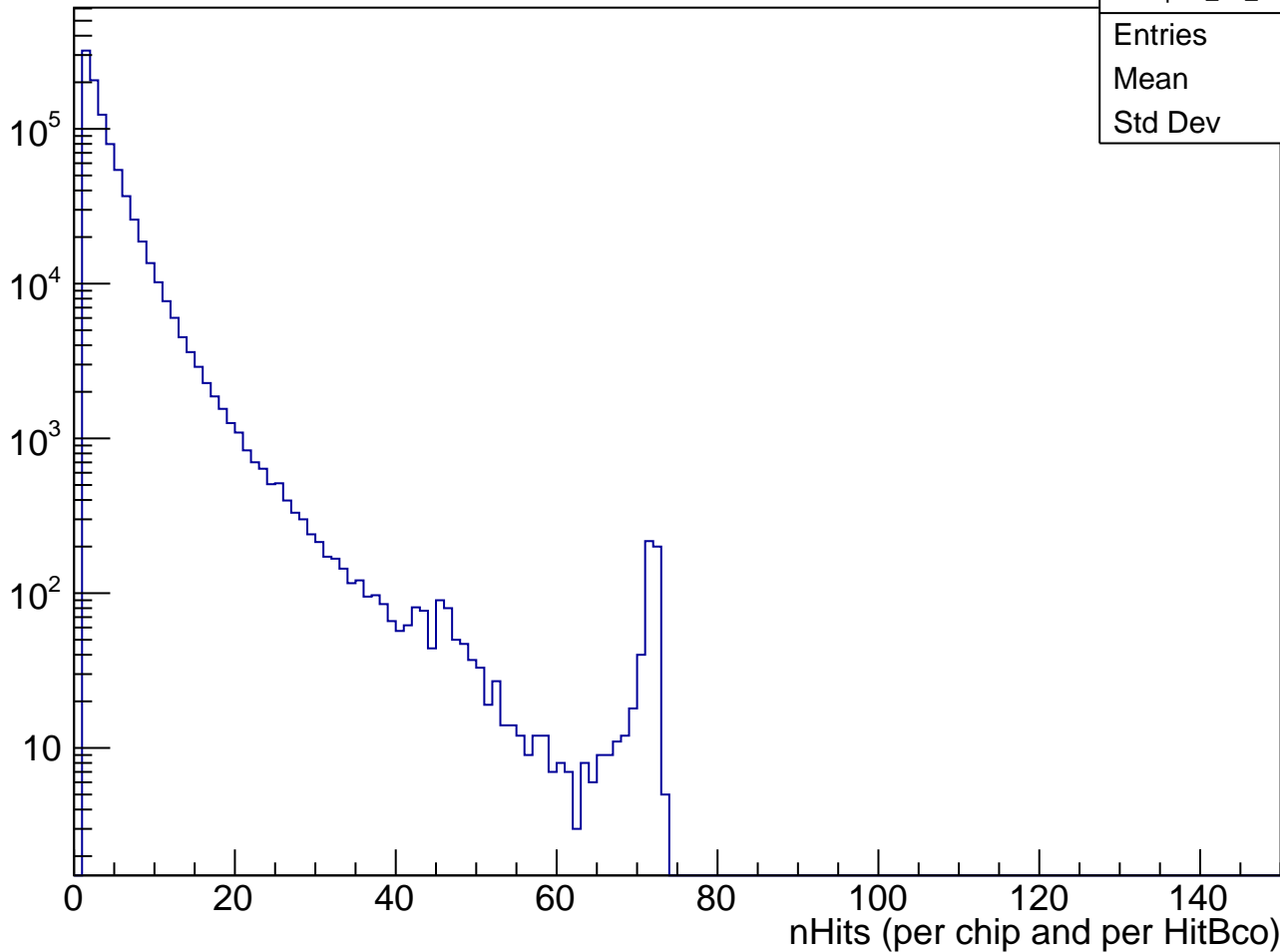
Entries



nChipHit_F3_Fch3_Chip19	
Entries	813071
Mean	3.104
Std Dev	3.704

nChipHit_F3_Fch3_Chip2

Entries

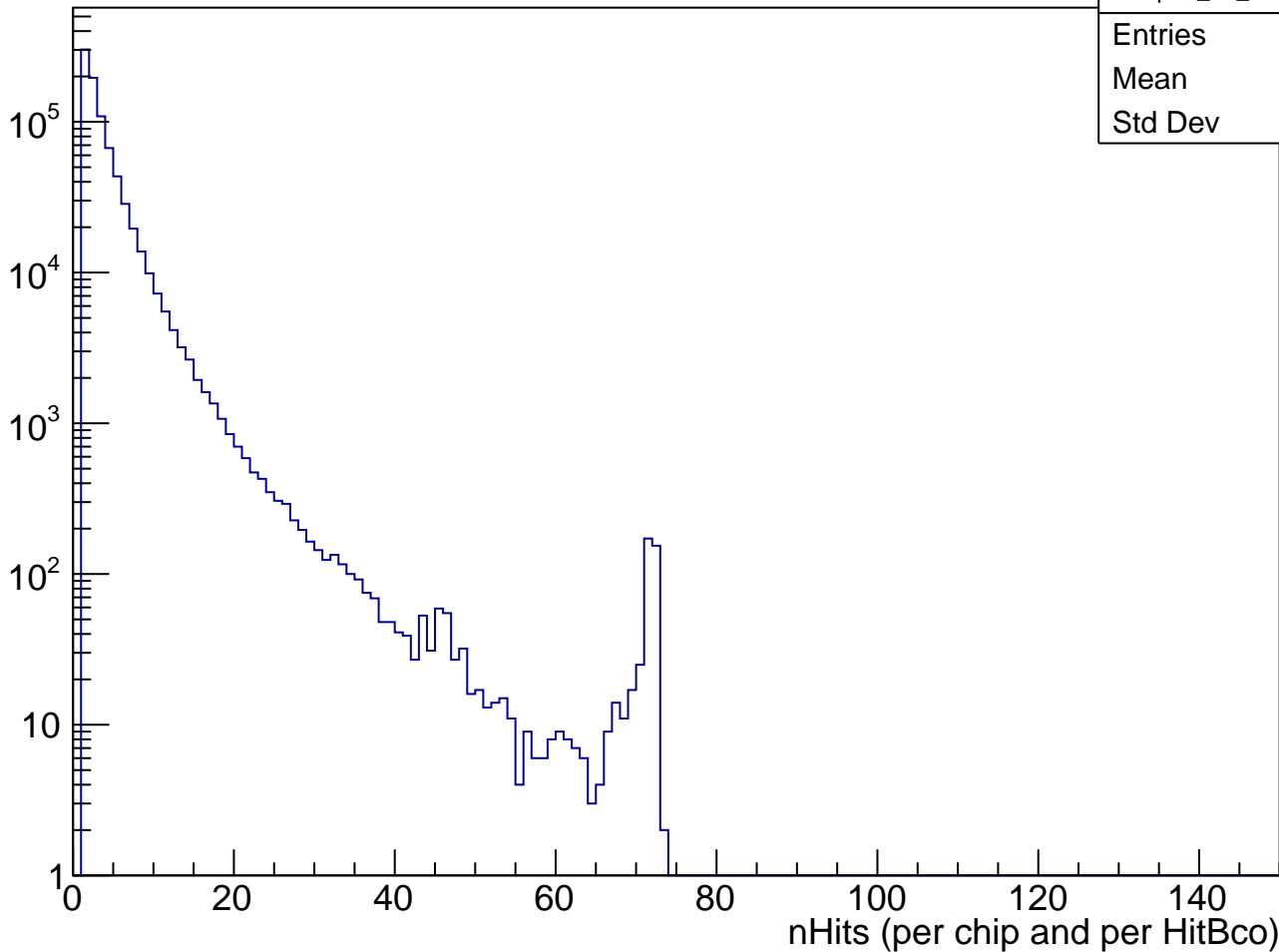


nChipHit_F3_Fch3_Chip2	
Entries	929093
Mean	3.386
Std Dev	4.026

nHits (per chip and per HitBco)

nChipHit_F3_Fch3_Chip20

Entries

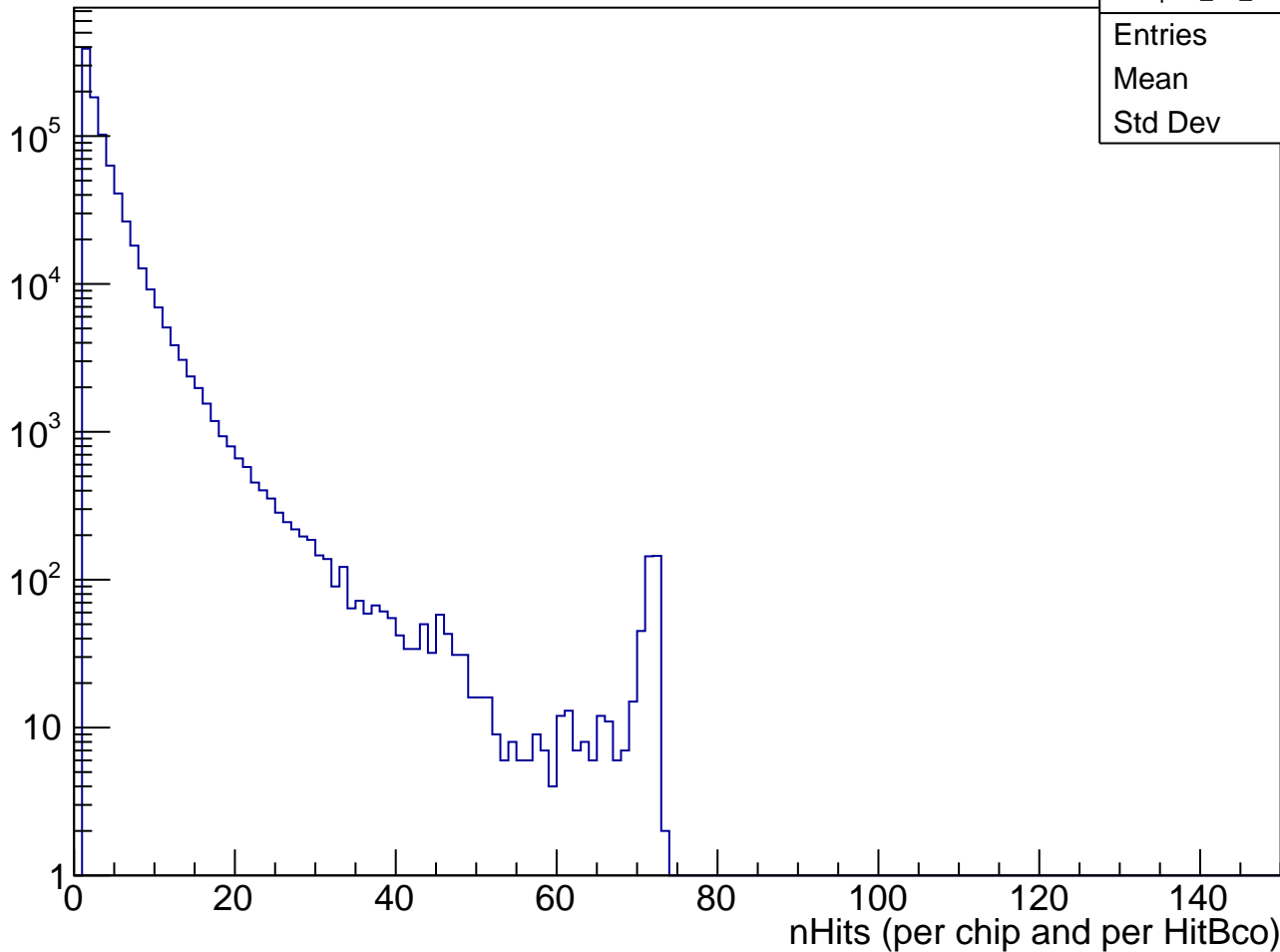


nChipHit_F3_Fch3_Chip20	
Entries	824222
Mean	3.106
Std Dev	3.689

nHits (per chip and per HitBco)

nChipHit_F3_Fch3_Chip21

Entries

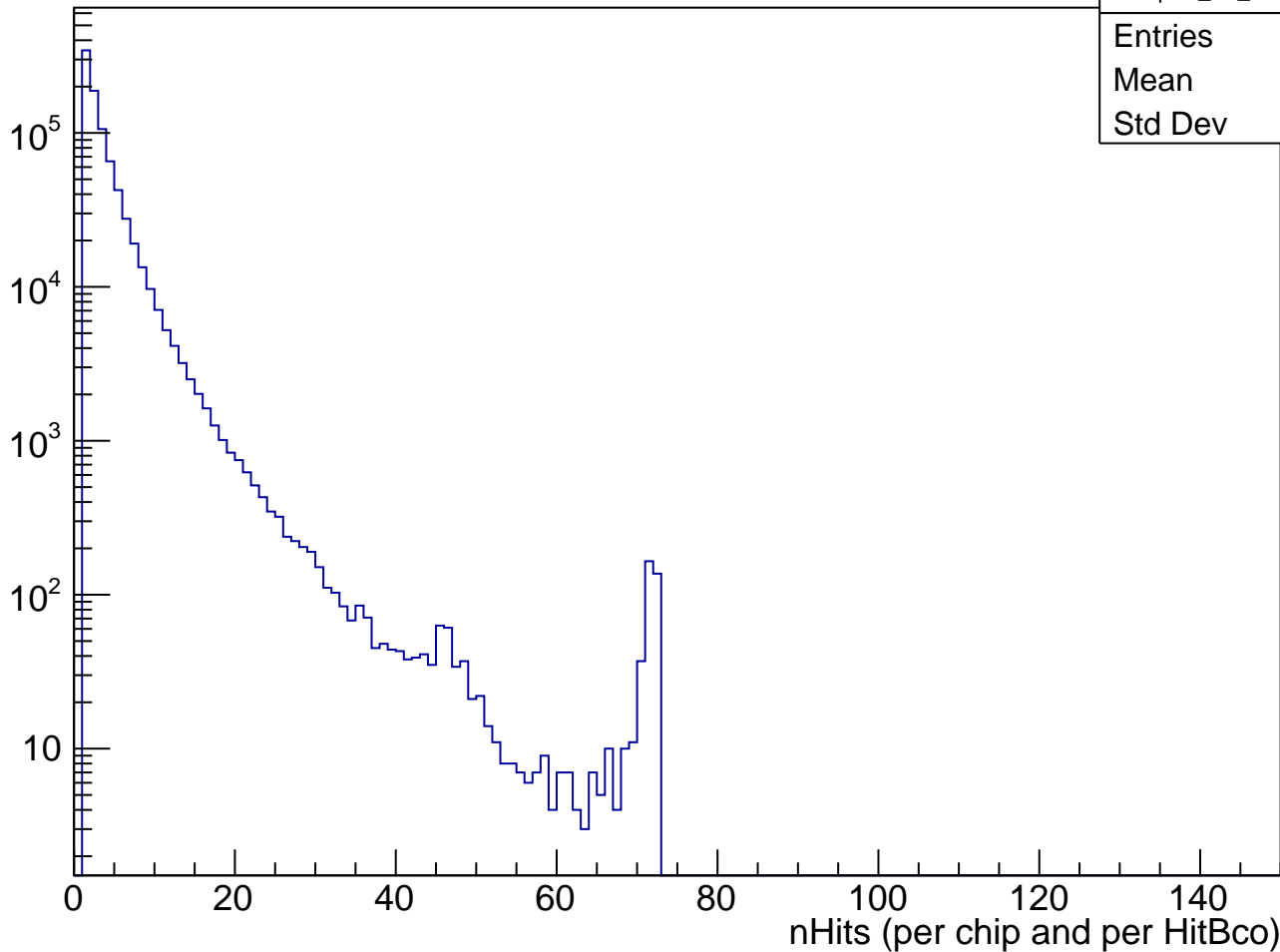


nChipHit_F3_Fch3_Chip21	
Entries	878697
Mean	2.851
Std Dev	3.539

nHits (per chip and per HitBco)

nChipHit_F3_Fch3_Chip22

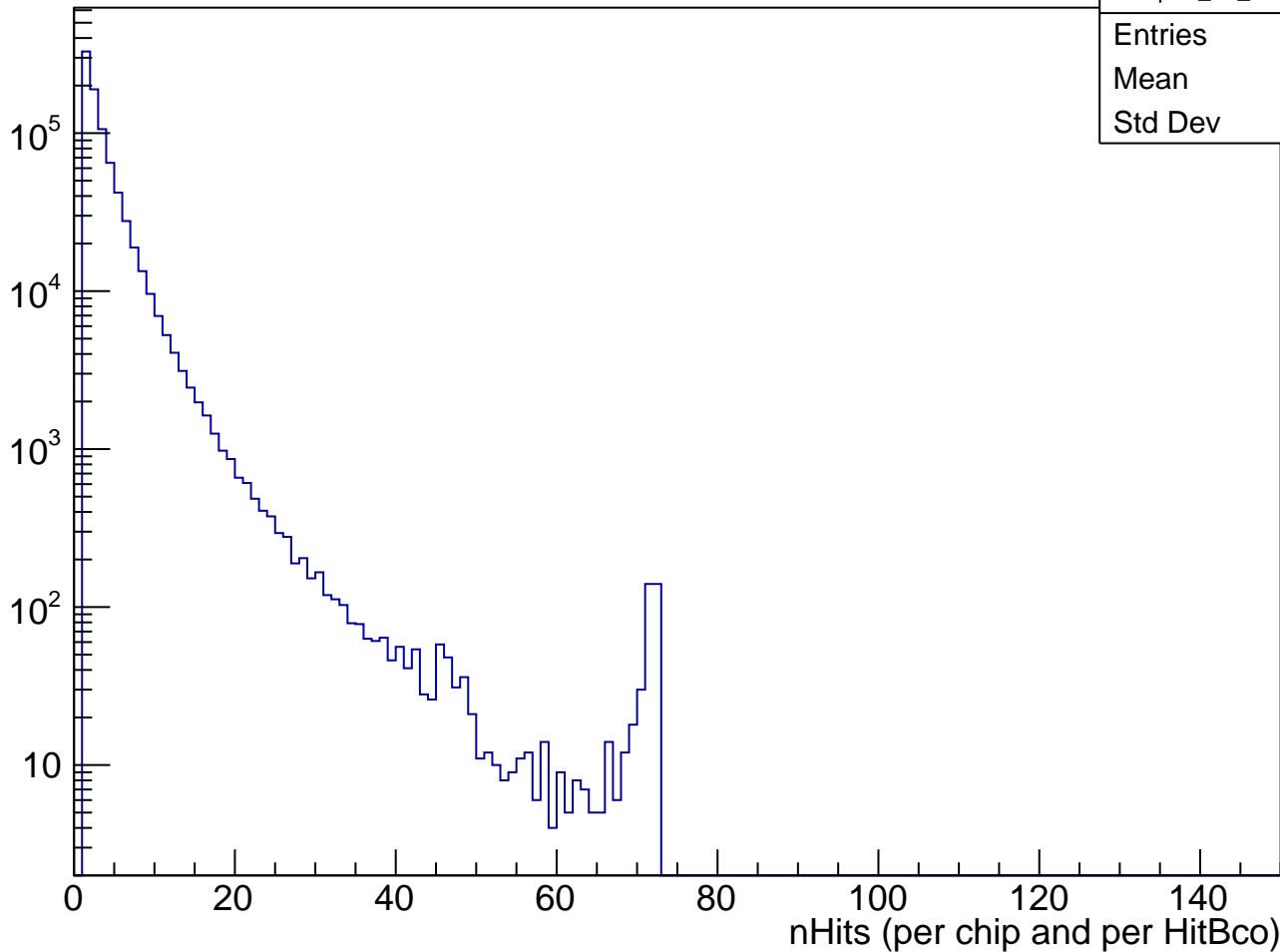
Entries



nChipHit_F3_Fch3_Chip22	
Entries	849901
Mean	2.989
Std Dev	3.61

nChipHit_F3_Fch3_Chip23

Entries

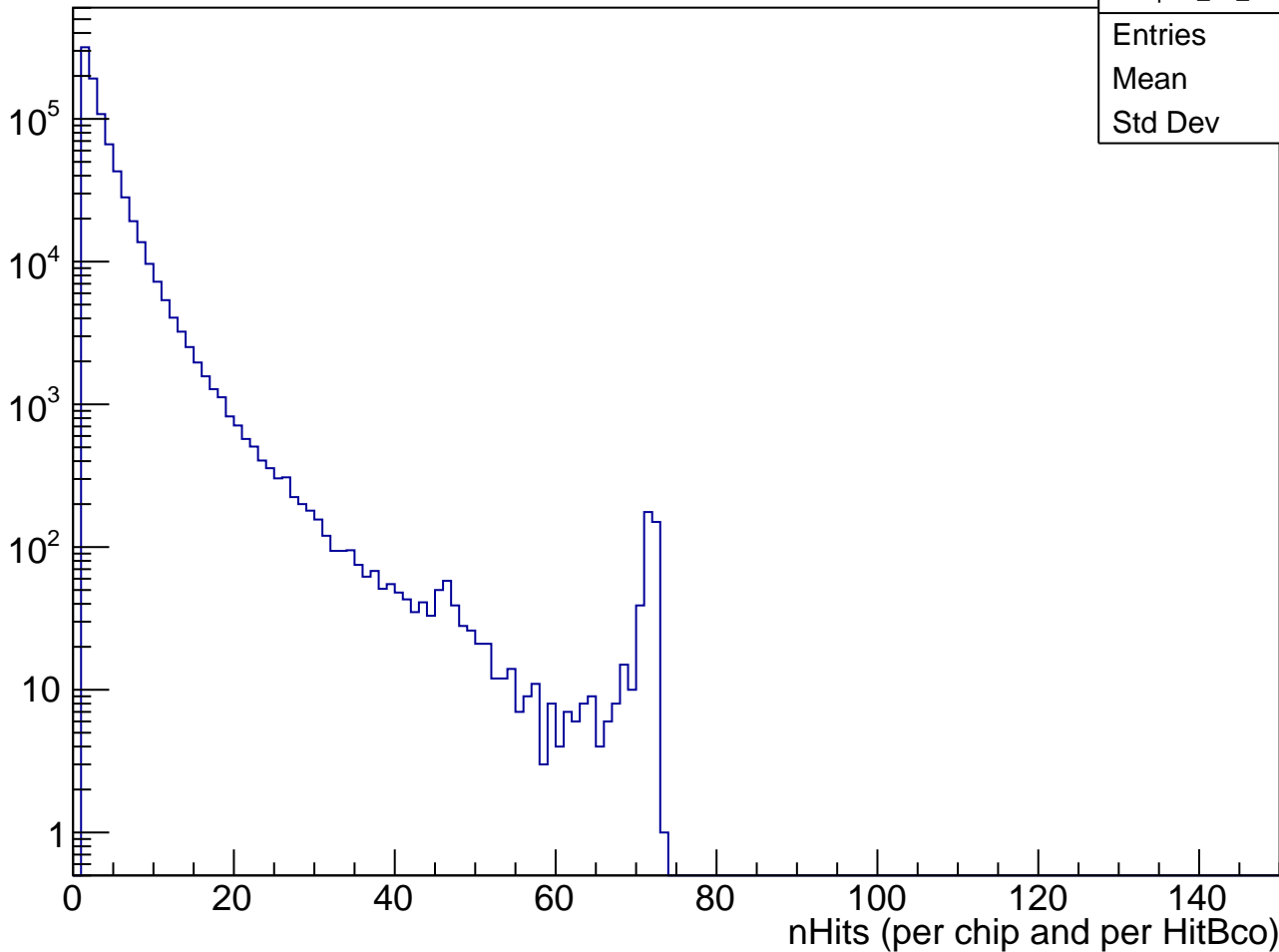


nChipHit_F3_Fch3_Chip23	
Entries	834559
Mean	3.013
Std Dev	3.616

nHits (per chip and per HitBco)

nChipHit_F3_Fch3_Chip24

Entries

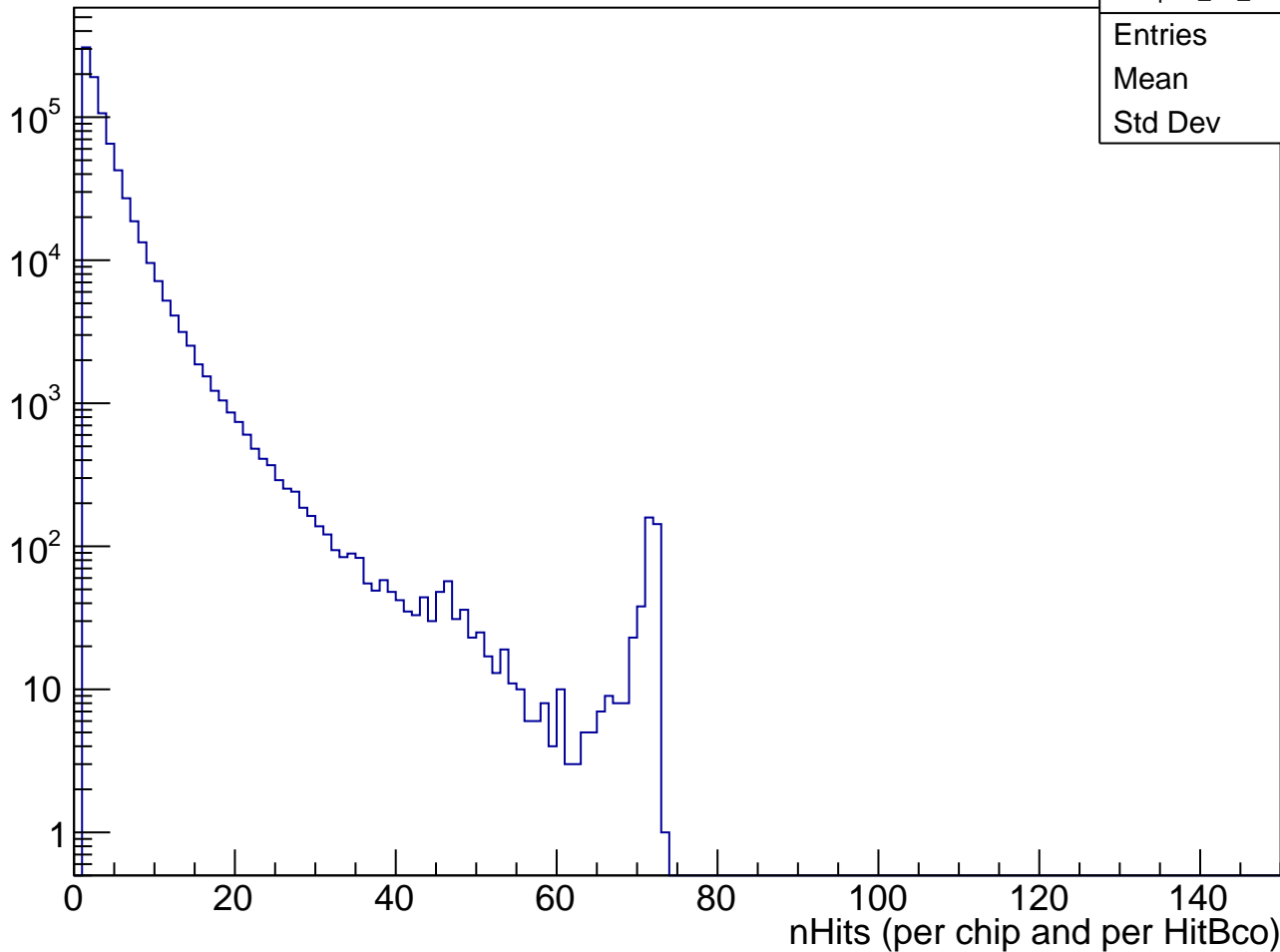


nChipHit_F3_Fch3_Chip24	
Entries	832545
Mean	3.06
Std Dev	3.676

nHits (per chip and per HitBco)

nChipHit_F3_Fch3_Chip25

Entries

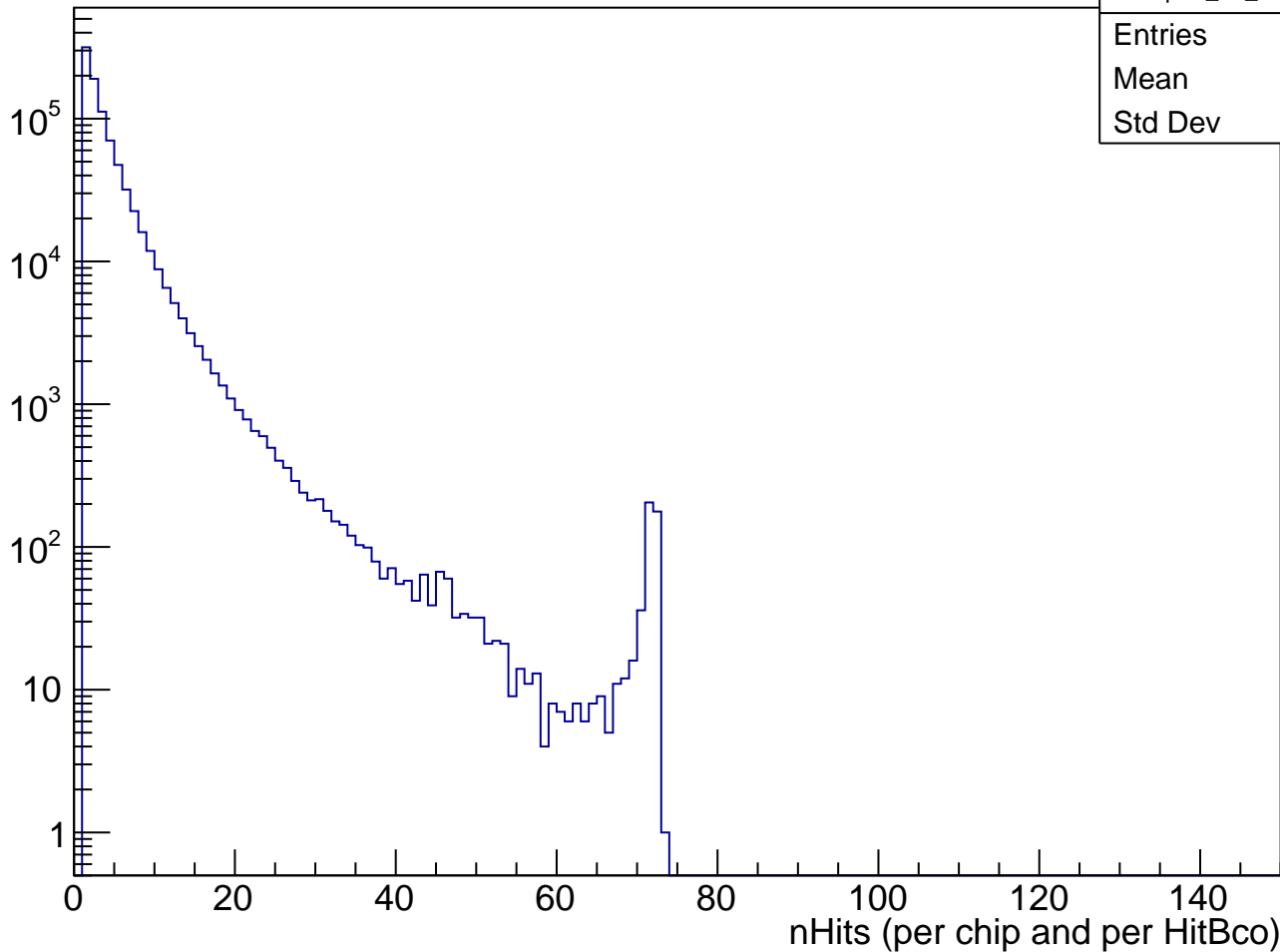


nChipHit_F3_Fch3_Chip25	
Entries	815436
Mean	3.064
Std Dev	3.659

nHits (per chip and per HitBco)

nChipHit_F3_Fch3_Chip3

Entries

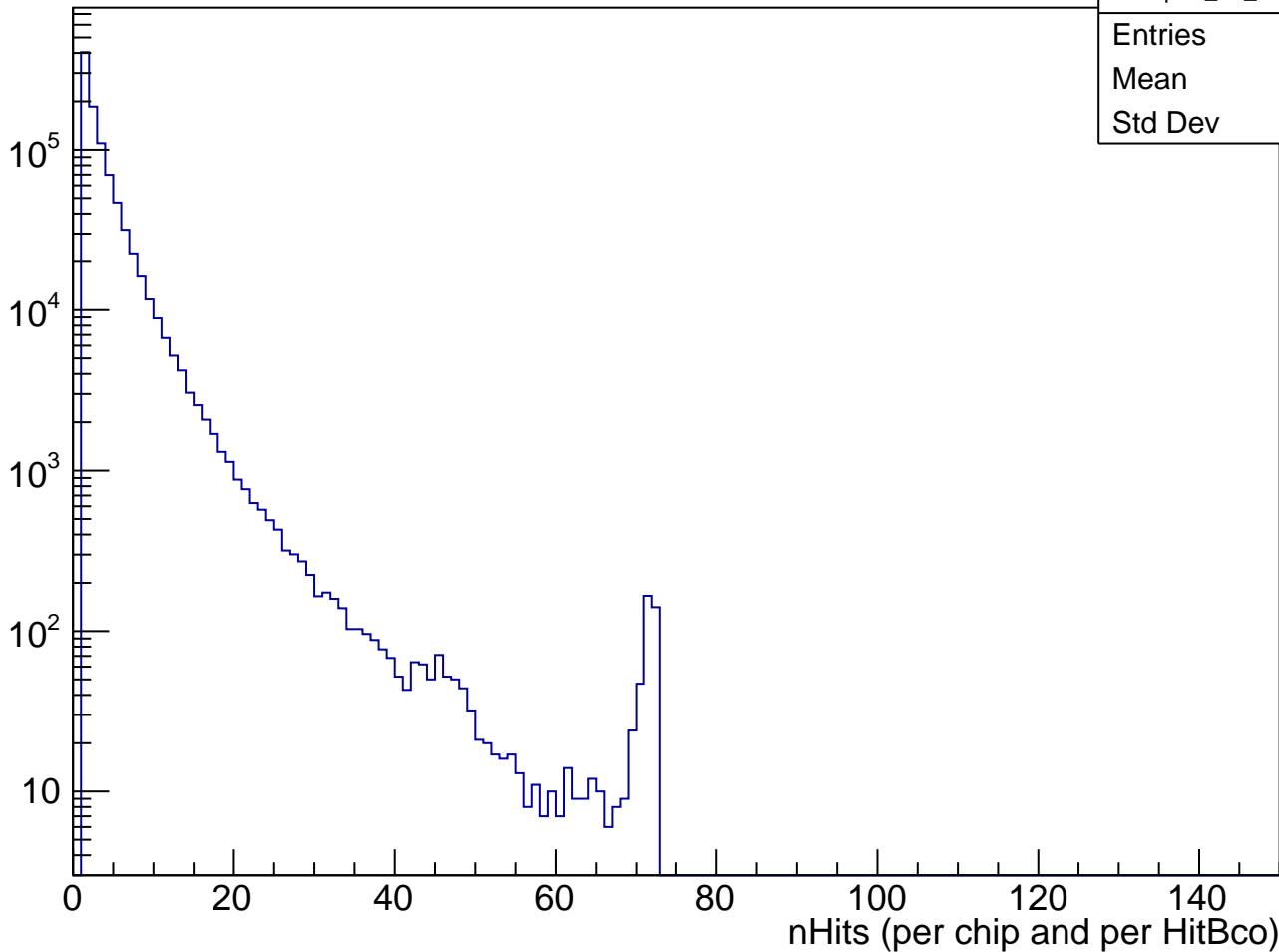


nChipHit_F3_Fch3_Chip3	
Entries	861852
Mean	3.267
Std Dev	3.956

nHits (per chip and per HitBco)

nChipHit_F3_Fch3_Chip4

Entries



nChipHit_F3_Fch3_Chip4

Entries 942276

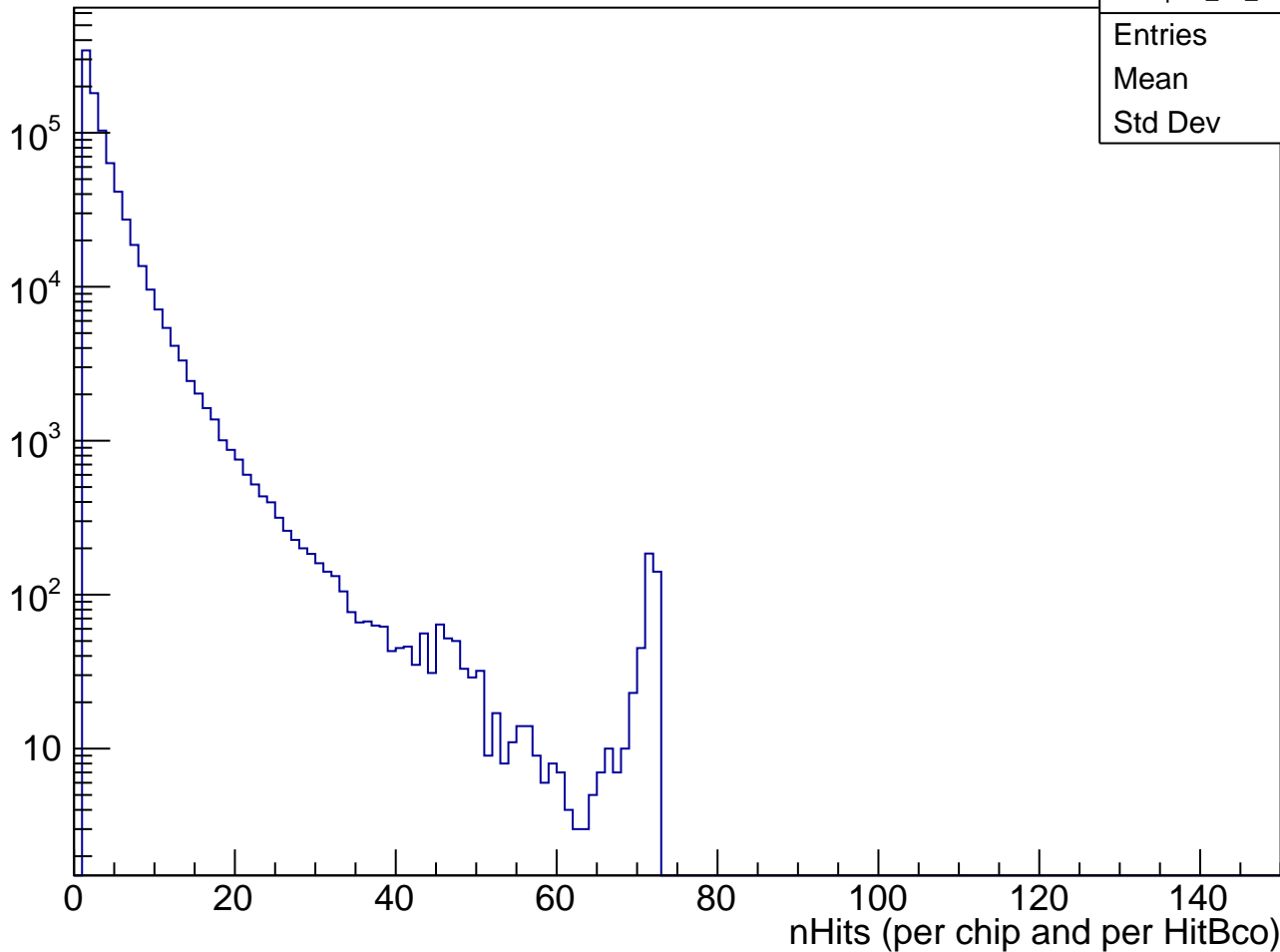
Mean 3.058

Std Dev 3.813

nHits (per chip and per HitBco)

nChipHit_F3_Fch3_Chip5

Entries



nChipHit_F3_Fch3_Chip5

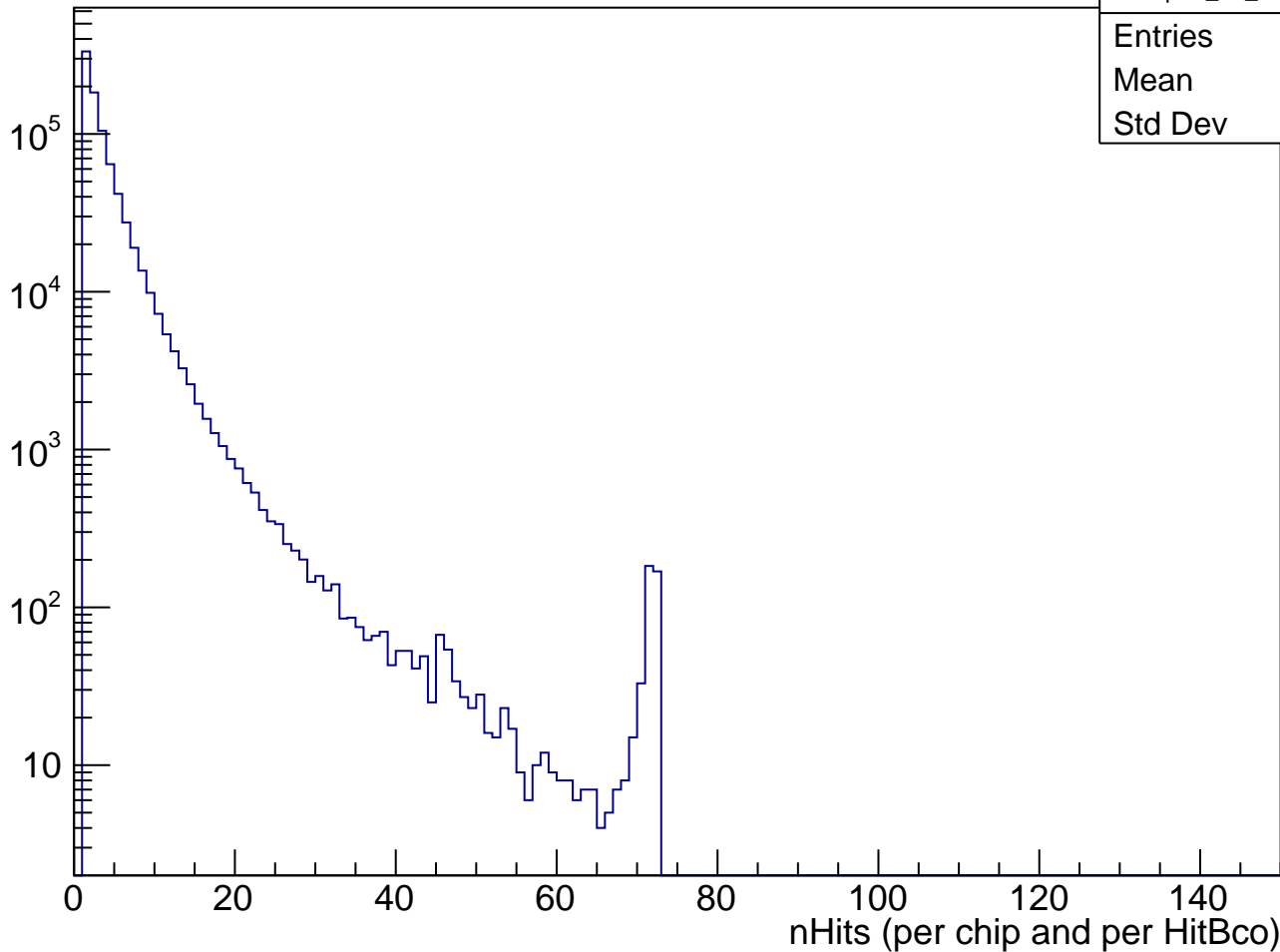
Entries 836995

Mean 3.008

Std Dev 3.716

nChipHit_F3_Fch3_Chip6

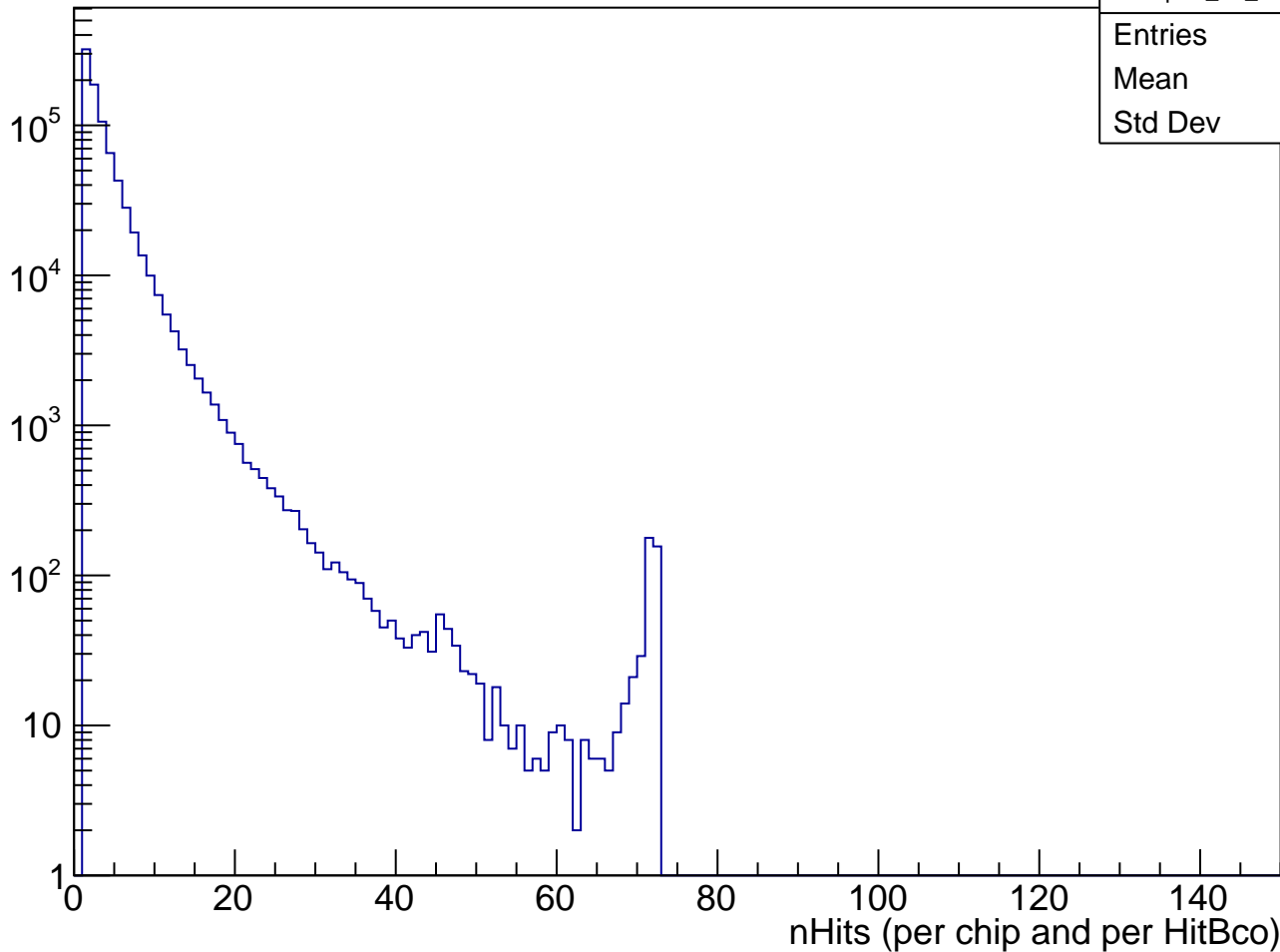
Entries



nChipHit_F3_Fch3_Chip6	
Entries	832581
Mean	3.034
Std Dev	3.724

nChipHit_F3_Fch3_Chip7

Entries



nChipHit_F3_Fch3_Chip7

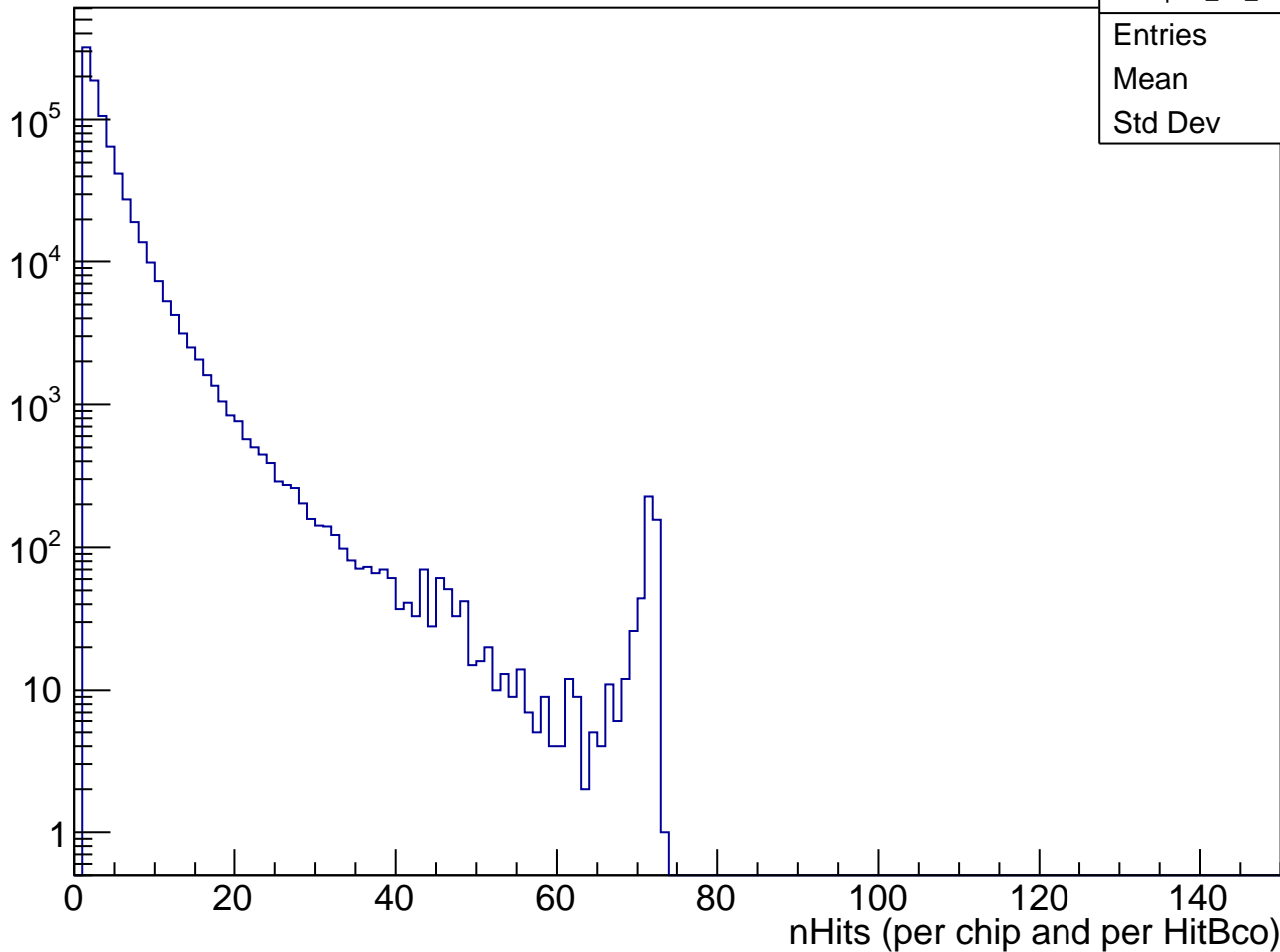
Entries 828889

Mean 3.068

Std Dev 3.698

nChipHit_F3_Fch3_Chip8

Entries



nChipHit_F3_Fch3_Chip8

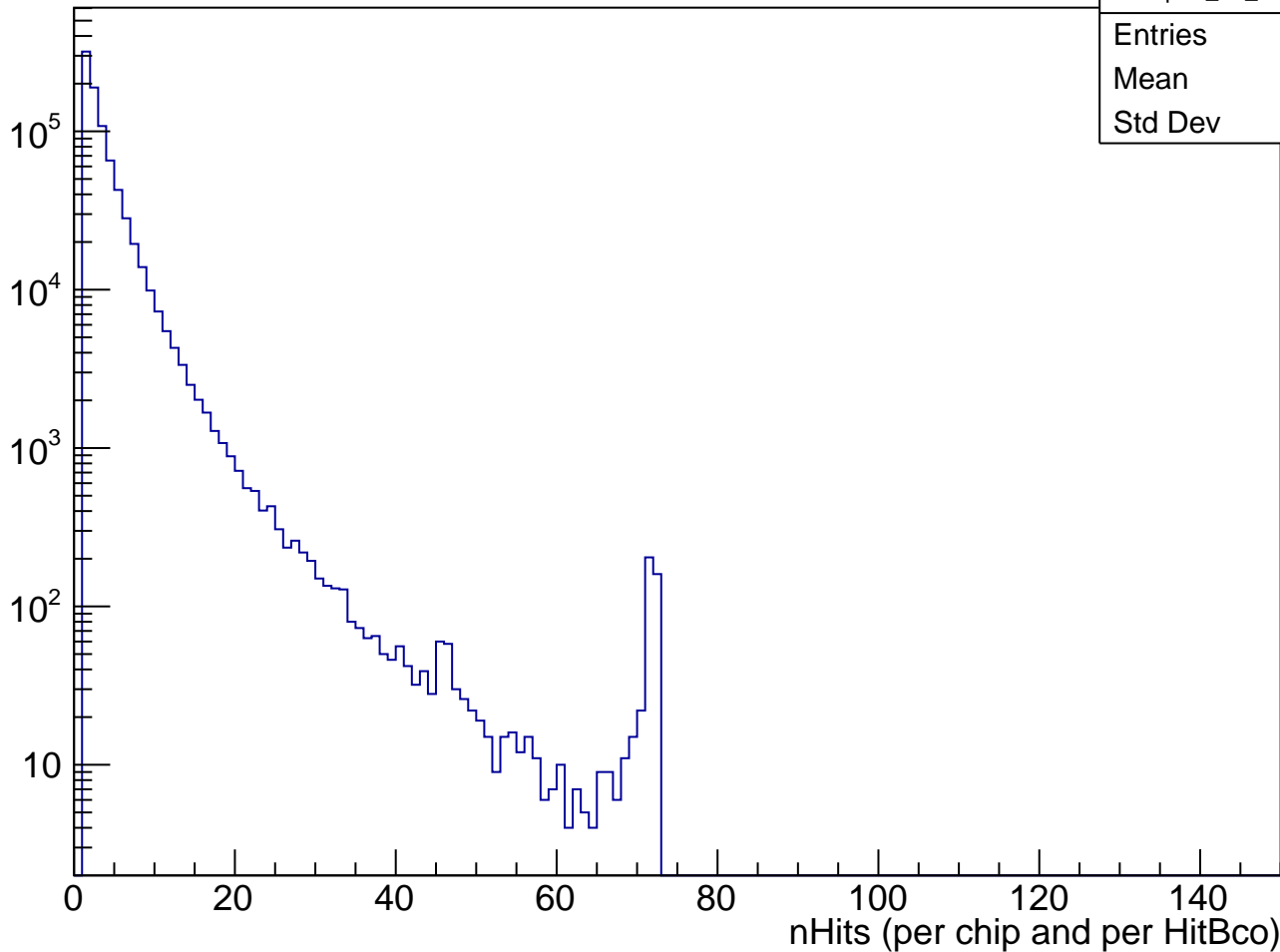
Entries 825035

Mean 3.067

Std Dev 3.764

nChipHit_F3_Fch3_Chip9

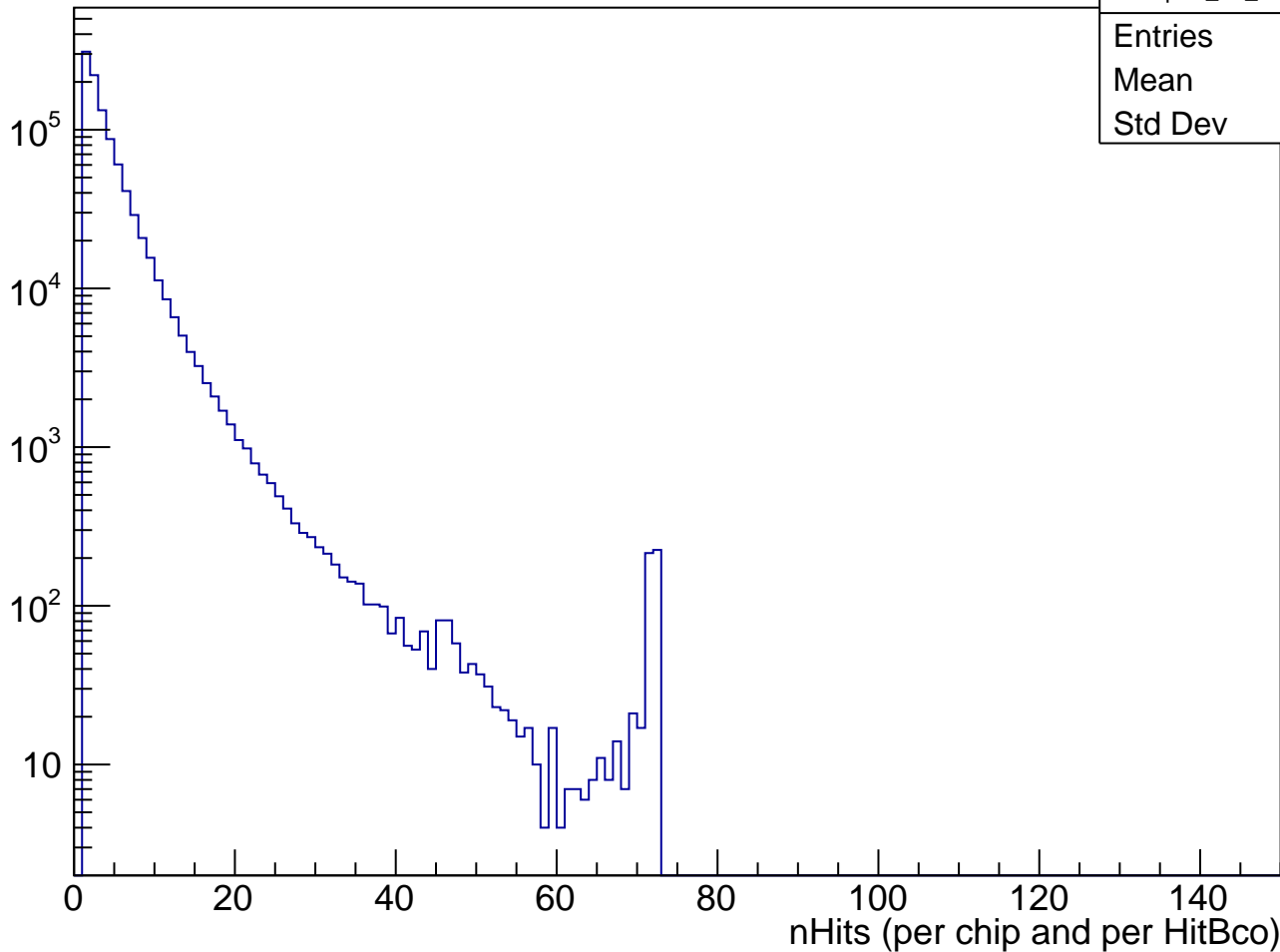
Entries



nChipHit_F3_Fch3_Chip9	
Entries	830889
Mean	3.078
Std Dev	3.726

nChipHit_F3_Fch4_Chip0

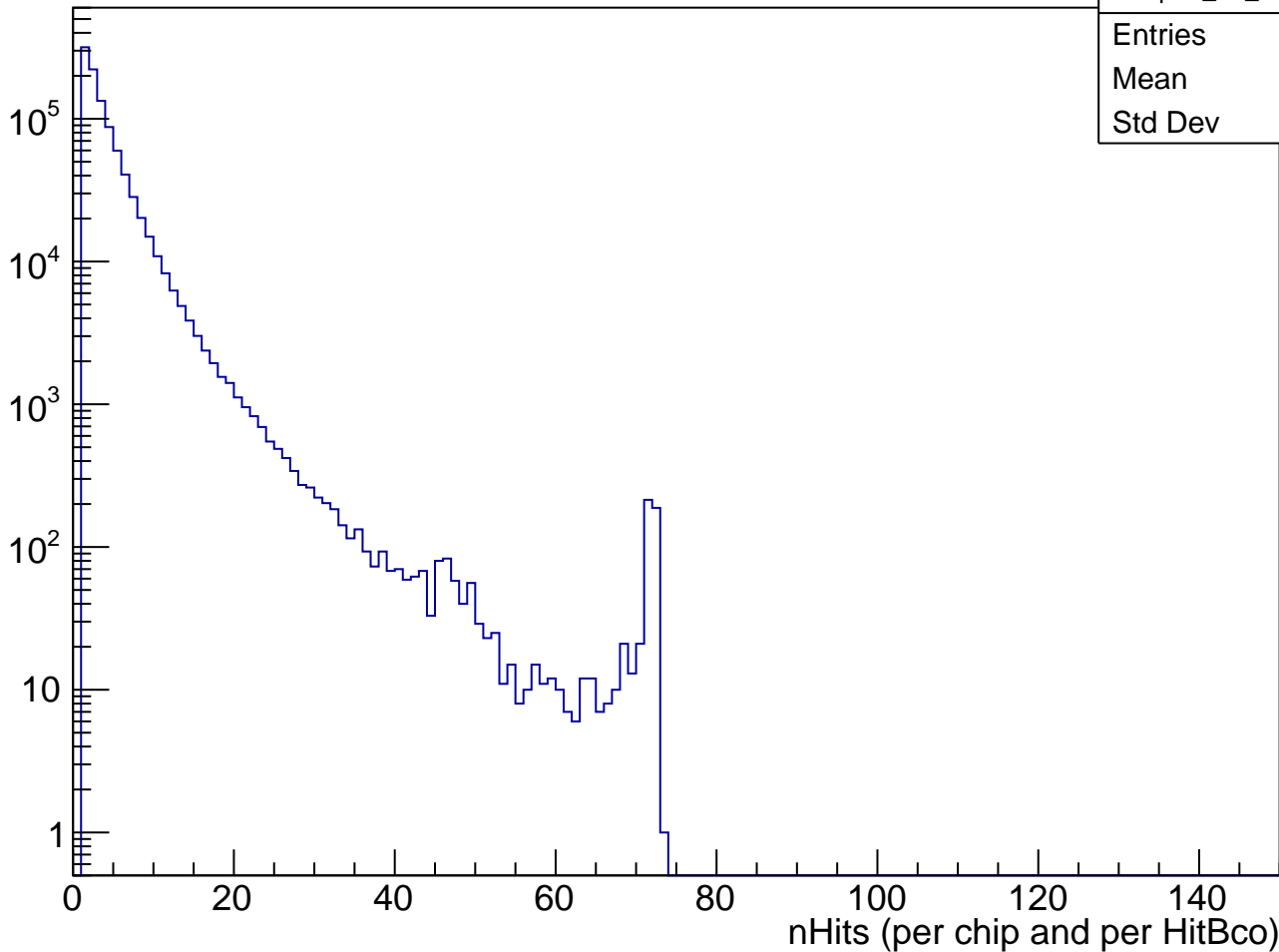
Entries



nChipHit_F3_Fch4_Chip0	
Entries	972821
Mean	3.502
Std Dev	4.049

nChipHit_F3_Fch4_Chip1

Entries

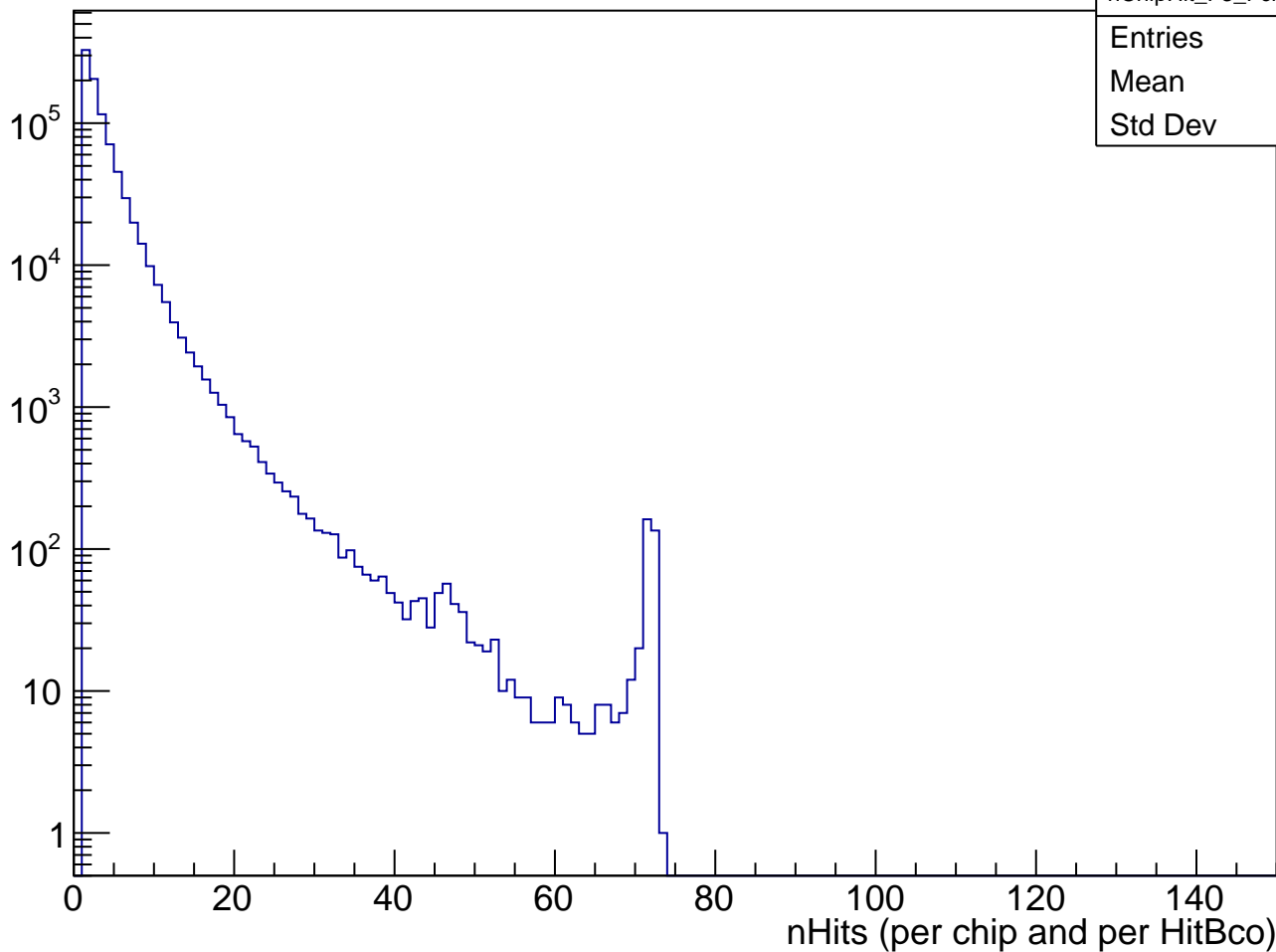


nChipHit_F3_Fch4_Chip1	
Entries	976918
Mean	3.446
Std Dev	3.979

nHits (per chip and per HitBco)

nChipHit_F3_Fch4_Chip10

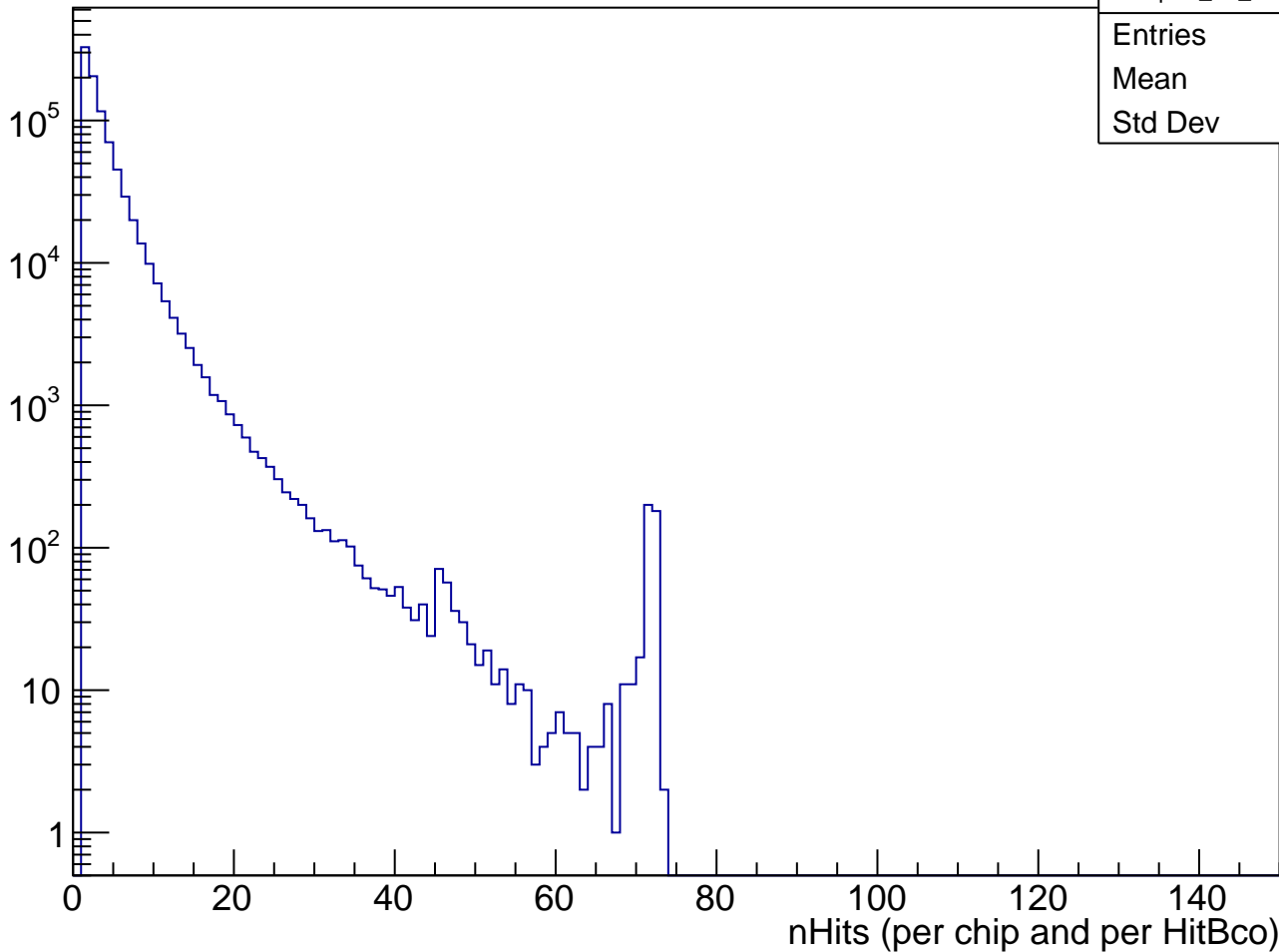
Entries



nChipHit_F3_Fch4_Chip10	
Entries	872154
Mean	3.031
Std Dev	3.557

nChipHit_F3_Fch4_Chip11

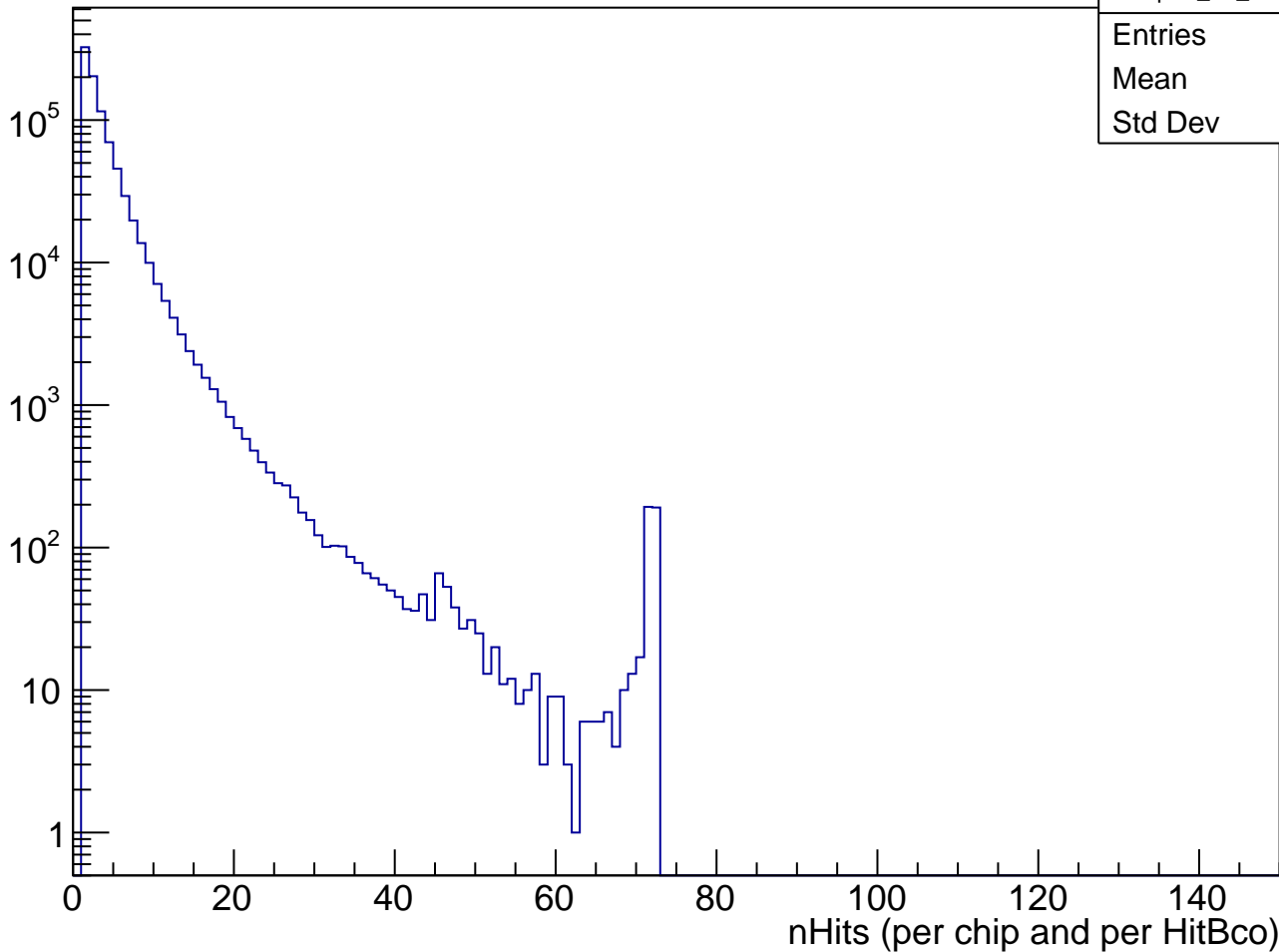
Entries



nChipHit_F3_Fch4_Chip11	
Entries	870473
Mean	3.036
Std Dev	3.608

nChipHit_F3_Fch4_Chip12

Entries

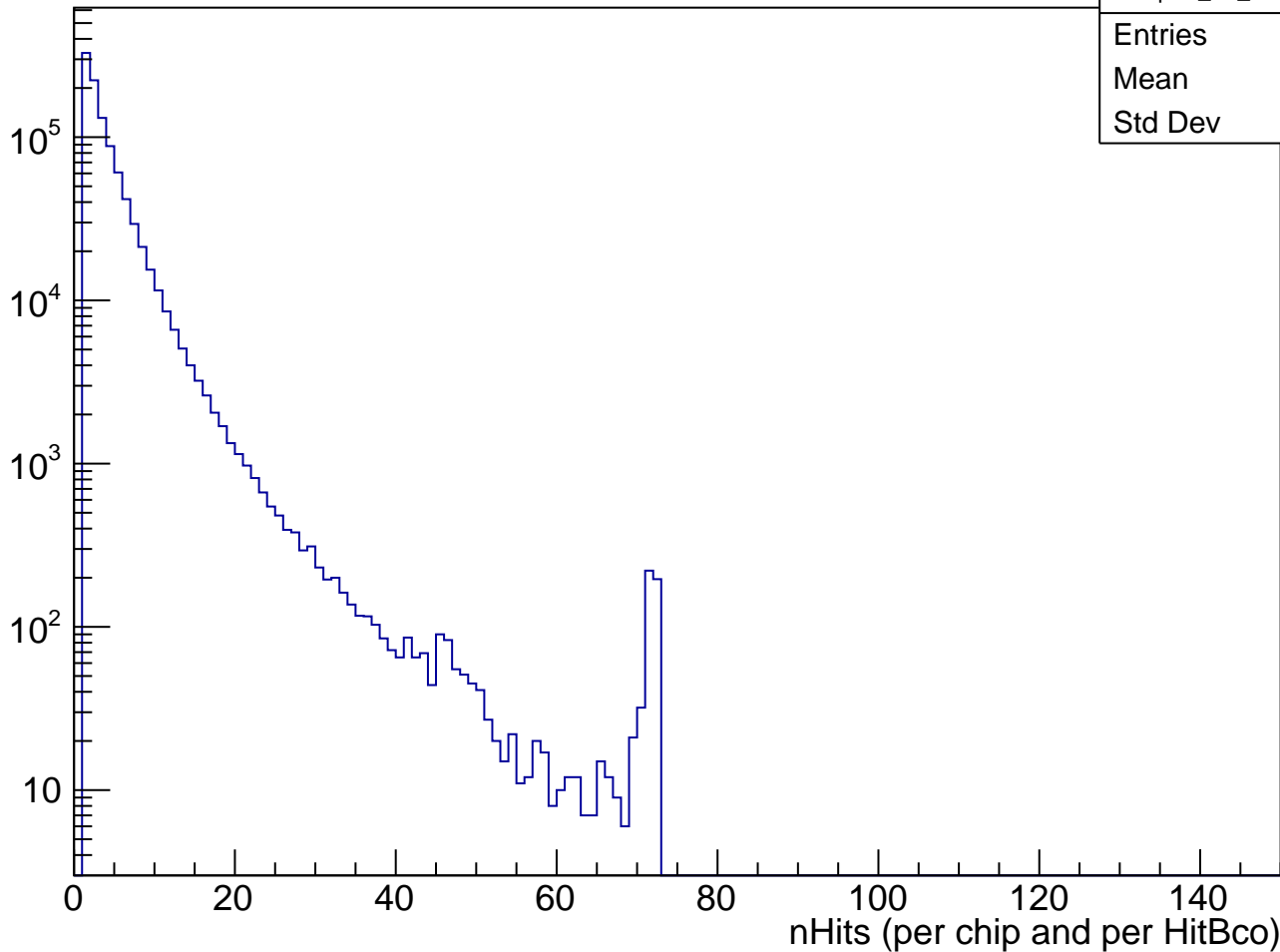


nChipHit_F3_Fch4_Chip12	
Entries	865245
Mean	3.039
Std Dev	3.619

nHits (per chip and per HitBco)

nChipHit_F3_Fch4_Chip13

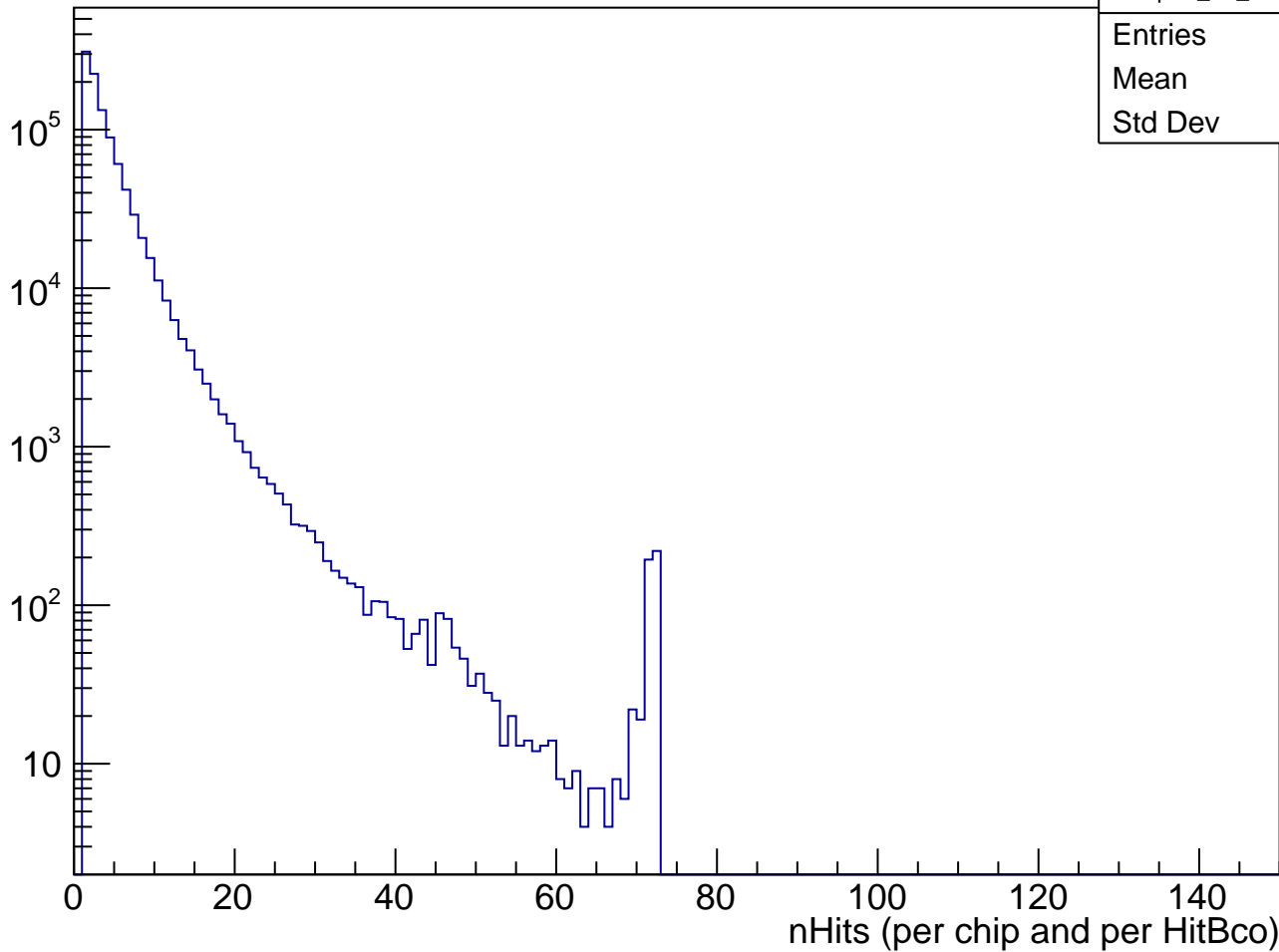
Entries



nChipHit_F3_Fch4_Chip13	
Entries	994373
Mean	3.465
Std Dev	4.038

nChipHit_F3_Fch4_Chip14

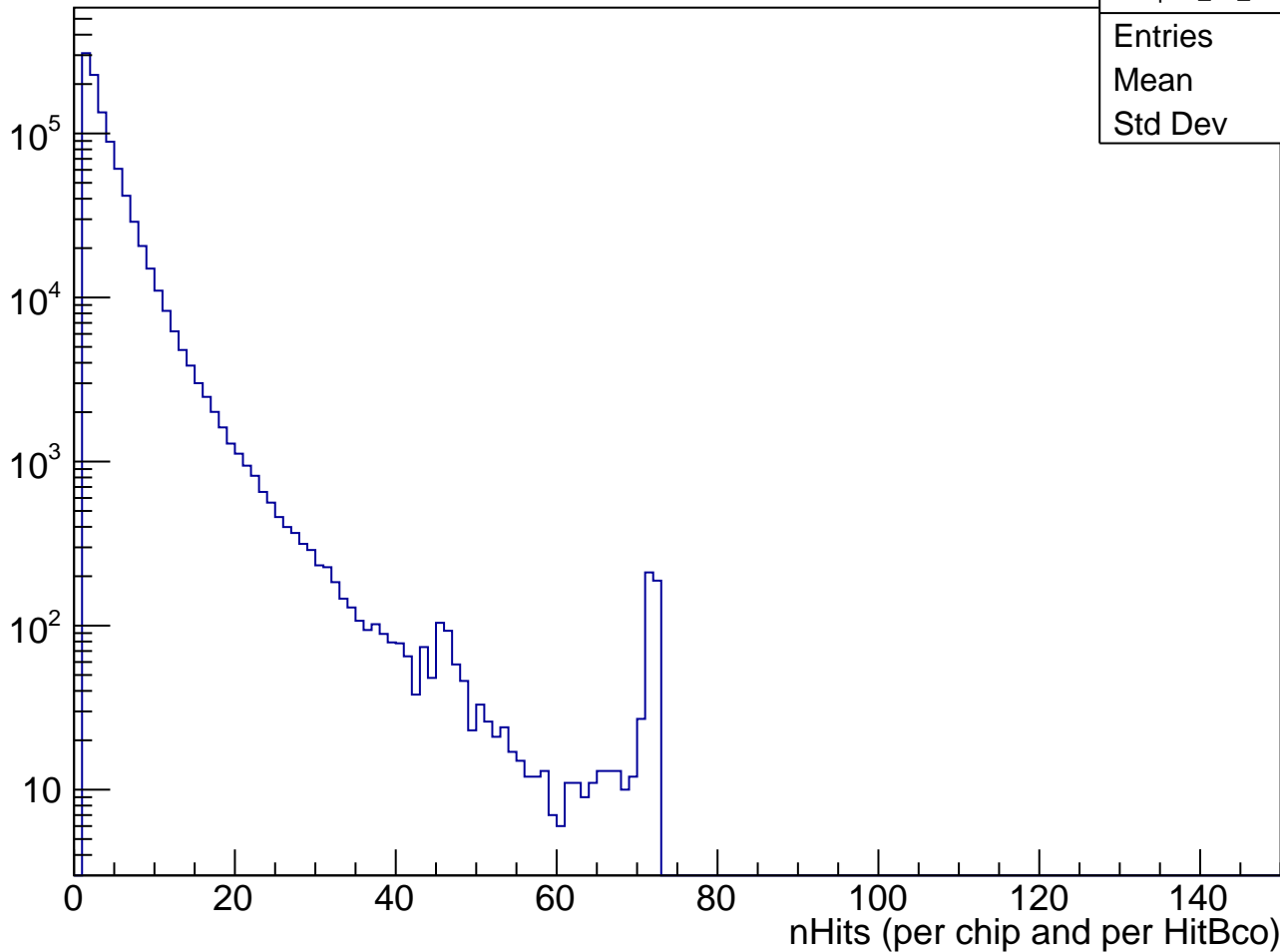
Entries



nChipHit_F3_Fch4_Chip14	
Entries	979685
Mean	3.482
Std Dev	4.002

nChipHit_F3_Fch4_Chip15

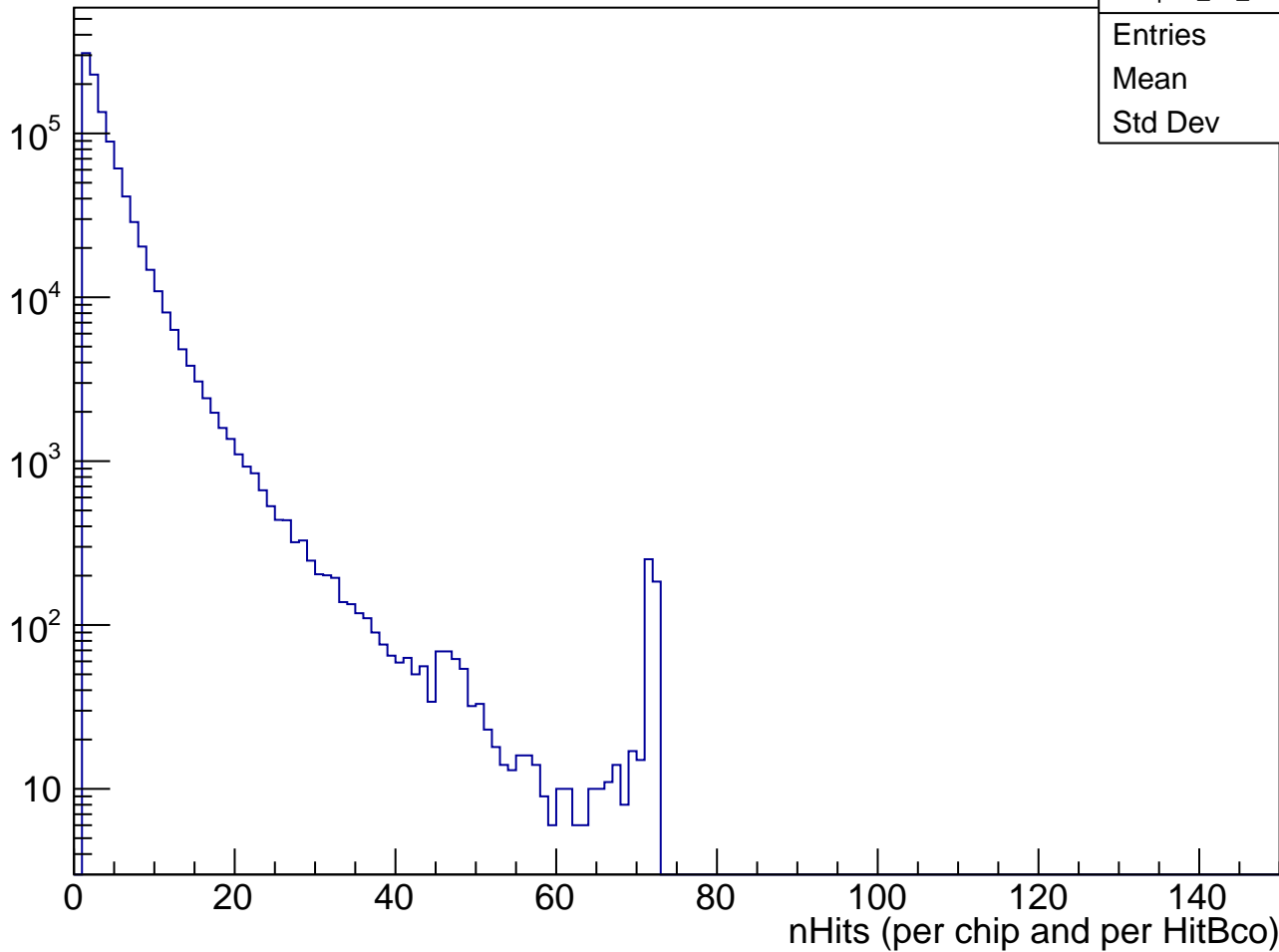
Entries



nChipHit_F3_Fch4_Chip15	
Entries	979751
Mean	3.474
Std Dev	3.994

nChipHit_F3_Fch4_Chip16

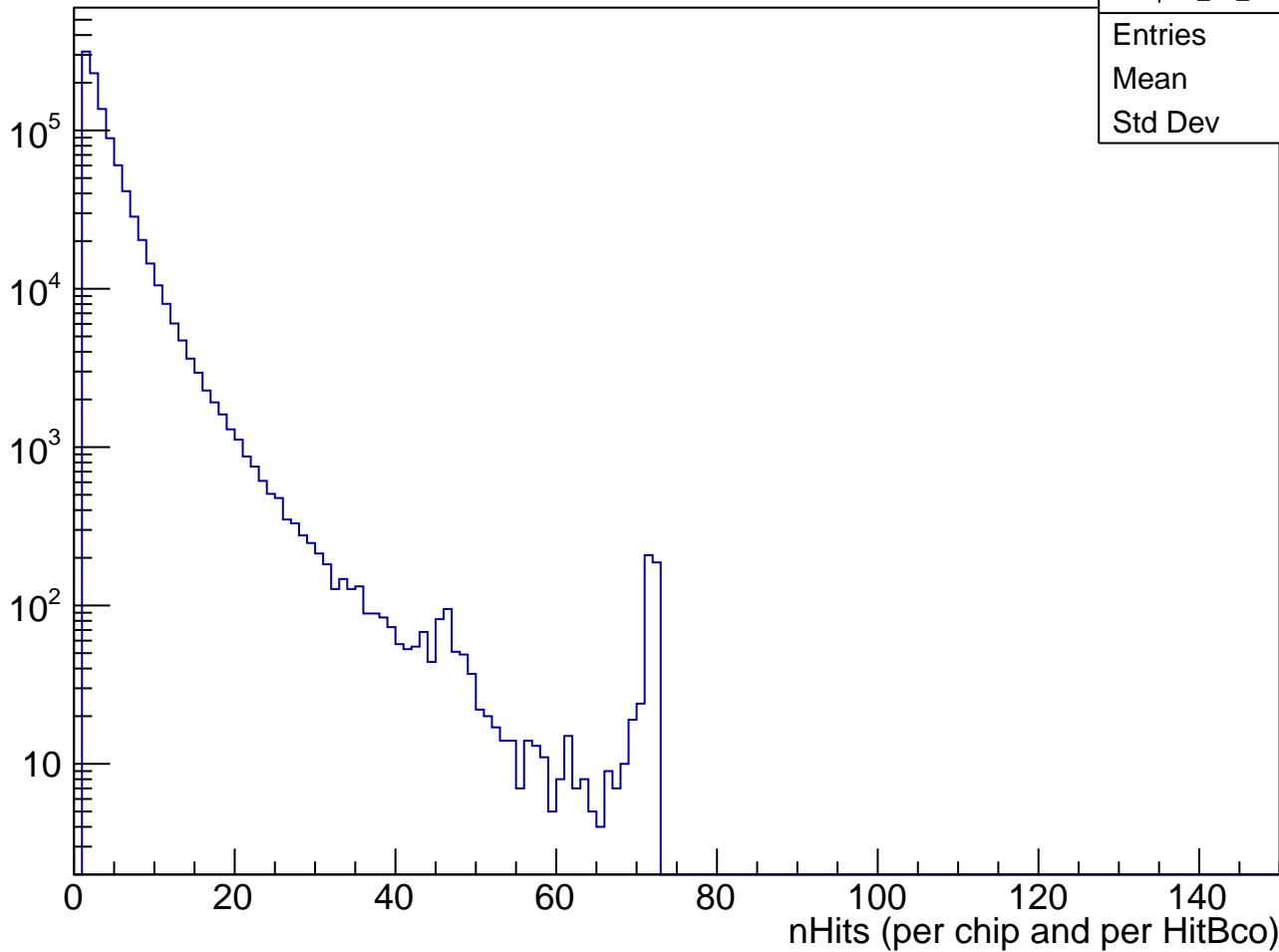
Entries



nChipHit_F3_Fch4_Chip16	
Entries	980843
Mean	3.458
Std Dev	3.967

nChipHit_F3_Fch4_Chip17

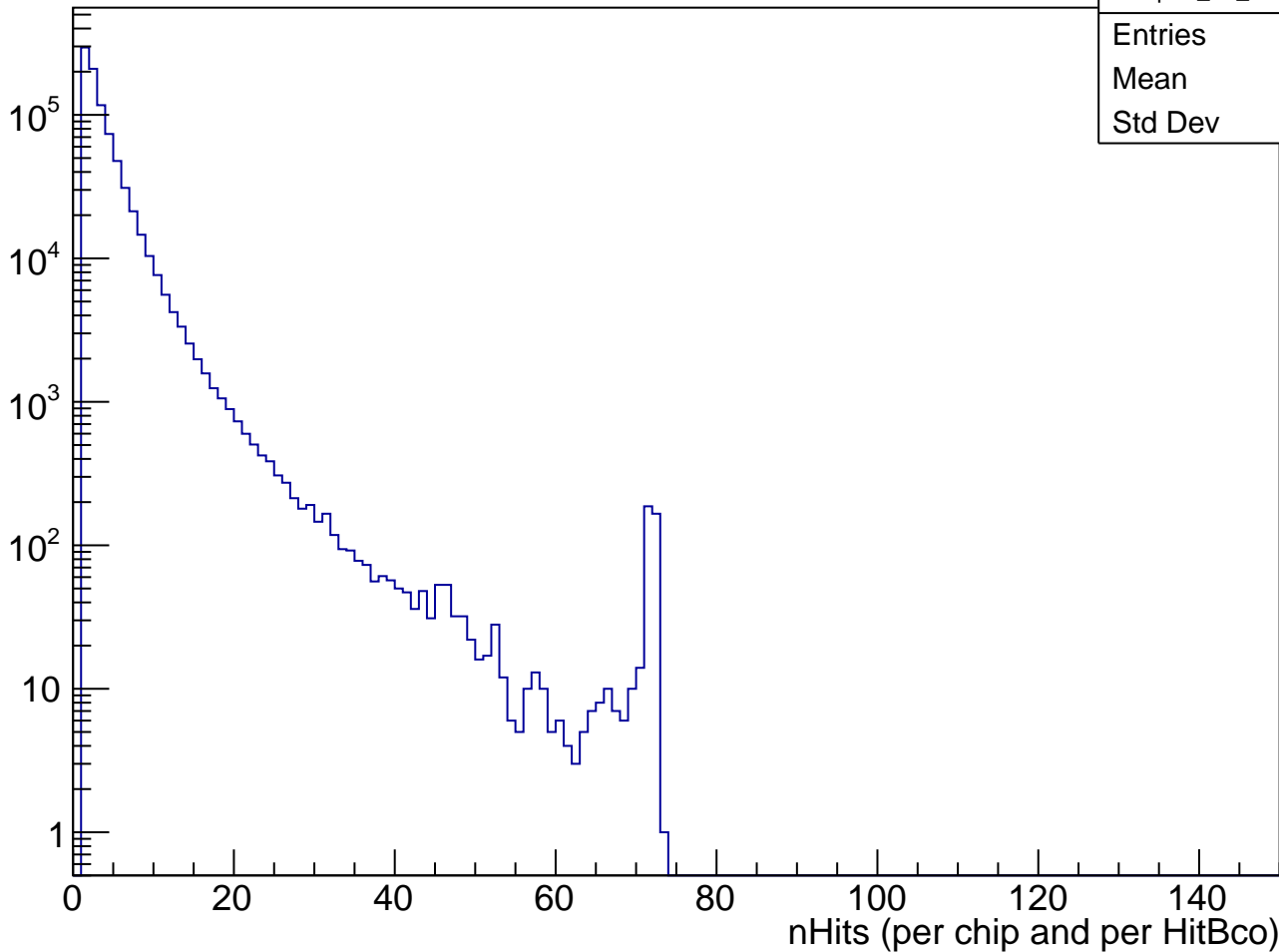
Entries



nChipHit_F3_Fch4_Chip17	
Entries	985926
Mean	3.415
Std Dev	3.897

nChipHit_F3_Fch4_Chip18

Entries

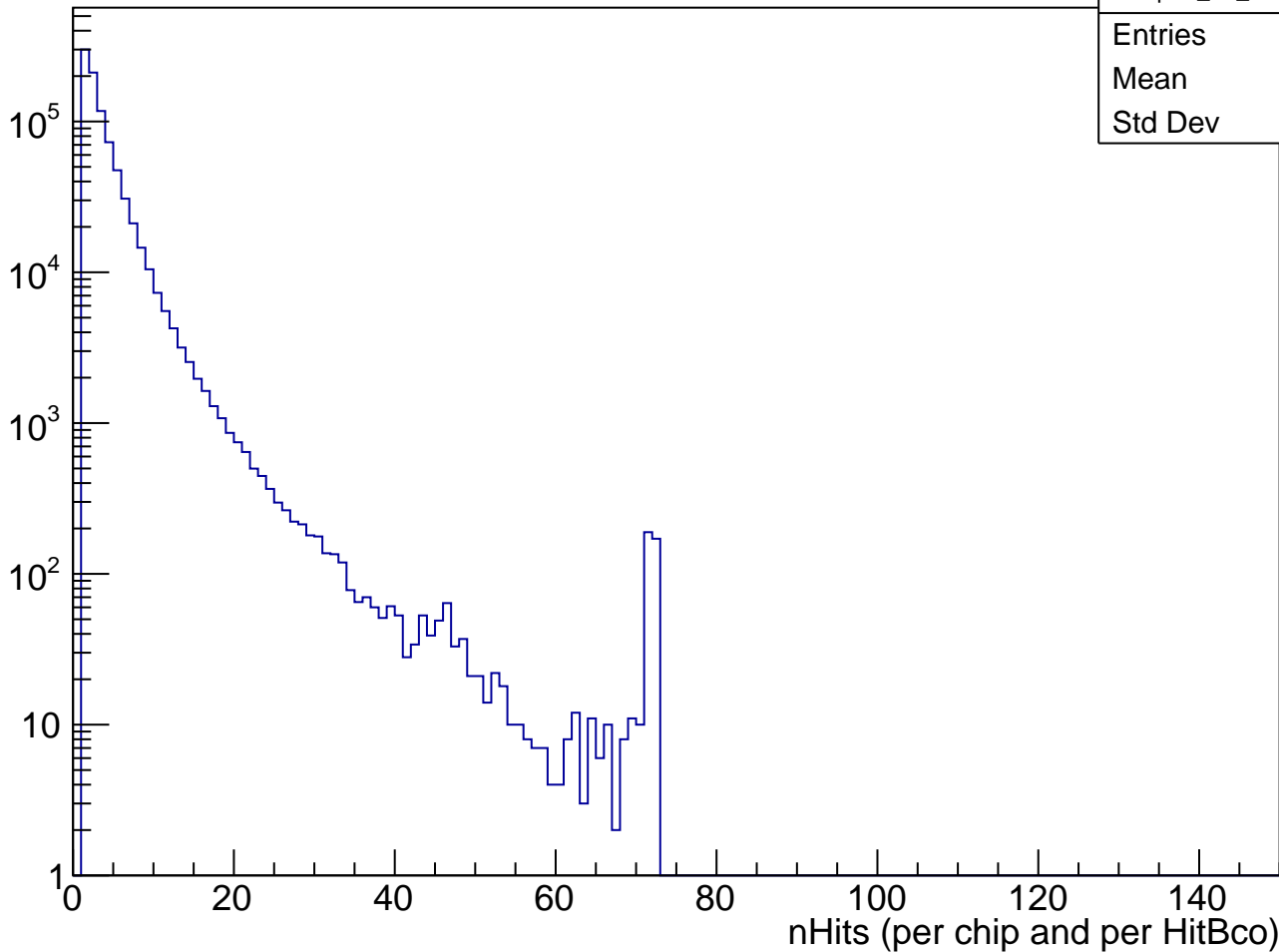


nChipHit_F3_Fch4_Chip18	
Entries	855719
Mean	3.152
Std Dev	3.652

nHits (per chip and per HitBco)

nChipHit_F3_Fch4_Chip19

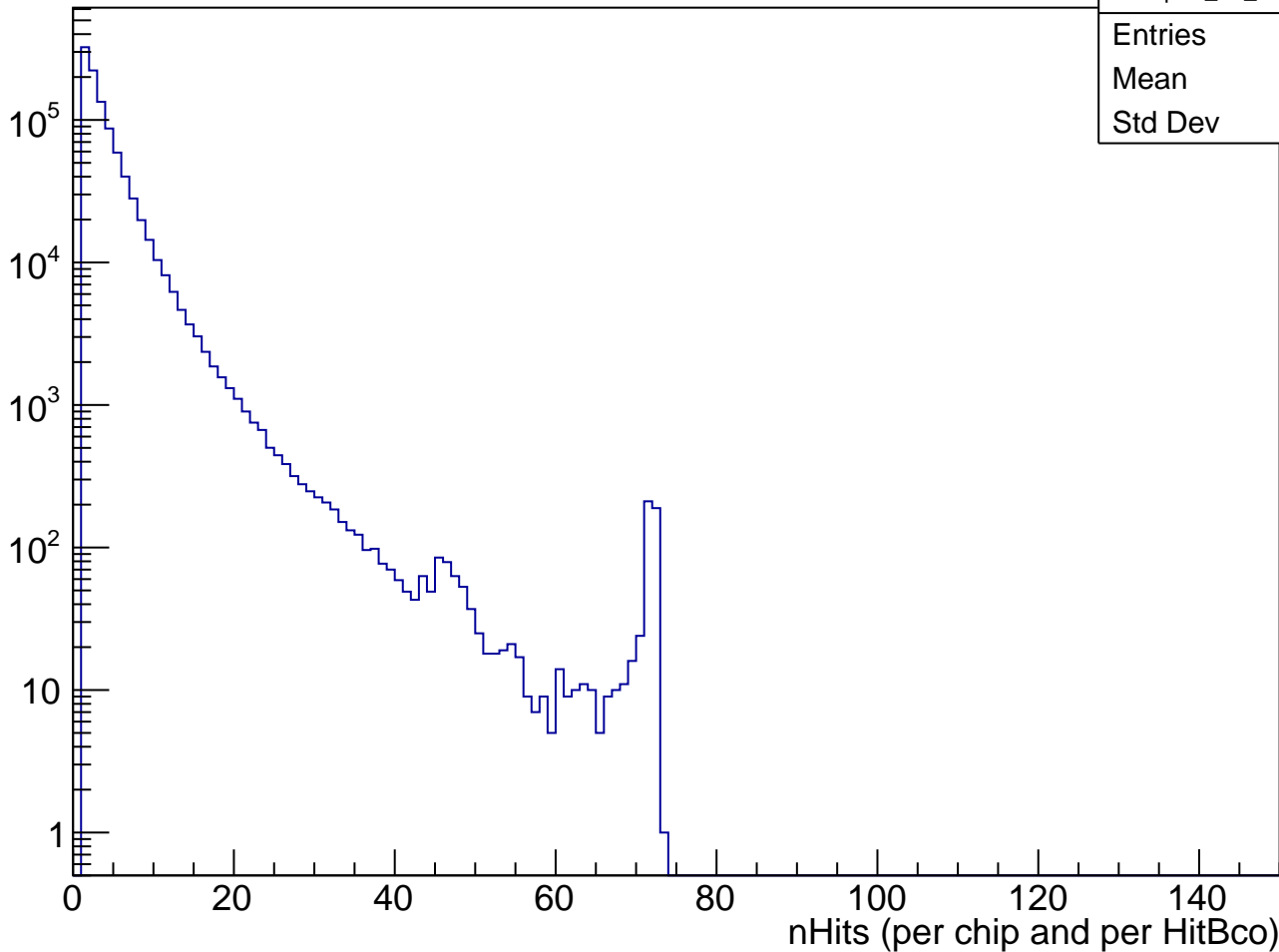
Entries



nChipHit_F3_Fch4_Chip19	
Entries	861332
Mean	3.134
Std Dev	3.653

nChipHit_F3_Fch4_Chip2

Entries



nChipHit_F3_Fch4_Chip2

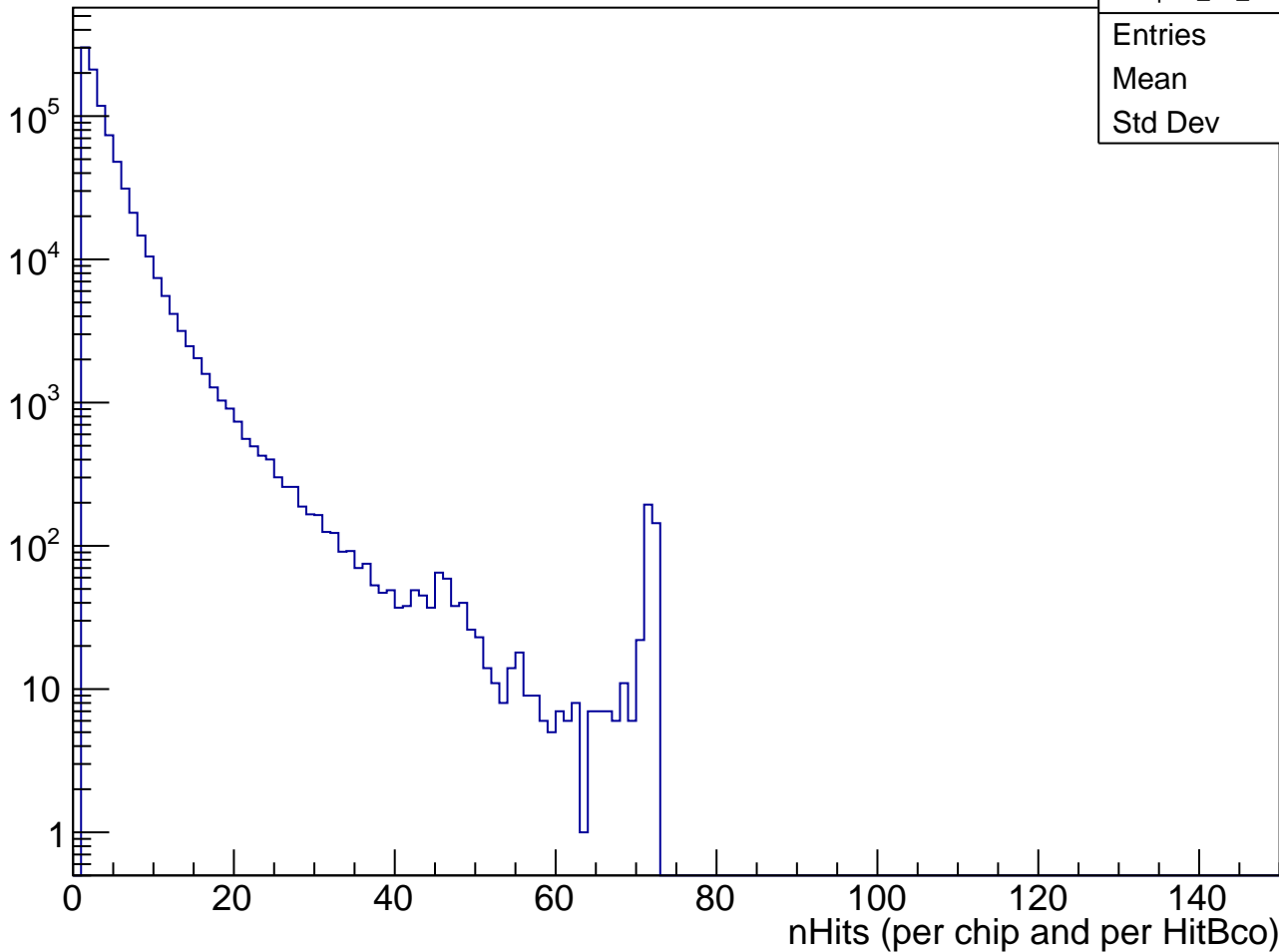
Entries 980440

Mean 3.401

Std Dev 3.937

nChipHit_F3_Fch4_Chip20

Entries

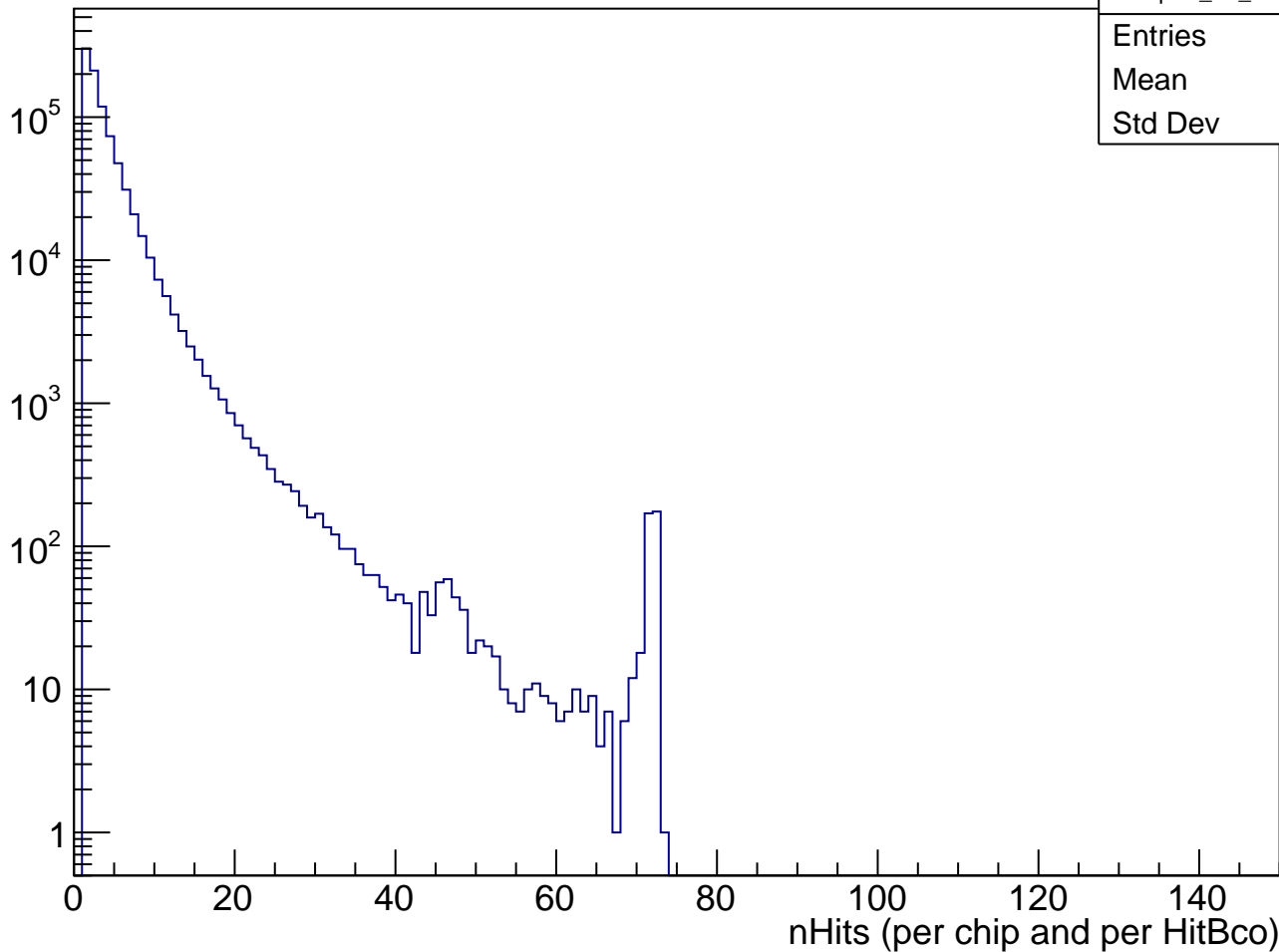


nChipHit_F3_Fch4_Chip20	
Entries	865192
Mean	3.128
Std Dev	3.625

nHits (per chip and per HitBco)

nChipHit_F3_Fch4_Chip21

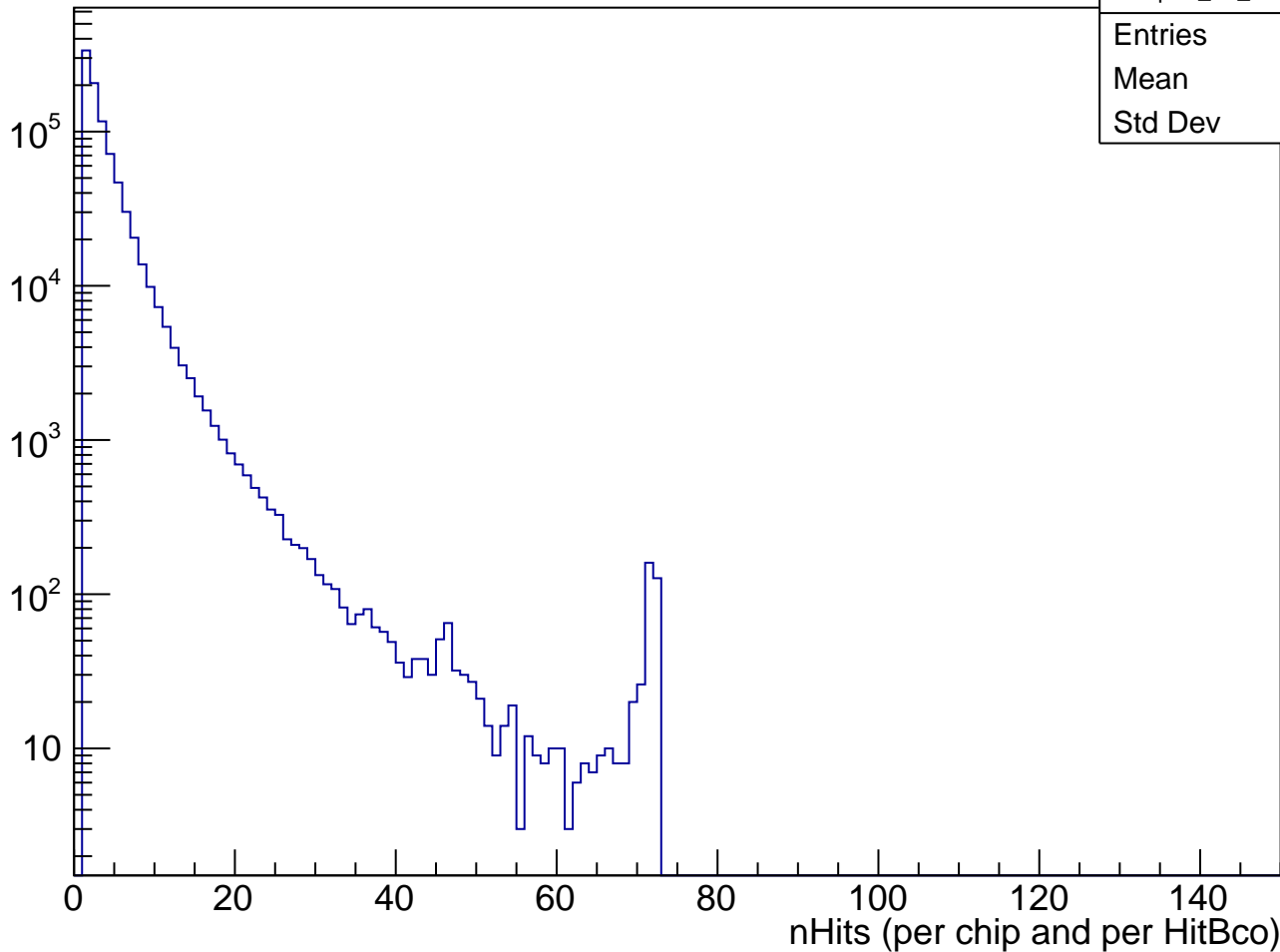
Entries



nChipHit_F3_Fch4_Chip21	
Entries	866195
Mean	3.121
Std Dev	3.614

nChipHit_F3_Fch4_Chip22

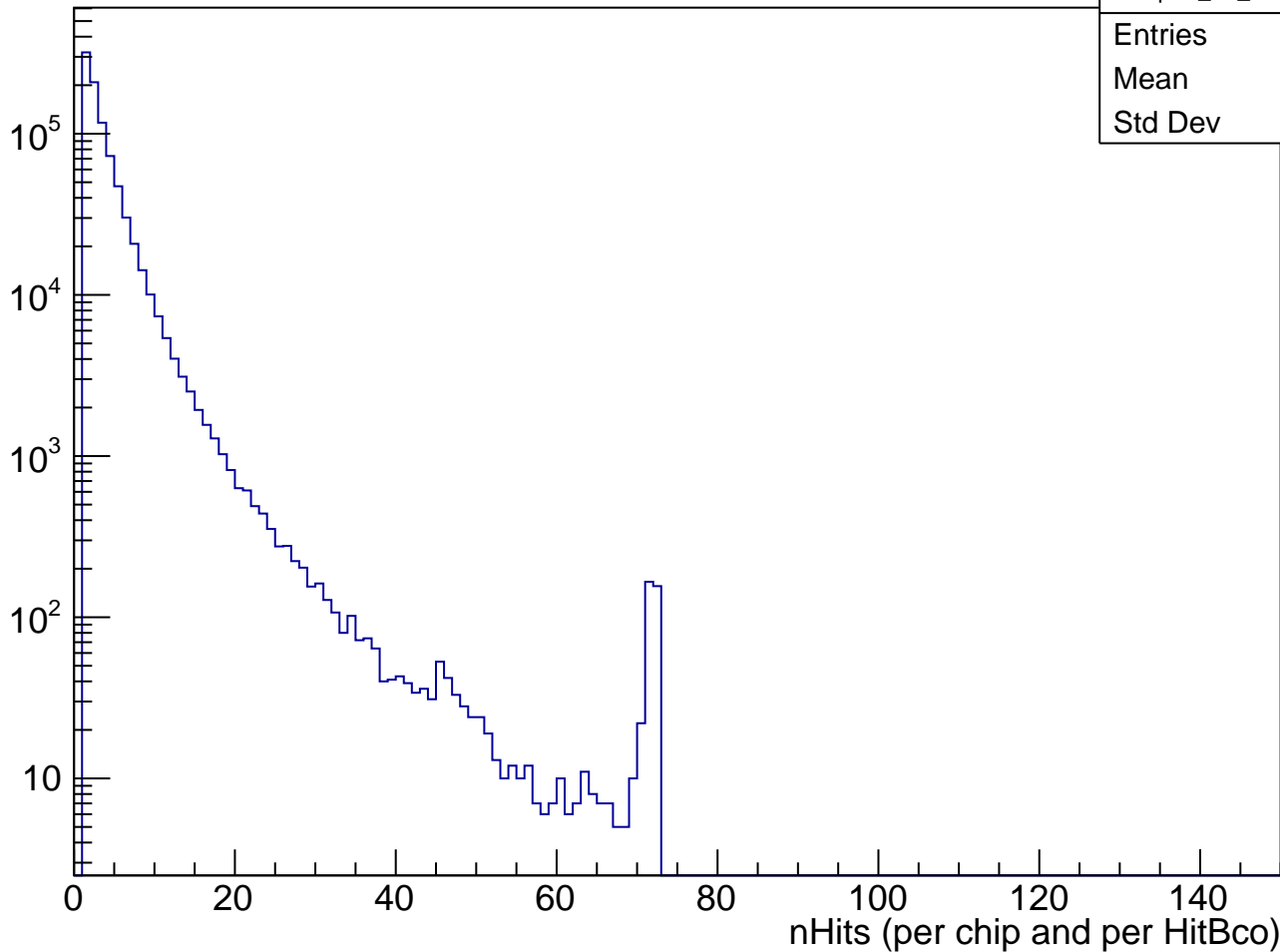
Entries



nChipHit_F3_Fch4_Chip22	
Entries	885680
Mean	3.014
Std Dev	3.53

nChipHit_F3_Fch4_Chip23

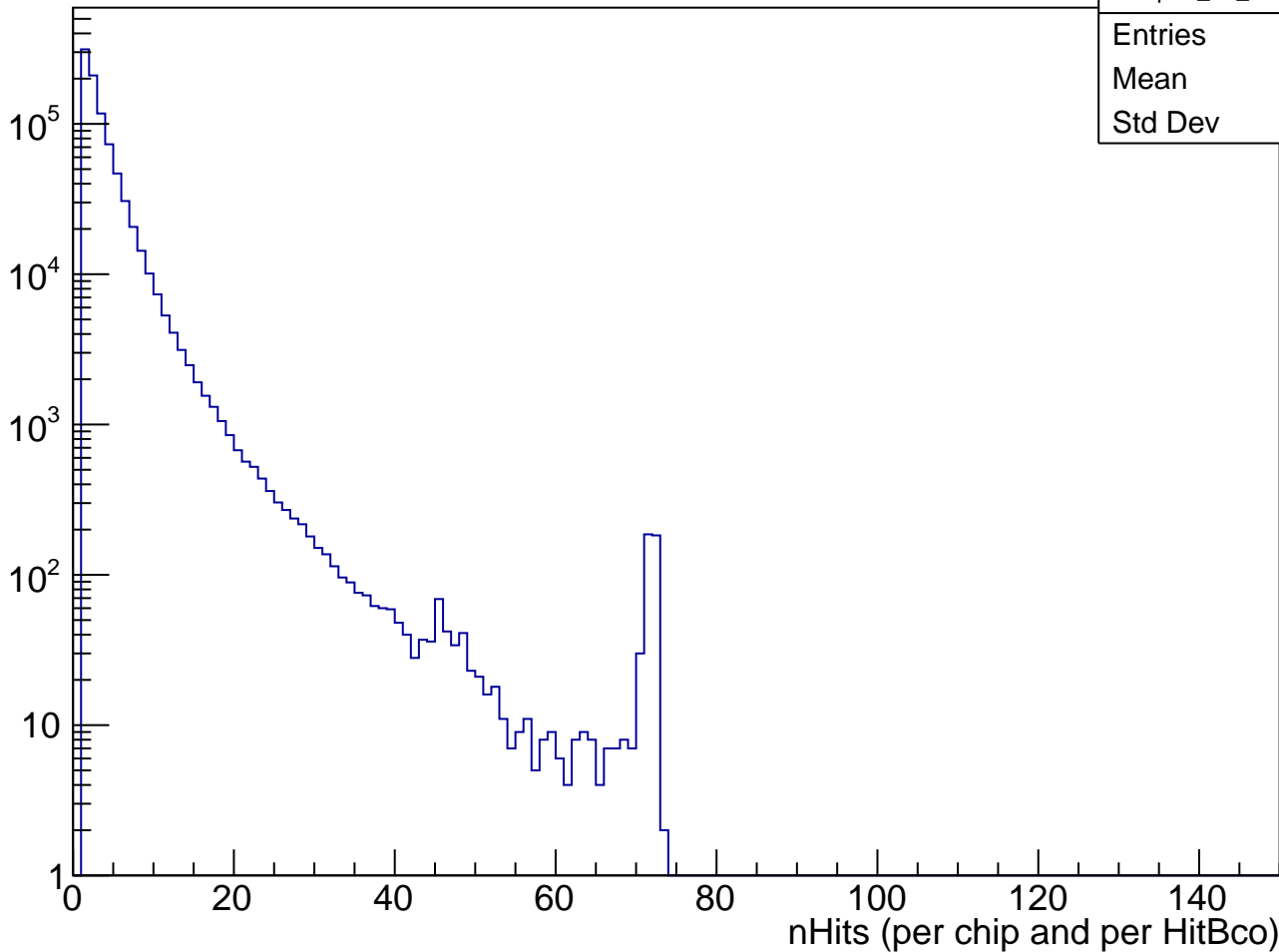
Entries



nChipHit_F3_Fch4_Chip23	
Entries	875000
Mean	3.061
Std Dev	3.563

nChipHit_F3_Fch4_Chip24

Entries

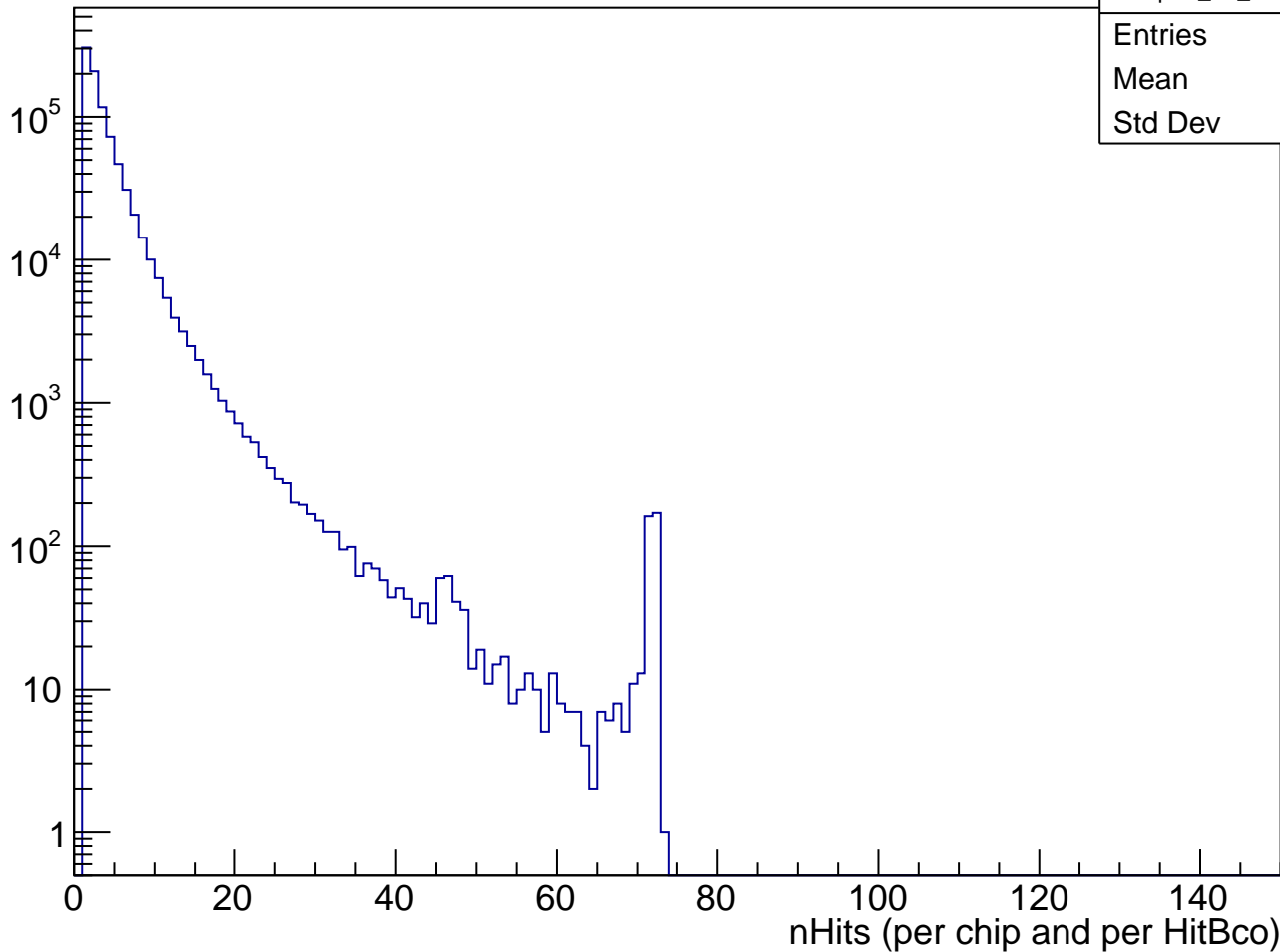


nChipHit_F3_Fch4_Chip24	
Entries	870994
Mean	3.087
Std Dev	3.633

nHits (per chip and per HitBco)

nChipHit_F3_Fch4_Chip25

Entries

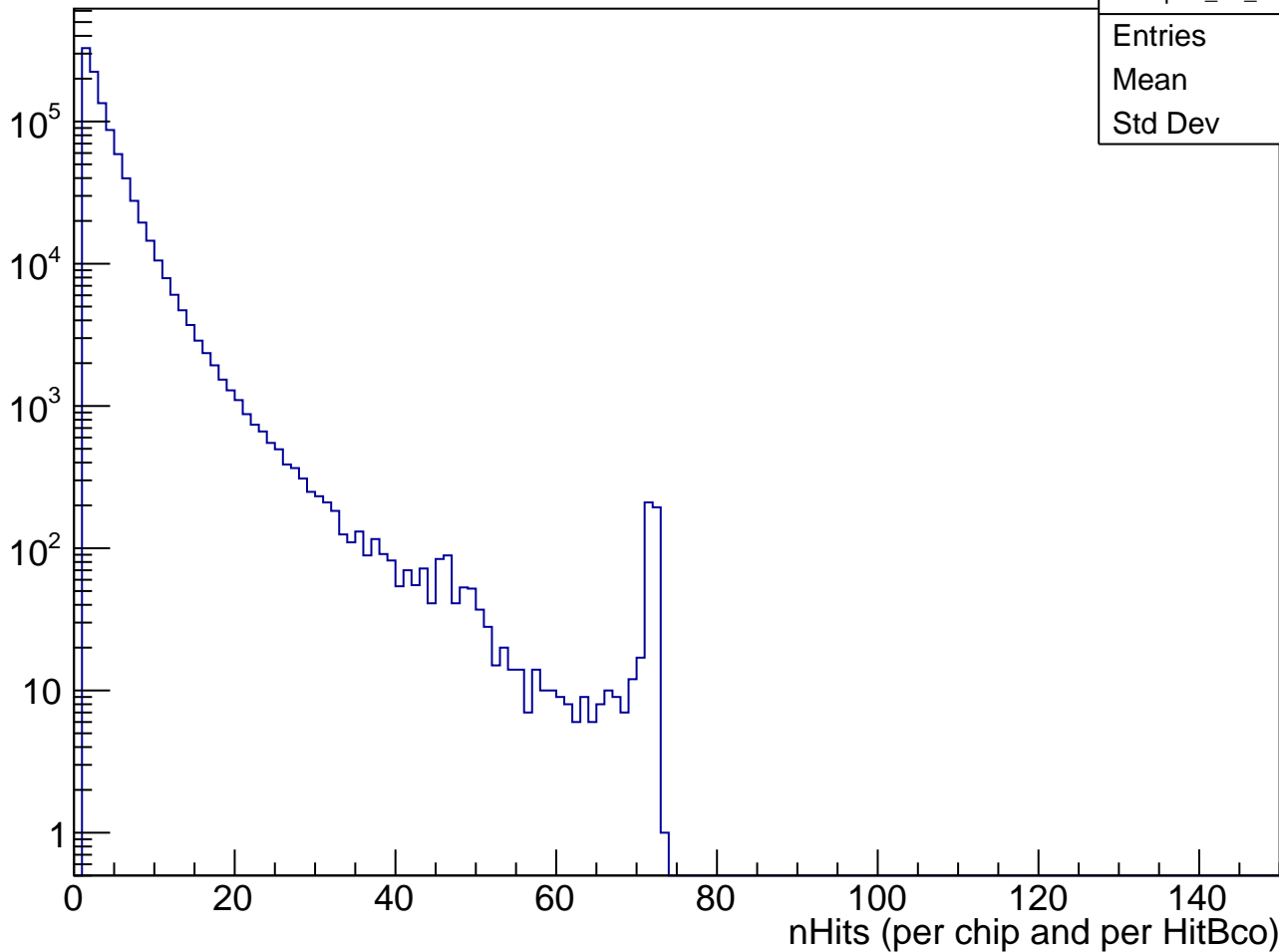


nChipHit_F3_Fch4_Chip25	
Entries	860297
Mean	3.104
Std Dev	3.607

nHits (per chip and per HitBco)

nChipHit_F3_Fch4_Chip3

Entries

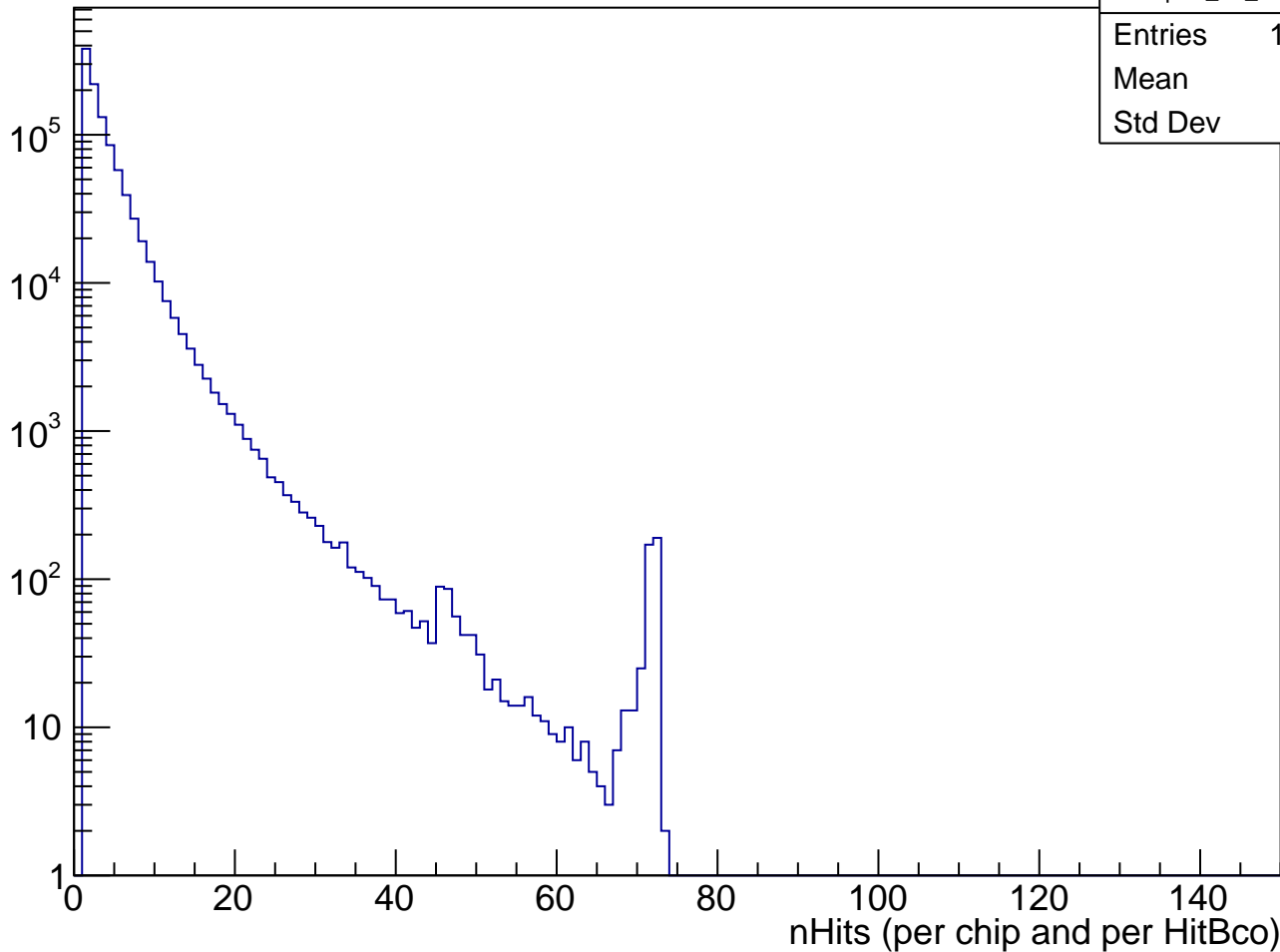


nChipHit_F3_Fch4_Chip3

Entries	985142
Mean	3.387
Std Dev	3.938

nChipHit_F3_Fch4_Chip4

Entries



nChipHit_F3_Fch4_Chip4

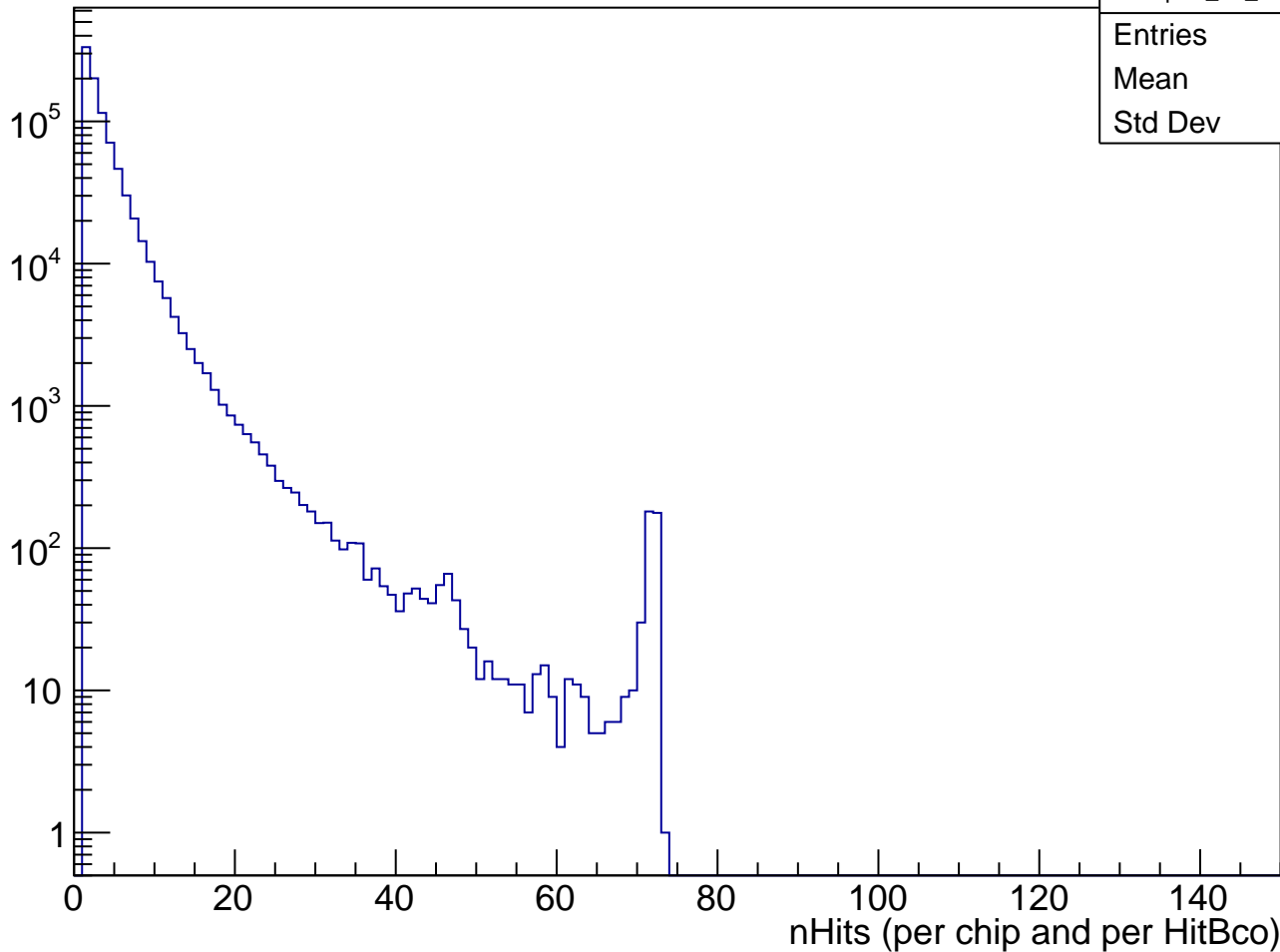
Entries 1023670

Mean 3.235

Std Dev 3.825

nChipHit_F3_Fch4_Chip5

Entries

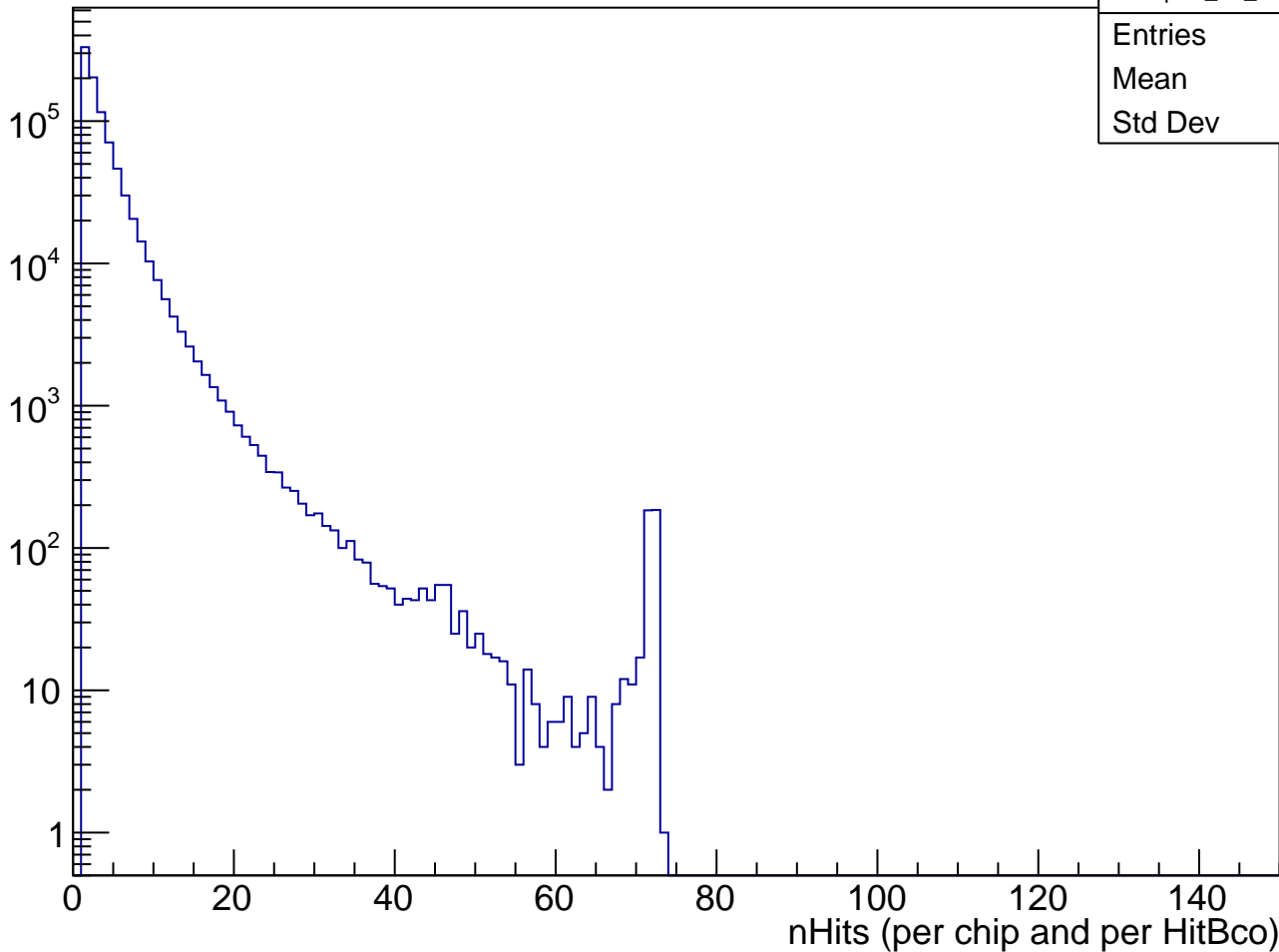


nChipHit_F3_Fch4_Chip5	
Entries	877167
Mean	3.067
Std Dev	3.668

nHits (per chip and per HitBco)

nChipHit_F3_Fch4_Chip6

Entries



nChipHit_F3_Fch4_Chip6

Entries 877887

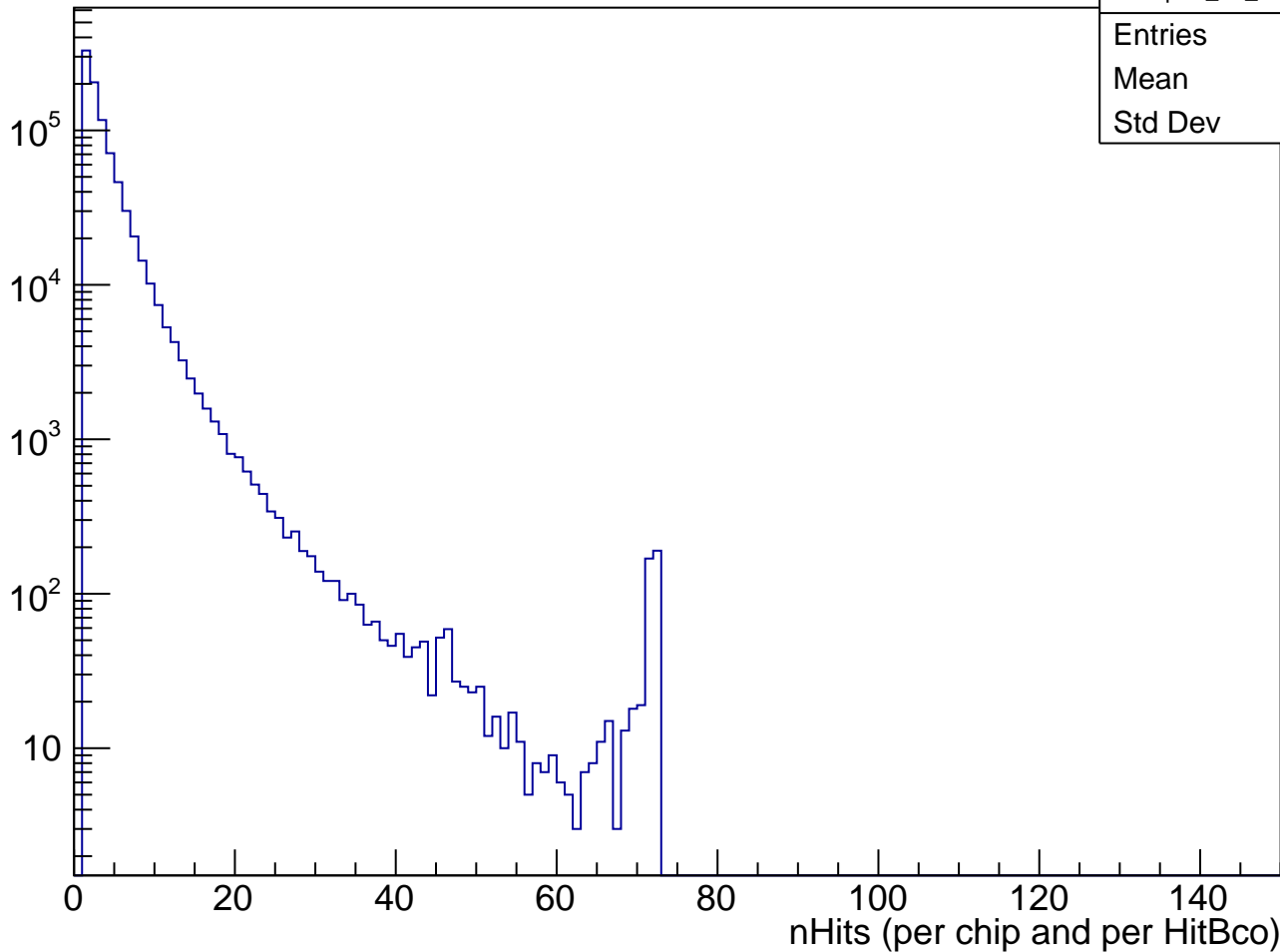
Mean 3.069

Std Dev 3.662

nHits (per chip and per HitBco)

nChipHit_F3_Fch4_Chip7

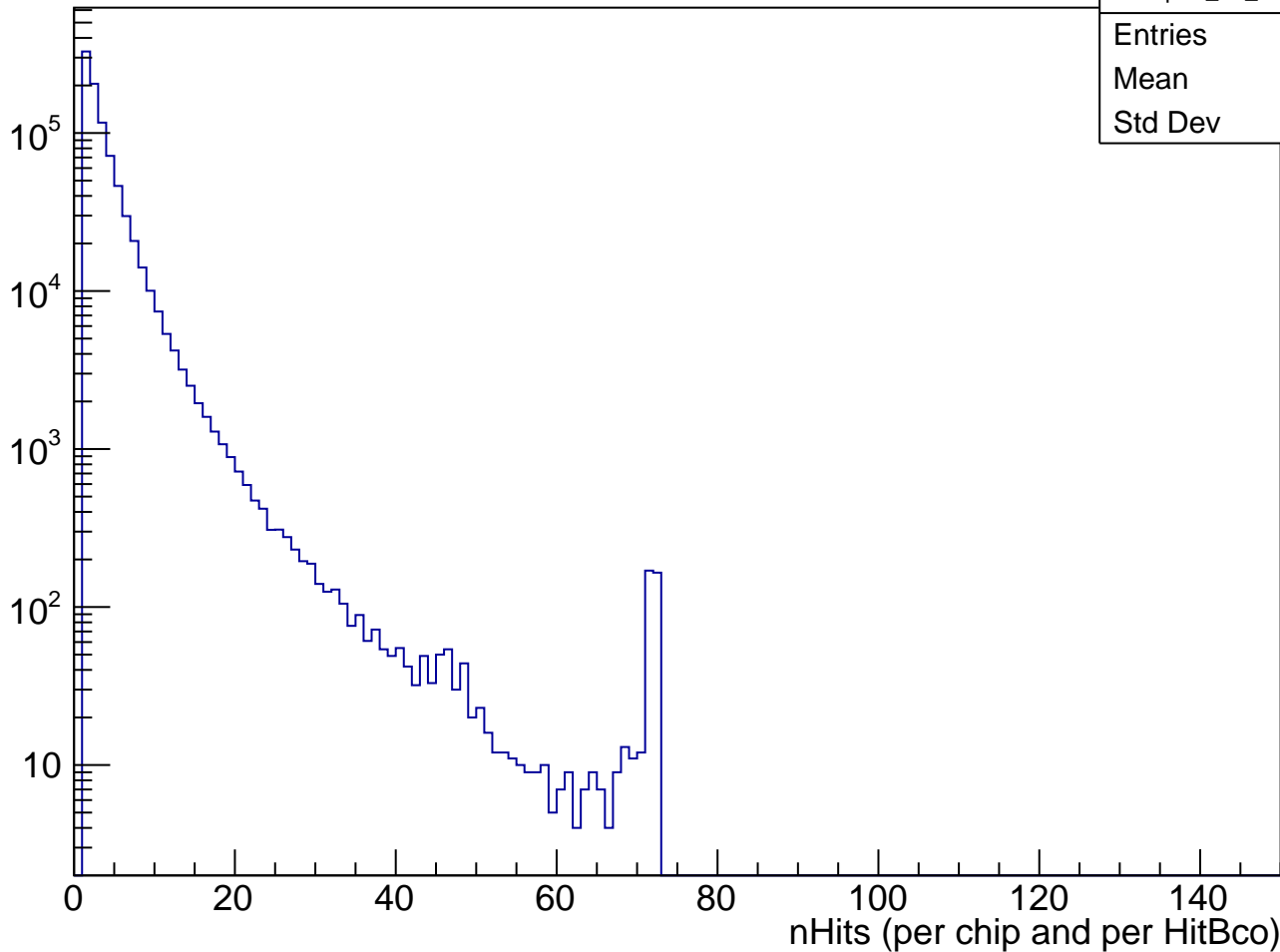
Entries



nChipHit_F3_Fch4_Chip7	
Entries	878170
Mean	3.055
Std Dev	3.625

nChipHit_F3_Fch4_Chip8

Entries



nChipHit_F3_Fch4_Chip8

Entries 877067

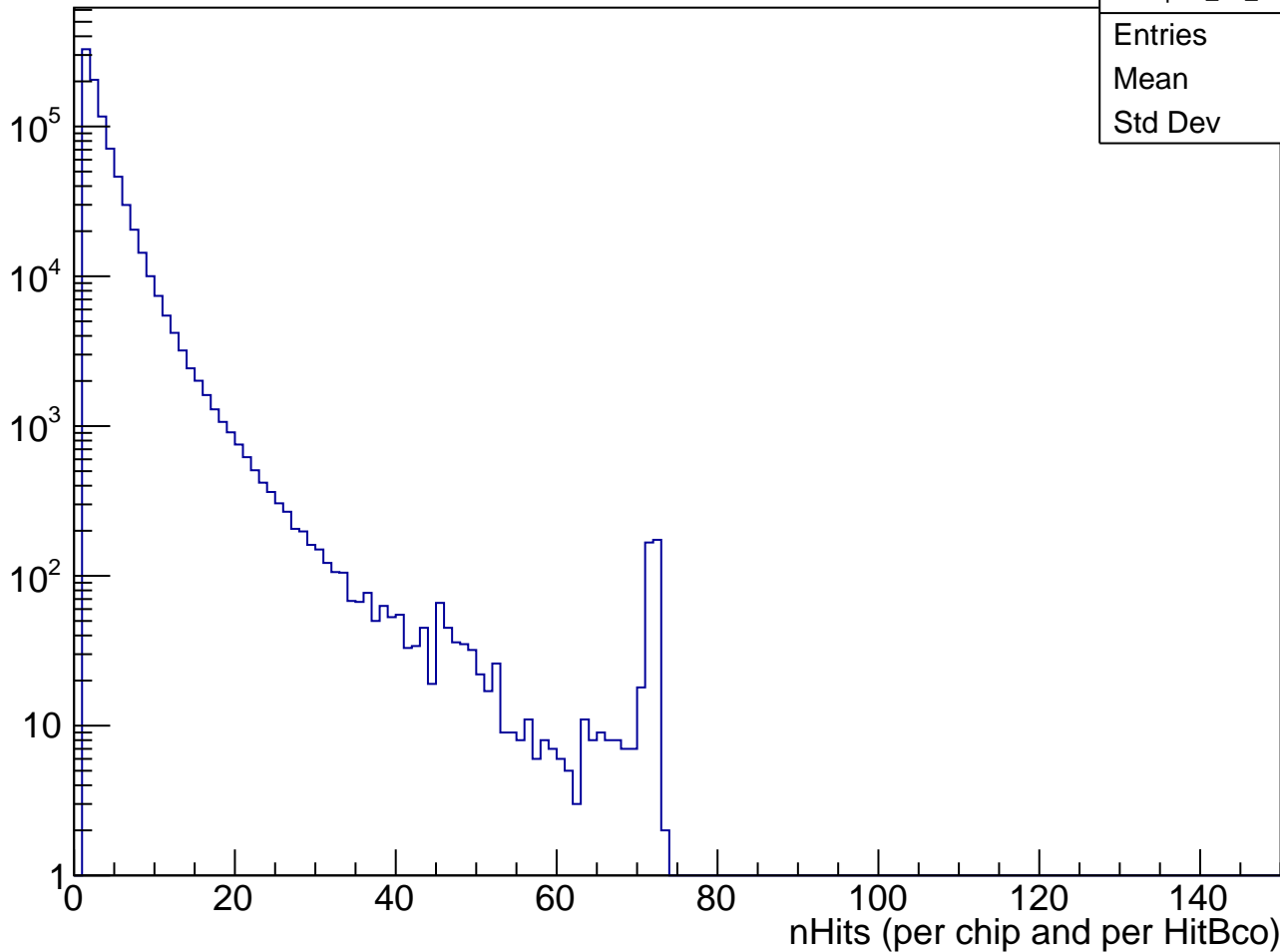
Mean 3.051

Std Dev 3.598

nHits (per chip and per HitBco)

nChipHit_F3_Fch4_Chip9

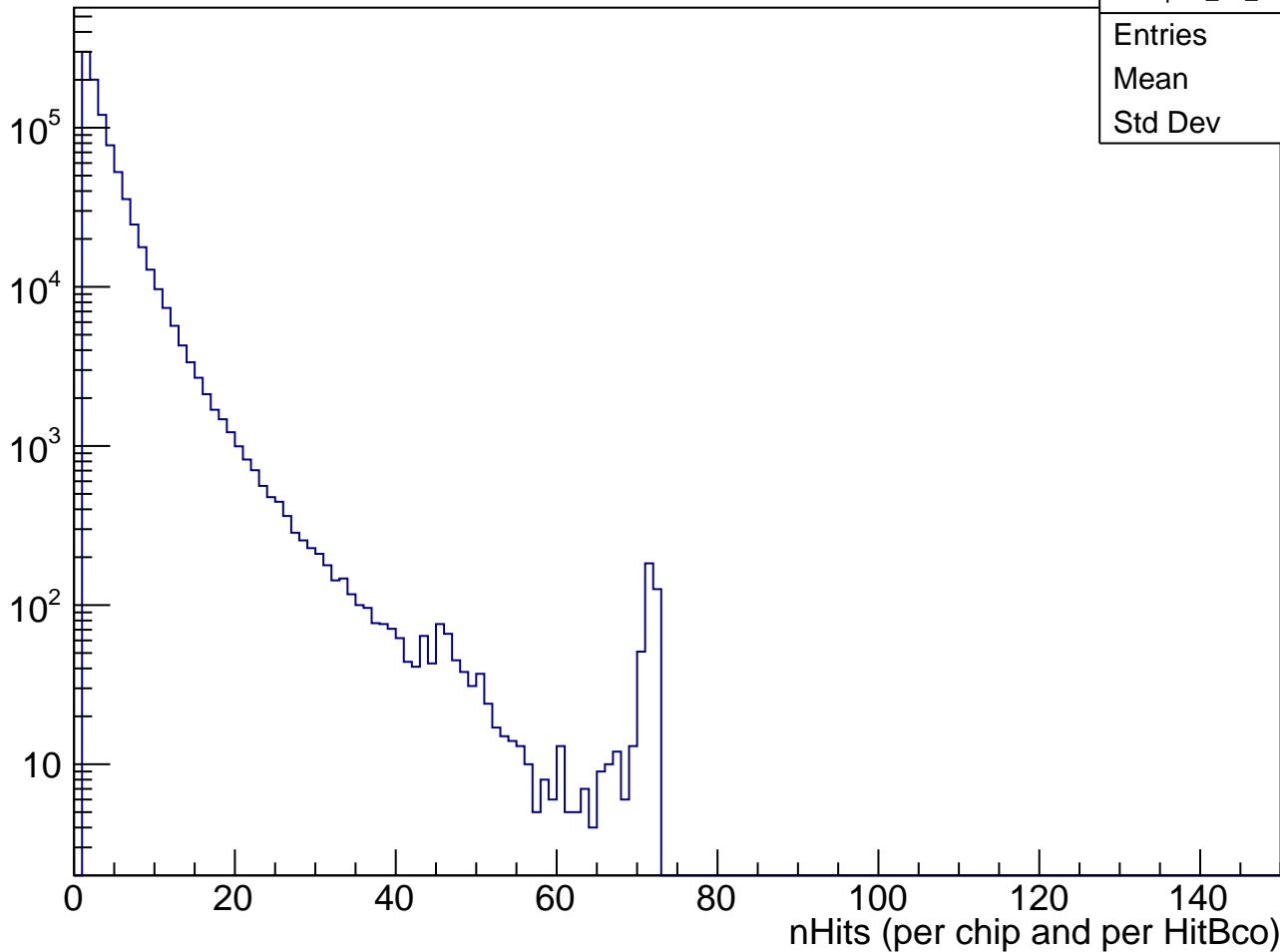
Entries



nChipHit_F3_Fch4_Chip9	
Entries	876605
Mean	3.053
Std Dev	3.604

nChipHit_F3_Fch5_Chip0

Entries



nChipHit_F3_Fch5_Chip0

Entries 888952

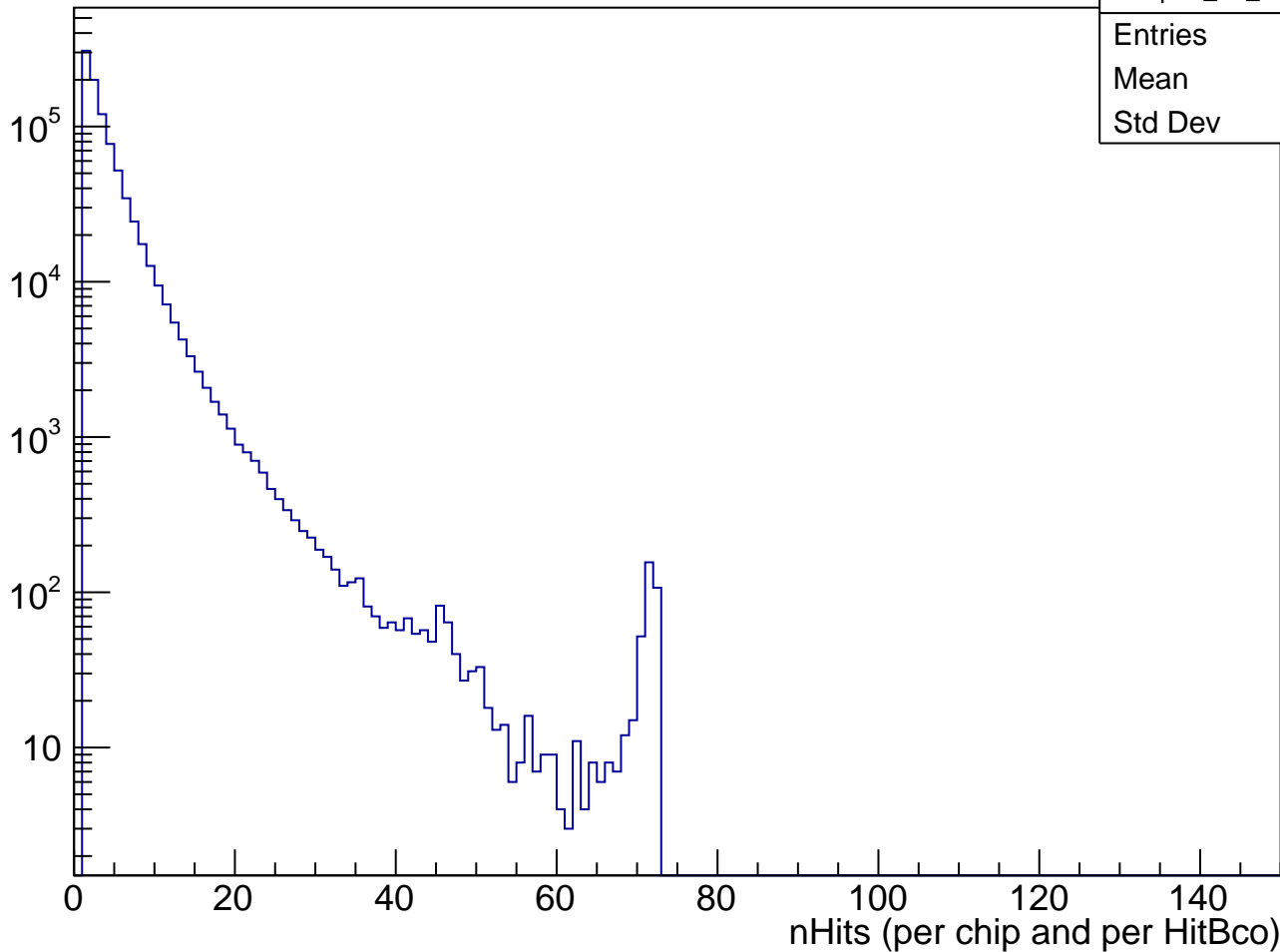
Mean 3.377

Std Dev 3.919

nHits (per chip and per HitBco)

nChipHit_F3_Fch5_Chip1

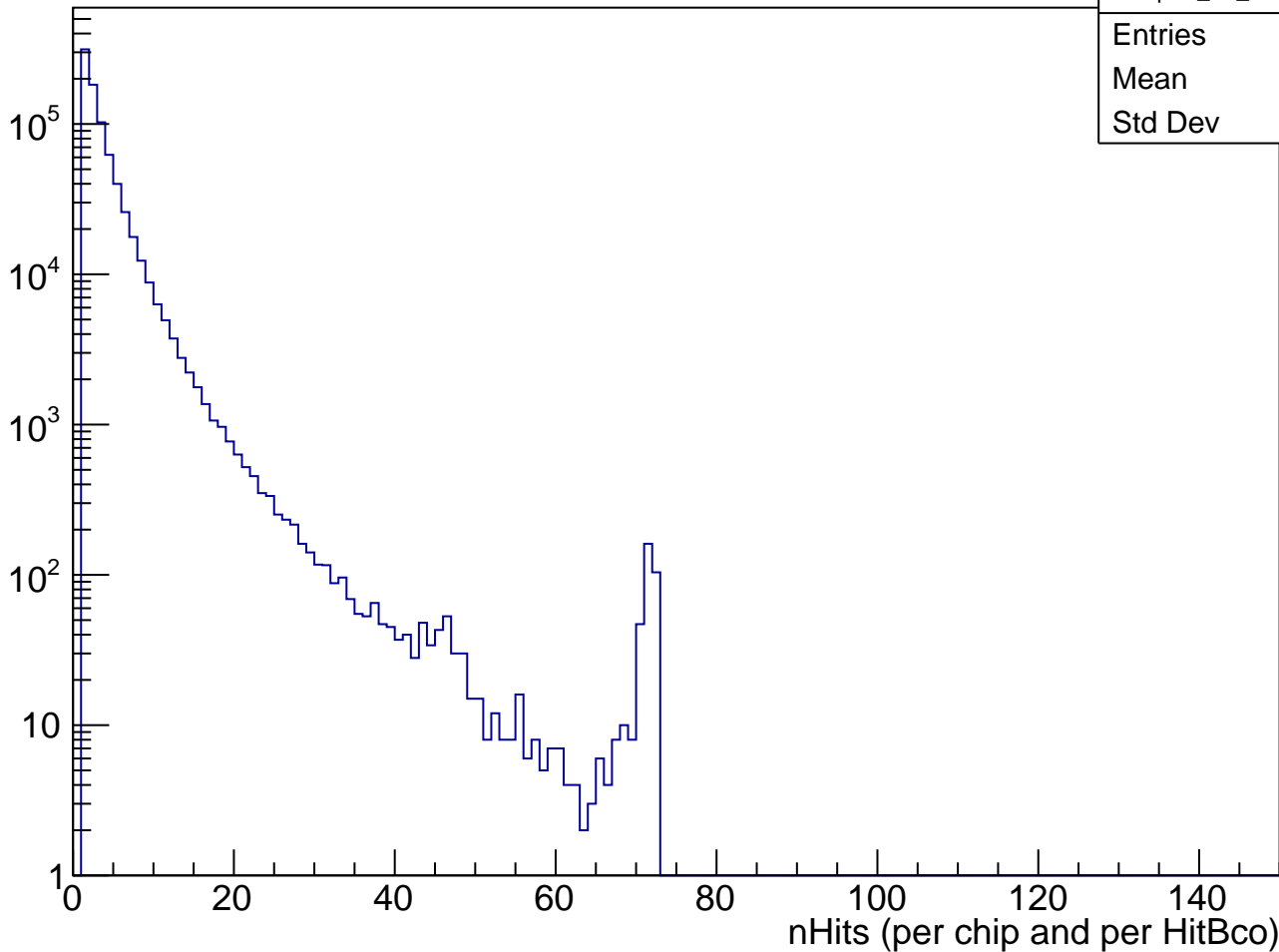
Entries



nChipHit_F3_Fch5_Chip1	
Entries	891526
Mean	3.328
Std Dev	3.843

nChipHit_F3_Fch5_Chip10

Entries

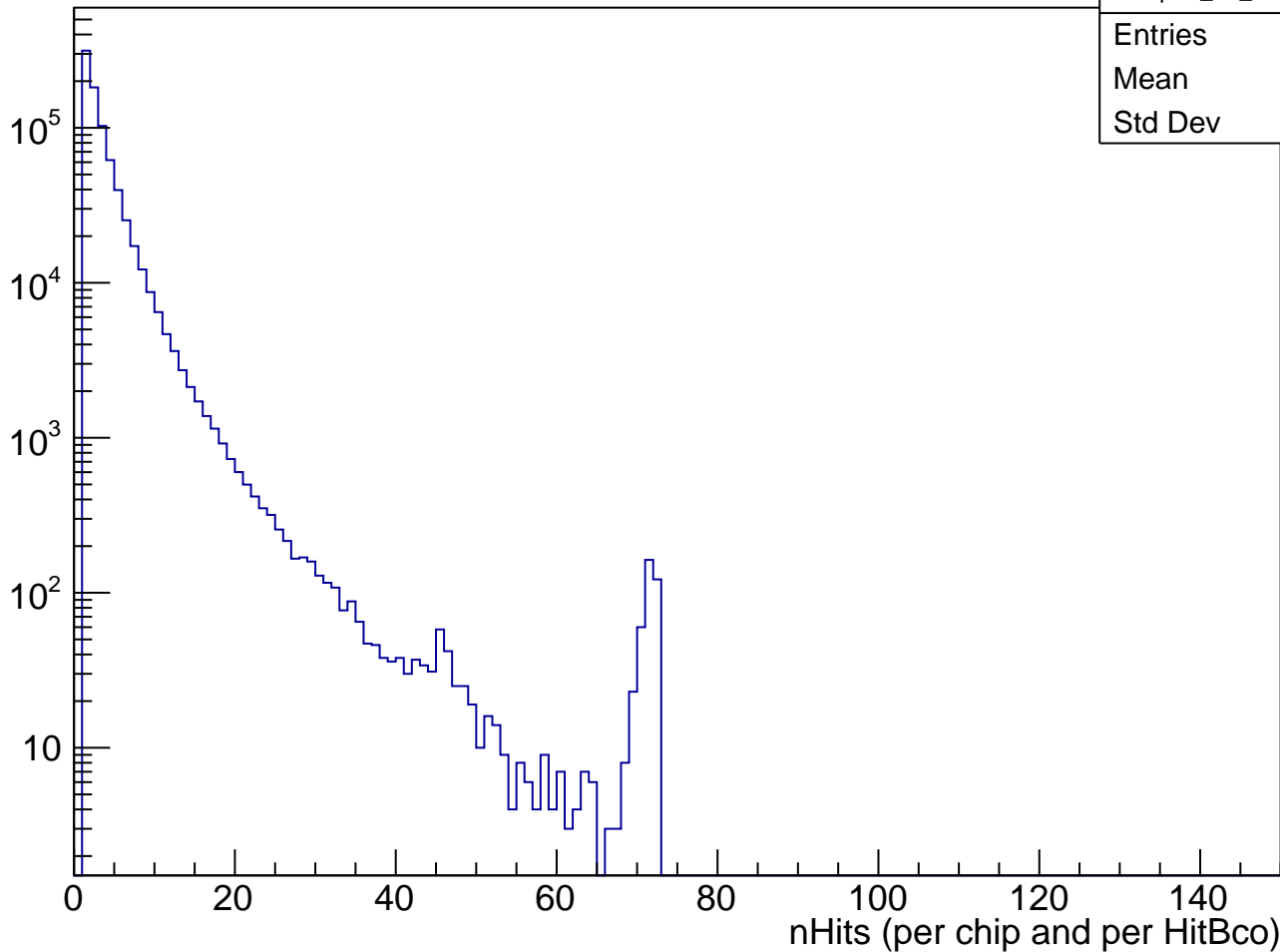


nChipHit_F3_Fch5_Chip10	
Entries	796798
Mean	2.974
Std Dev	3.539

nHits (per chip and per HitBco)

nChipHit_F3_Fch5_Chip11

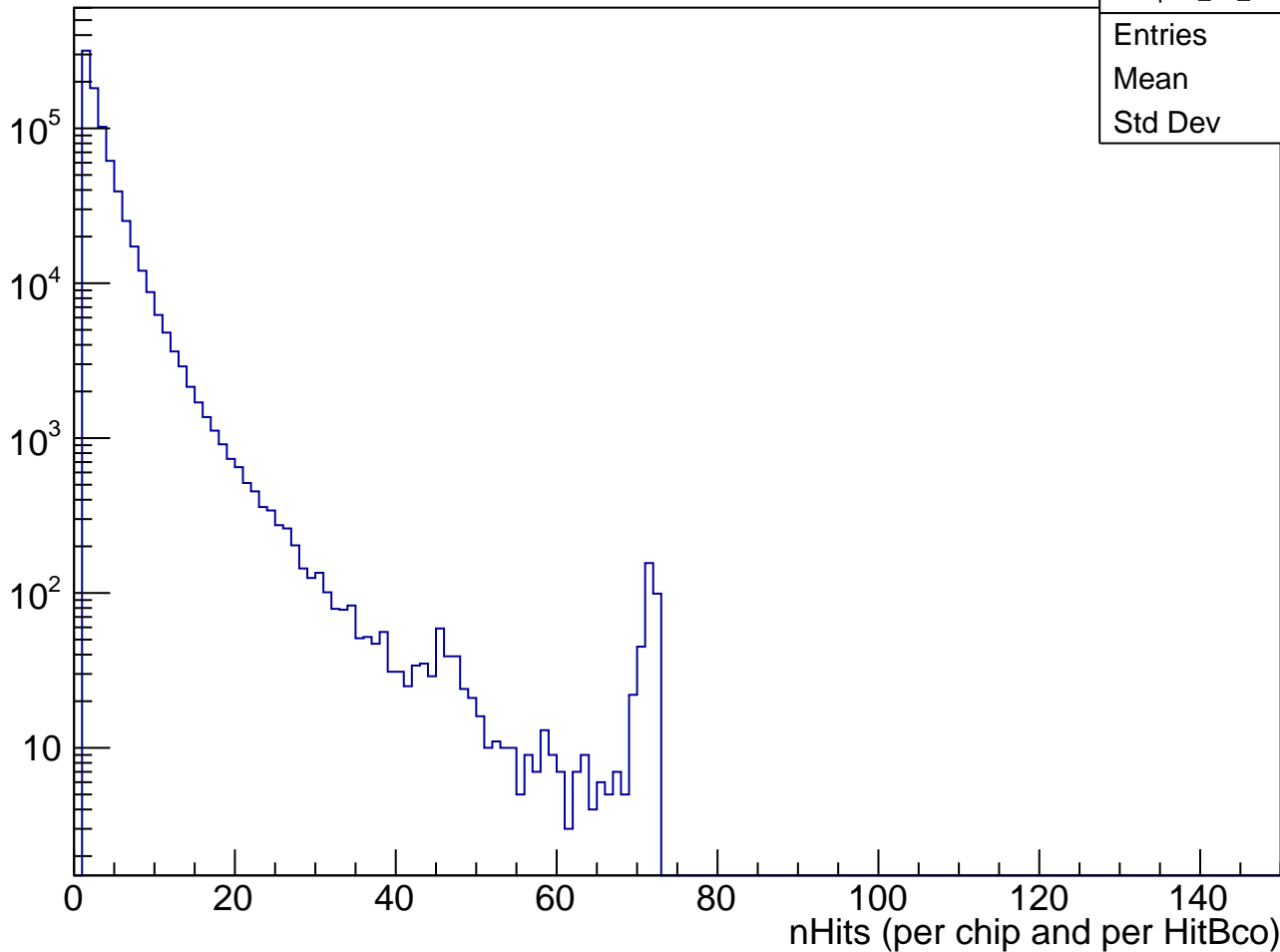
Entries



nChipHit_F3_Fch5_Chip11	
Entries	794974
Mean	2.957
Std Dev	3.543

nChipHit_F3_Fch5_Chip12

Entries

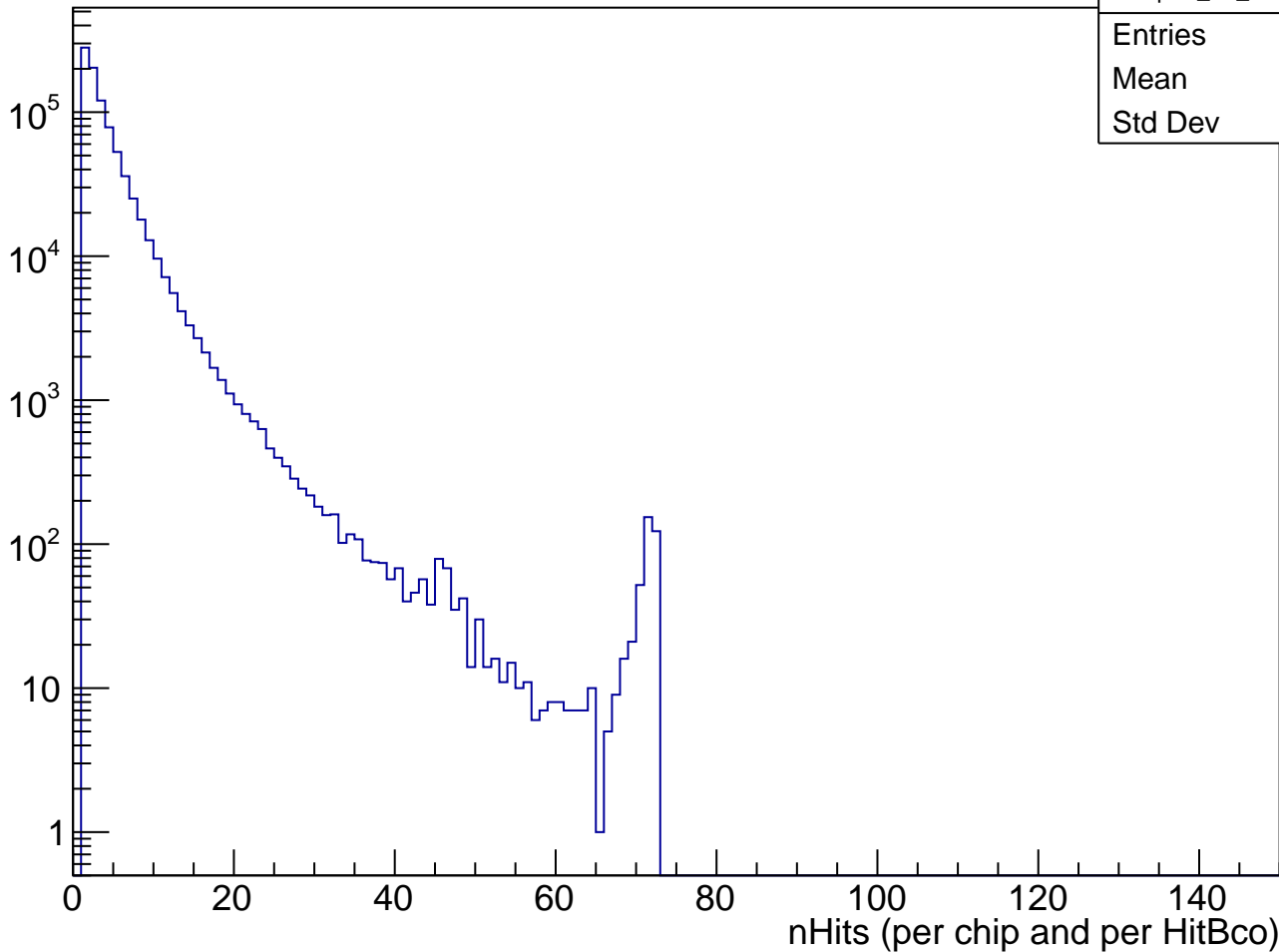


nChipHit_F3_Fch5_Chip12	
Entries	796176
Mean	2.95
Std Dev	3.526

nHits (per chip and per HitBco)

nChipHit_F3_Fch5_Chip13

Entries

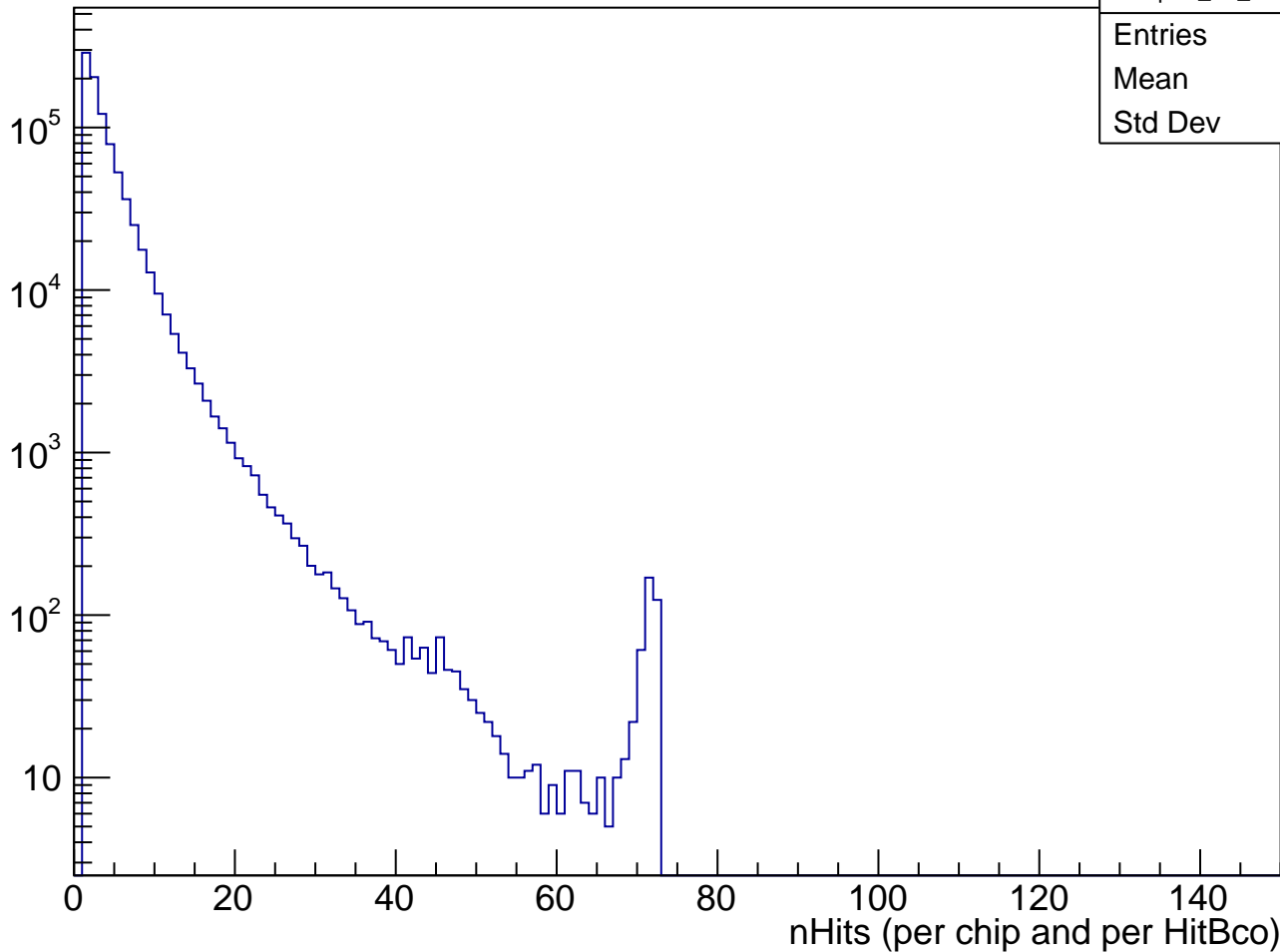


nChipHit_F3_Fch5_Chip13	
Entries	873959
Mean	3.411
Std Dev	3.877

nHits (per chip and per HitBco)

nChipHit_F3_Fch5_Chip14

Entries

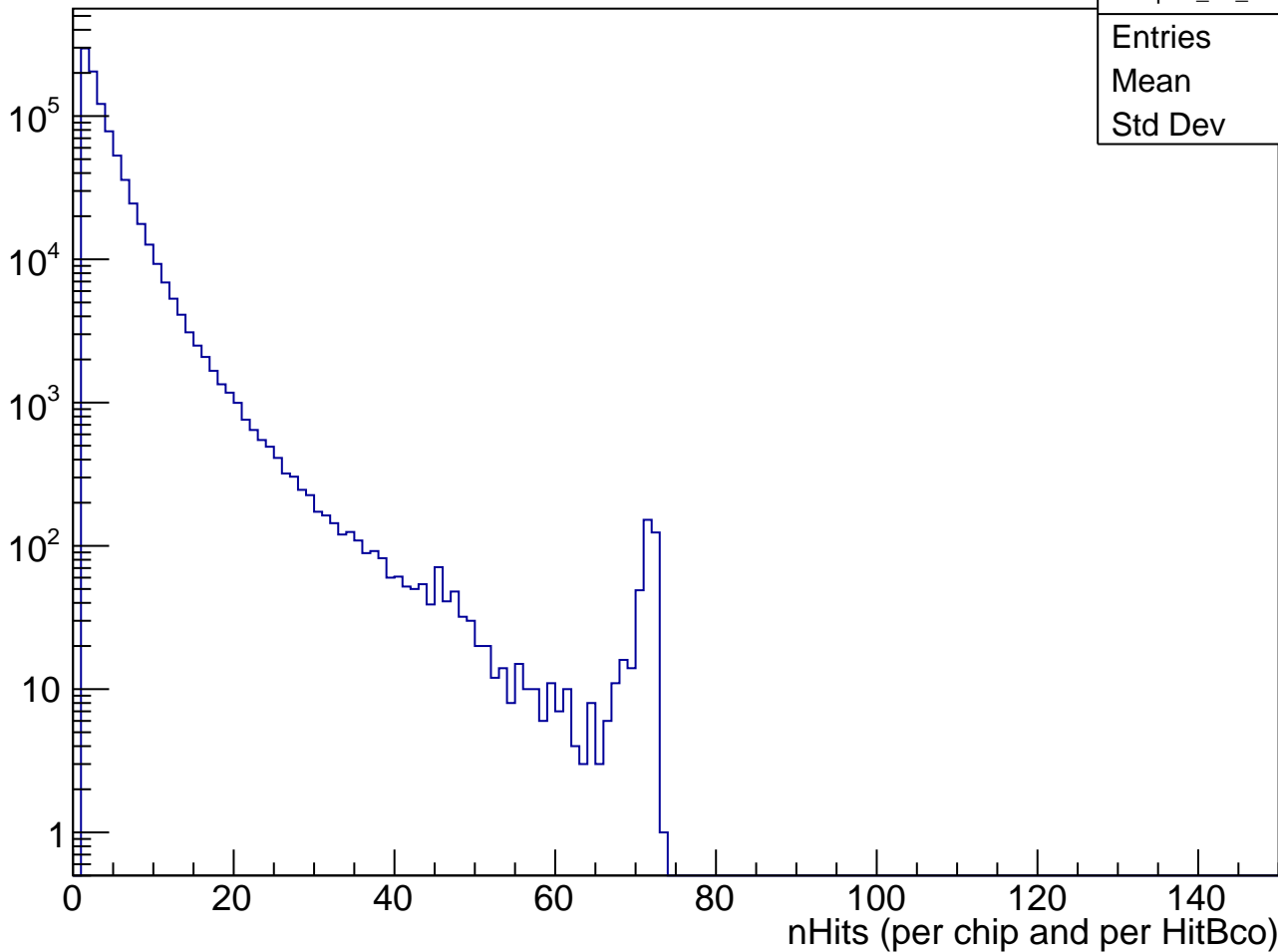


nChipHit_F3_Fch5_Chip14	
Entries	883248
Mean	3.388
Std Dev	3.891

nHits (per chip and per HitBco)

nChipHit_F3_Fch5_Chip15

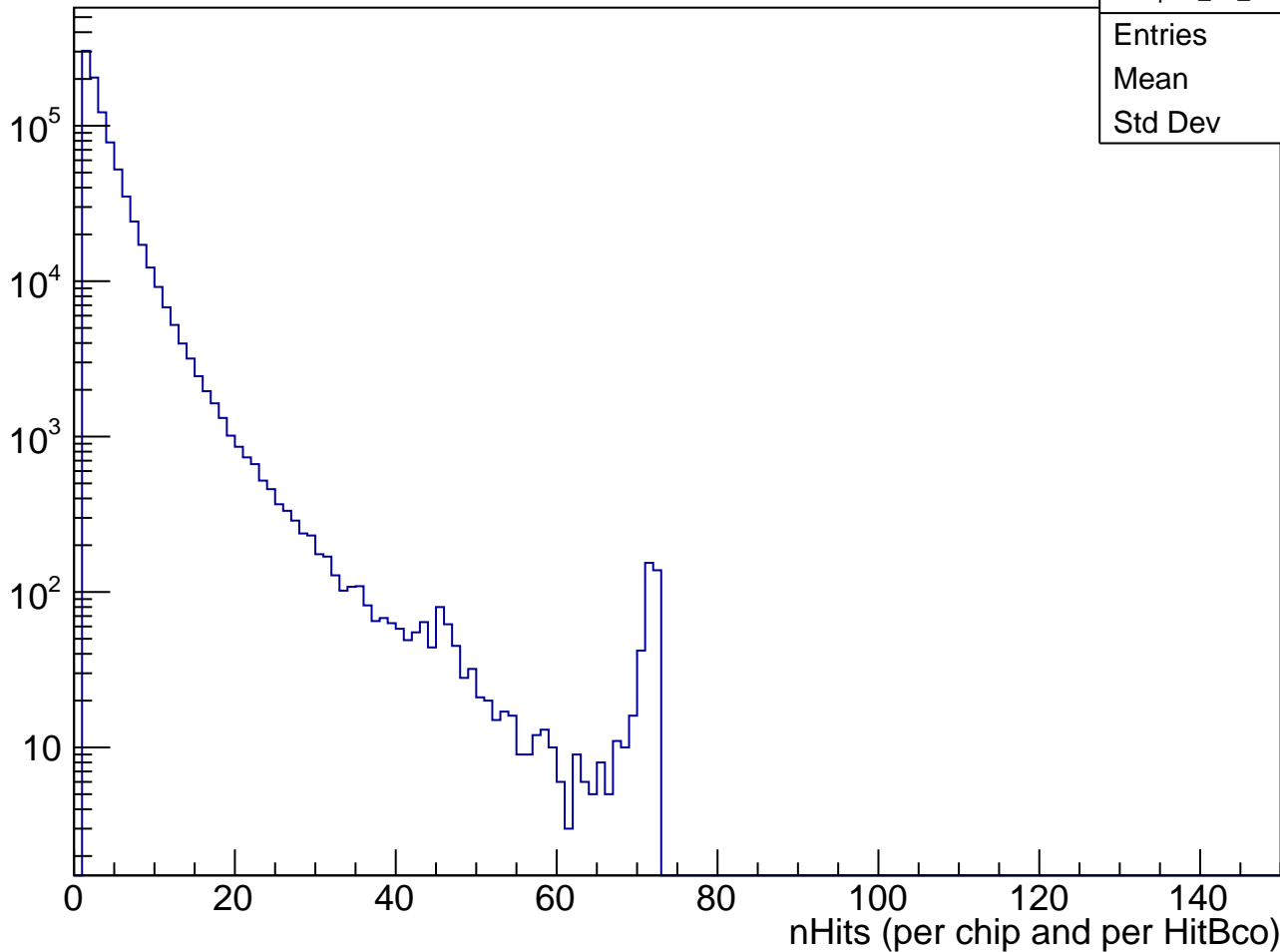
Entries



nChipHit_F3_Fch5_Chip15	
Entries	889261
Mean	3.345
Std Dev	3.832

nChipHit_F3_Fch5_Chip16

Entries

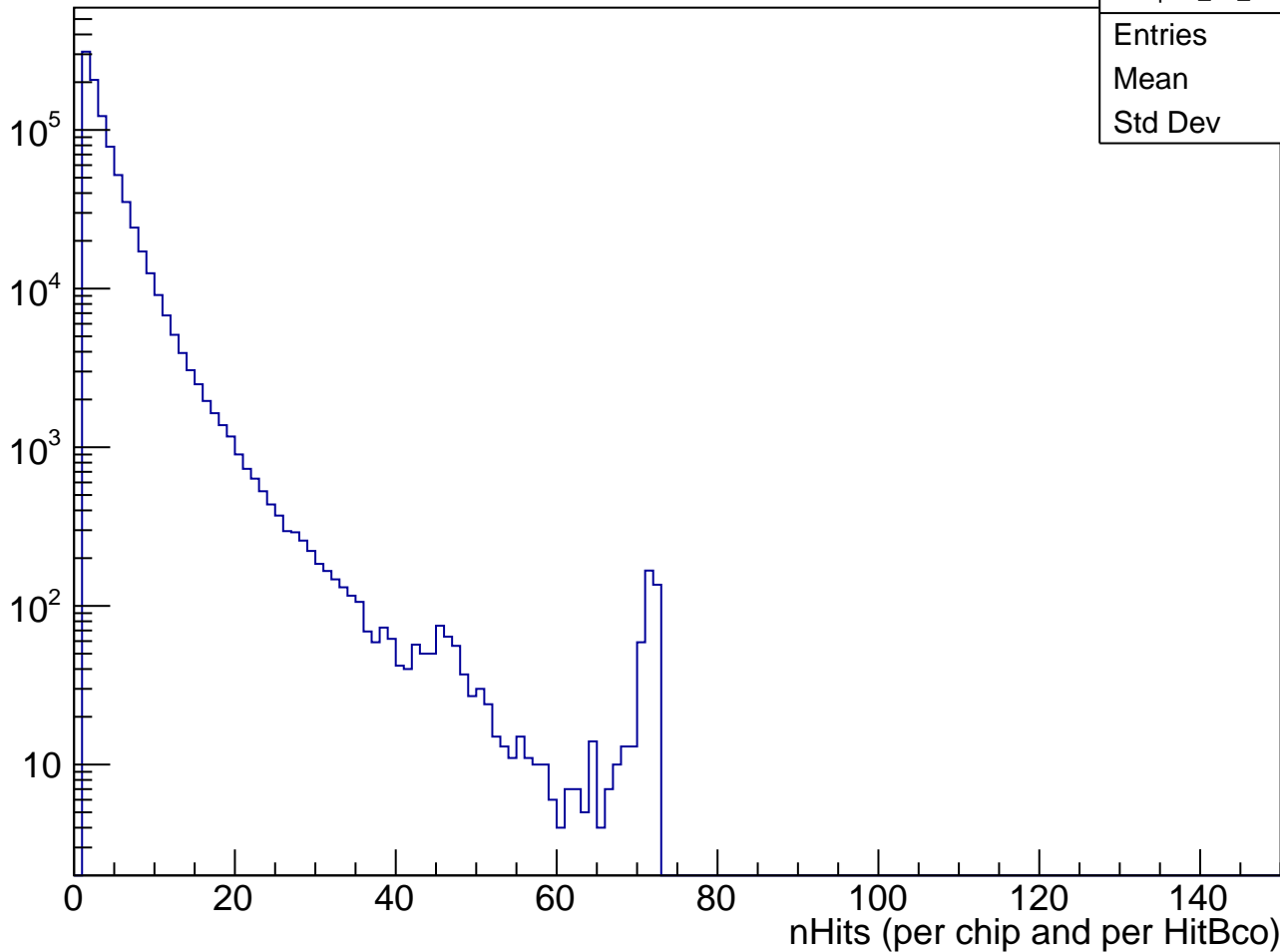


nChipHit_F3_Fch5_Chip16	
Entries	892880
Mean	3.303
Std Dev	3.805

nHits (per chip and per HitBco)

nChipHit_F3_Fch5_Chip17

Entries

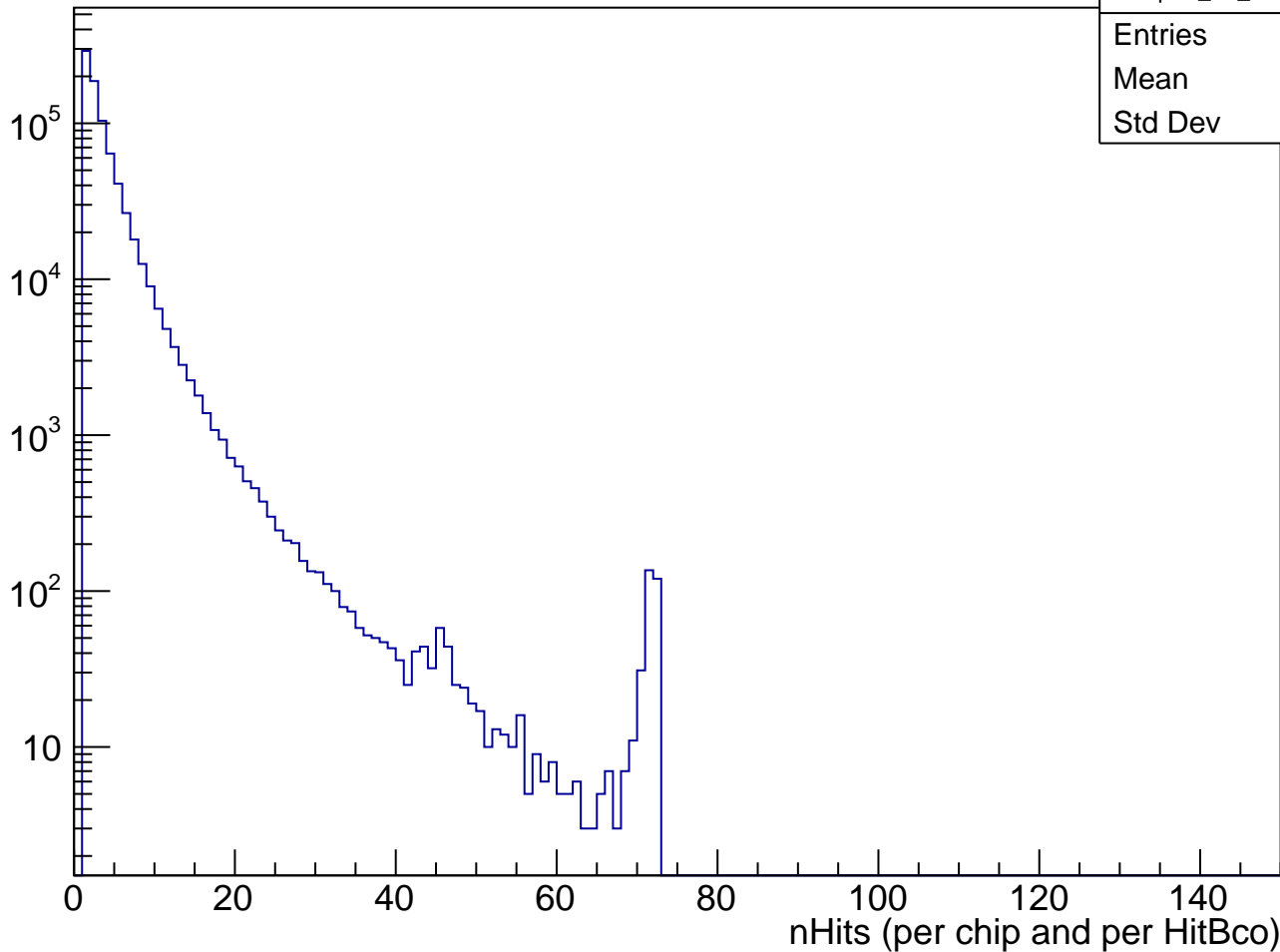


nChipHit_F3_Fch5_Chip17	
Entries	903345
Mean	3.284
Std Dev	3.815

nHits (per chip and per HitBco)

nChipHit_F3_Fch5_Chip18

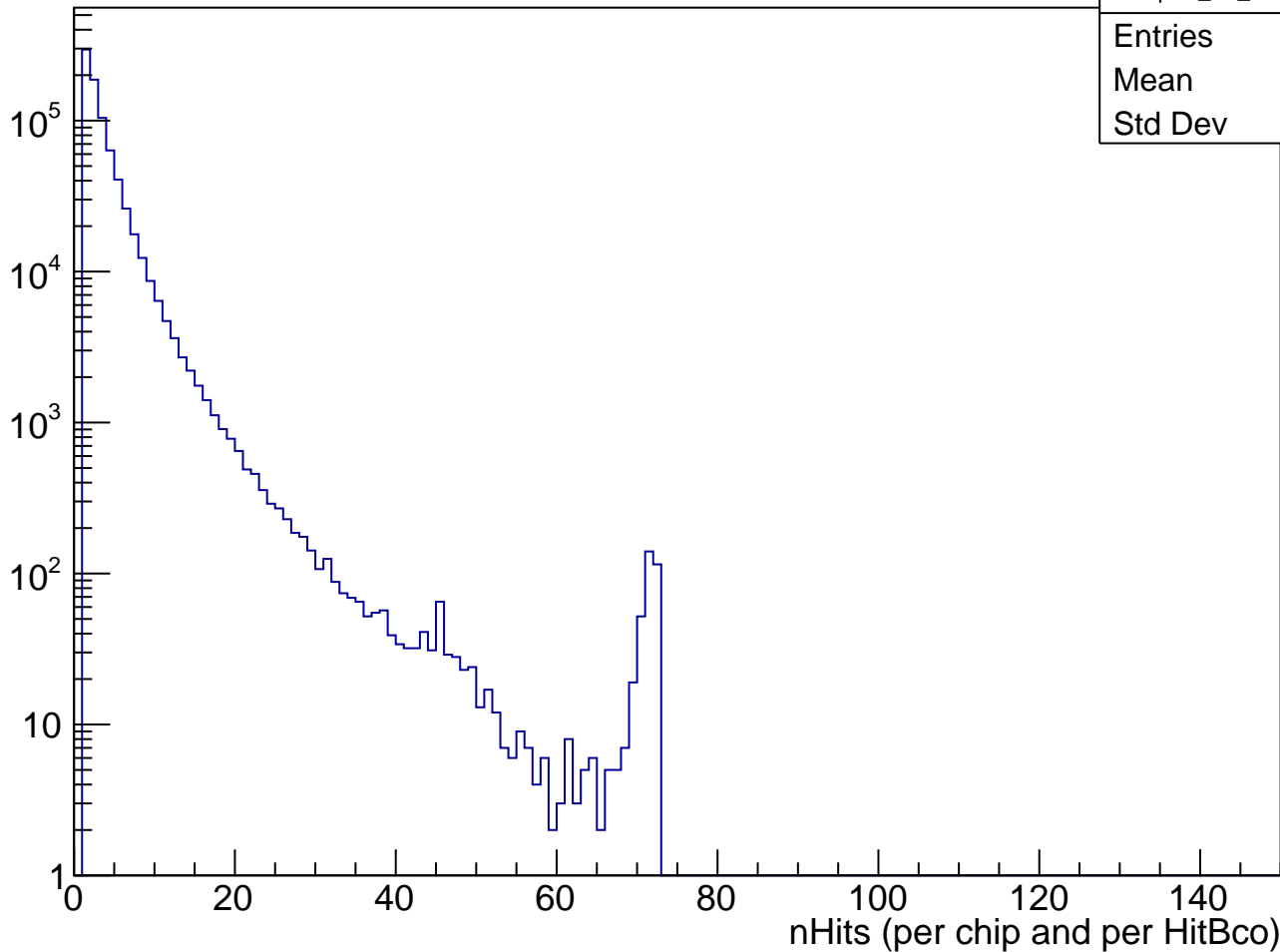
Entries



nChipHit_F3_Fch5_Chip18	
Entries	783513
Mean	3.032
Std Dev	3.53

nChipHit_F3_Fch5_Chip19

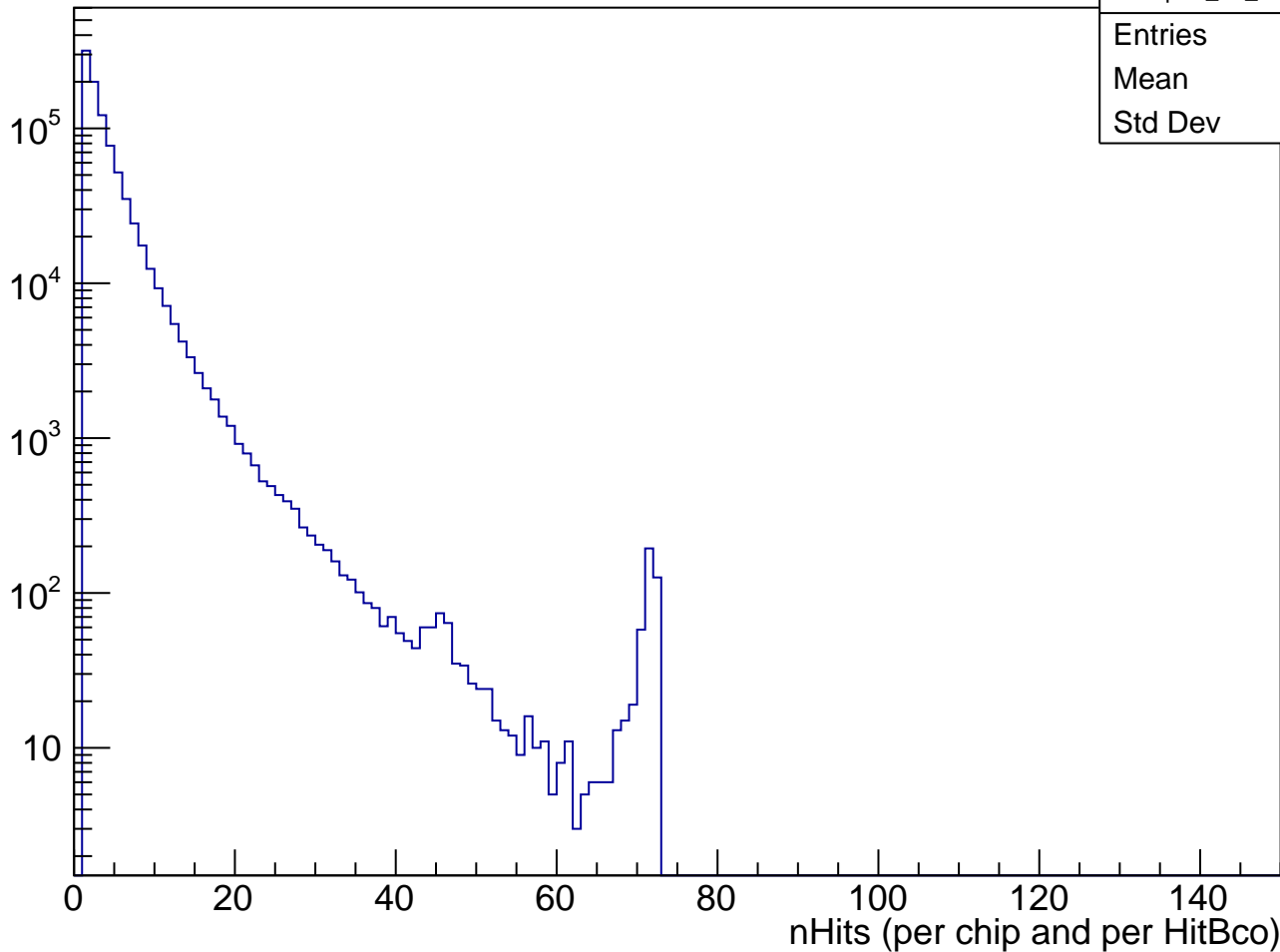
Entries



nChipHit_F3_Fch5_Chip19	
Entries	785961
Mean	3.01
Std Dev	3.53

nChipHit_F3_Fch5_Chip2

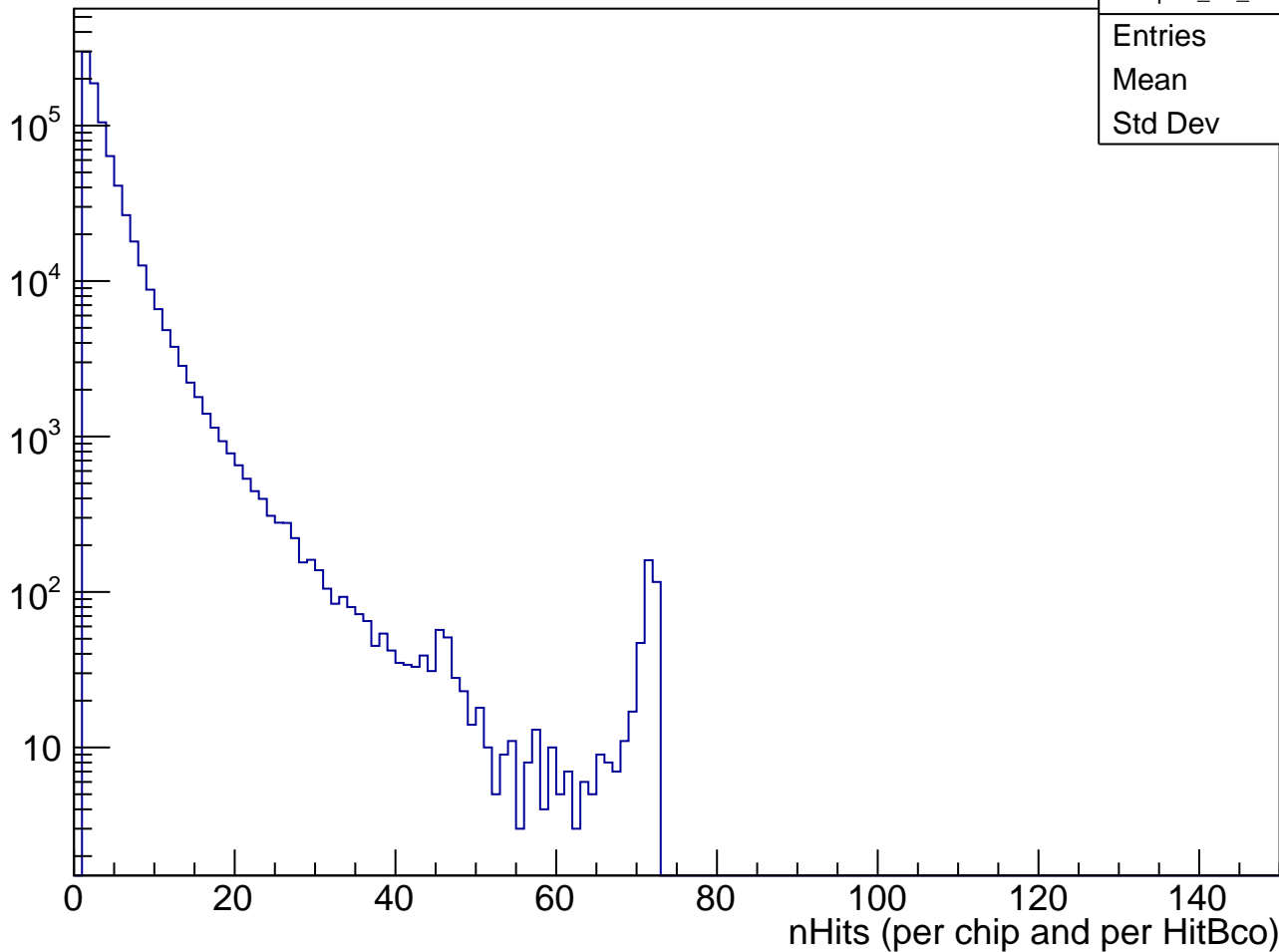
Entries



nChipHit_F3_Fch5_Chip2	
Entries	903729
Mean	3.312
Std Dev	3.894

nChipHit_F3_Fch5_Chip20

Entries

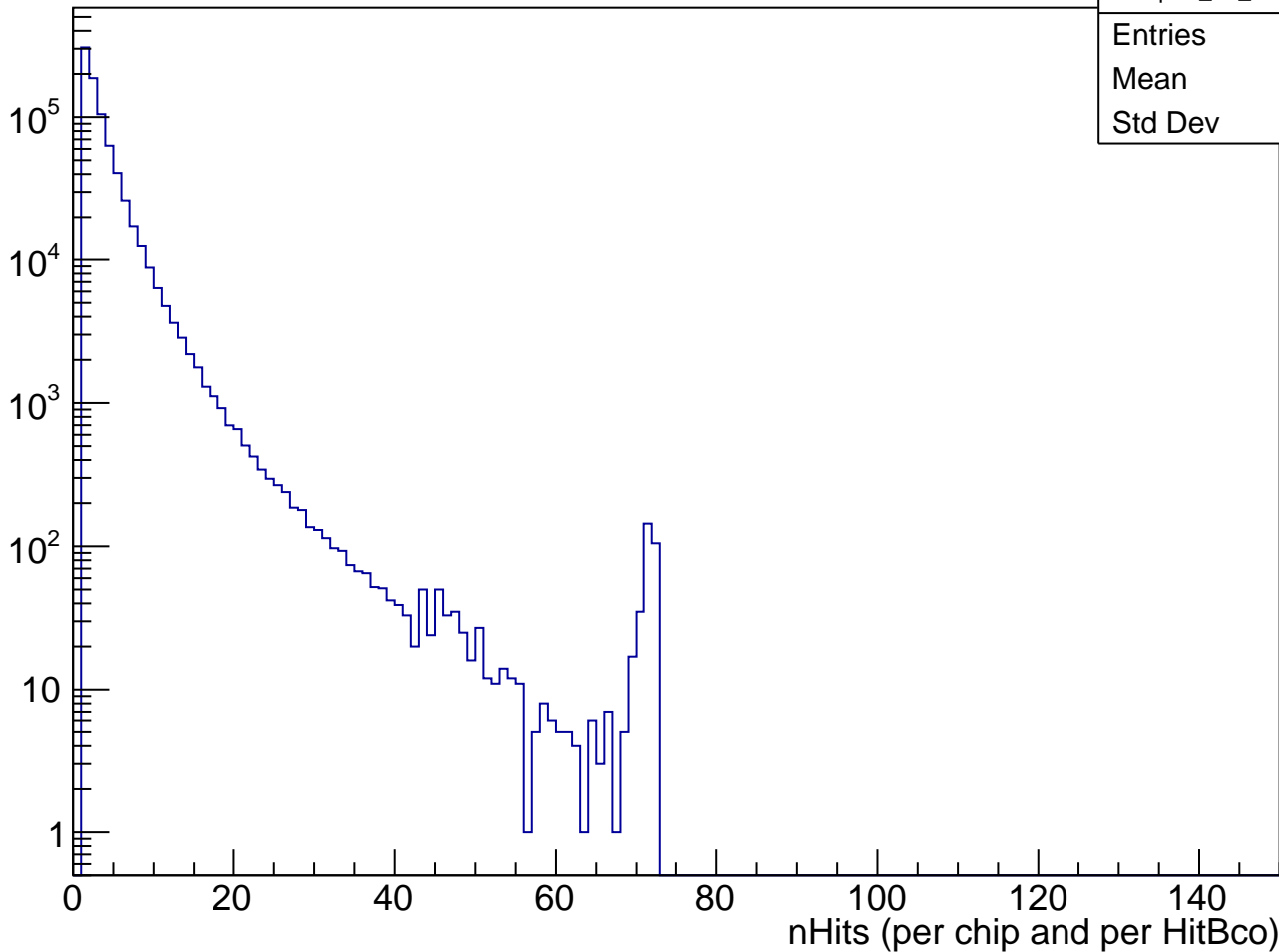


nChipHit_F3_Fch5_Chip20	
Entries	792127
Mean	3.028
Std Dev	3.585

nHits (per chip and per HitBco)

nChipHit_F3_Fch5_Chip21

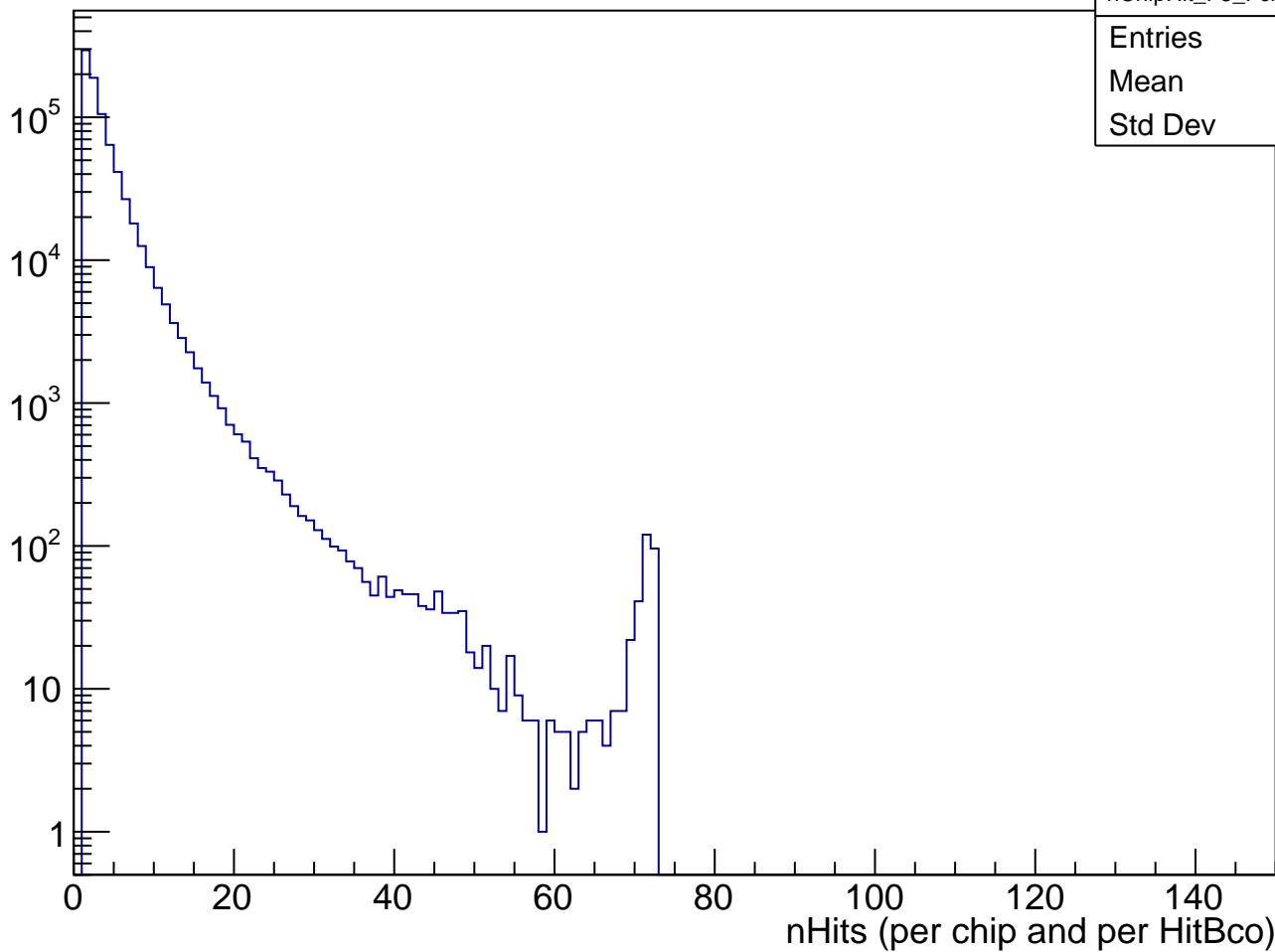
Entries



nChipHit_F3_Fch5_Chip21	
Entries	796916
Mean	2.981
Std Dev	3.498

nChipHit_F3_Fch5_Chip22

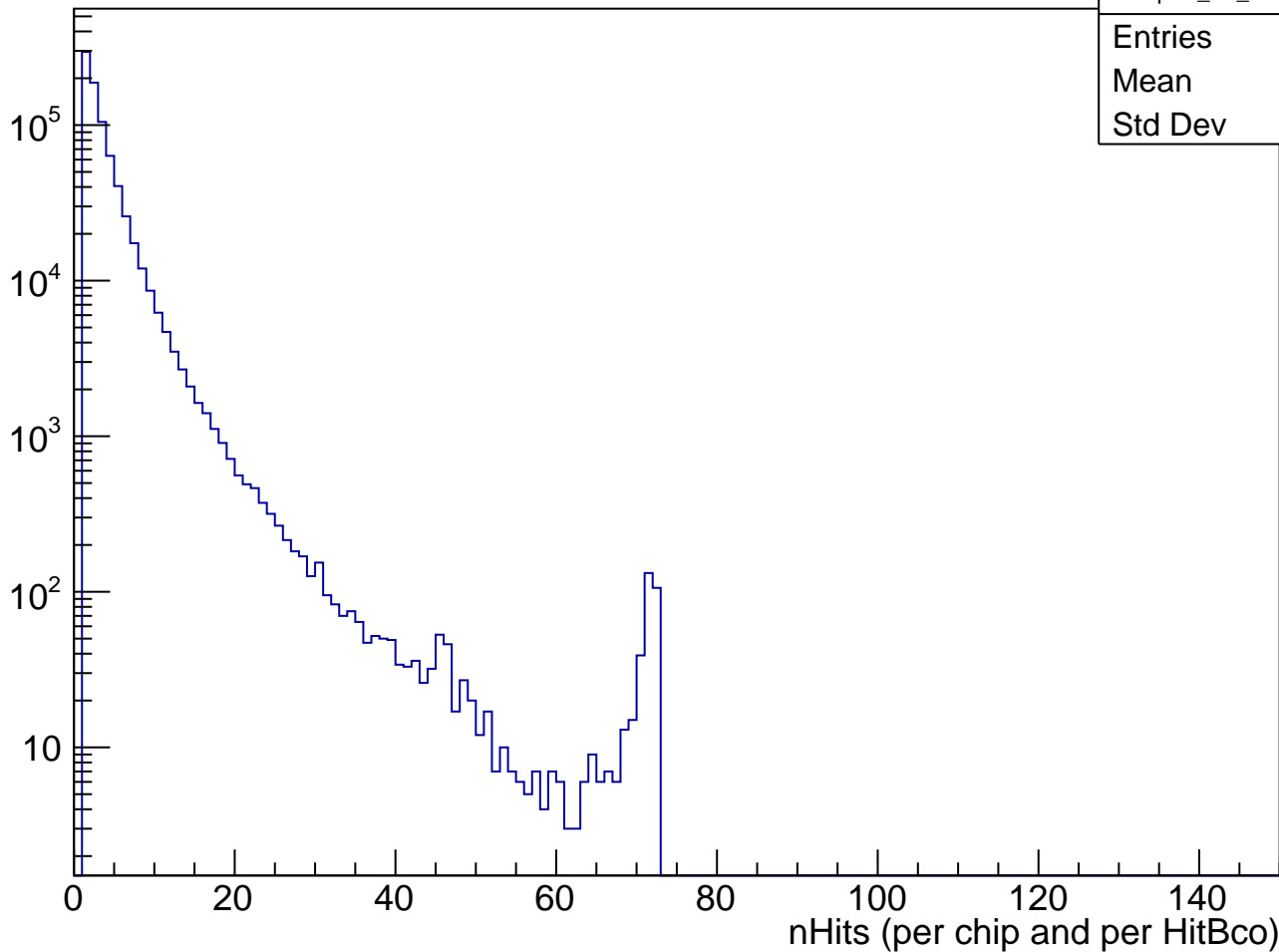
Entries



nChipHit_F3_Fch5_Chip22	
Entries	790795
Mean	3.026
Std Dev	3.519

nChipHit_F3_Fch5_Chip23

Entries



nChipHit_F3_Fch5_Chip23

Entries 785611

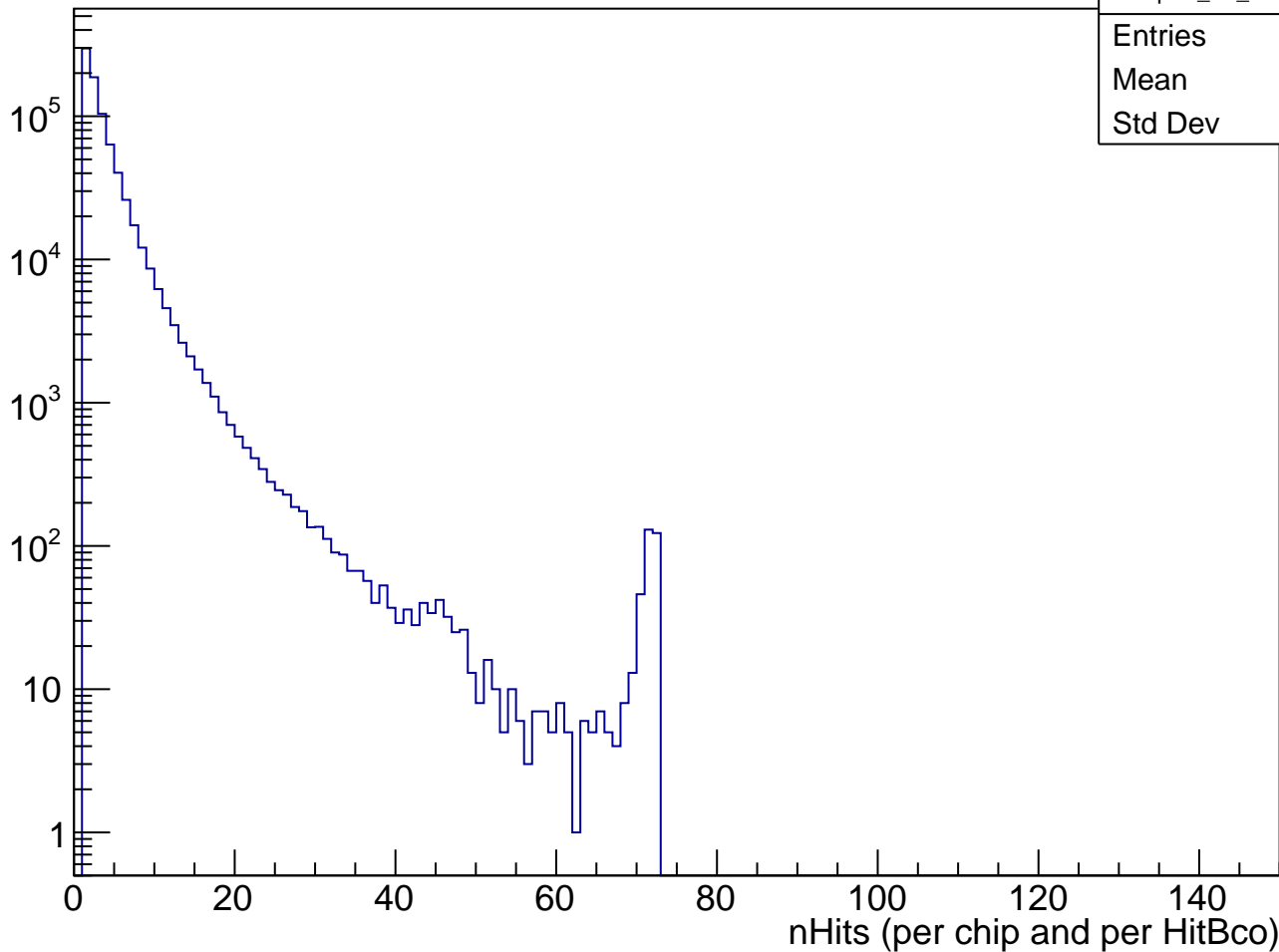
Mean 2.991

Std Dev 3.488

nHits (per chip and per HitBco)

nChipHit_F3_Fch5_Chip24

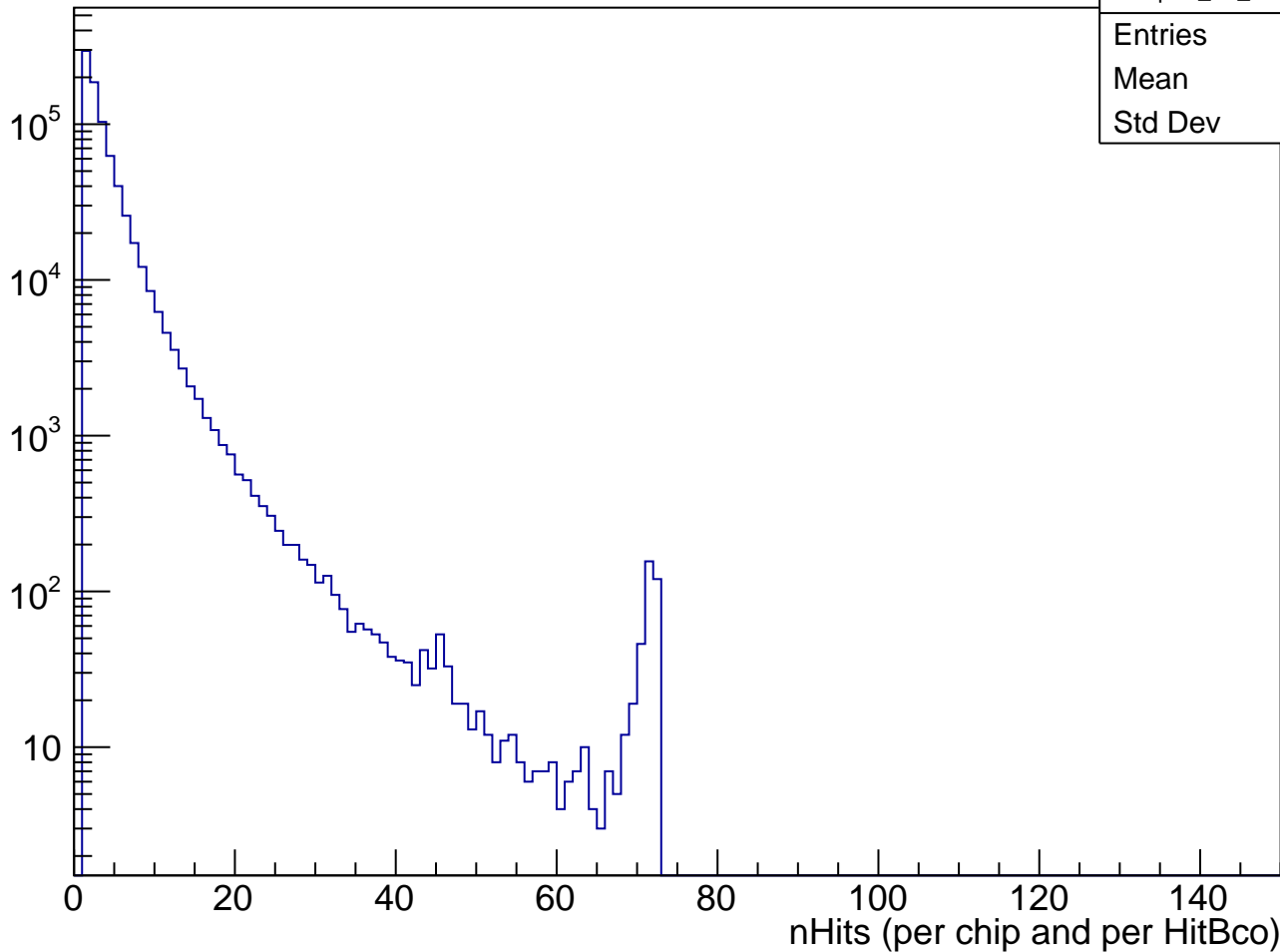
Entries



nChipHit_F3_Fch5_Chip24	
Entries	785937
Mean	2.982
Std Dev	3.48

nChipHit_F3_Fch5_Chip25

Entries

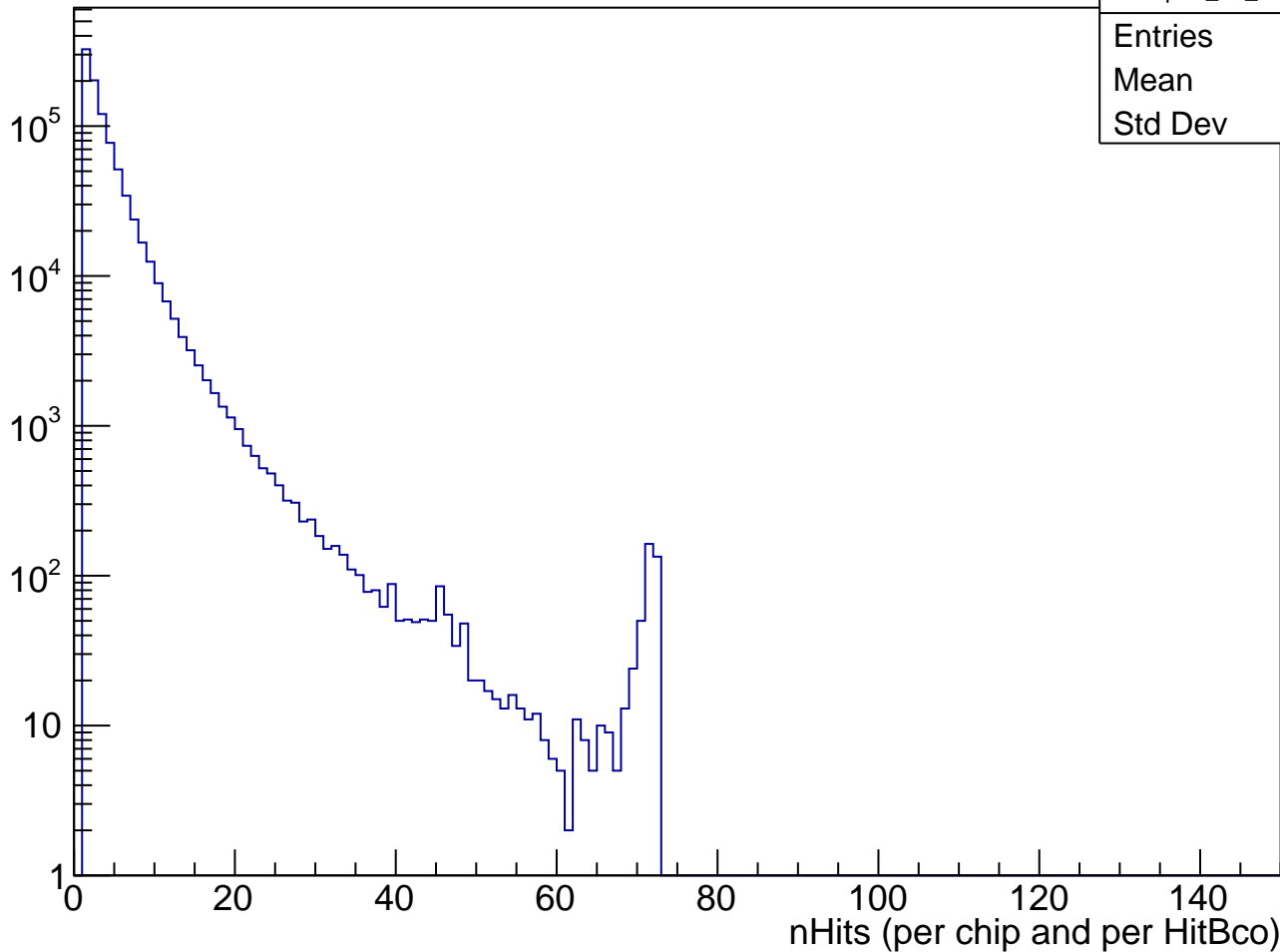


nChipHit_F3_Fch5_Chip25	
Entries	781156
Mean	2.99
Std Dev	3.528

nHits (per chip and per HitBco)

nChipHit_F3_Fch5_Chip3

Entries



nChipHit_F3_Fch5_Chip3

Entries 907731

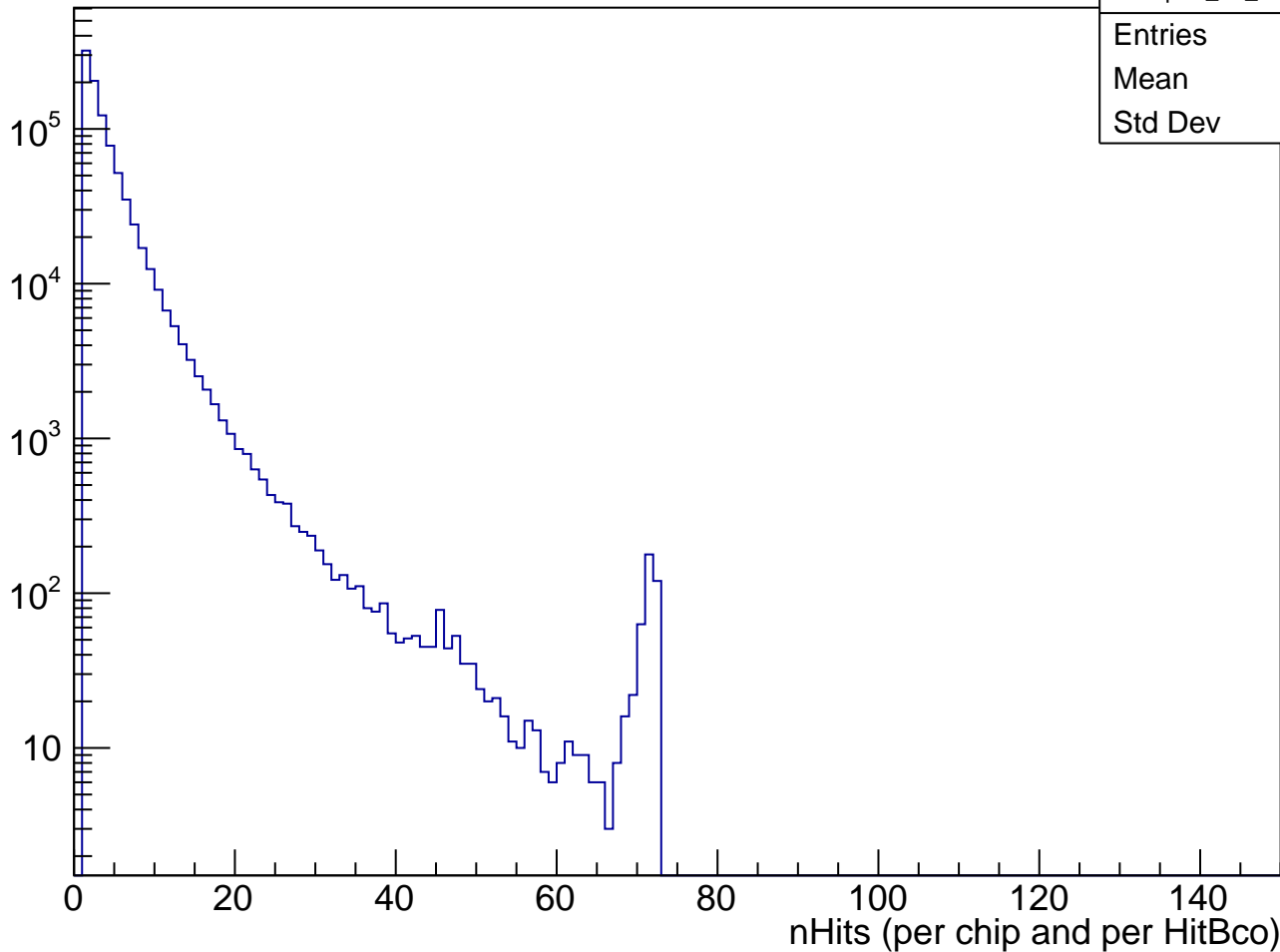
Mean 3.251

Std Dev 3.817

nHits (per chip and per HitBco)

nChipHit_F3_Fch5_Chip4

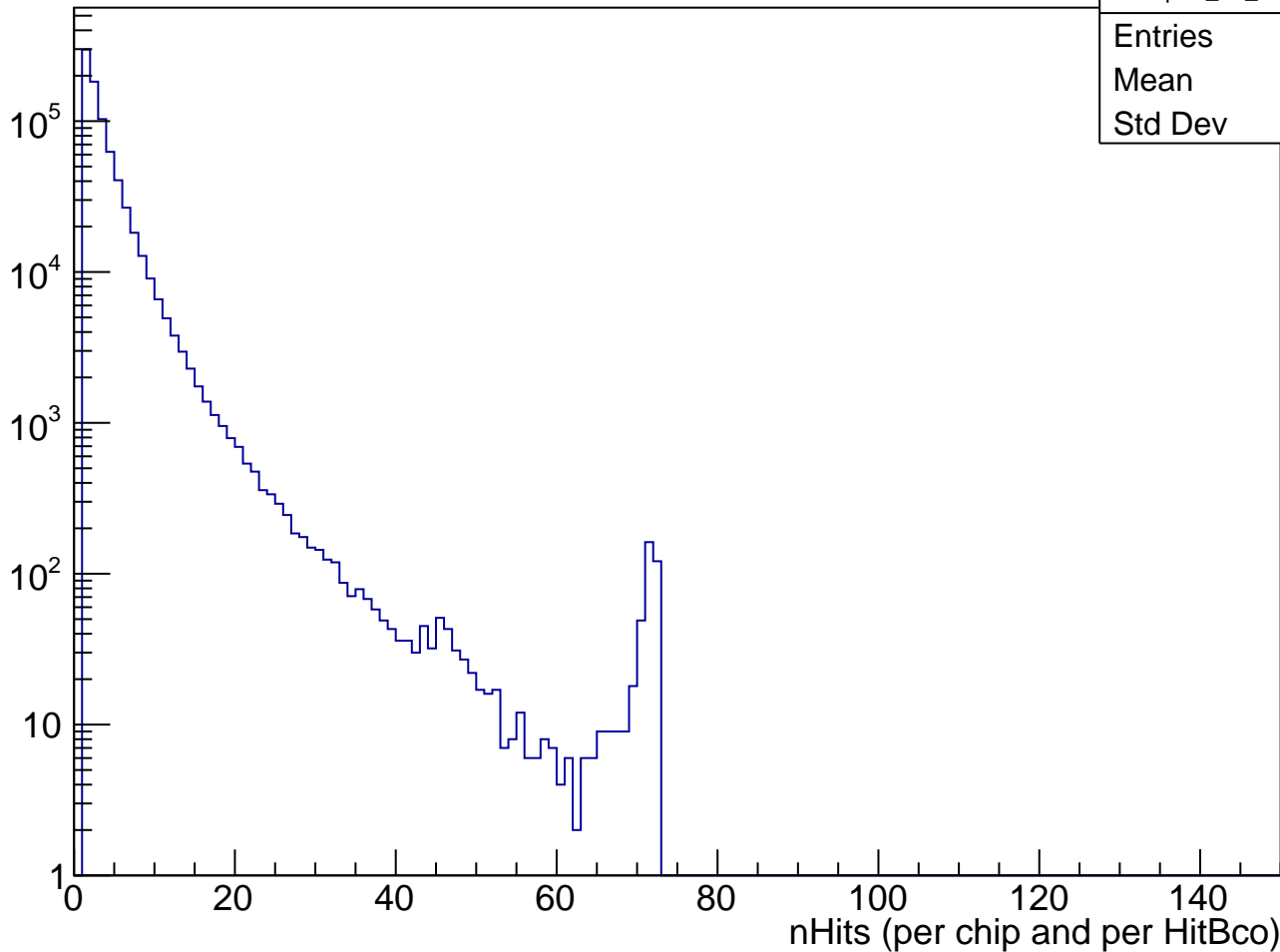
Entries



nChipHit_F3_Fch5_Chip4	
Entries	909977
Mean	3.268
Std Dev	3.822

nChipHit_F3_Fch5_Chip5

Entries



nChipHit_F3_Fch5_Chip5

Entries 785691

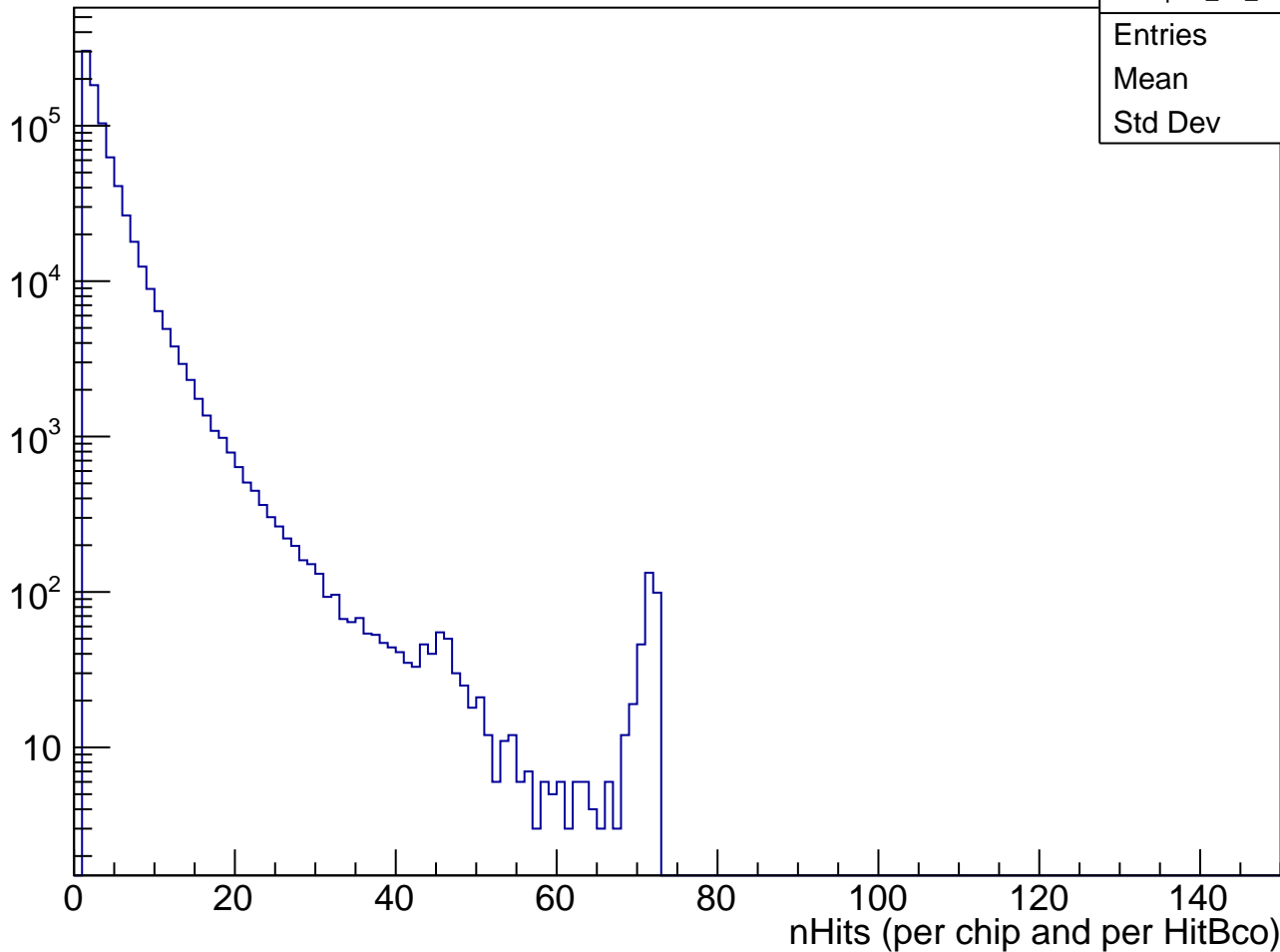
Mean 3.043

Std Dev 3.63

nHits (per chip and per HitBco)

nChipHit_F3_Fch5_Chip6

Entries



nChipHit_F3_Fch5_Chip6

Entries 789865

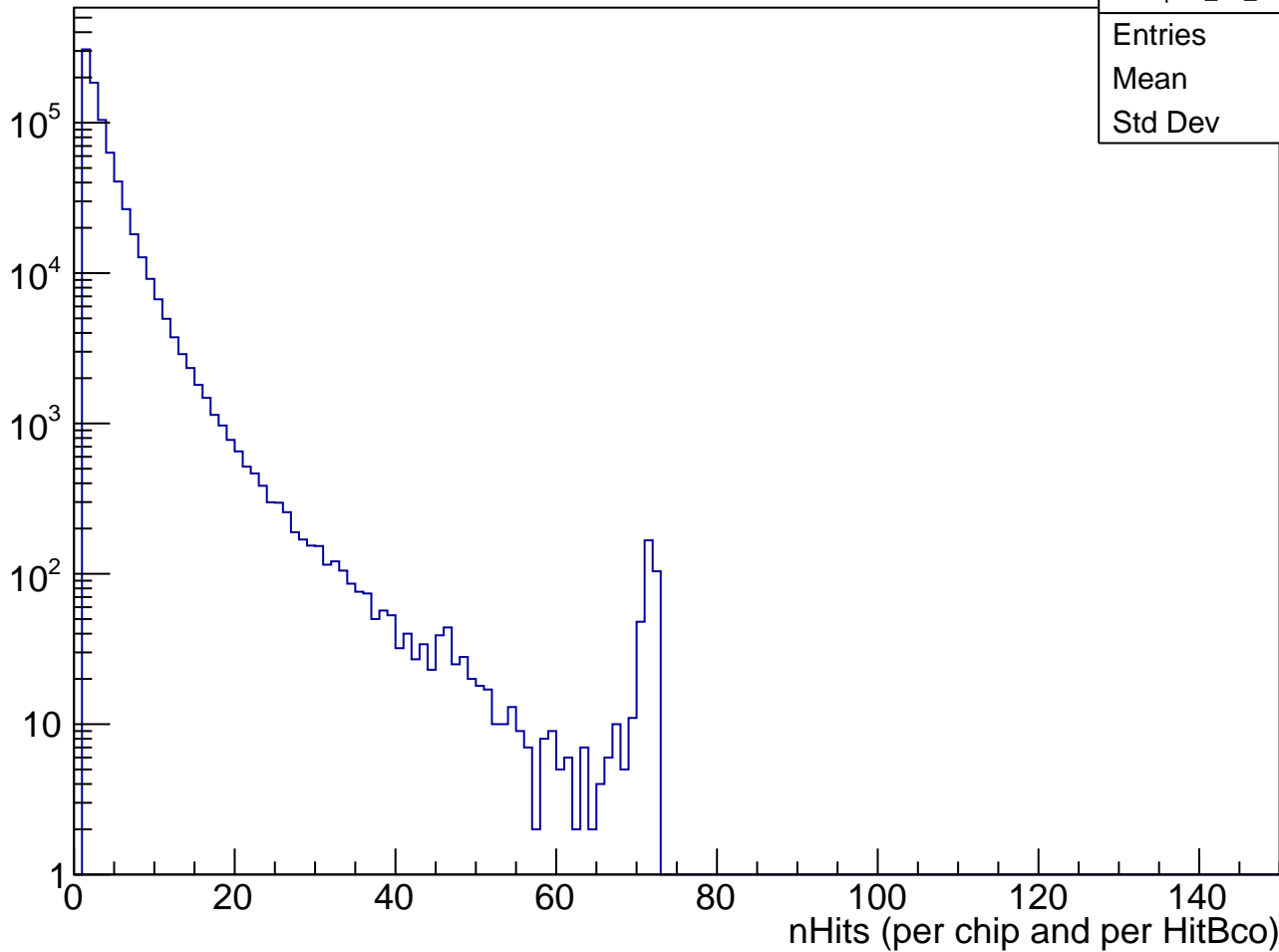
Mean 3.008

Std Dev 3.532

nHits (per chip and per HitBco)

nChipHit_F3_Fch5_Chip7

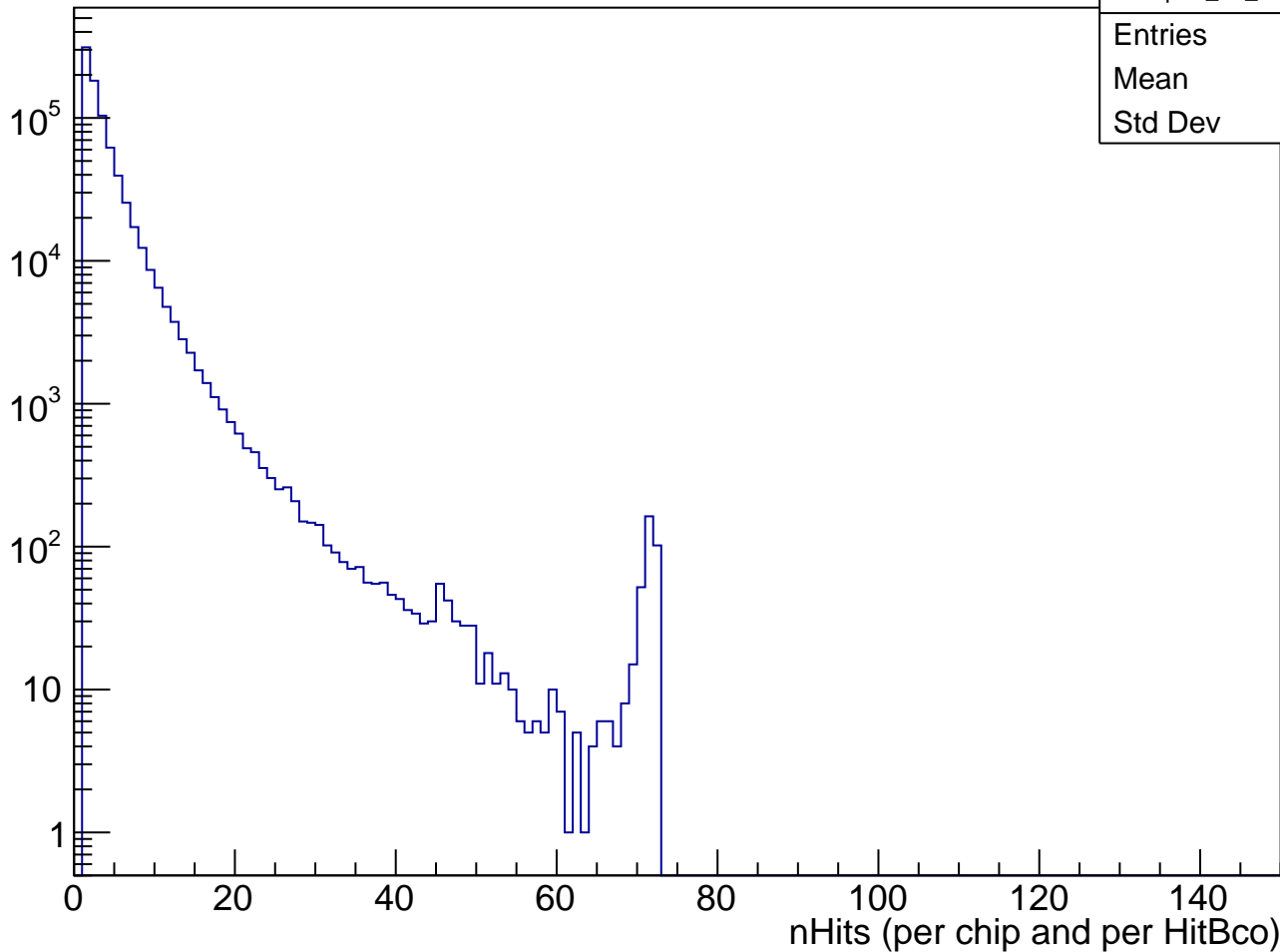
Entries



nChipHit_F3_Fch5_Chip7	
Entries	798665
Mean	3.017
Std Dev	3.58

nChipHit_F3_Fch5_Chip8

Entries



nChipHit_F3_Fch5_Chip8

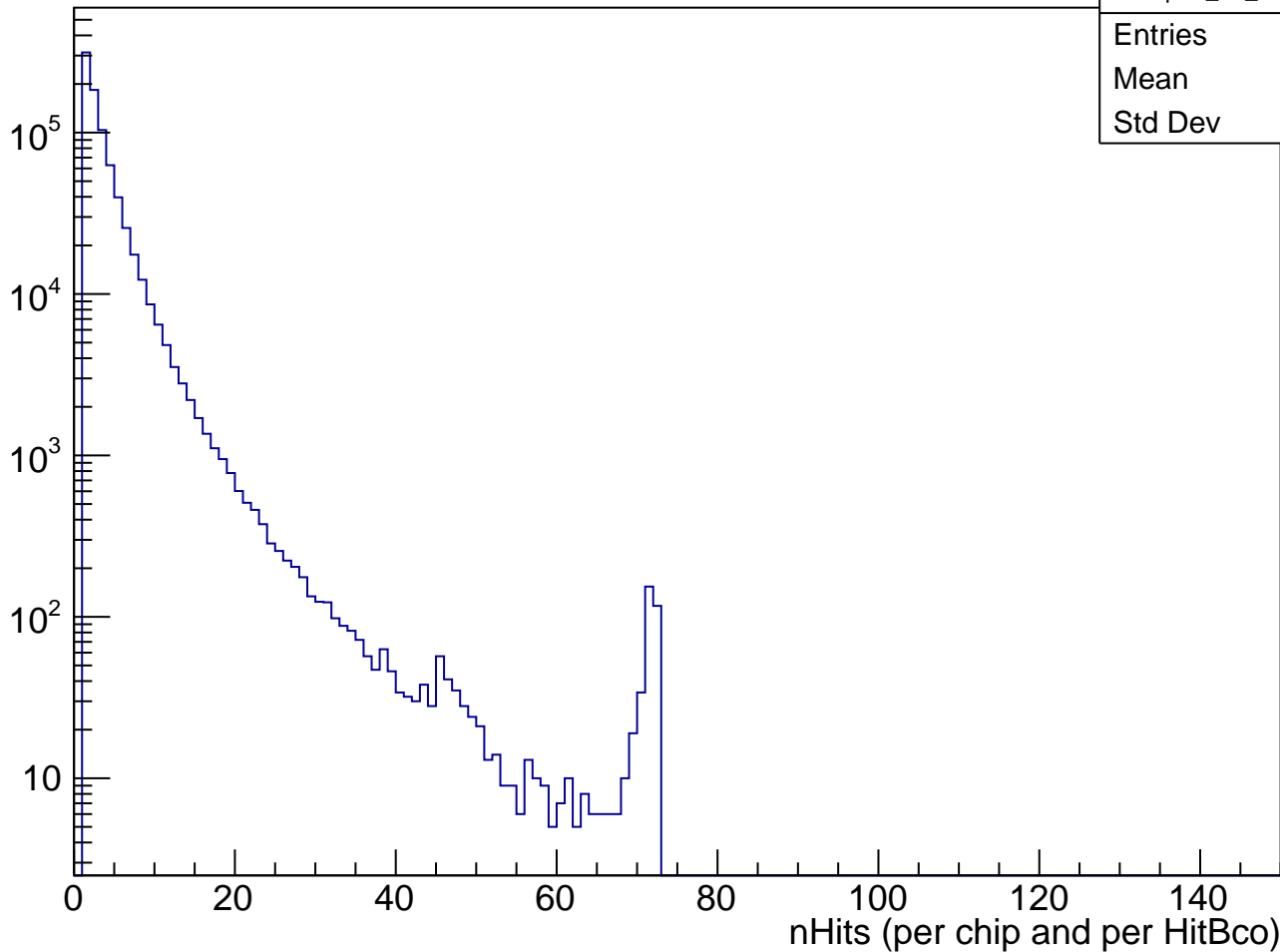
Entries 794144

Mean 2.971

Std Dev 3.545

nChipHit_F3_Fch5_Chip9

Entries



nChipHit_F3_Fch5_Chip9

Entries 798310

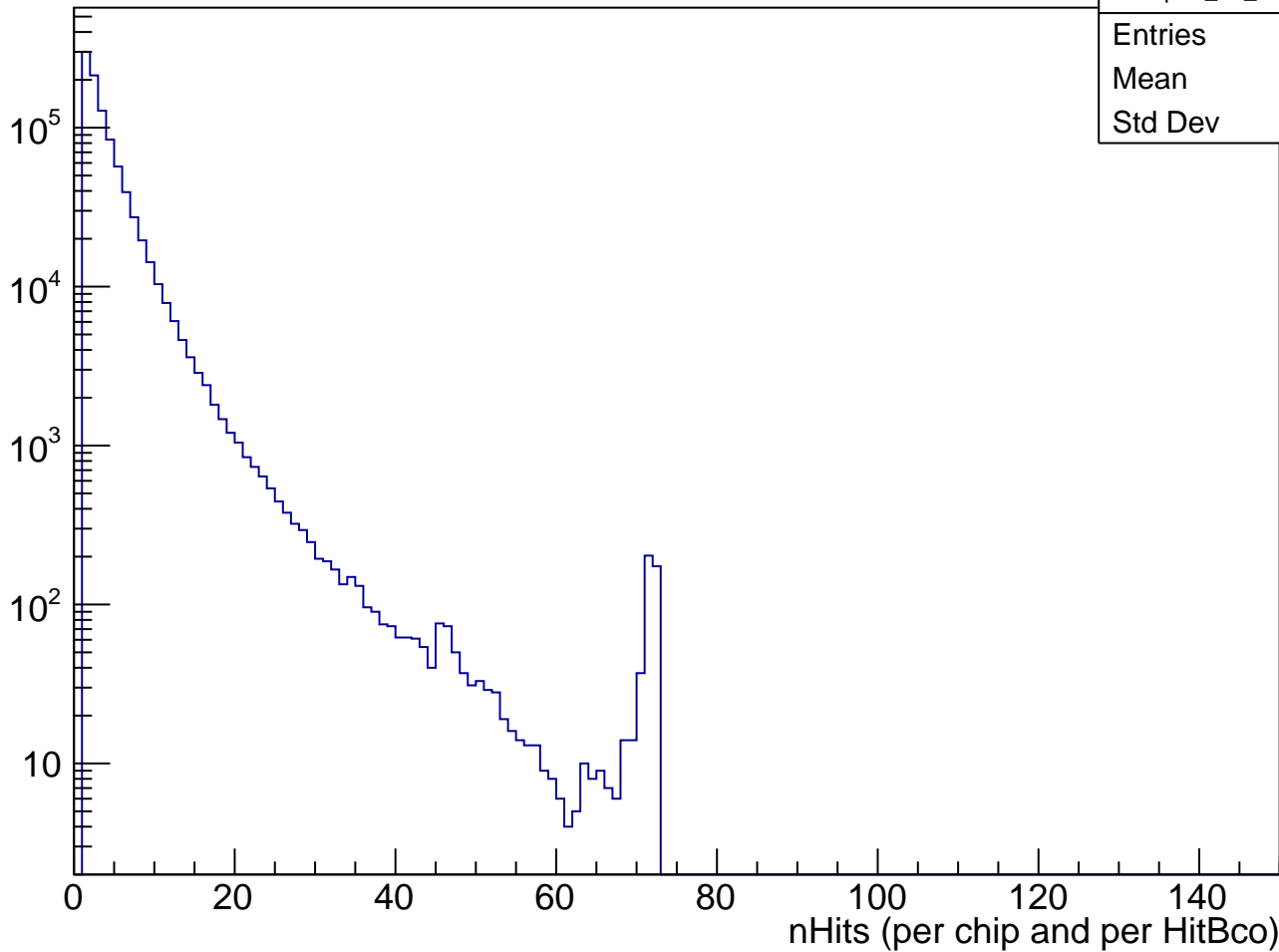
Mean 2.968

Std Dev 3.551

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip0

Entries



nChipHit_F3_Fch6_Chip0

Entries 933474

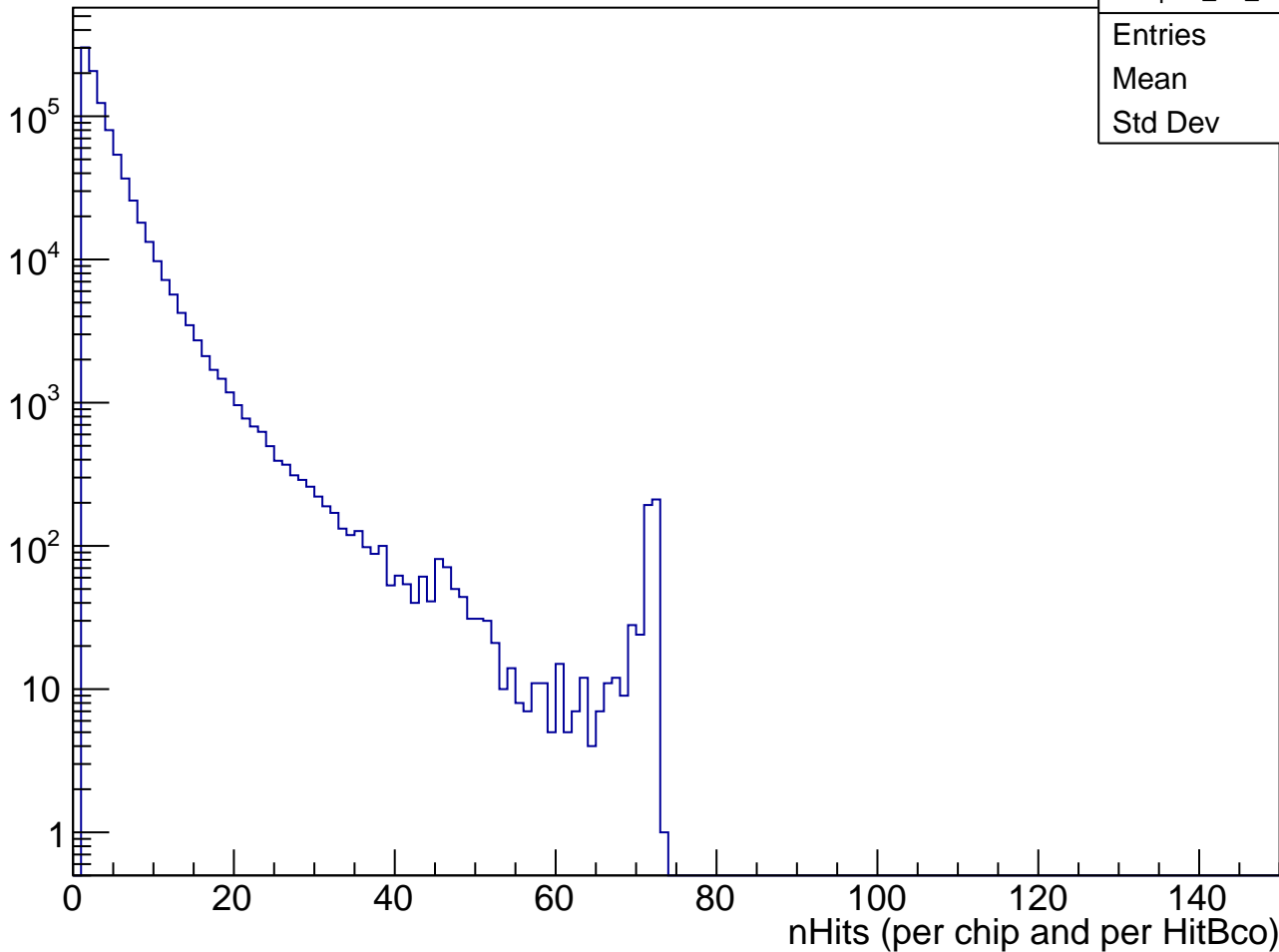
Mean 3.448

Std Dev 3.967

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip1

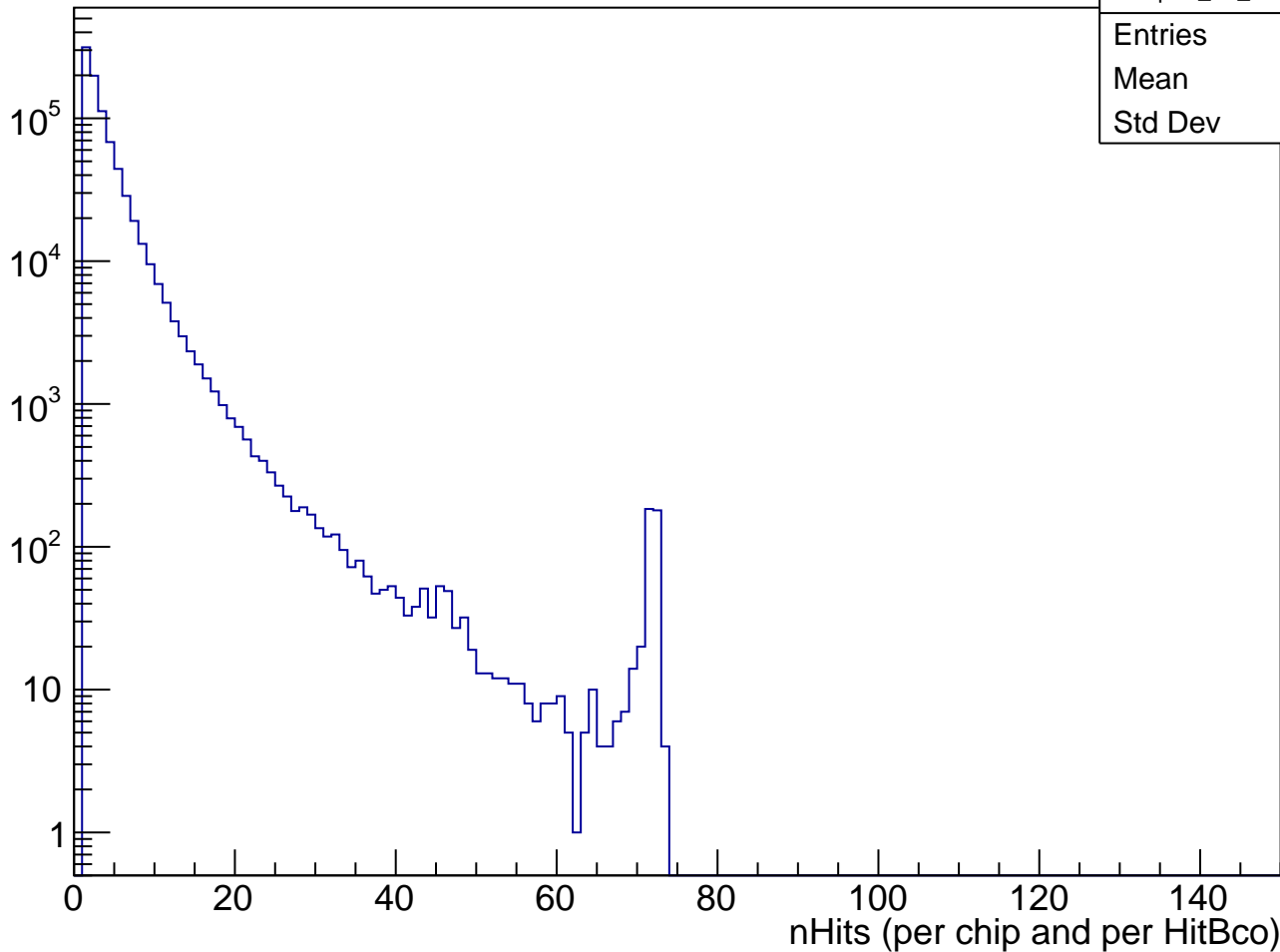
Entries



nChipHit_F3_Fch6_Chip1	
Entries	908067
Mean	3.387
Std Dev	3.97

nChipHit_F3_Fch6_Chip10

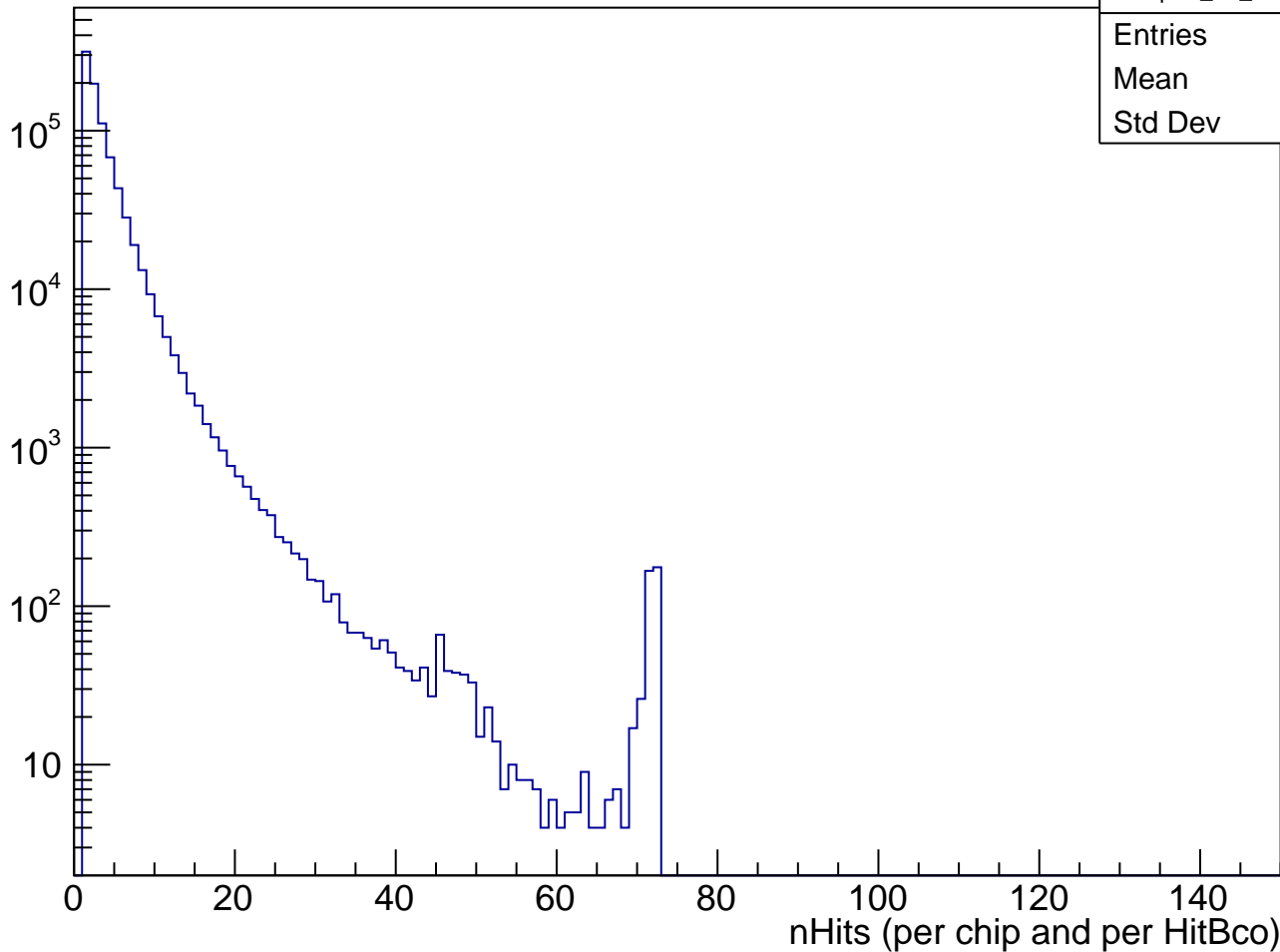
Entries



nChipHit_F3_Fch6_Chip10	
Entries	840858
Mean	3.032
Std Dev	3.595

nChipHit_F3_Fch6_Chip11

Entries

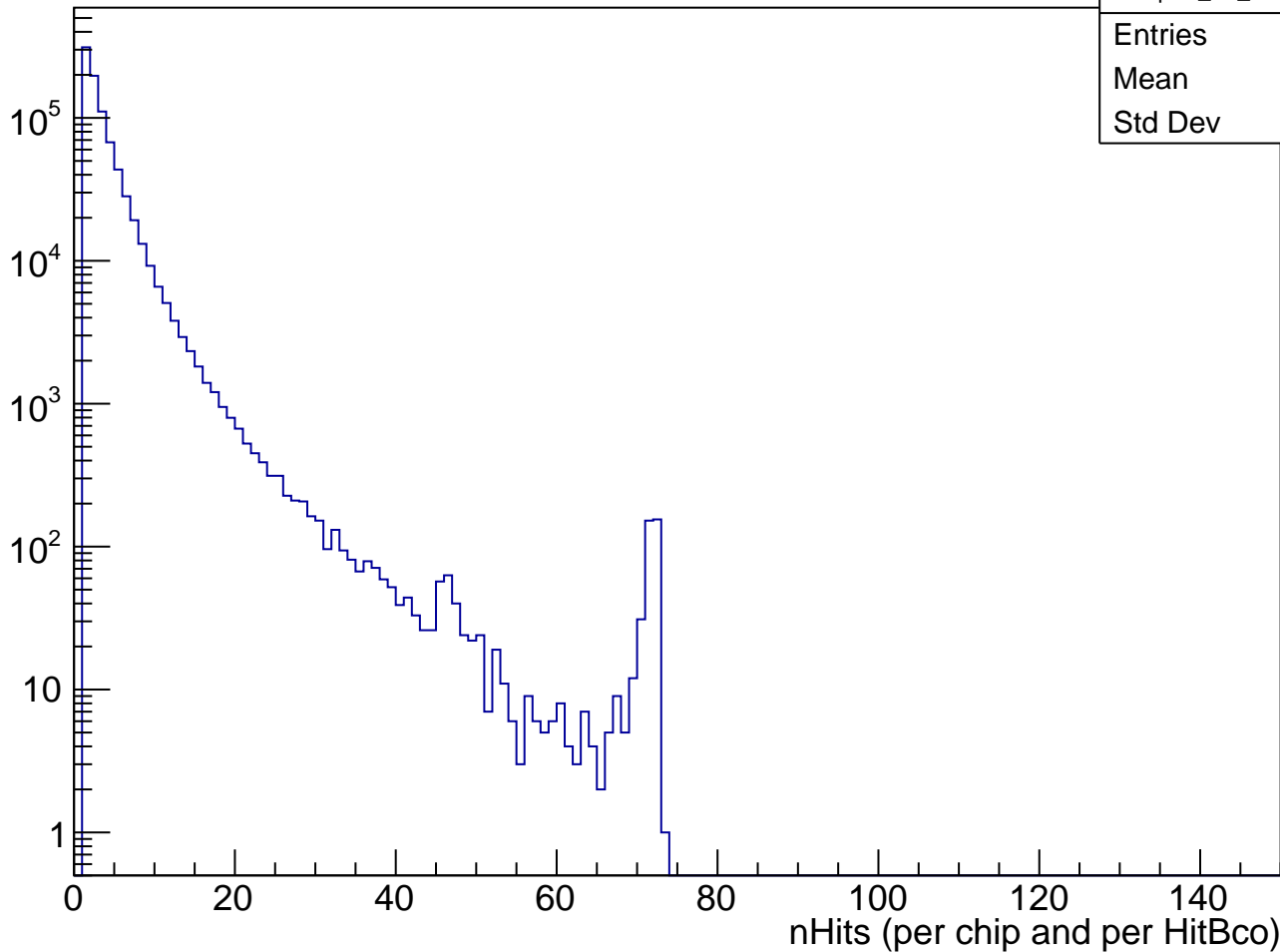


nChipHit_F3_Fch6_Chip11	
Entries	837537
Mean	3.018
Std Dev	3.584

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip12

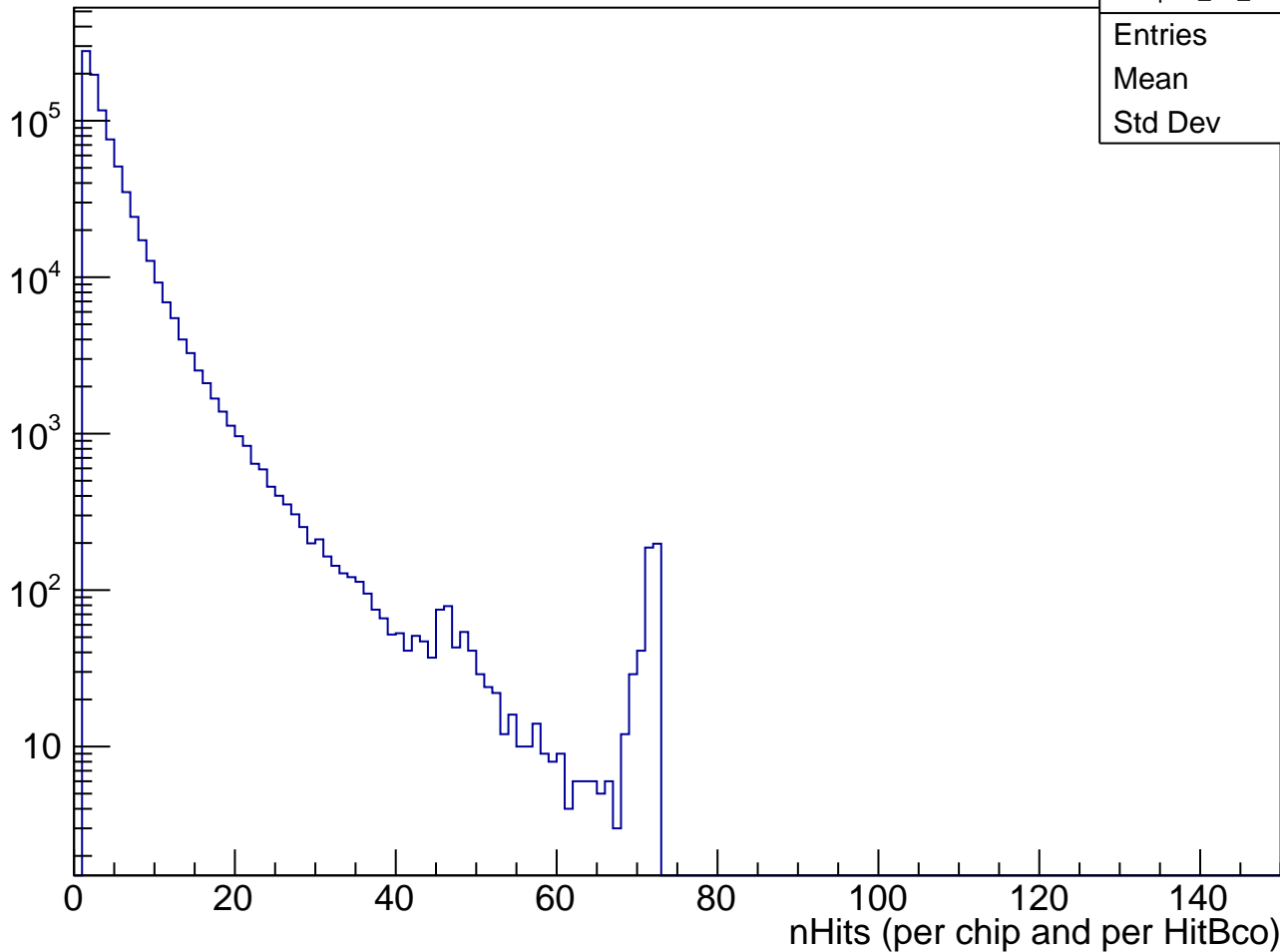
Entries



nChipHit_F3_Fch6_Chip12	
Entries	832578
Mean	3.025
Std Dev	3.564

nChipHit_F3_Fch6_Chip13

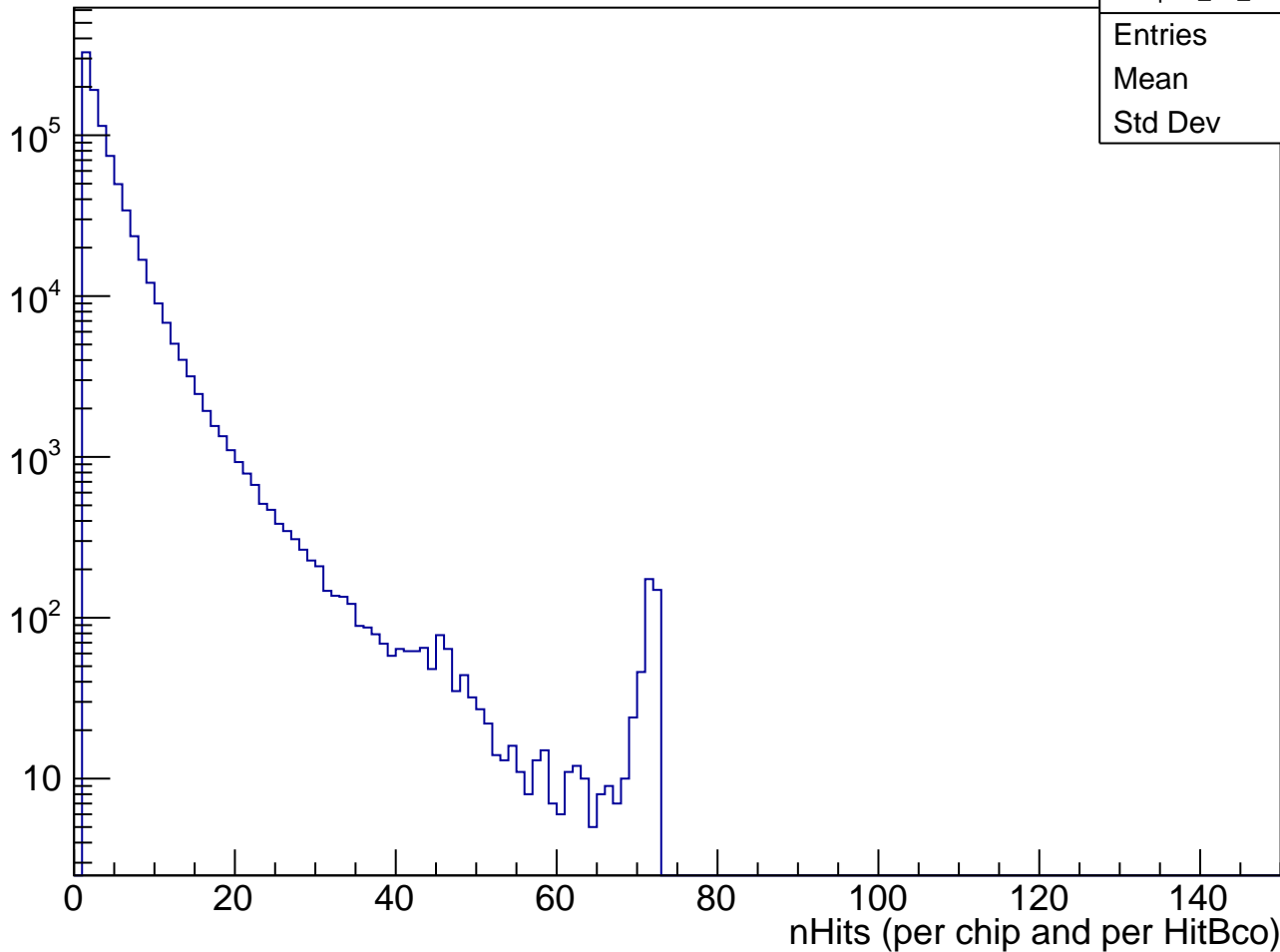
Entries



nChipHit_F3_Fch6_Chip13	
Entries	853542
Mean	3.414
Std Dev	3.992

nChipHit_F3_Fch6_Chip14

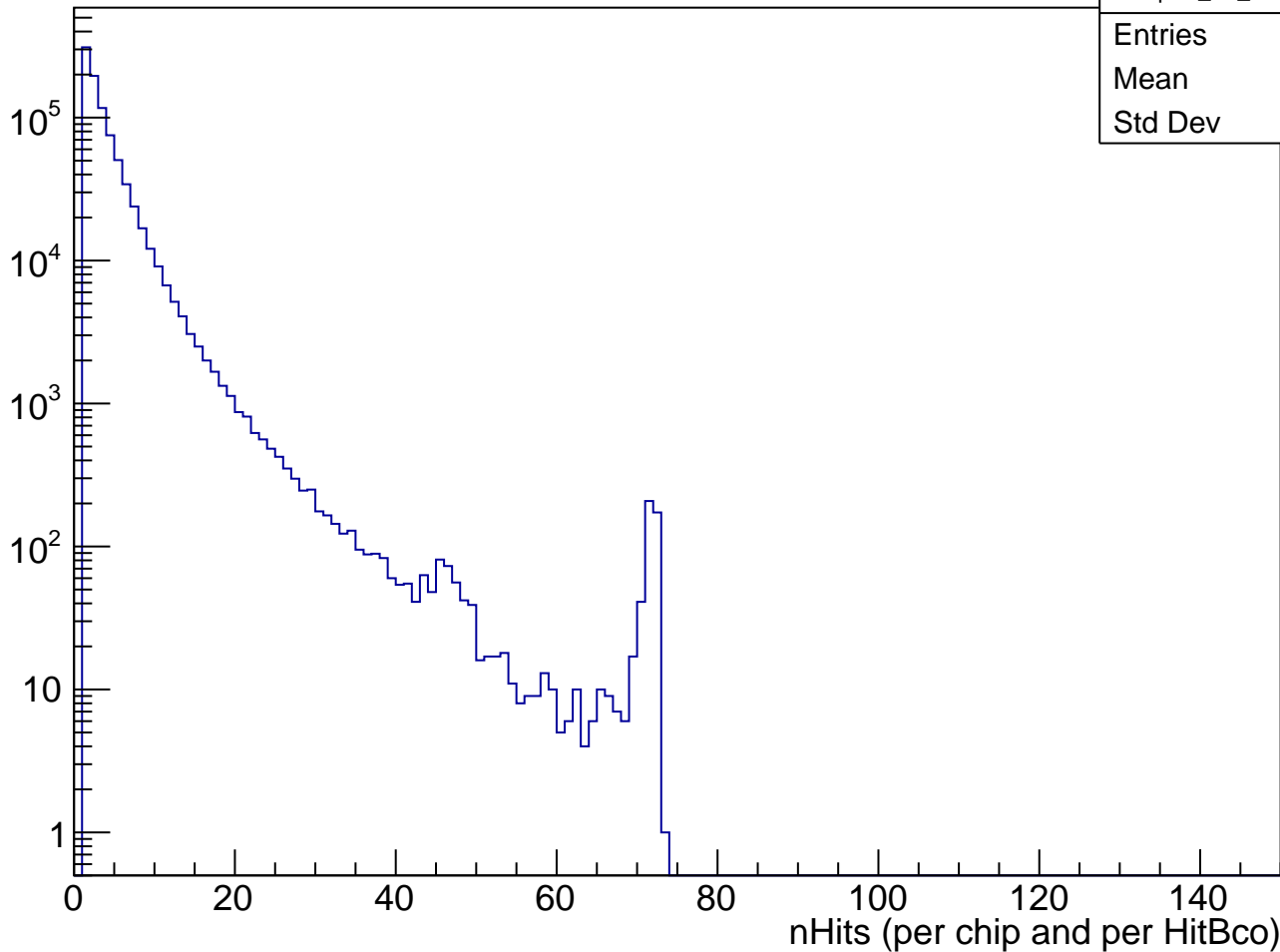
Entries



nChipHit_F3_Fch6_Chip14	
Entries	887784
Mean	3.256
Std Dev	3.889

nChipHit_F3_Fch6_Chip15

Entries

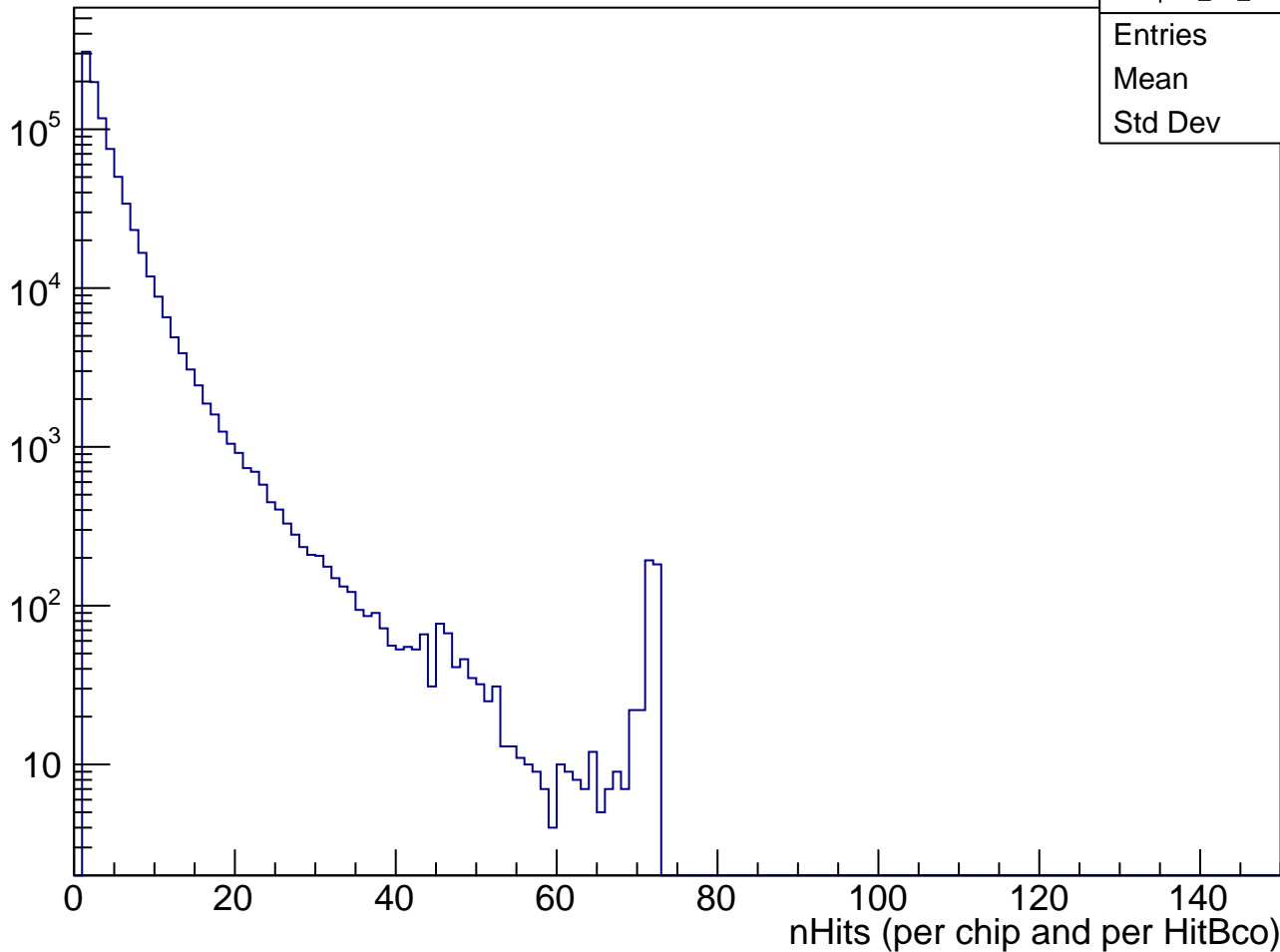


nChipHit_F3_Fch6_Chip15	
Entries	879354
Mean	3.305
Std Dev	3.93

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip16

Entries

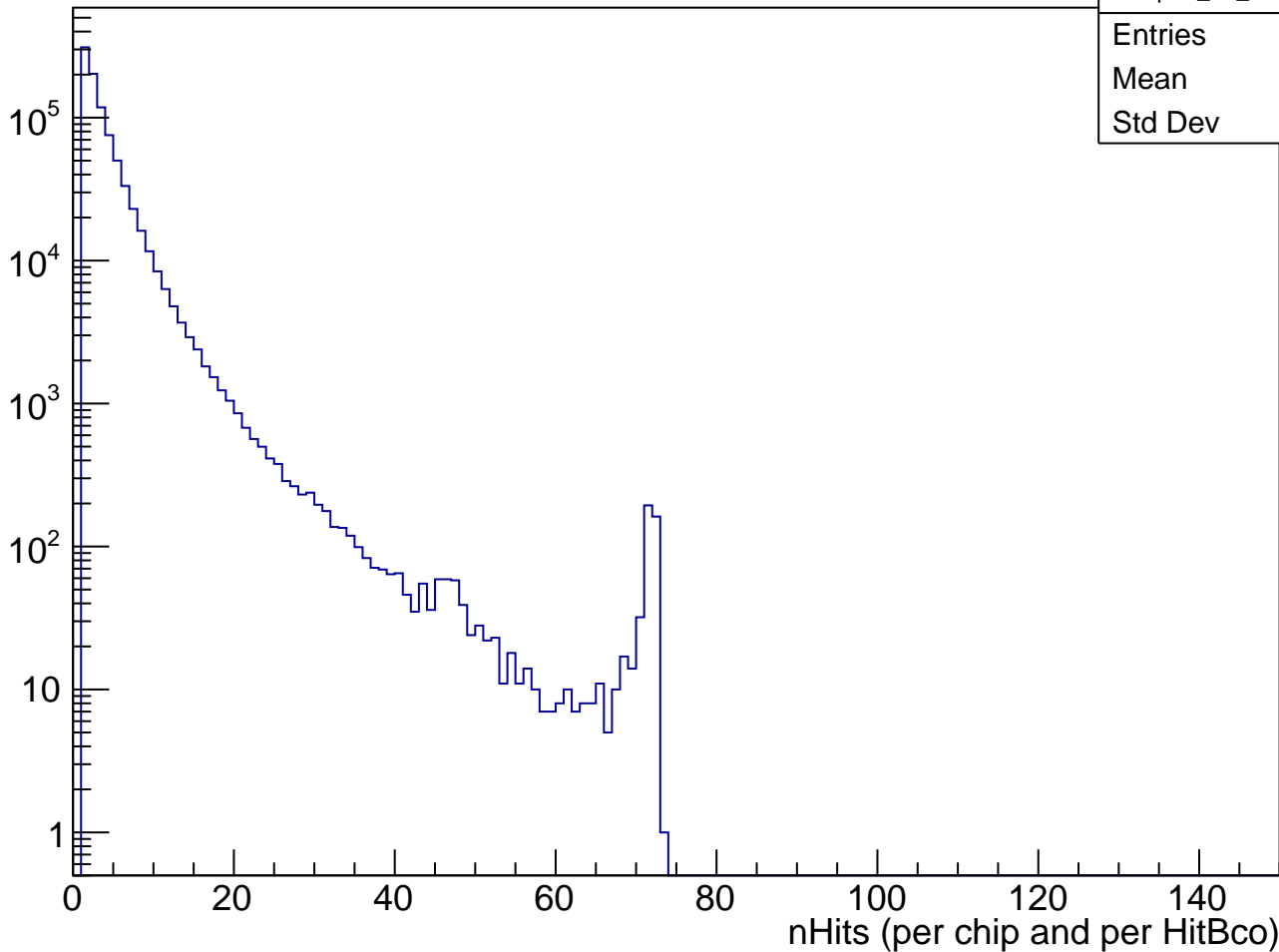


nChipHit_F3_Fch6_Chip16	
Entries	877170
Mean	3.284
Std Dev	3.895

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip17

Entries

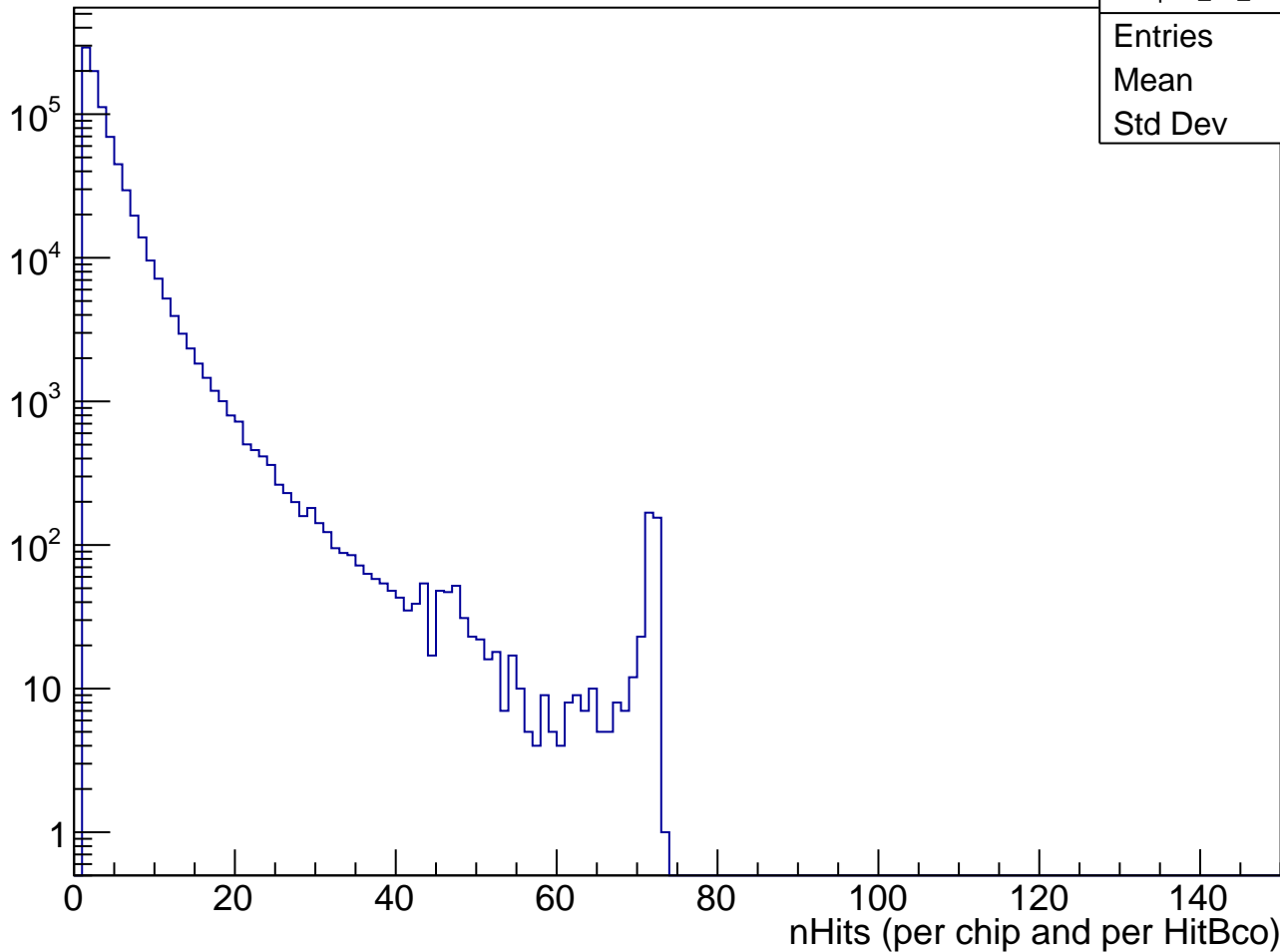


nChipHit_F3_Fch6_Chip17	
Entries	881309
Mean	3.237
Std Dev	3.825

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip18

Entries

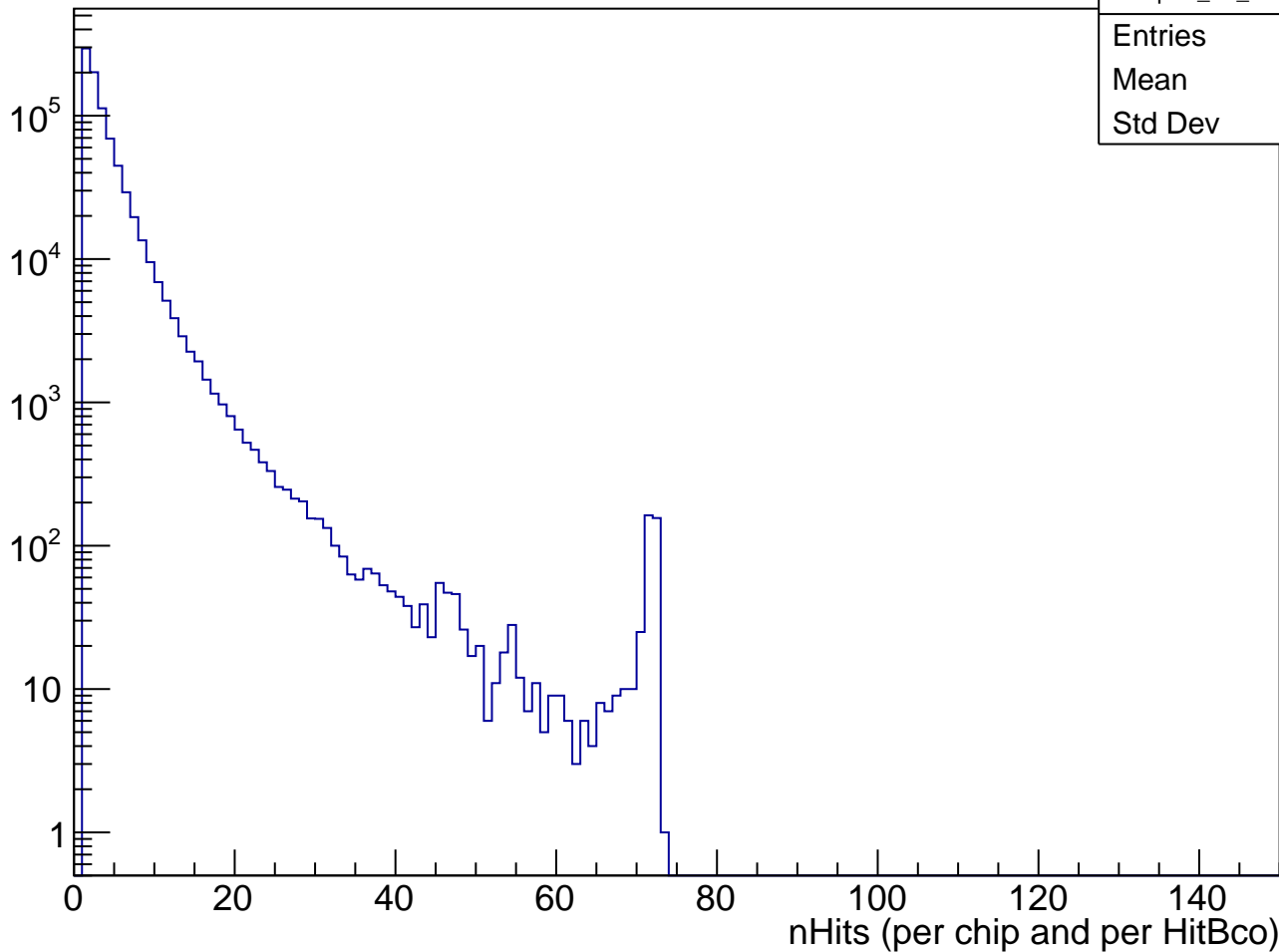


nChipHit_F3_Fch6_Chip18	
Entries	822831
Mean	3.103
Std Dev	3.609

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip19

Entries

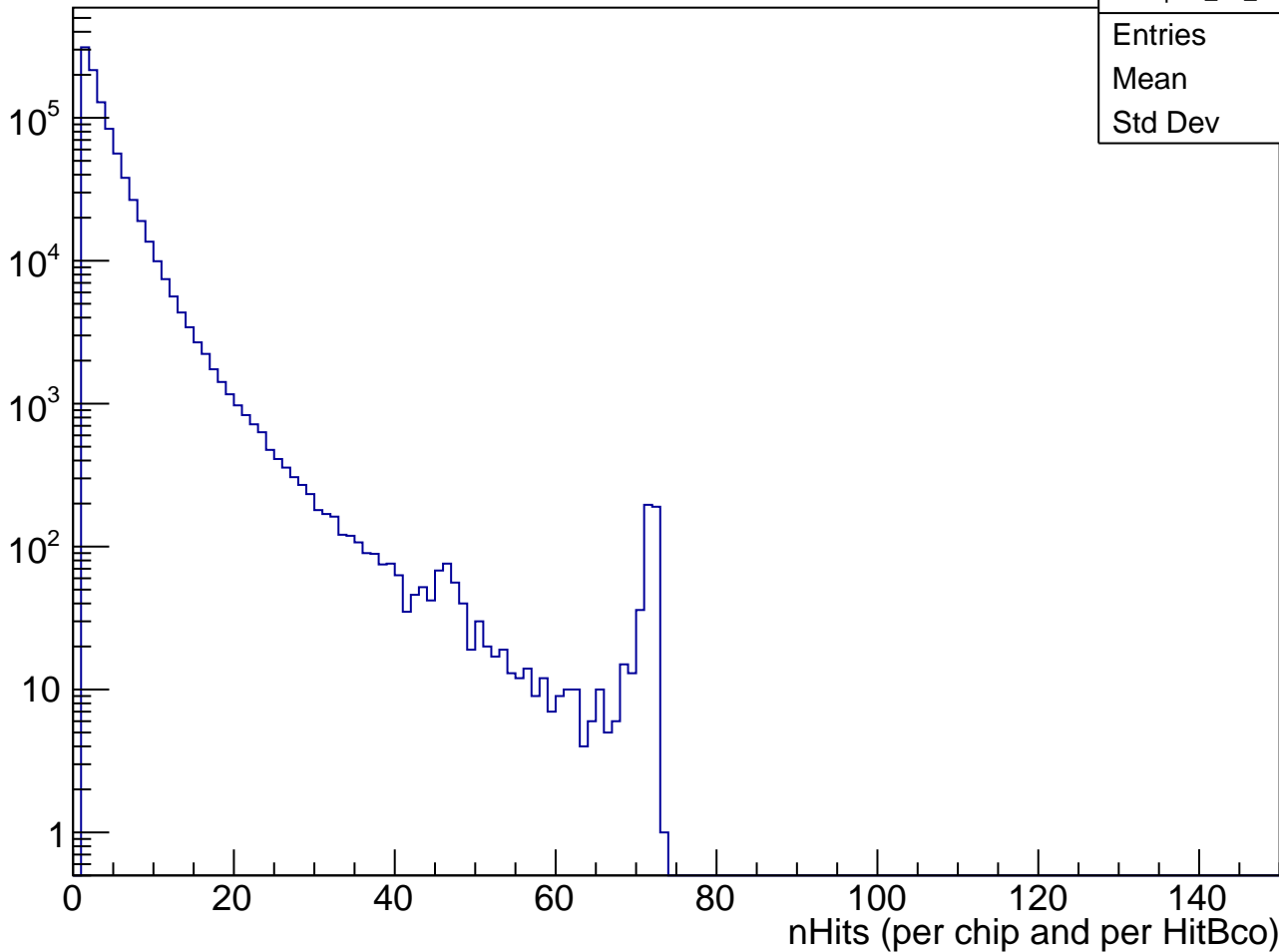


nChipHit_F3_Fch6_Chip19	
Entries	826944
Mean	3.08
Std Dev	3.587

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip2

Entries



nChipHit_F3_Fch6_Chip2

Entries 940418

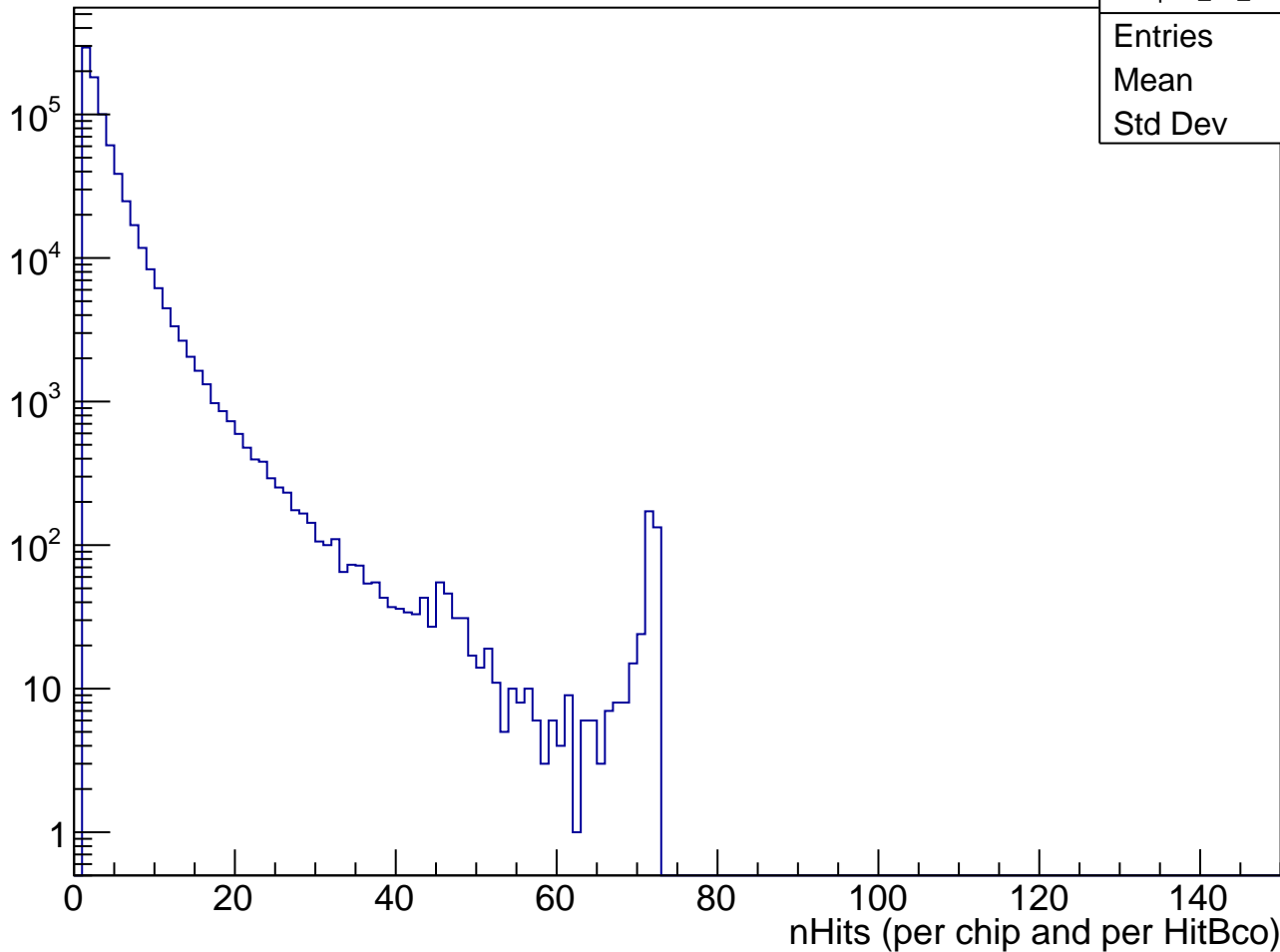
Mean 3.367

Std Dev 3.886

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip20

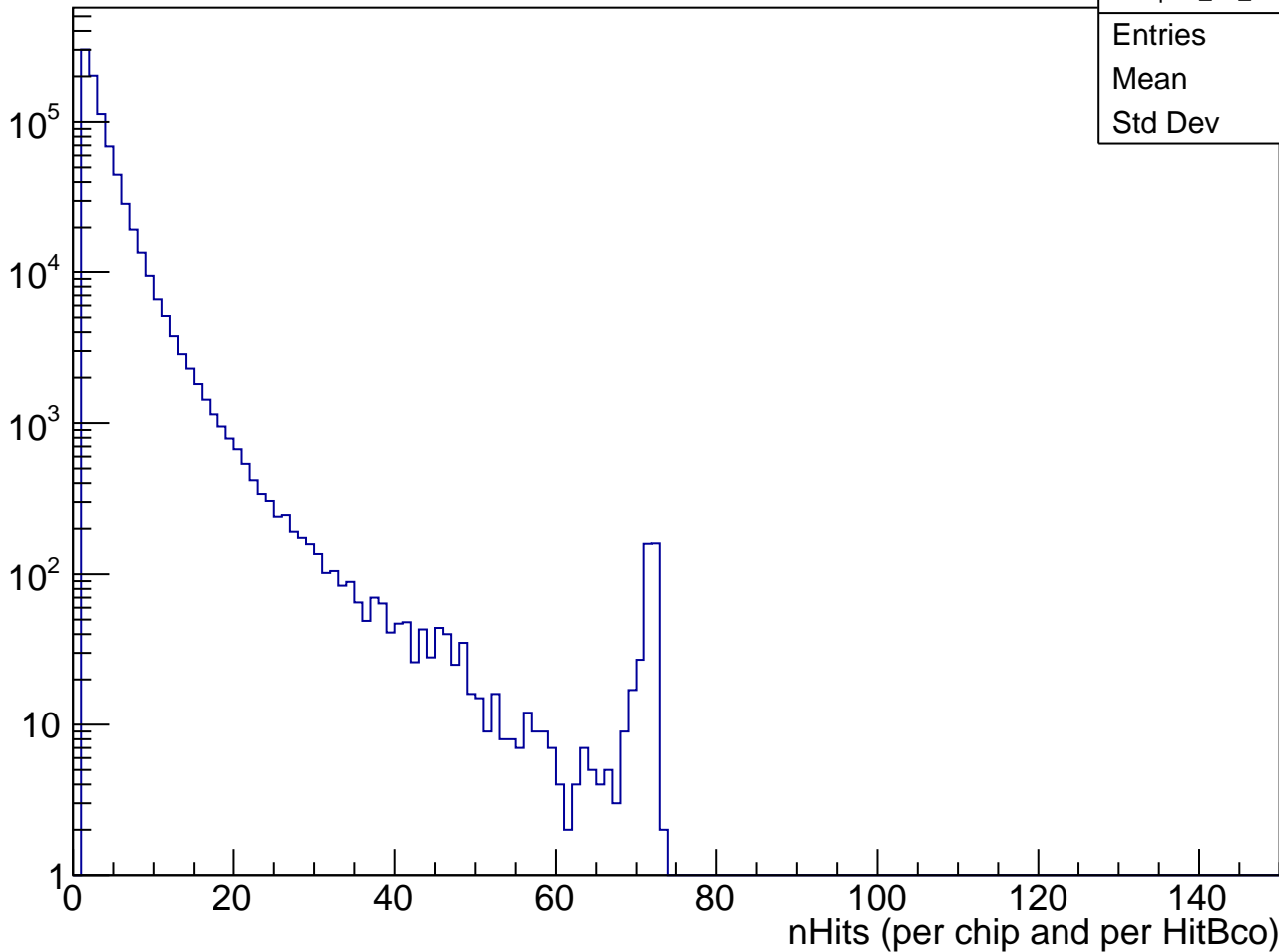
Entries



nChipHit_F3_Fch6_Chip20	
Entries	764551
Mean	2.982
Std Dev	3.555

nChipHit_F3_Fch6_Chip21

Entries

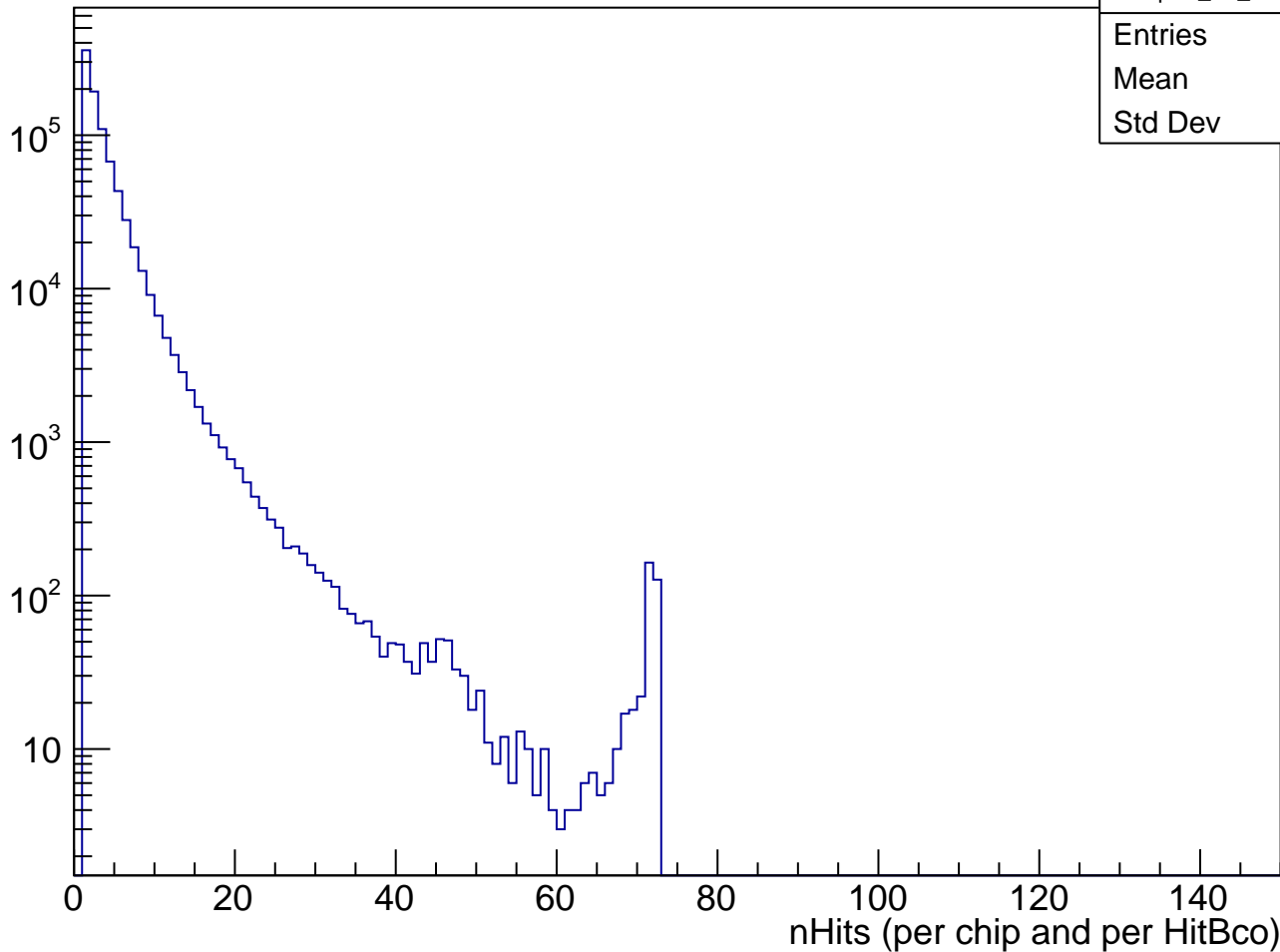


nChipHit_F3_Fch6_Chip21	
Entries	832309
Mean	3.045
Std Dev	3.535

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip22

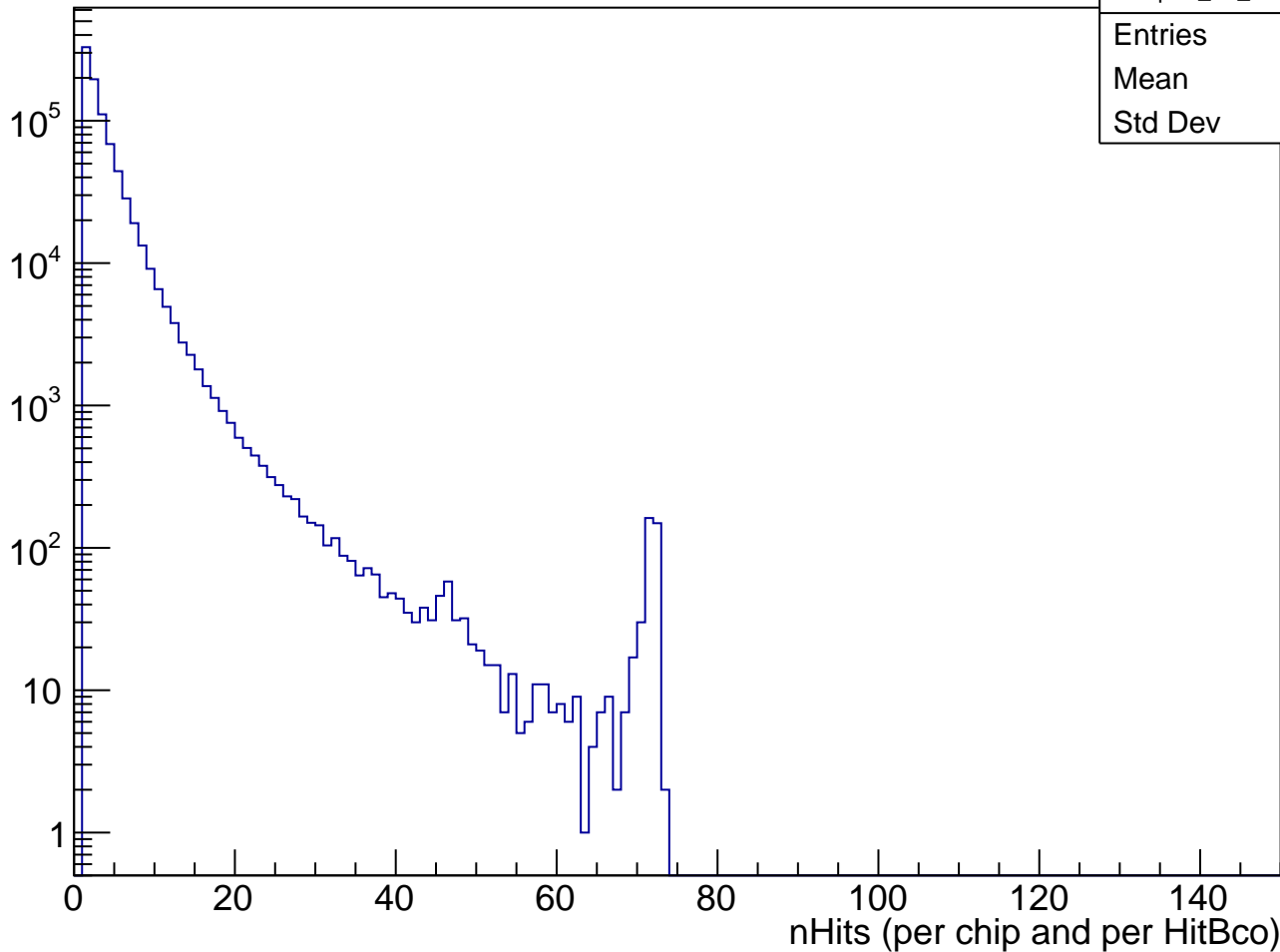
Entries



nChipHit_F3_Fch6_Chip22	
Entries	870609
Mean	2.902
Std Dev	3.472

nChipHit_F3_Fch6_Chip23

Entries

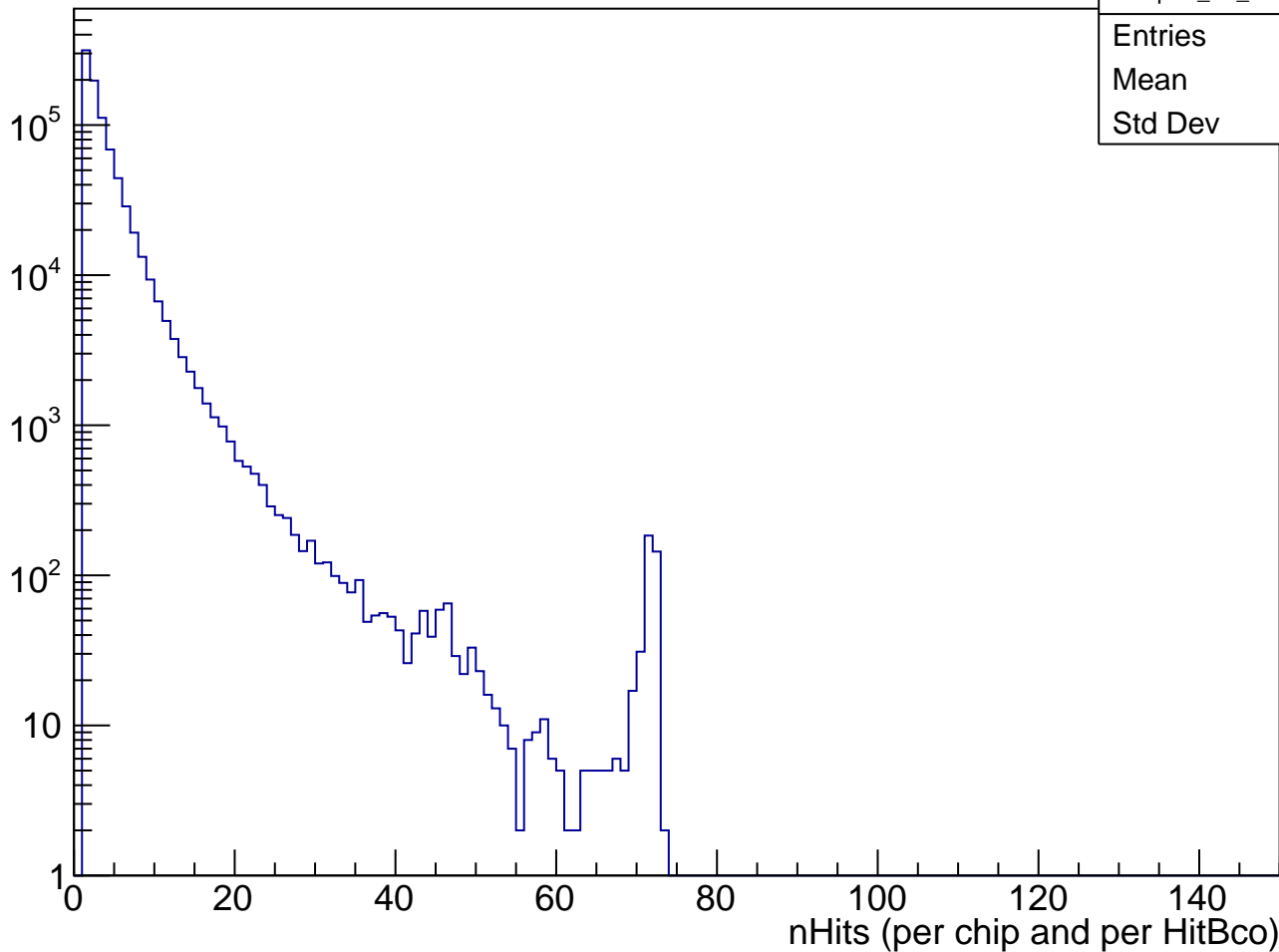


nChipHit_F3_Fch6_Chip23	
Entries	849104
Mean	2.977
Std Dev	3.519

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip24

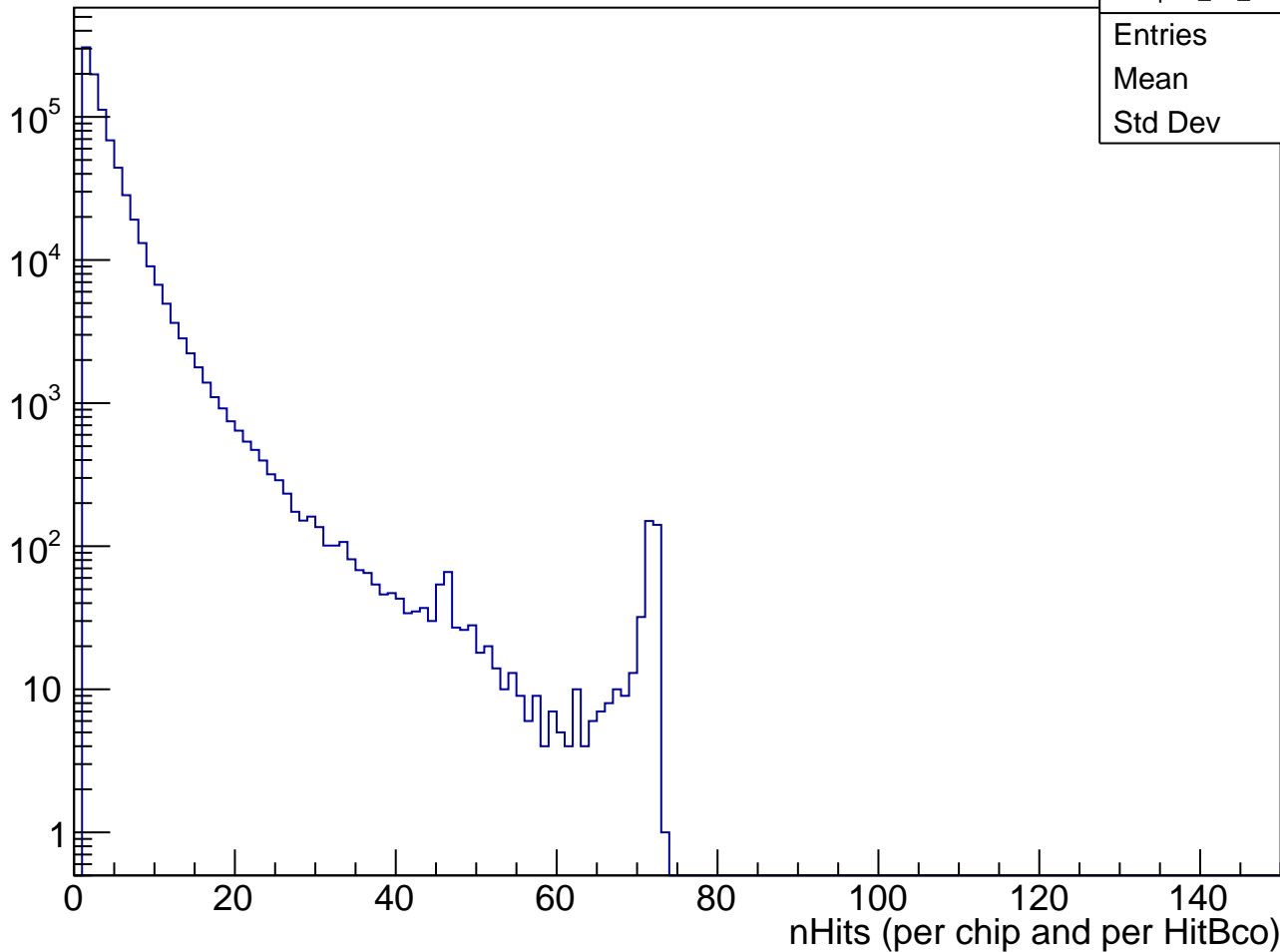
Entries



nChipHit_F3_Fch6_Chip24	
Entries	839367
Mean	3.015
Std Dev	3.553

nChipHit_F3_Fch6_Chip25

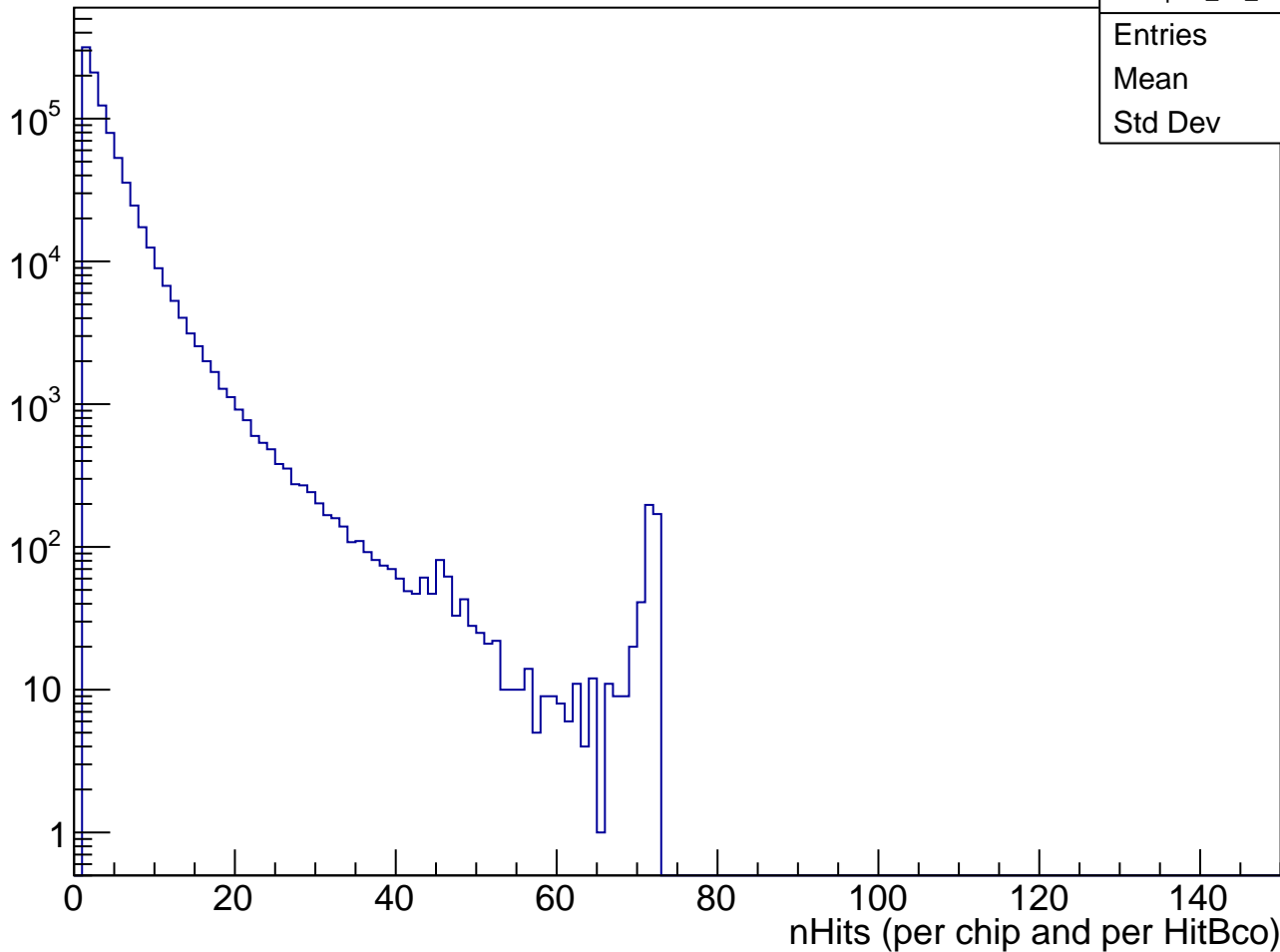
Entries



nChipHit_F3_Fch6_Chip25	
Entries	830734
Mean	3.025
Std Dev	3.534

nChipHit_F3_Fch6_Chip3

Entries



nChipHit_F3_Fch6_Chip3

Entries 916634

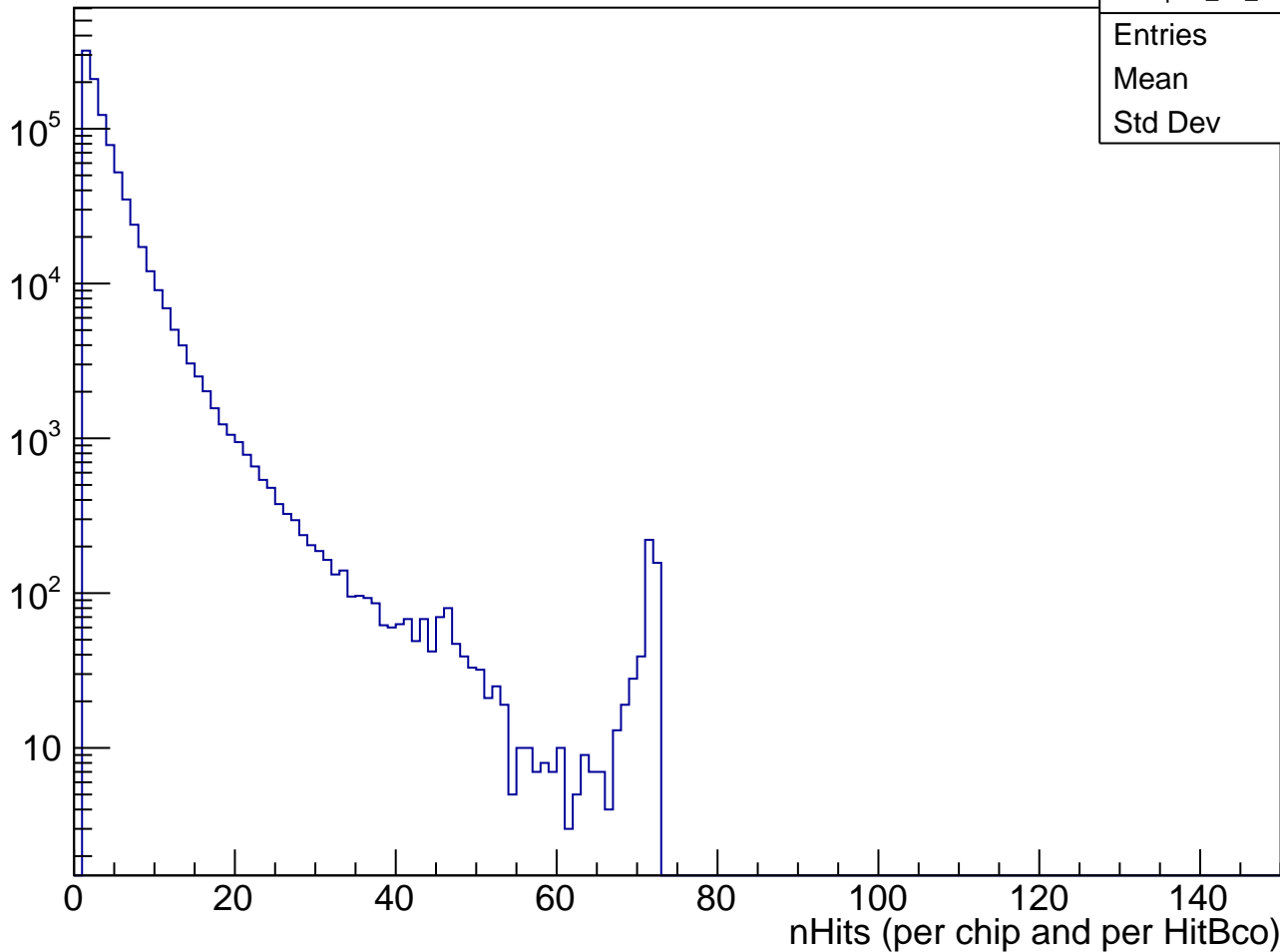
Mean 3.286

Std Dev 3.852

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip4

Entries



nChipHit_F3_Fch6_Chip4

Entries 914532

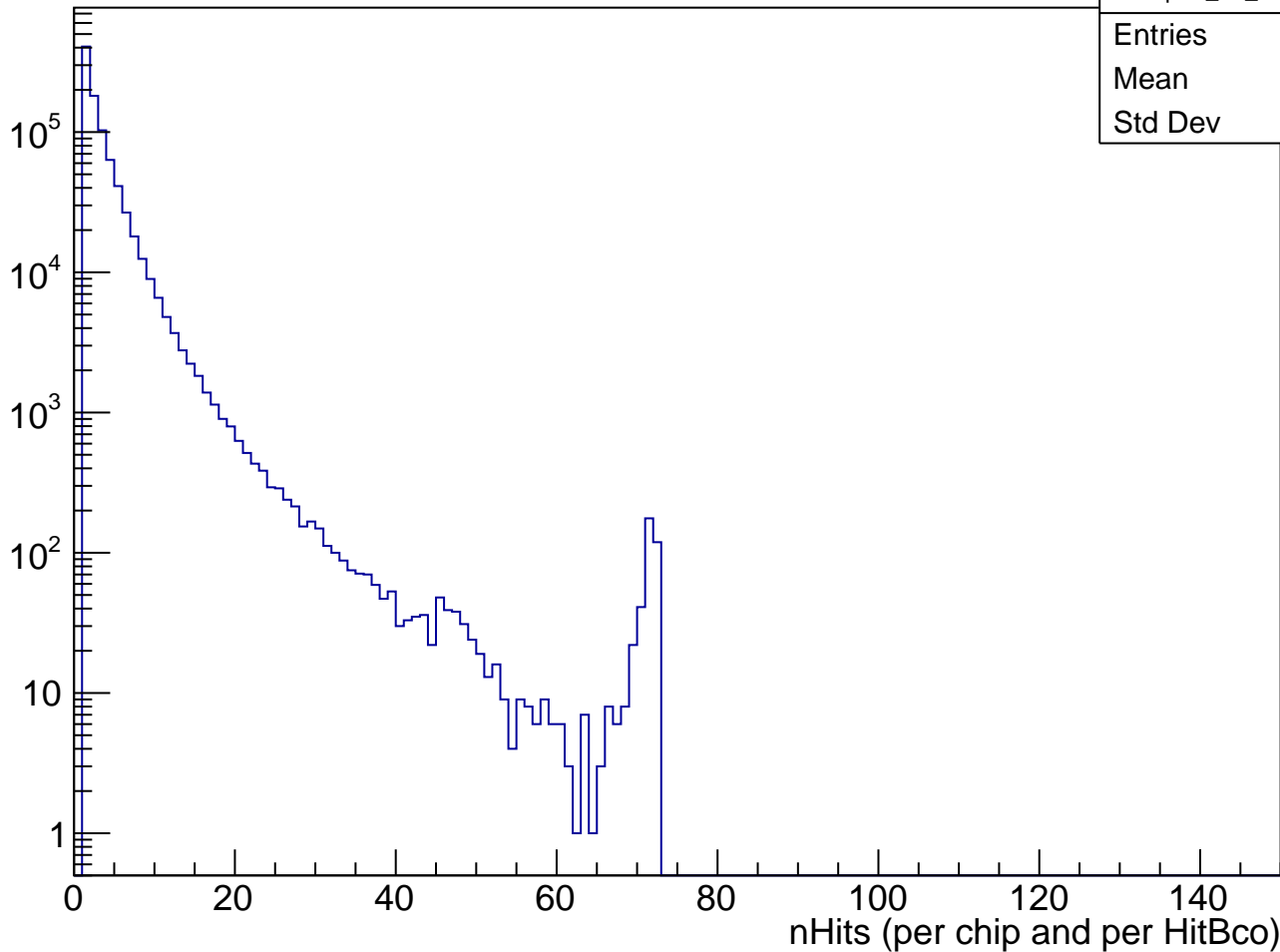
Mean 3.263

Std Dev 3.858

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip5

Entries



nChipHit_F3_Fch6_Chip5

Entries 893285

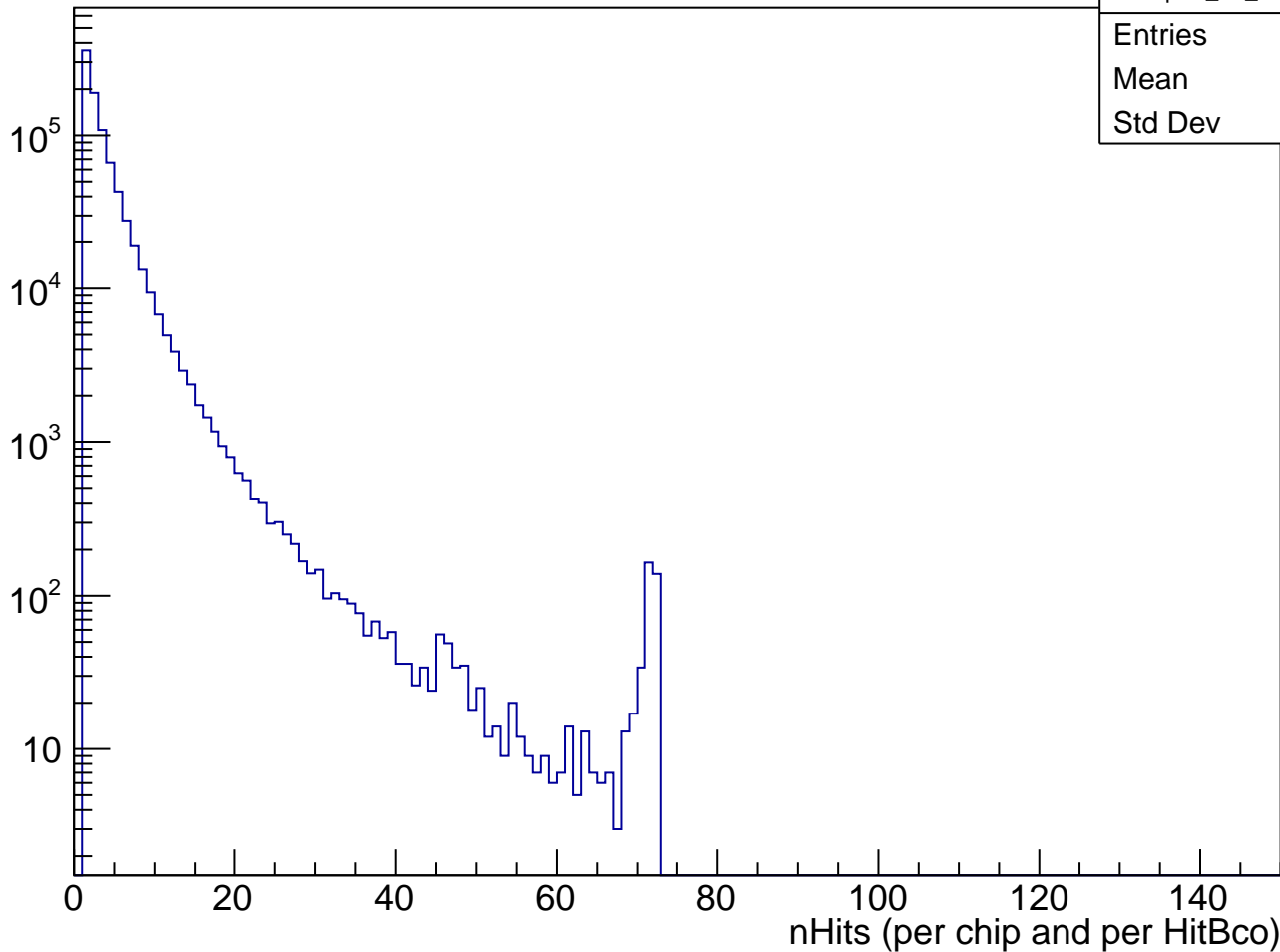
Mean 2.786

Std Dev 3.442

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip6

Entries



nChipHit_F3_Fch6_Chip6

Entries 866227

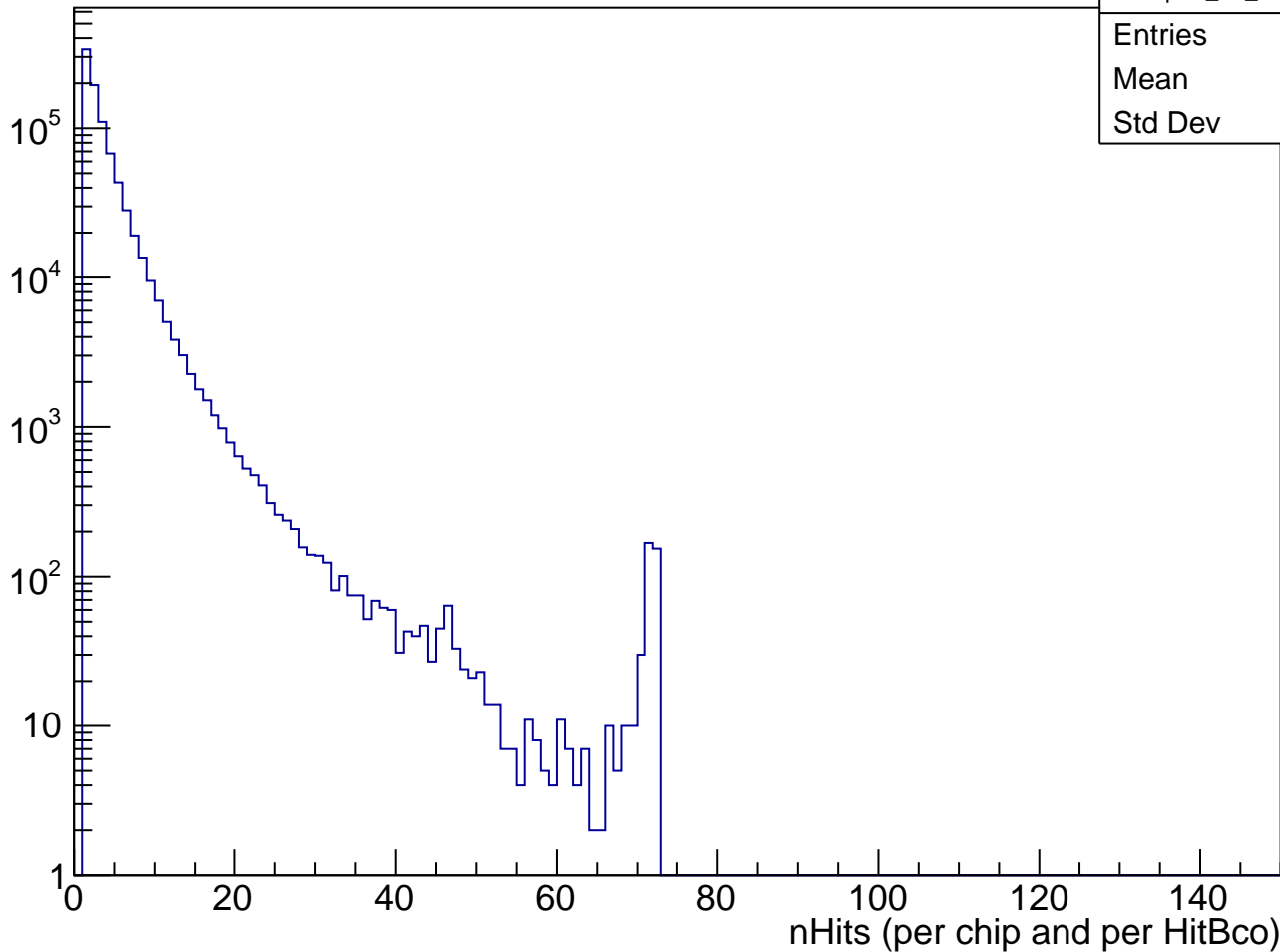
Mean 2.923

Std Dev 3.532

nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip7

Entries



nChipHit_F3_Fch6_Chip7	
Entries	855806
Mean	2.973
Std Dev	3.538

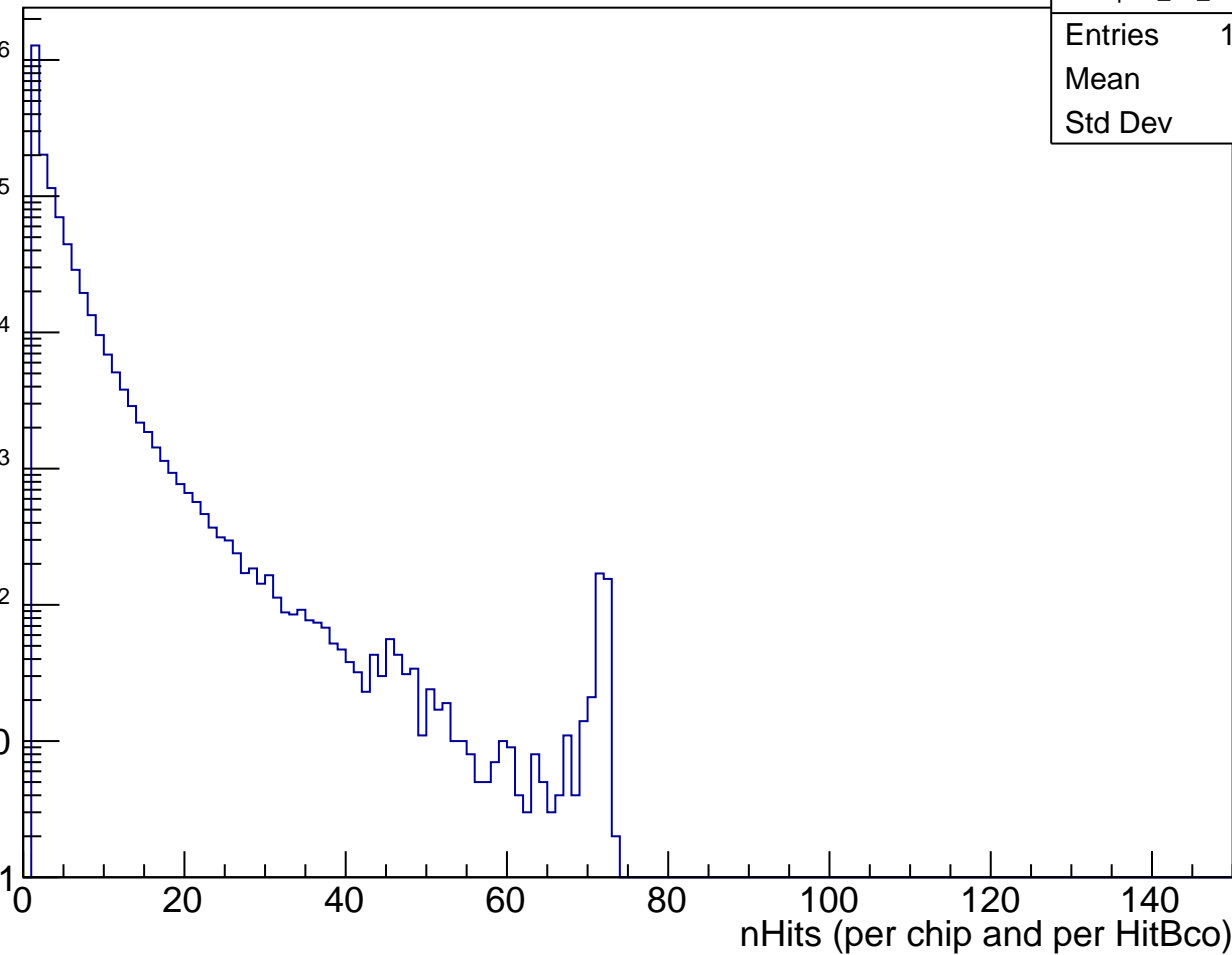
nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip8

Entries

10^6
 10^5
 10^4
 10^3
 10^2
10
1

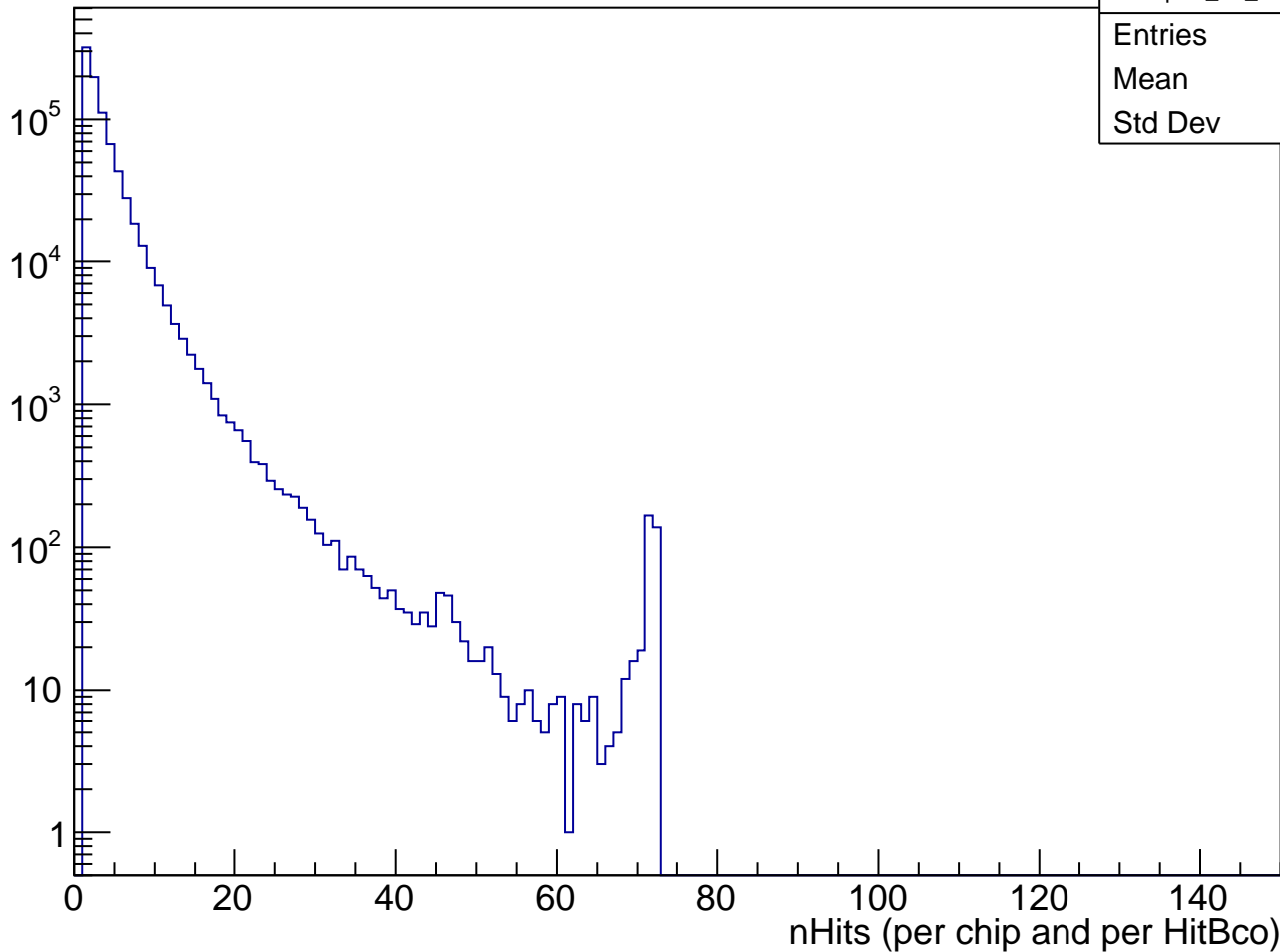
nChipHit_F3_Fch6_Chip8	
Entries	1812104
Mean	1.947
Std Dev	2.62



nHits (per chip and per HitBco)

nChipHit_F3_Fch6_Chip9

Entries



nChipHit_F3_Fch6_Chip9

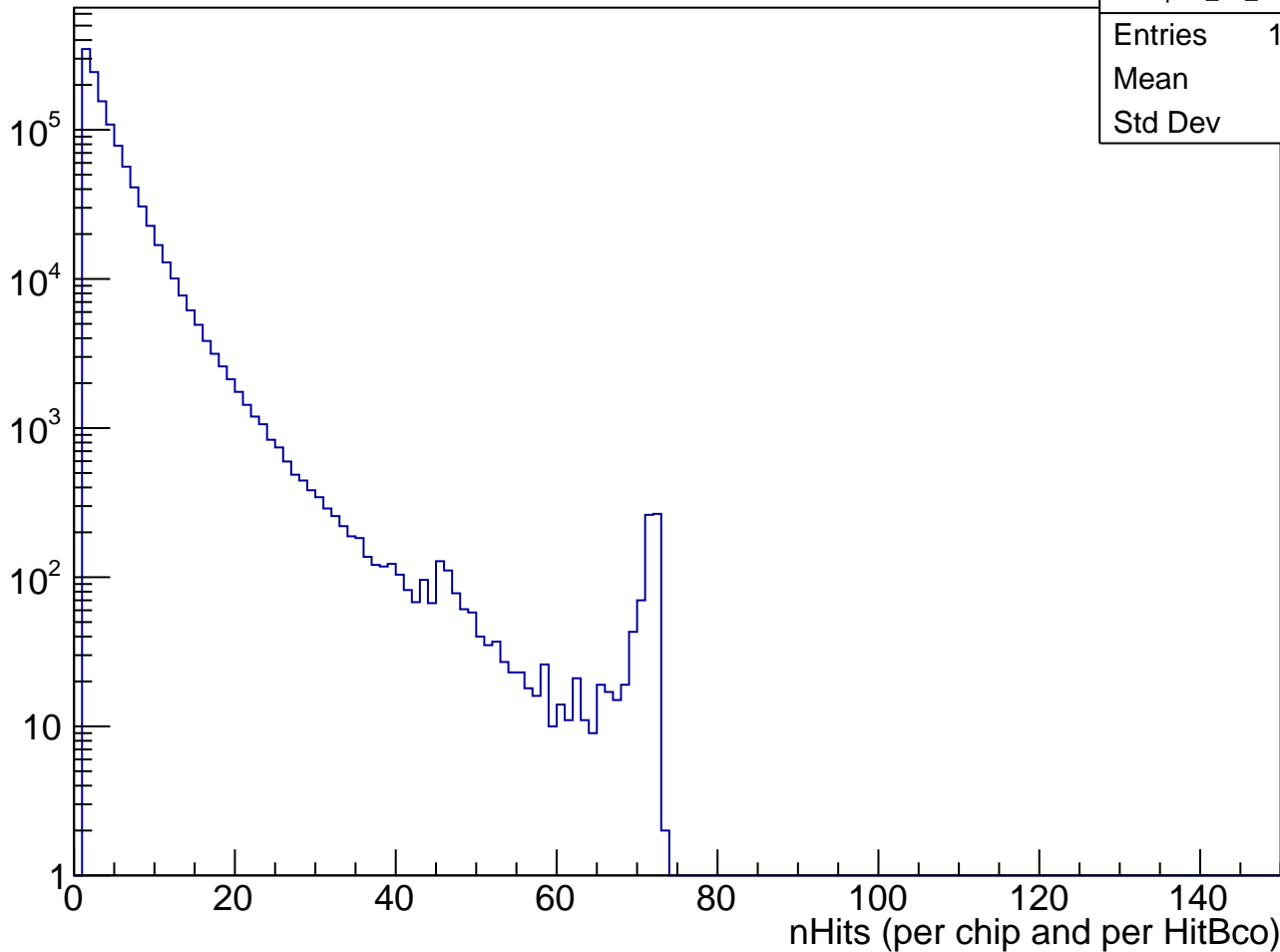
Entries 838635

Mean 2.981

Std Dev 3.493

nChipHit_F3_Fch7_Chip0

Entries



nChipHit_F3_Fch7_Chip0

Entries 1168584

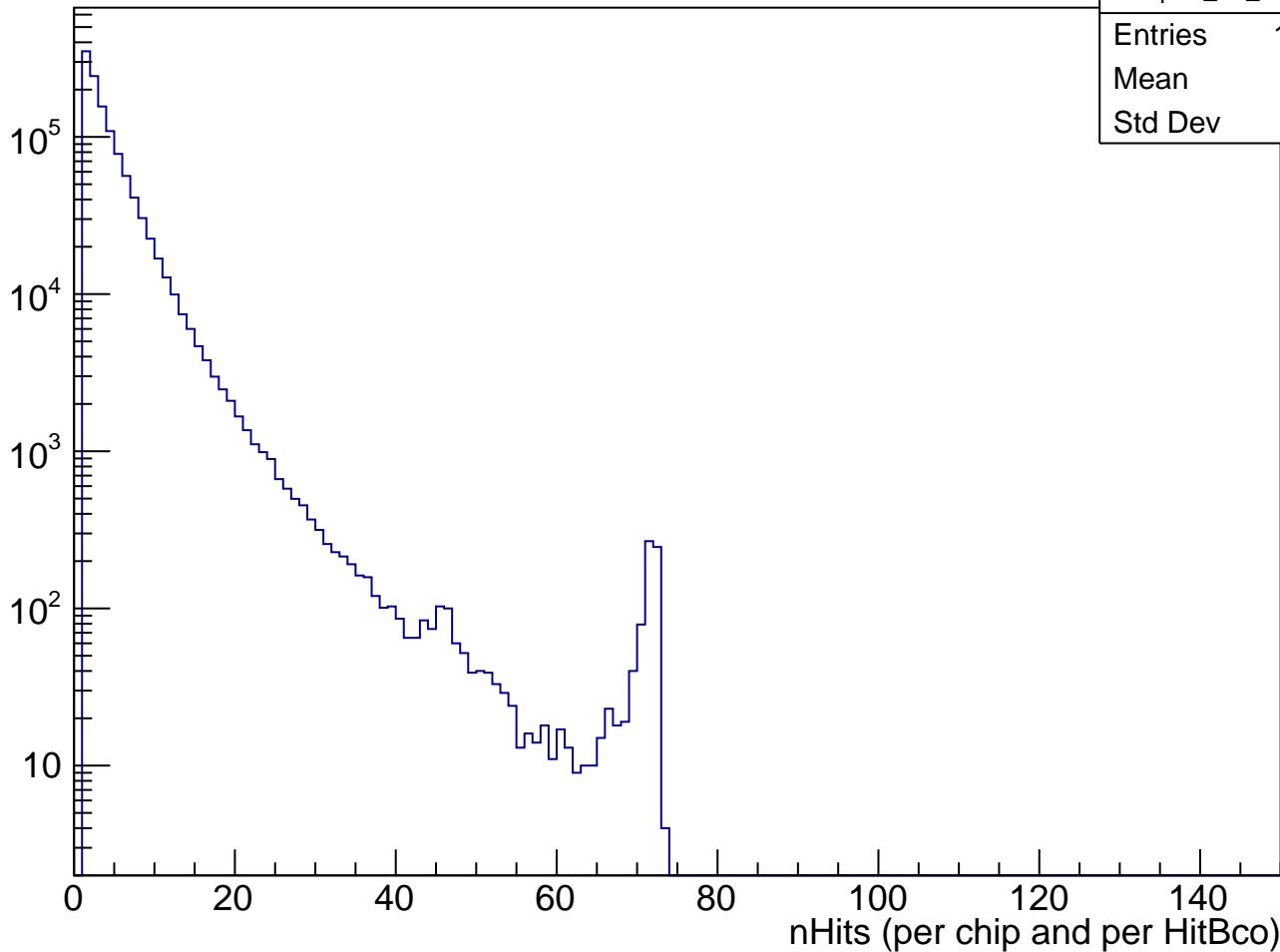
Mean 3.832

Std Dev 4.363

nHits (per chip and per HitBco)

nChipHit_F3_Fch7_Chip1

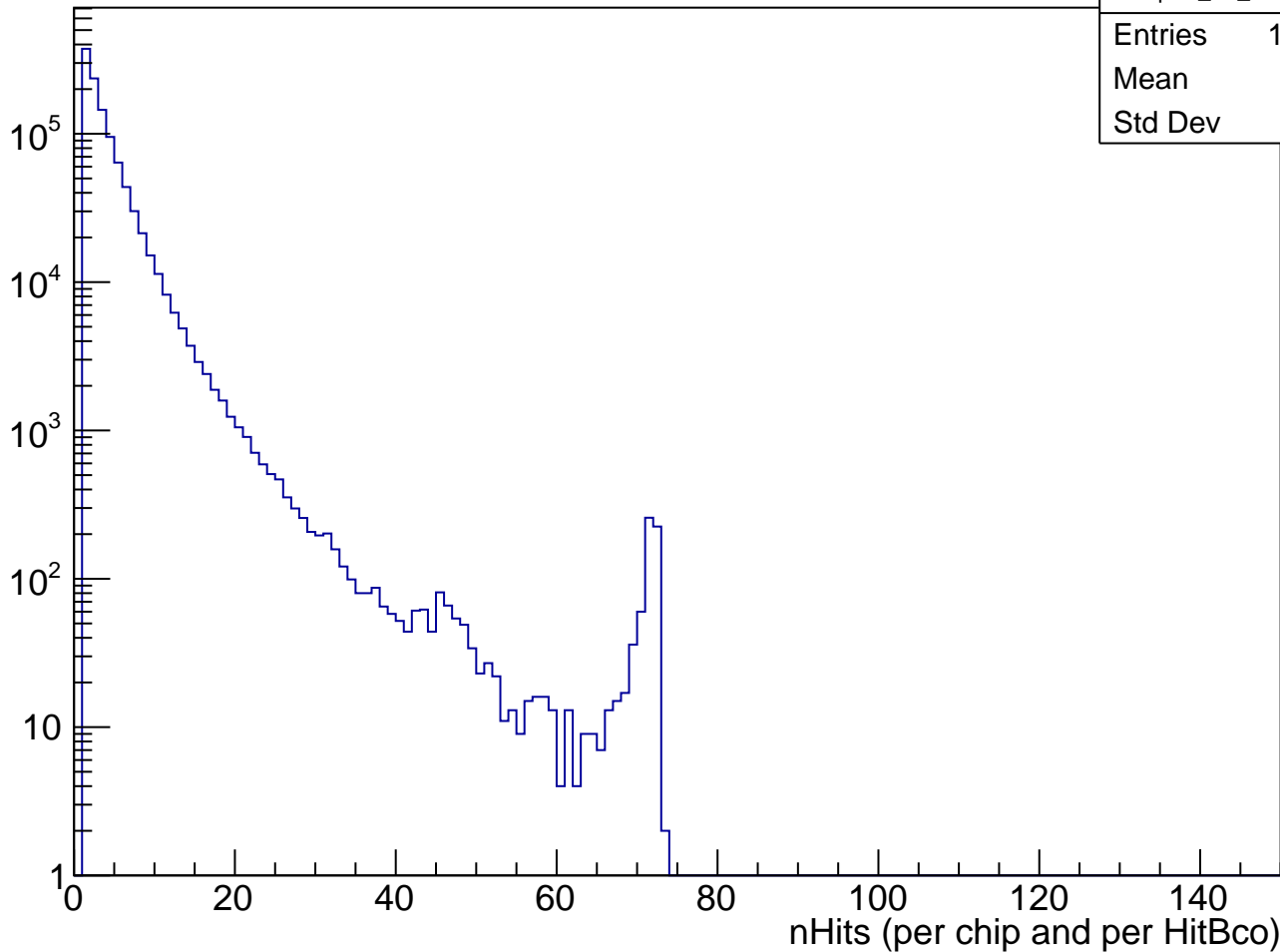
Entries



nChipHit_F3_Fch7_Chip1	
Entries	1169371
Mean	3.797
Std Dev	4.295

nChipHit_F3_Fch7_Chip10

Entries

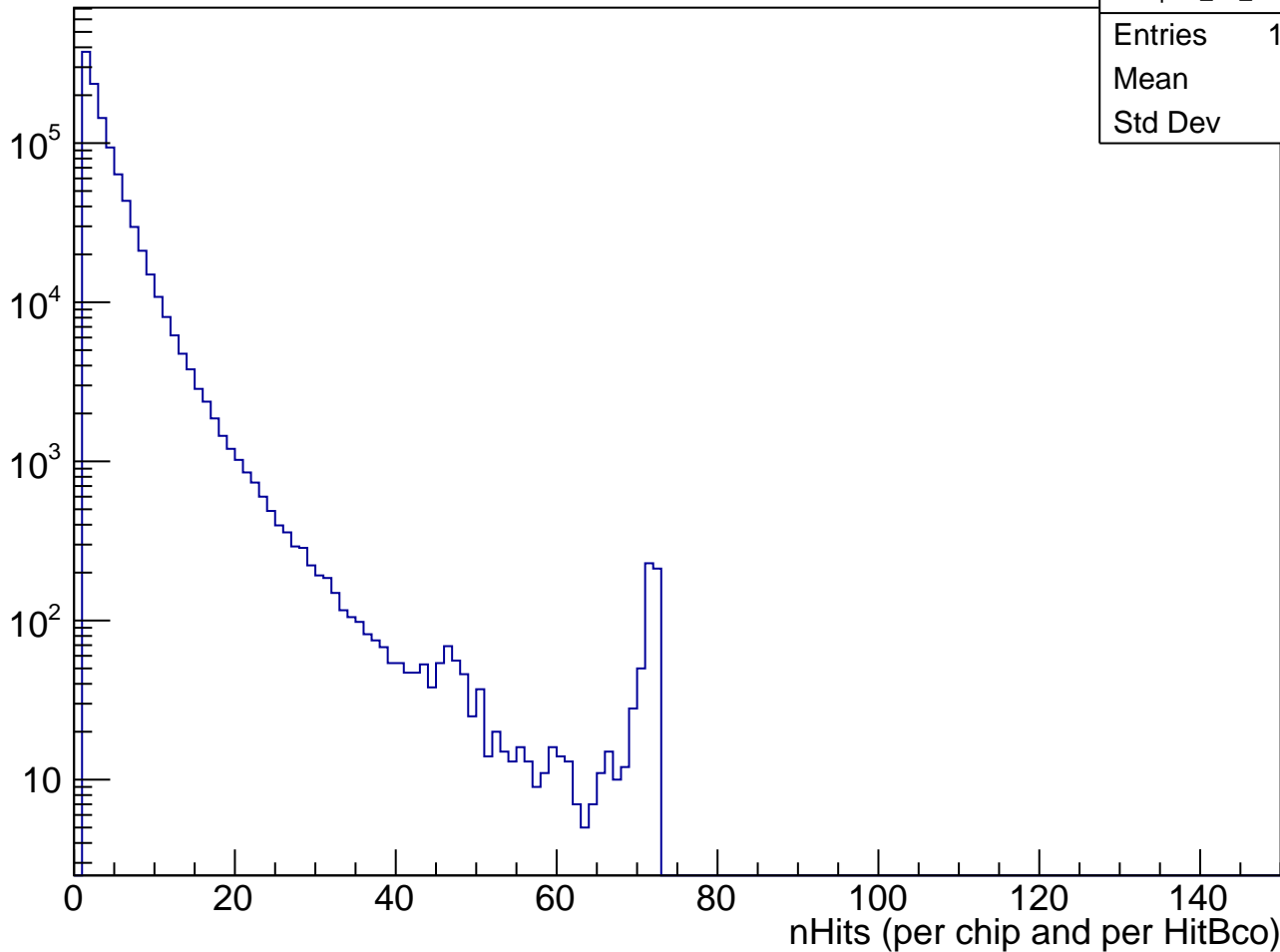


nChipHit_F3_Fch7_Chip10	
Entries	1077062
Mean	3.31
Std Dev	3.855

nHits (per chip and per HitBco)

nChipHit_F3_Fch7_Chip11

Entries



nChipHit_F3_Fch7_Chip11

Entries 1071946

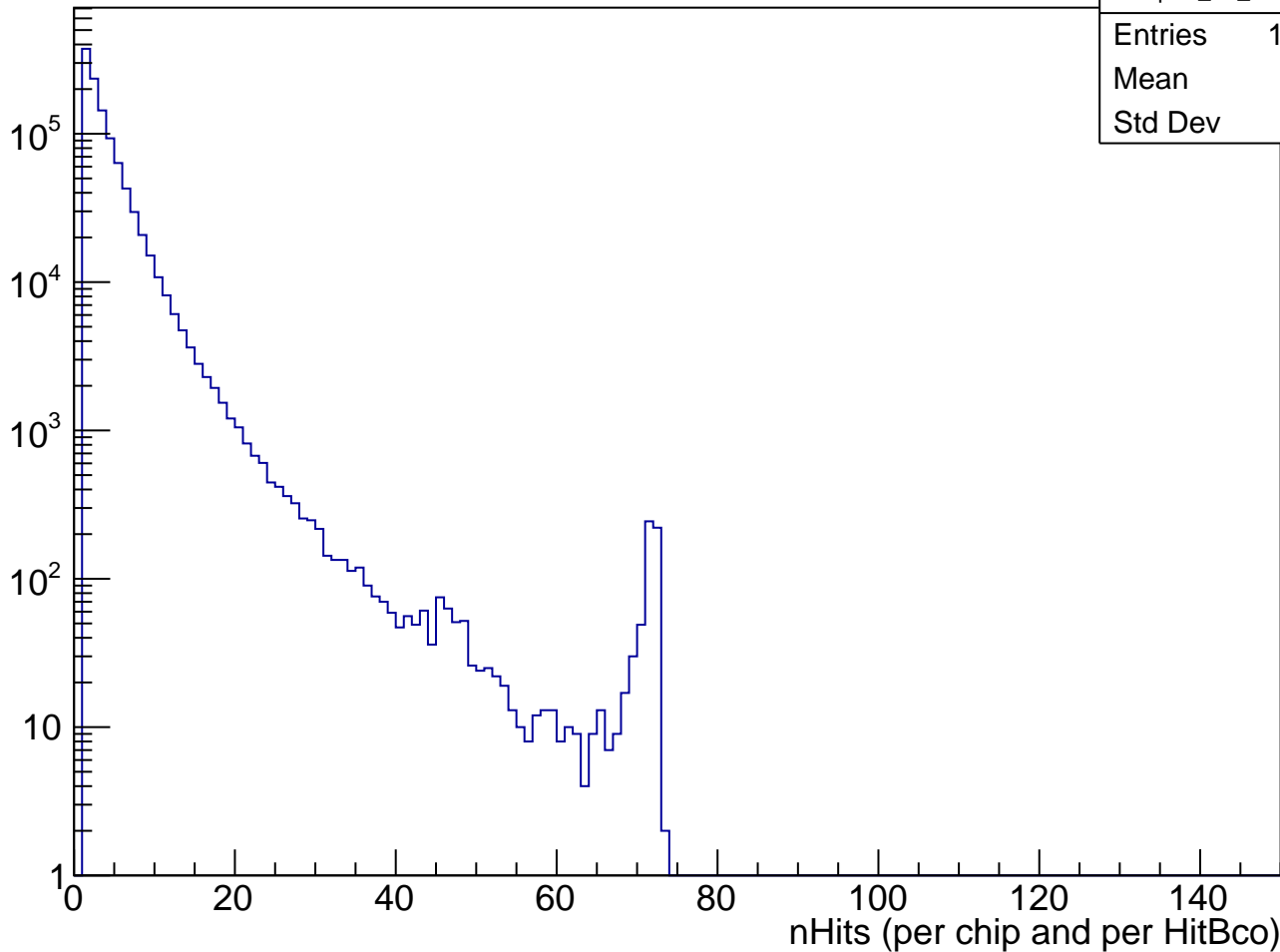
Mean 3.286

Std Dev 3.798

nHits (per chip and per HitBco)

nChipHit_F3_Fch7_Chip12

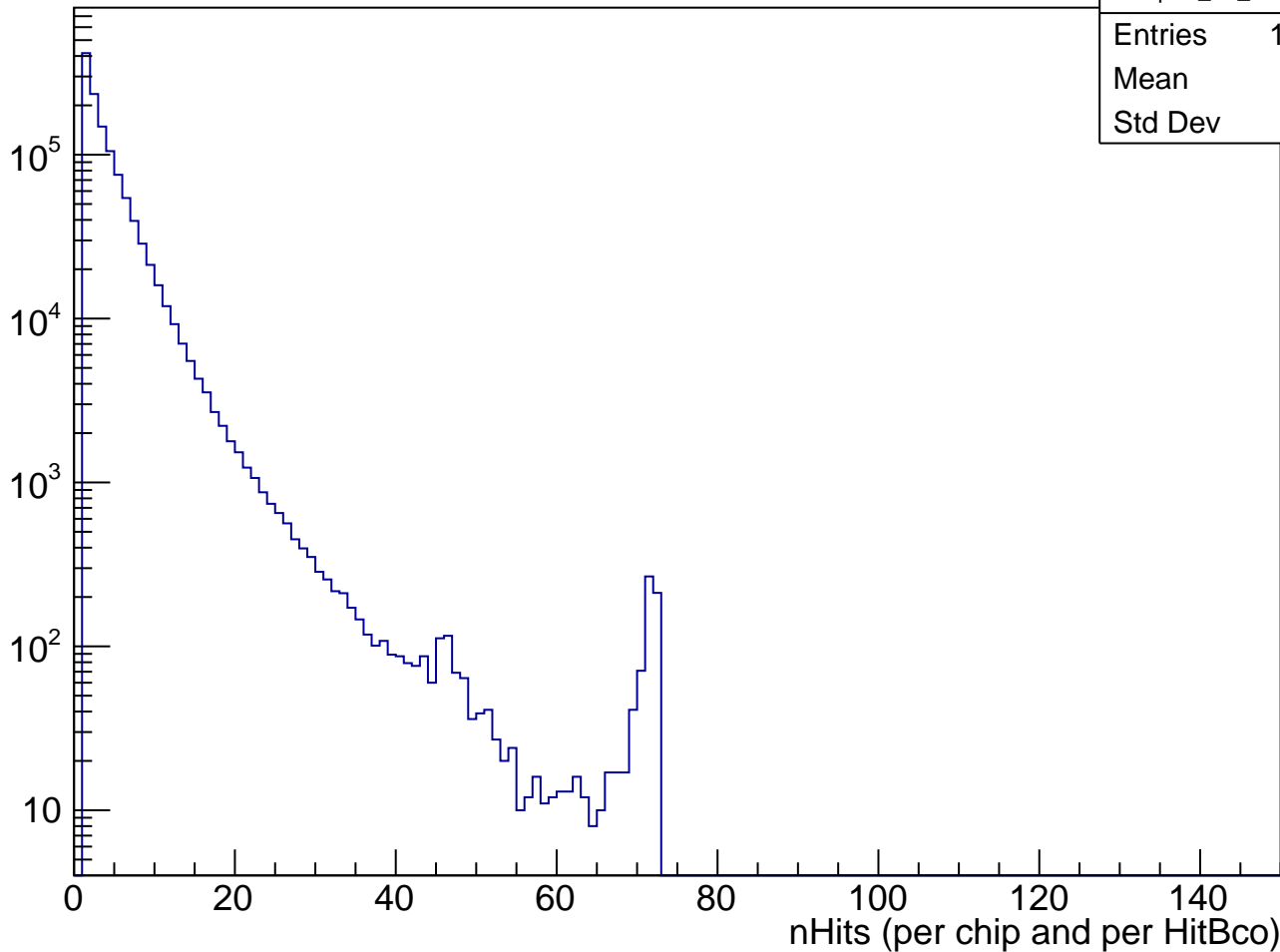
Entries



nChipHit_F3_Fch7_Chip12	
Entries	1068689
Mean	3.287
Std Dev	3.82

nChipHit_F3_Fch7_Chip13

Entries

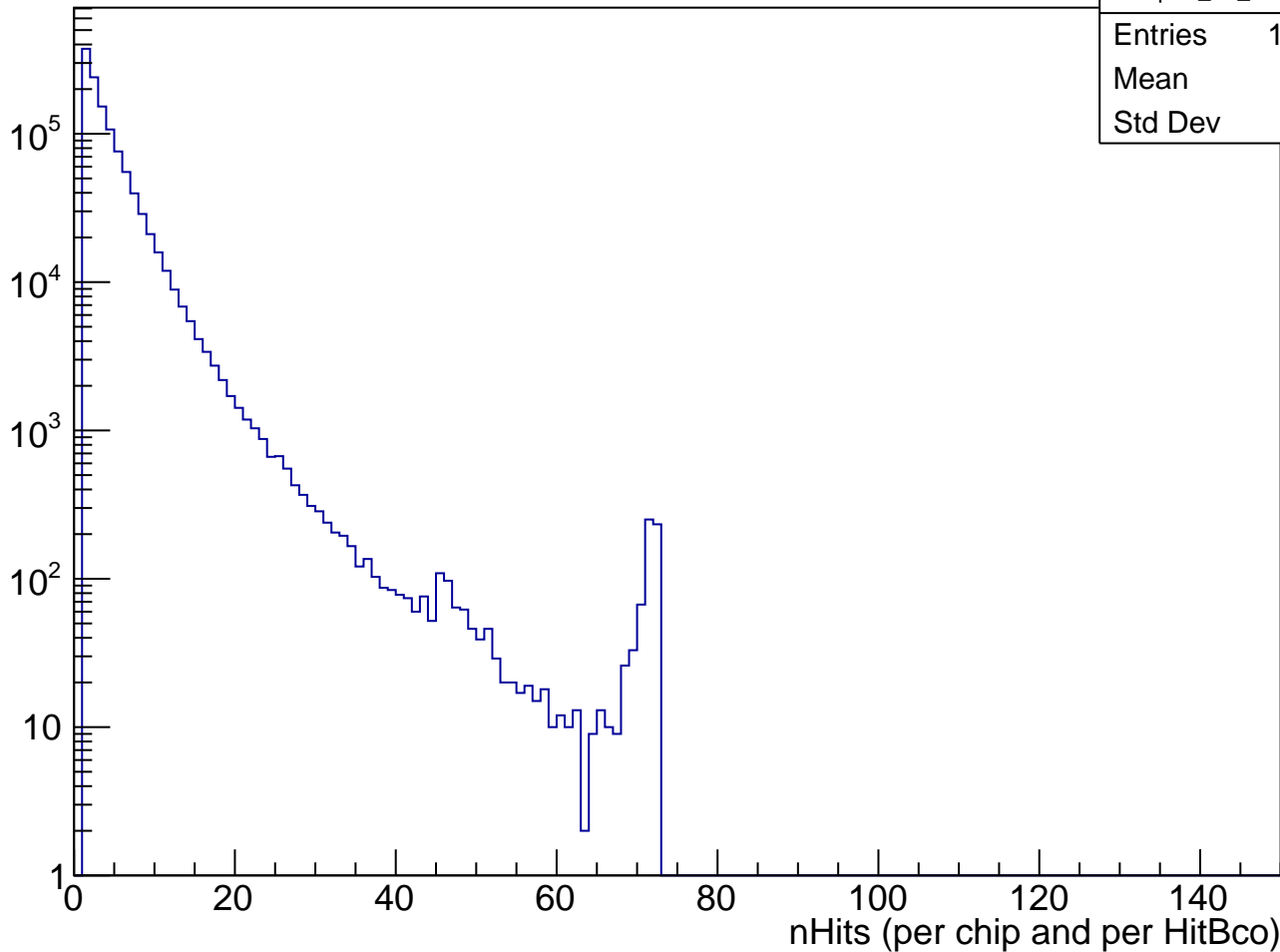


nChipHit_F3_Fch7_Chip13	
Entries	1199290
Mean	3.583
Std Dev	4.164

nHits (per chip and per HitBco)

nChipHit_F3_Fch7_Chip14

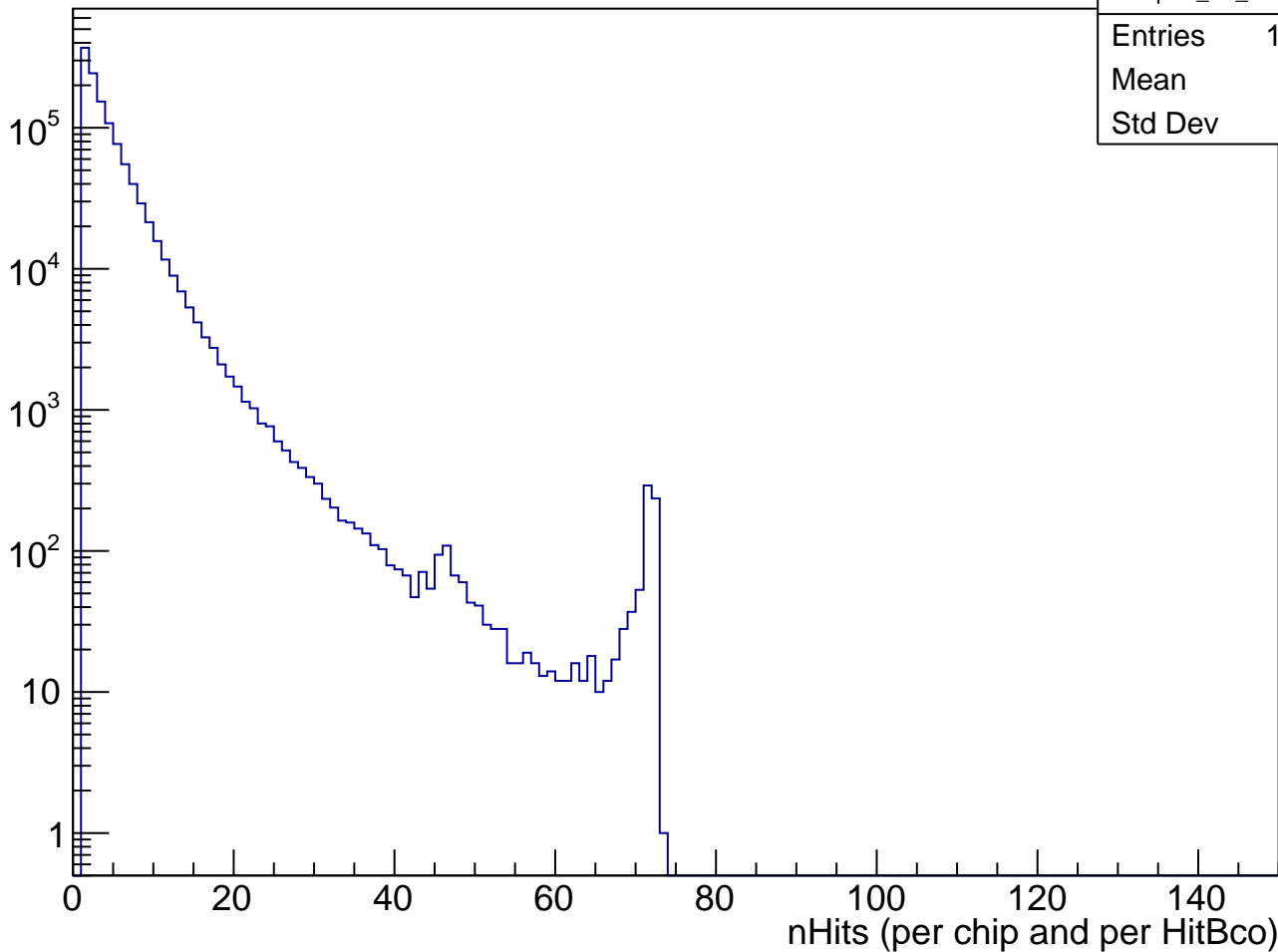
Entries



nChipHit_F3_Fch7_Chip14	
Entries	1168564
Mean	3.651
Std Dev	4.143

nChipHit_F3_Fch7_Chip15

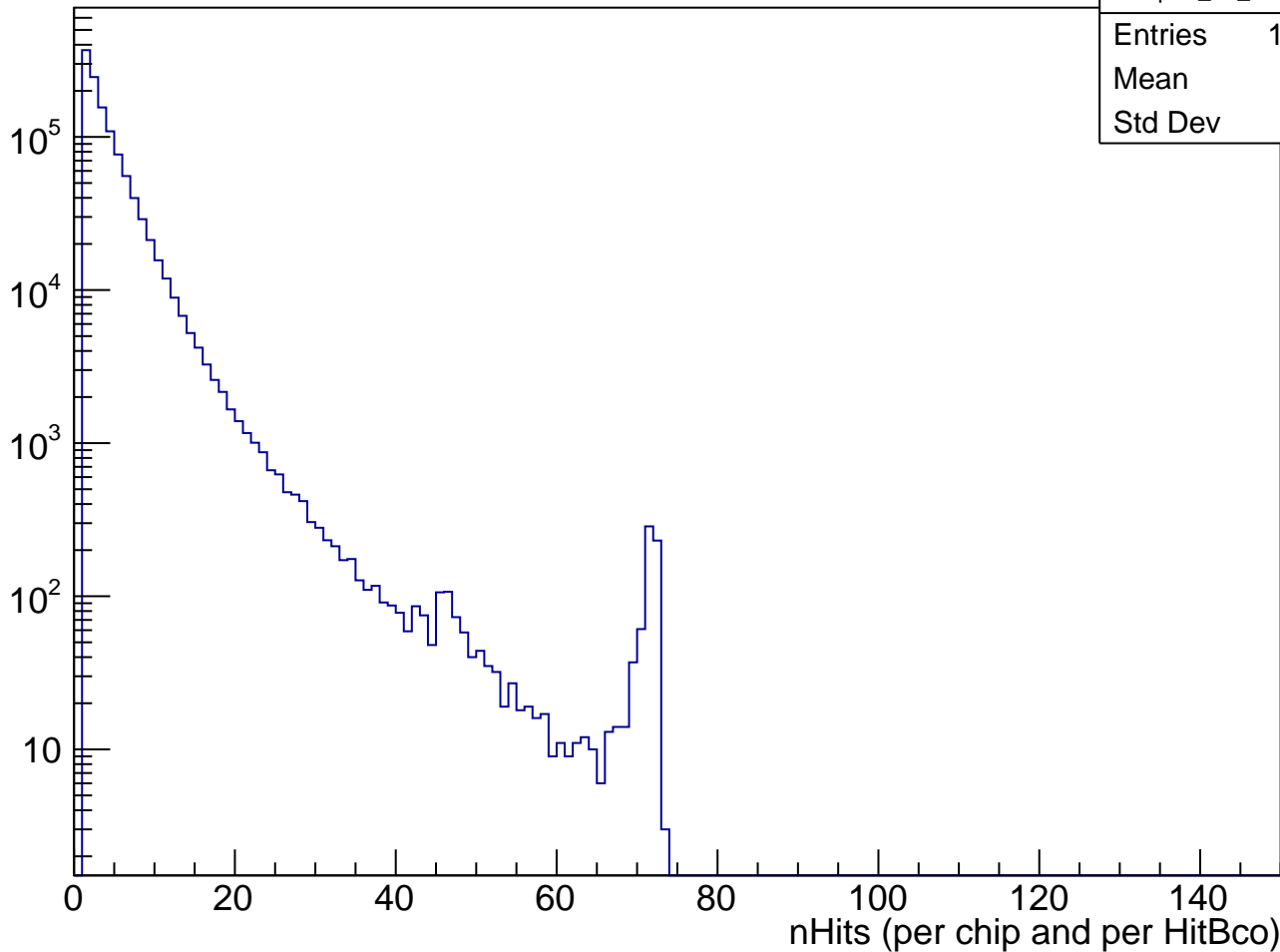
Entries



nChipHit_F3_Fch7_Chip15	
Entries	1169257
Mean	3.659
Std Dev	4.157

nChipHit_F3_Fch7_Chip16

Entries

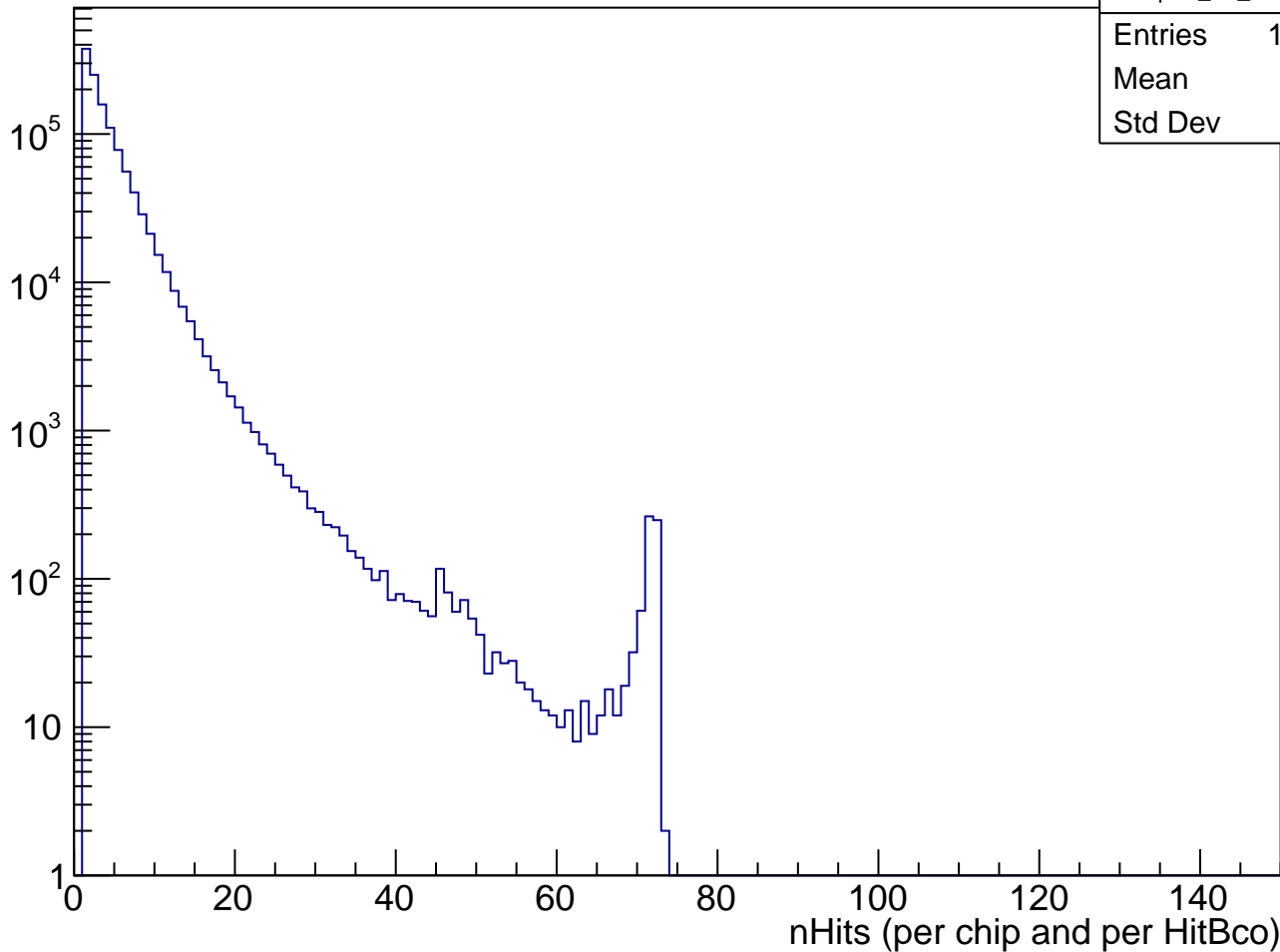


nChipHit_F3_Fch7_Chip16	
Entries	1175248
Mean	3.65
Std Dev	4.133

nHits (per chip and per HitBco)

nChipHit_F3_Fch7_Chip17

Entries

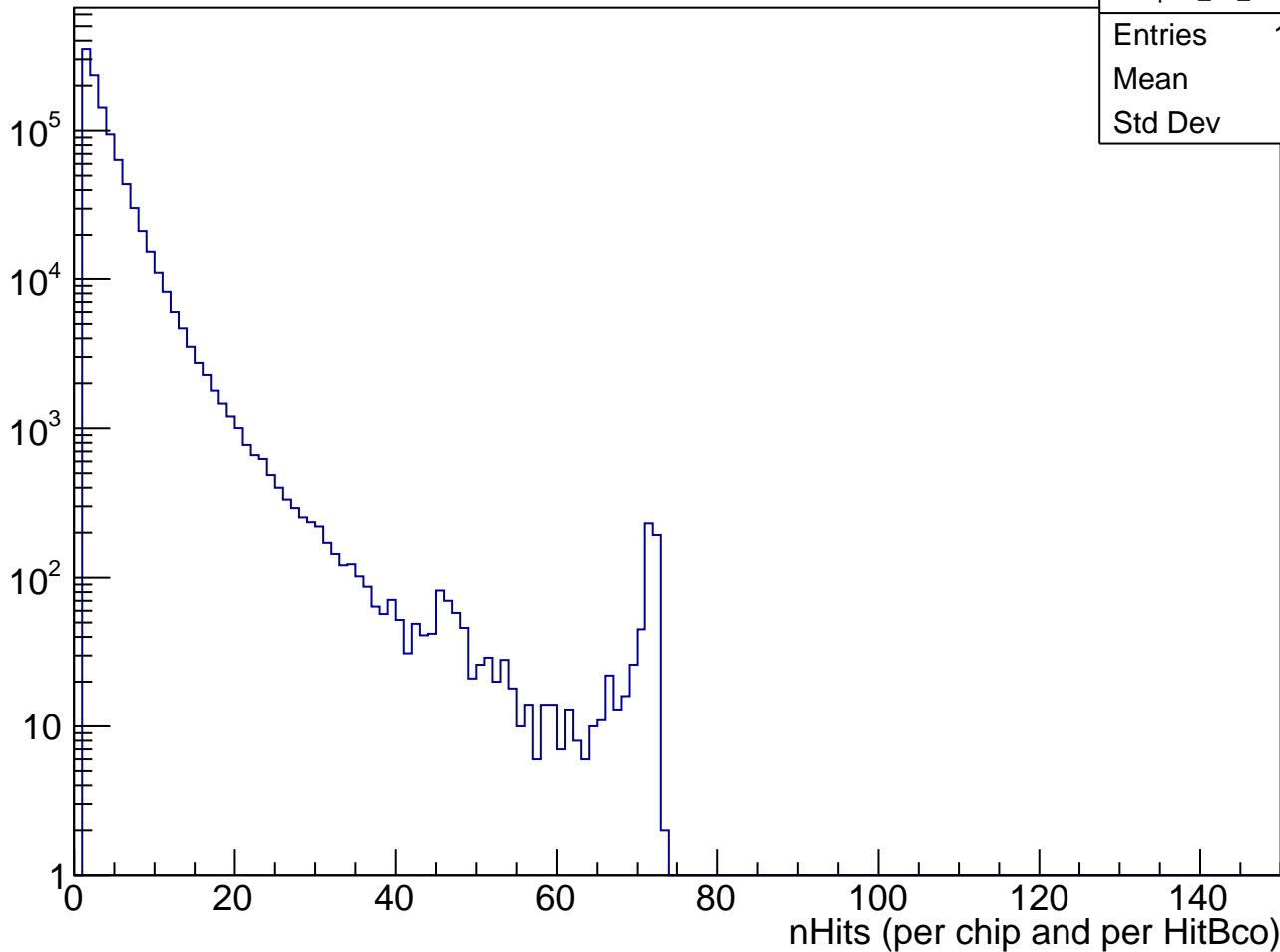


nChipHit_F3_Fch7_Chip17	
Entries	1191478
Mean	3.624
Std Dev	4.102

nHits (per chip and per HitBco)

nChipHit_F3_Fch7_Chip18

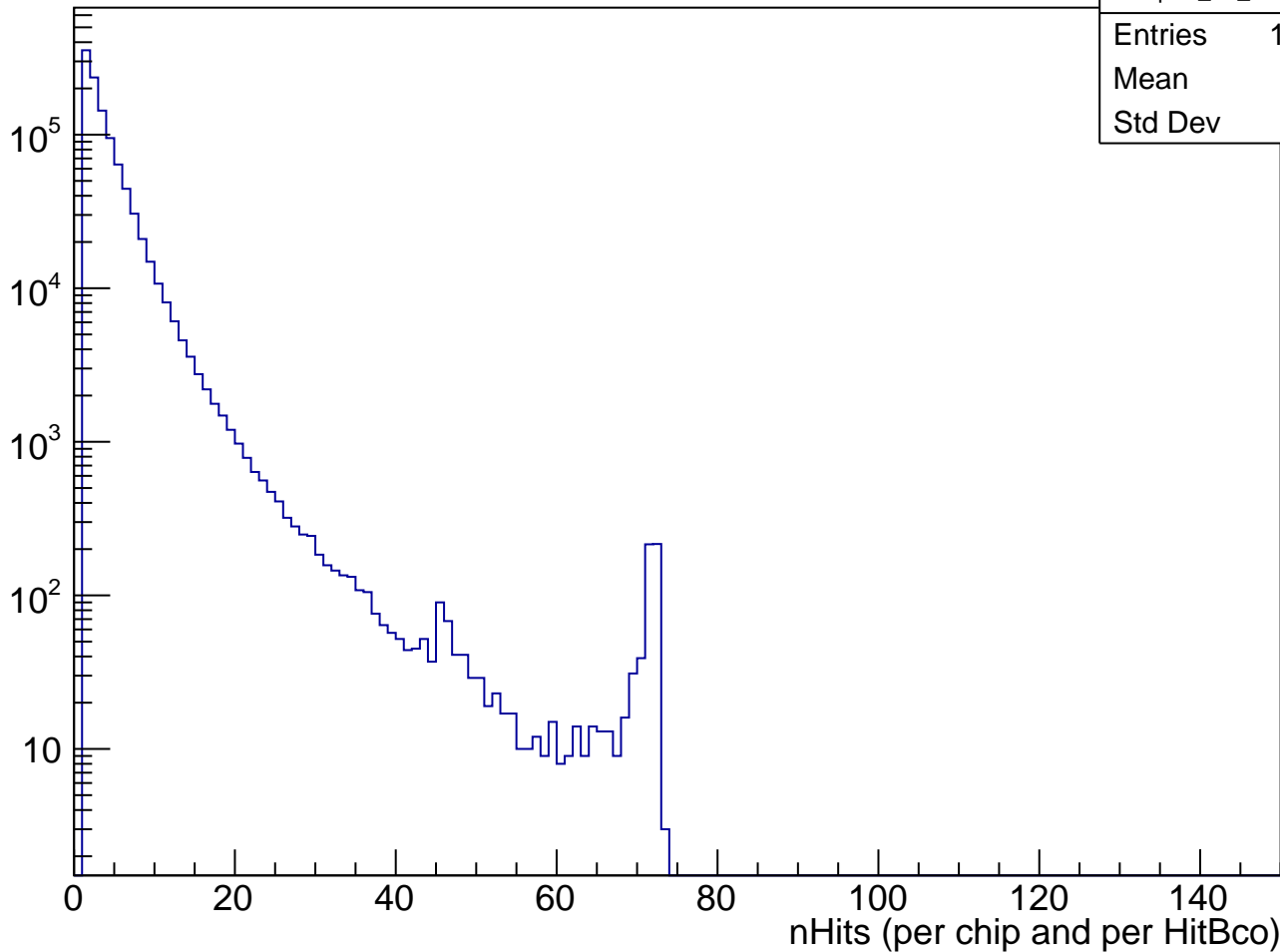
Entries



nChipHit_F3_Fch7_Chip18	
Entries	1047851
Mean	3.336
Std Dev	3.813

nChipHit_F3_Fch7_Chip19

Entries

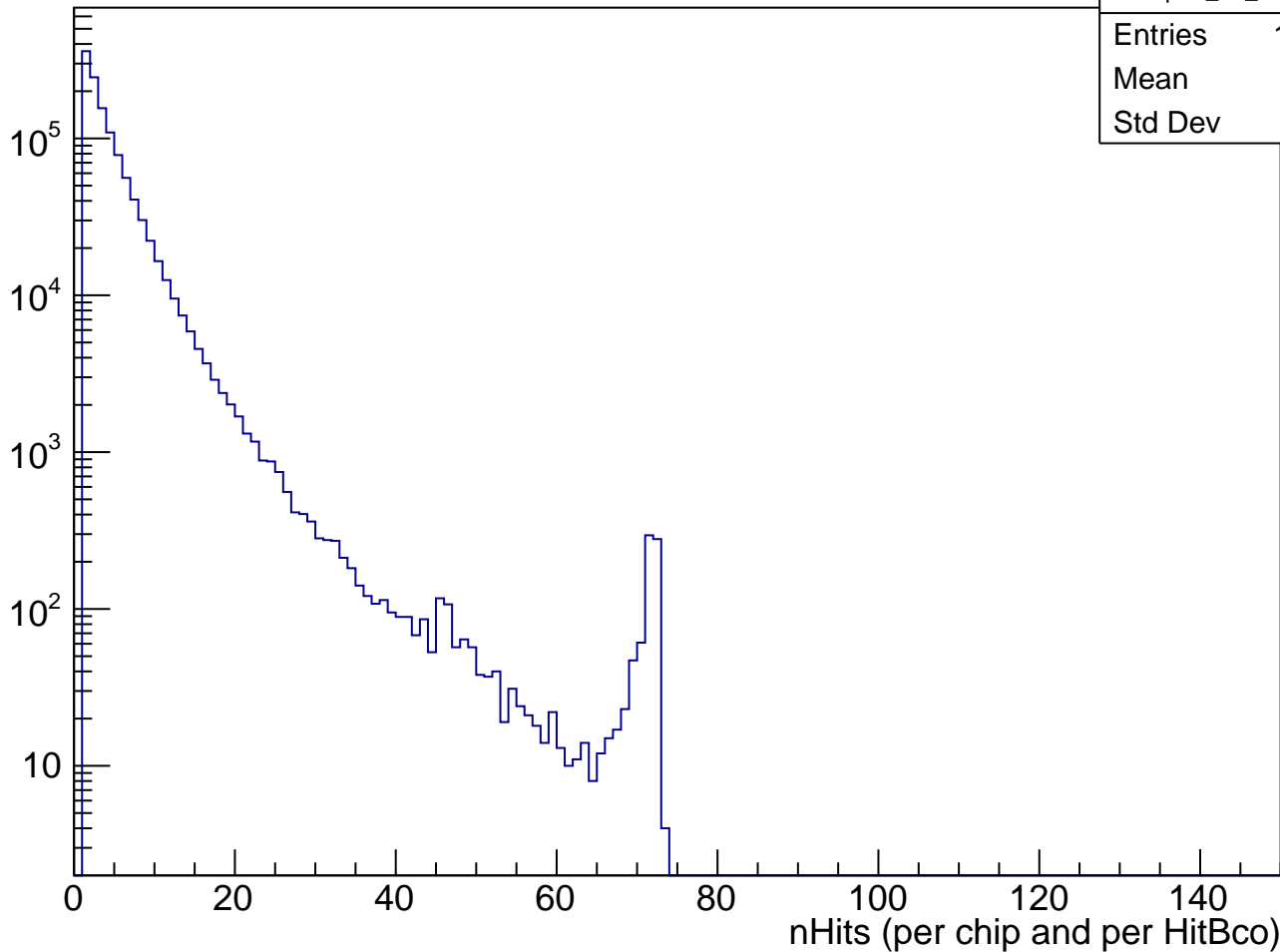


nChipHit_F3_Fch7_Chip19	
Entries	1053100
Mean	3.323
Std Dev	3.796

nHits (per chip and per HitBco)

nChipHit_F3_Fch7_Chip2

Entries



nChipHit_F3_Fch7_Chip2

Entries 1177361

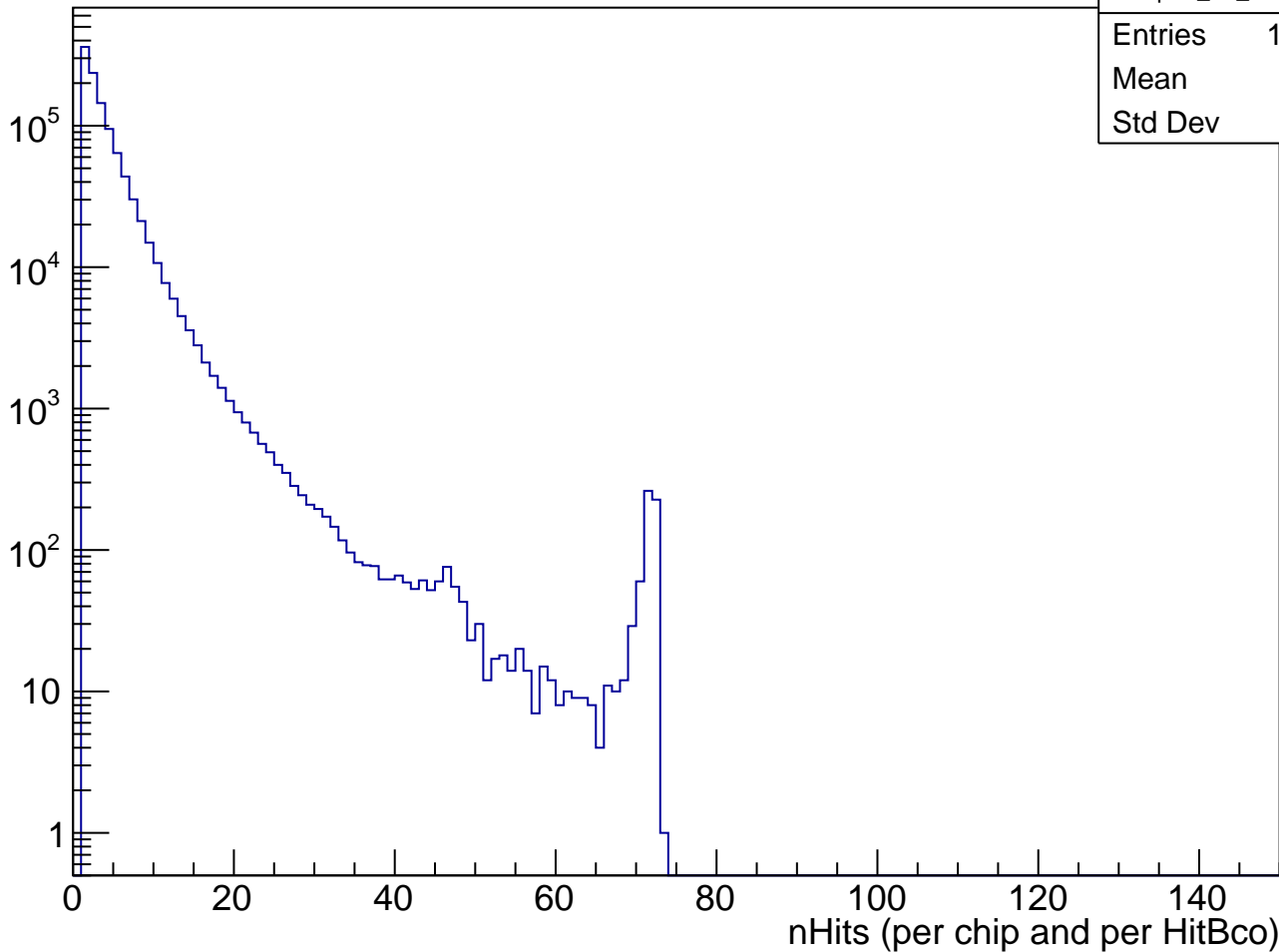
Mean 3.757

Std Dev 4.293

nHits (per chip and per HitBco)

nChipHit_F3_Fch7_Chip20

Entries

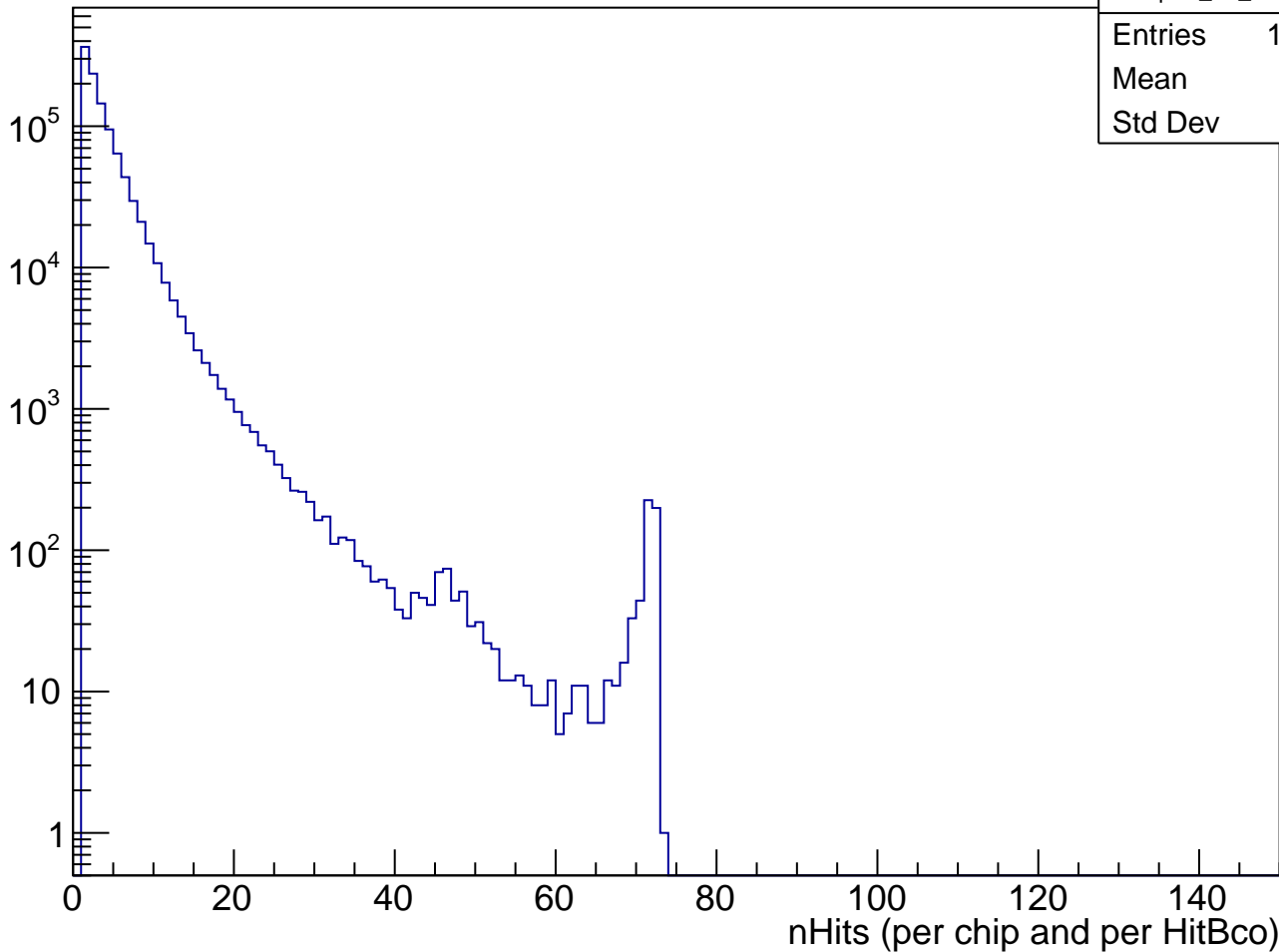


nChipHit_F3_Fch7_Chip20	
Entries	1059417
Mean	3.302
Std Dev	3.809

nHits (per chip and per HitBco)

nChipHit_F3_Fch7_Chip21

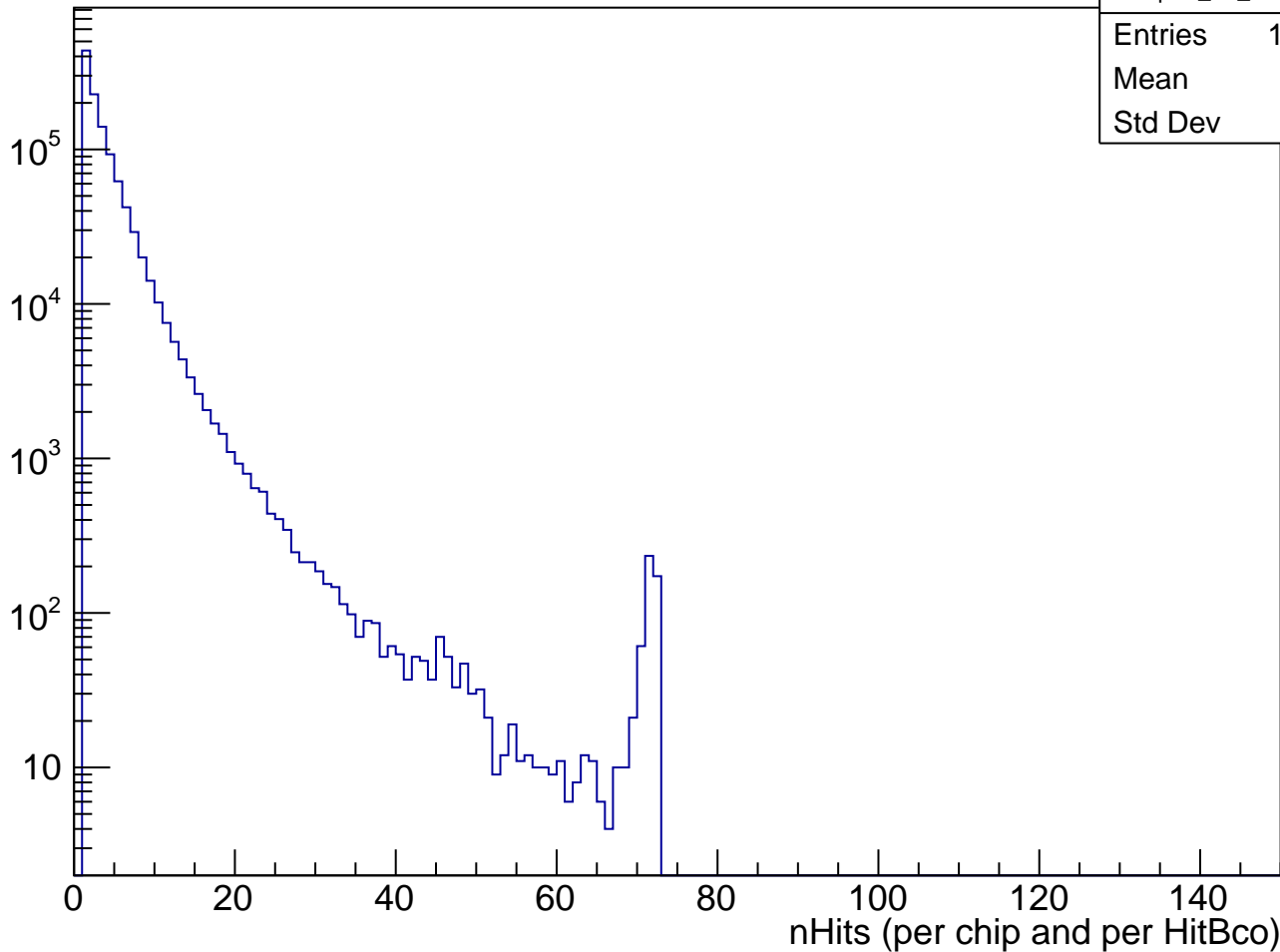
Entries



nChipHit_F3_Fch7_Chip21	
Entries	1060935
Mean	3.278
Std Dev	3.735

nChipHit_F3_Fch7_Chip22

Entries

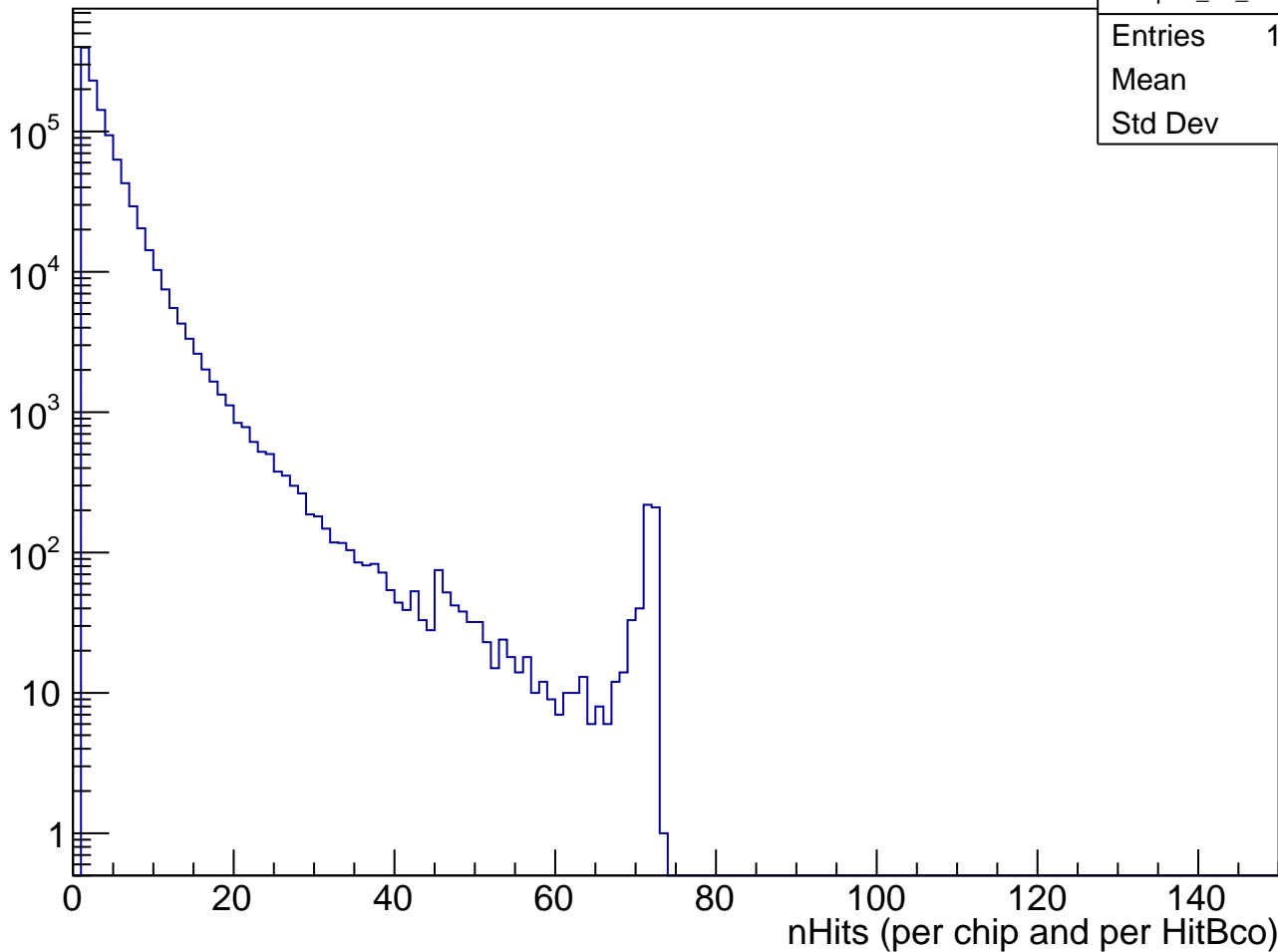


nChipHit_F3_Fch7_Chip22	
Entries	1111936
Mean	3.111
Std Dev	3.649

nHits (per chip and per HitBco)

nChipHit_F3_Fch7_Chip23

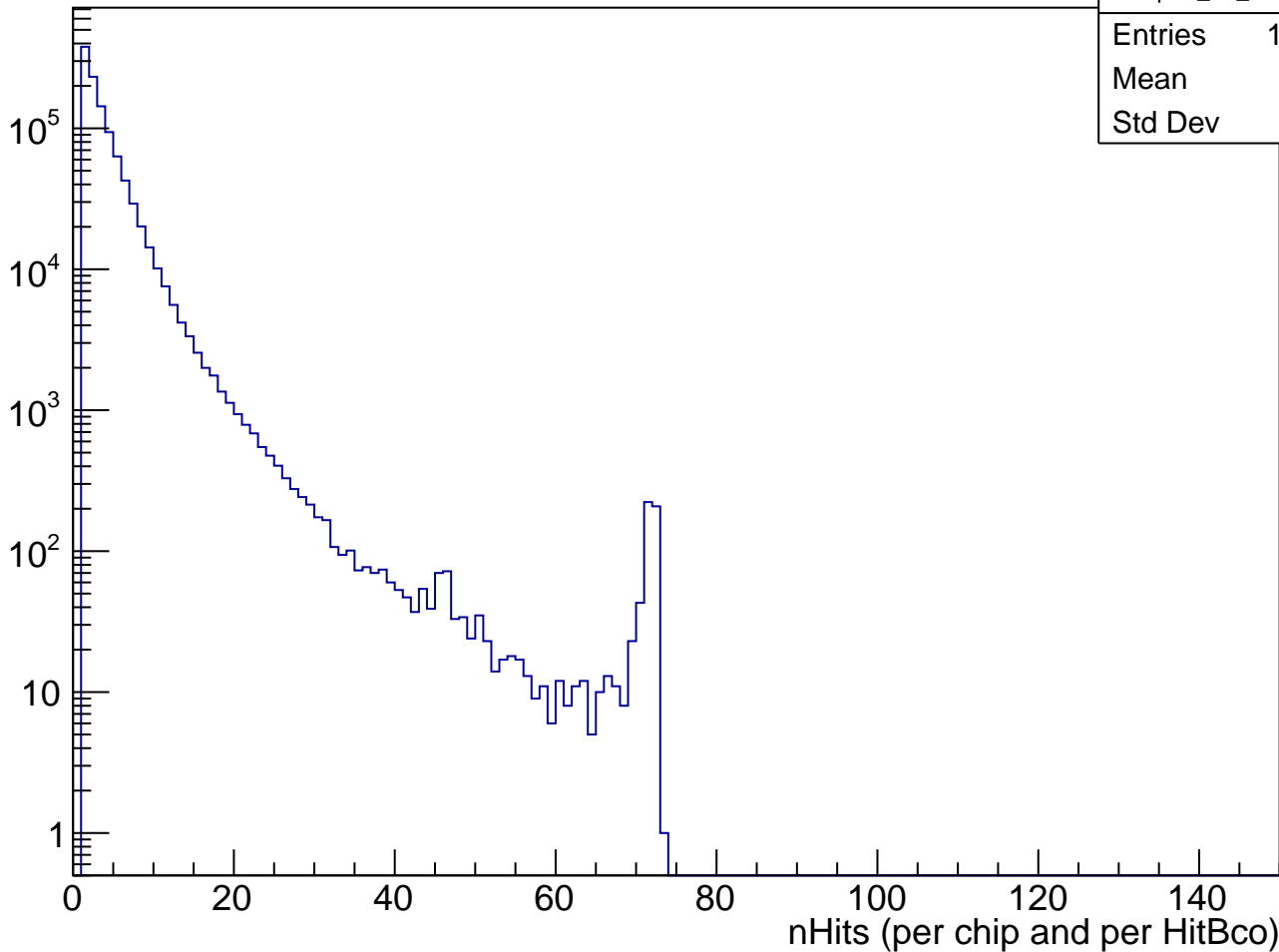
Entries



nChipHit_F3_Fch7_Chip23	
Entries	1079255
Mean	3.19
Std Dev	3.693

nChipHit_F3_Fch7_Chip24

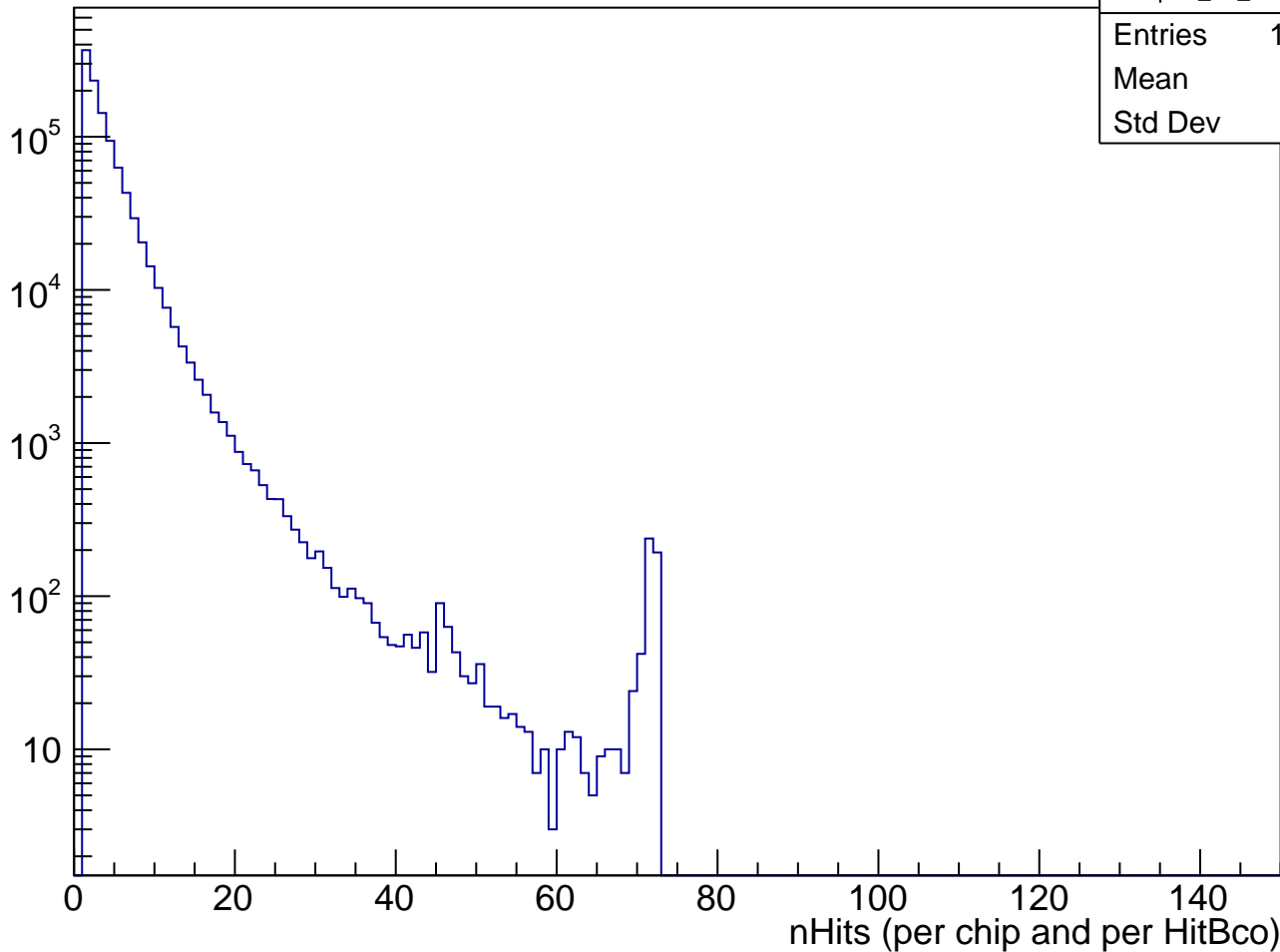
Entries



nChipHit_F3_Fch7_Chip24	
Entries	1064829
Mean	3.223
Std Dev	3.707

nChipHit_F3_Fch7_Chip25

Entries

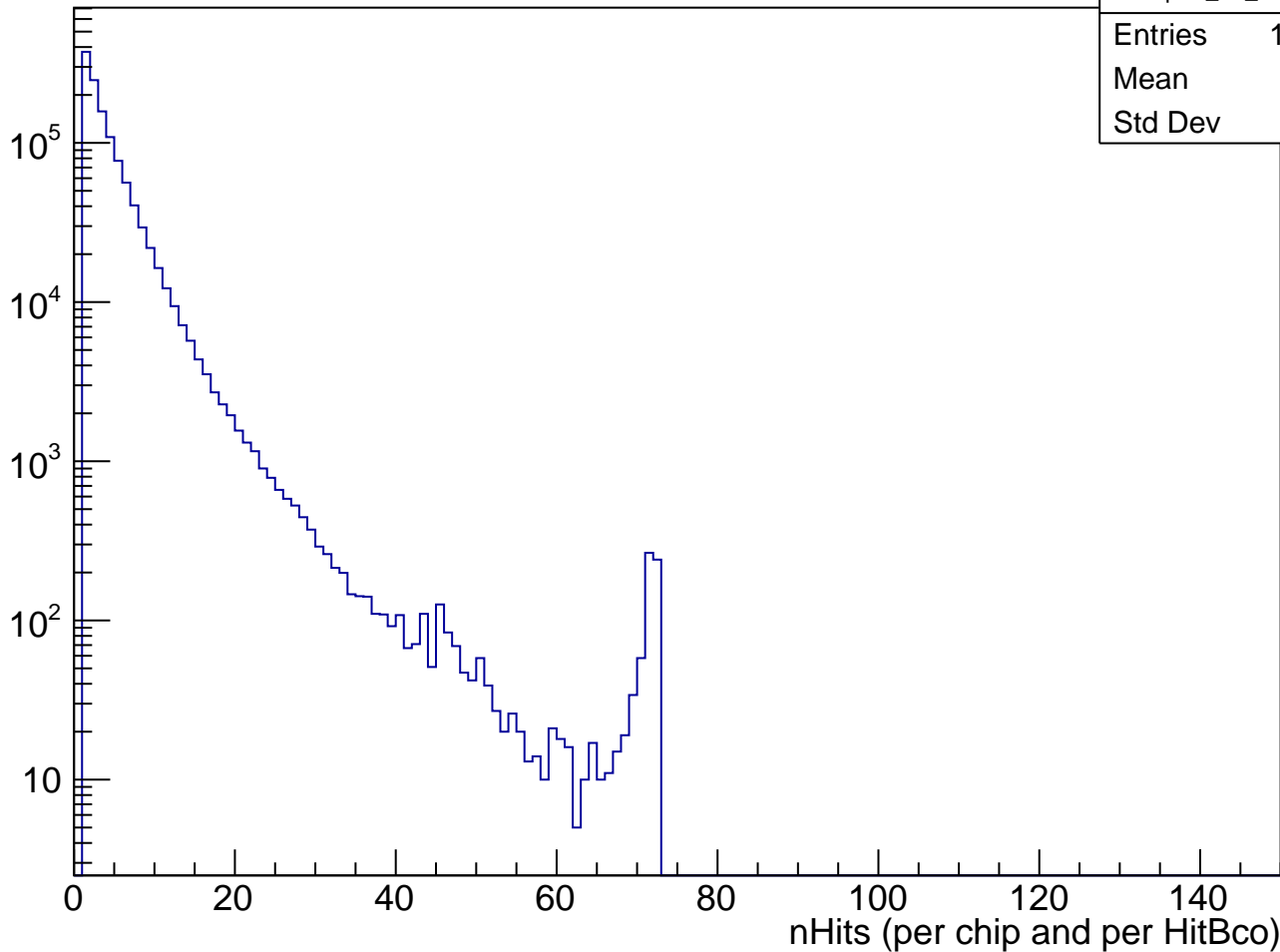


nChipHit_F3_Fch7_Chip25	
Entries	1054753
Mean	3.247
Std Dev	3.71

nHits (per chip and per HitBco)

nChipHit_F3_Fch7_Chip3

Entries



nChipHit_F3_Fch7_Chip3

Entries 1190310

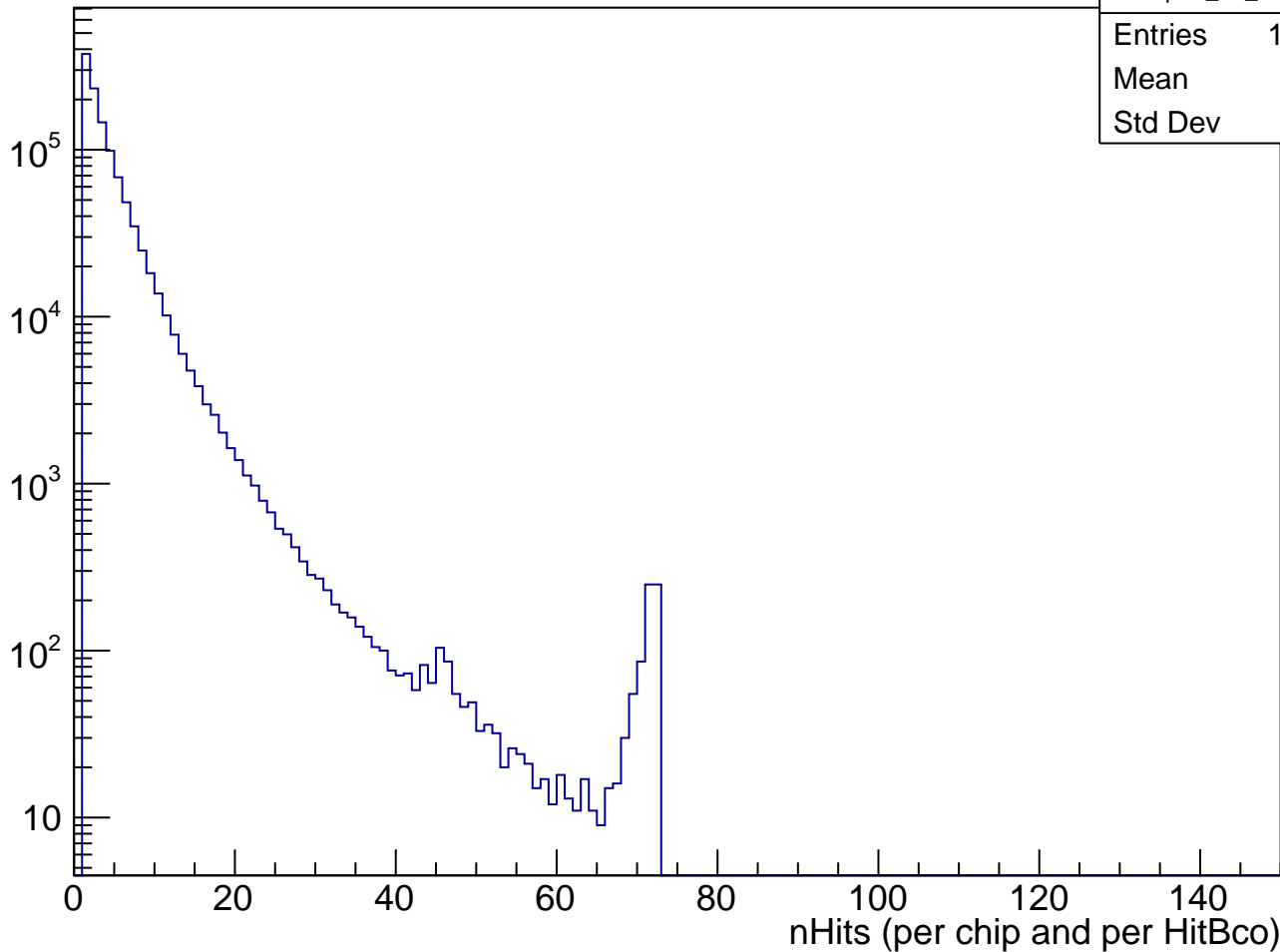
Mean 3.691

Std Dev 4.202

nHits (per chip and per HitBco)

nChipHit_F3_Fch7_Chip4

Entries



nChipHit_F3_Fch7_Chip4

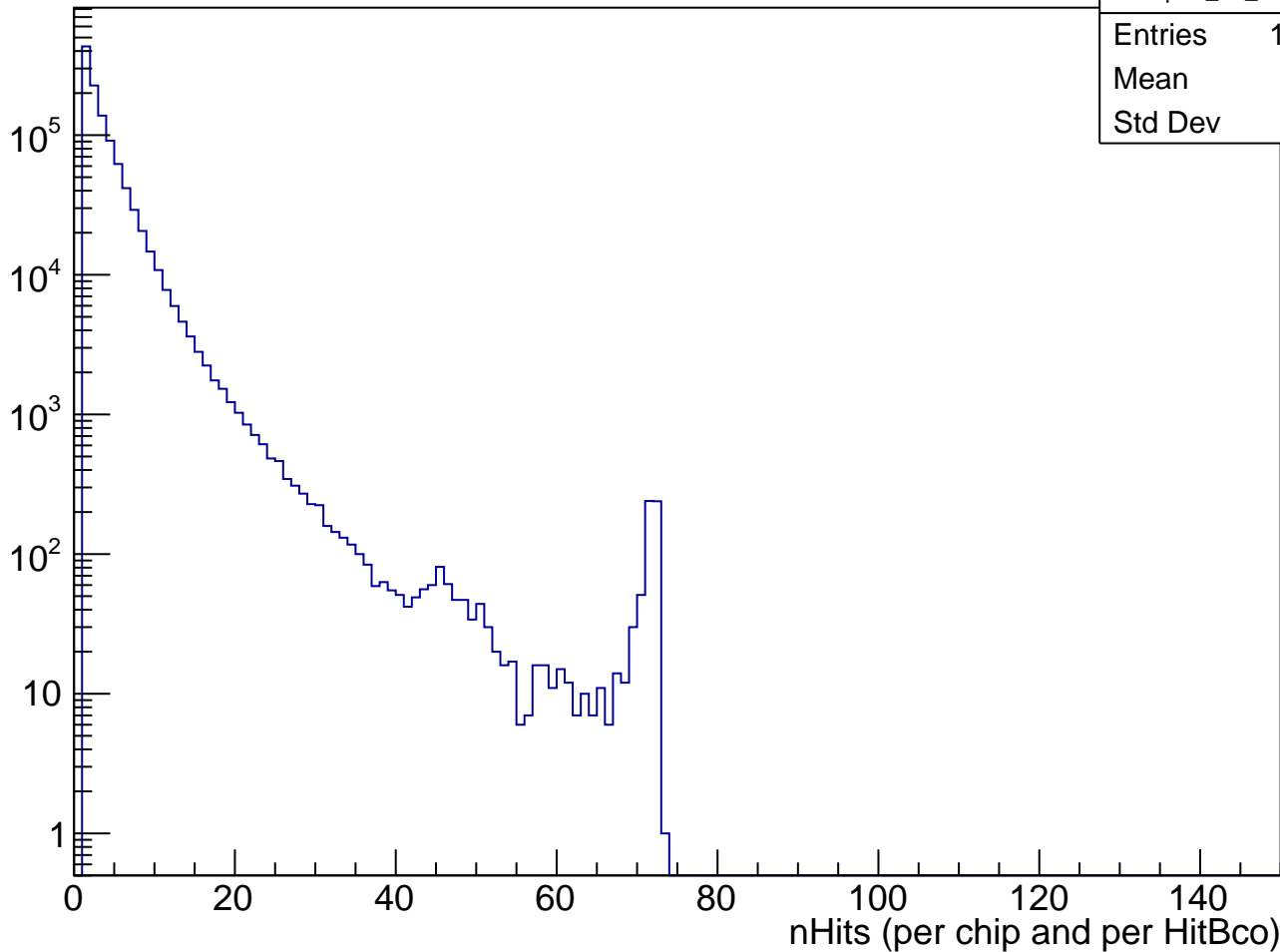
Entries 1112654

Mean 3.531

Std Dev 4.16

nChipHit_F3_Fch7_Chip5

Entries



nChipHit_F3_Fch7_Chip5

Entries 1106360

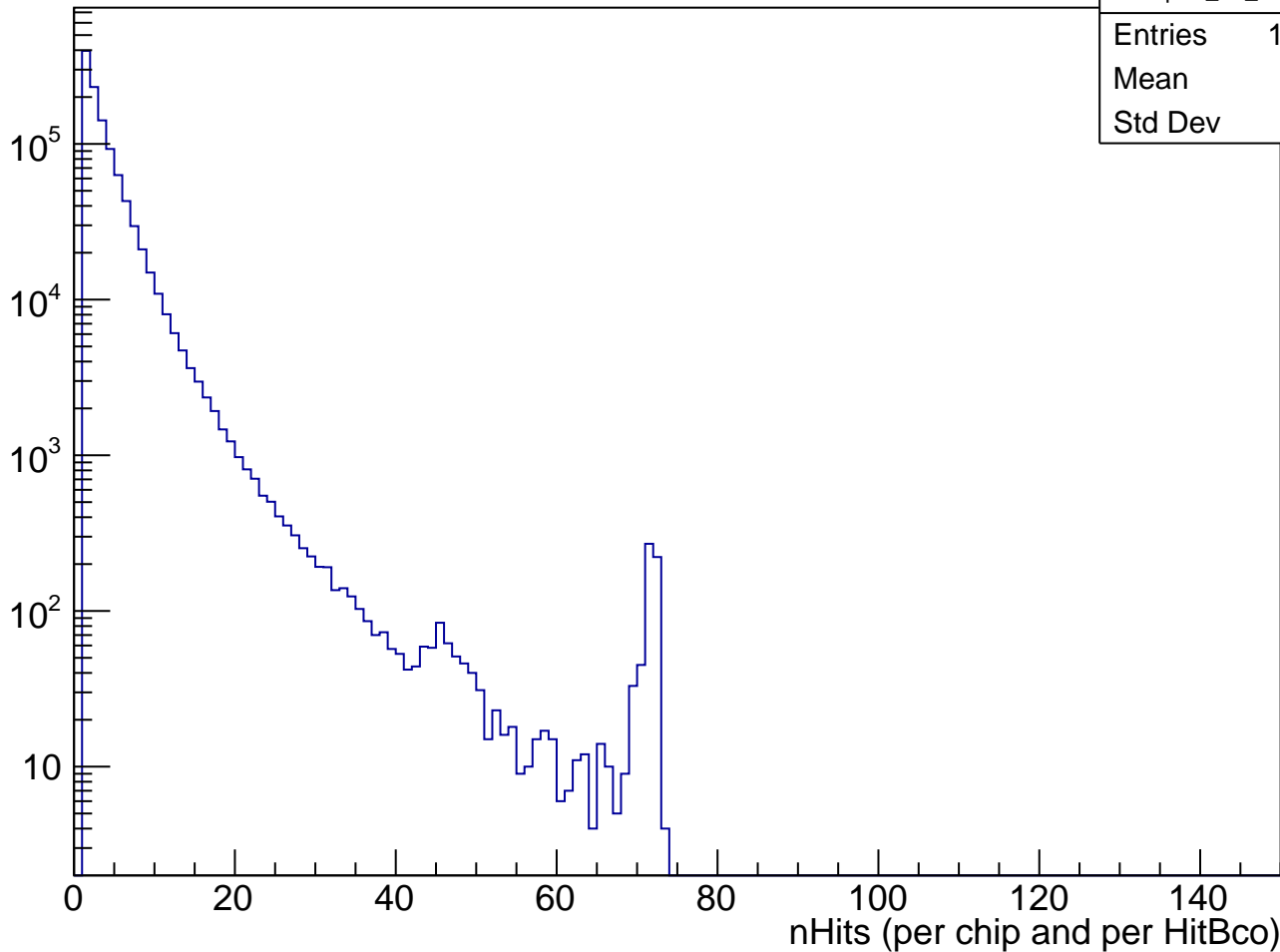
Mean 3.164

Std Dev 3.794

nHits (per chip and per HitBco)

nChipHit_F3_Fch7_Chip6

Entries



nChipHit_F3_Fch7_Chip6

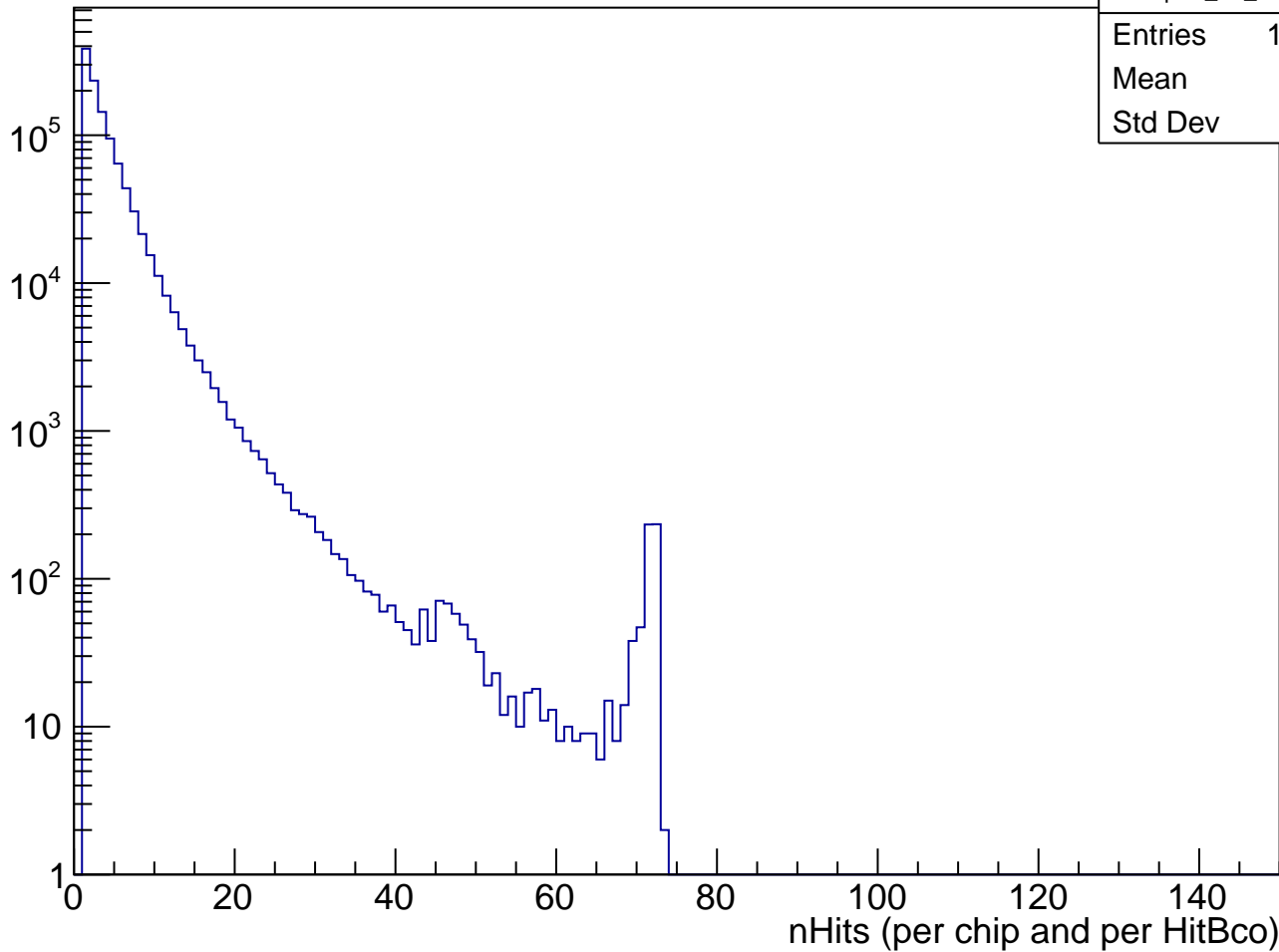
Entries 1085175

Mean 3.246

Std Dev 3.822

nChipHit_F3_Fch7_Chip7

Entries

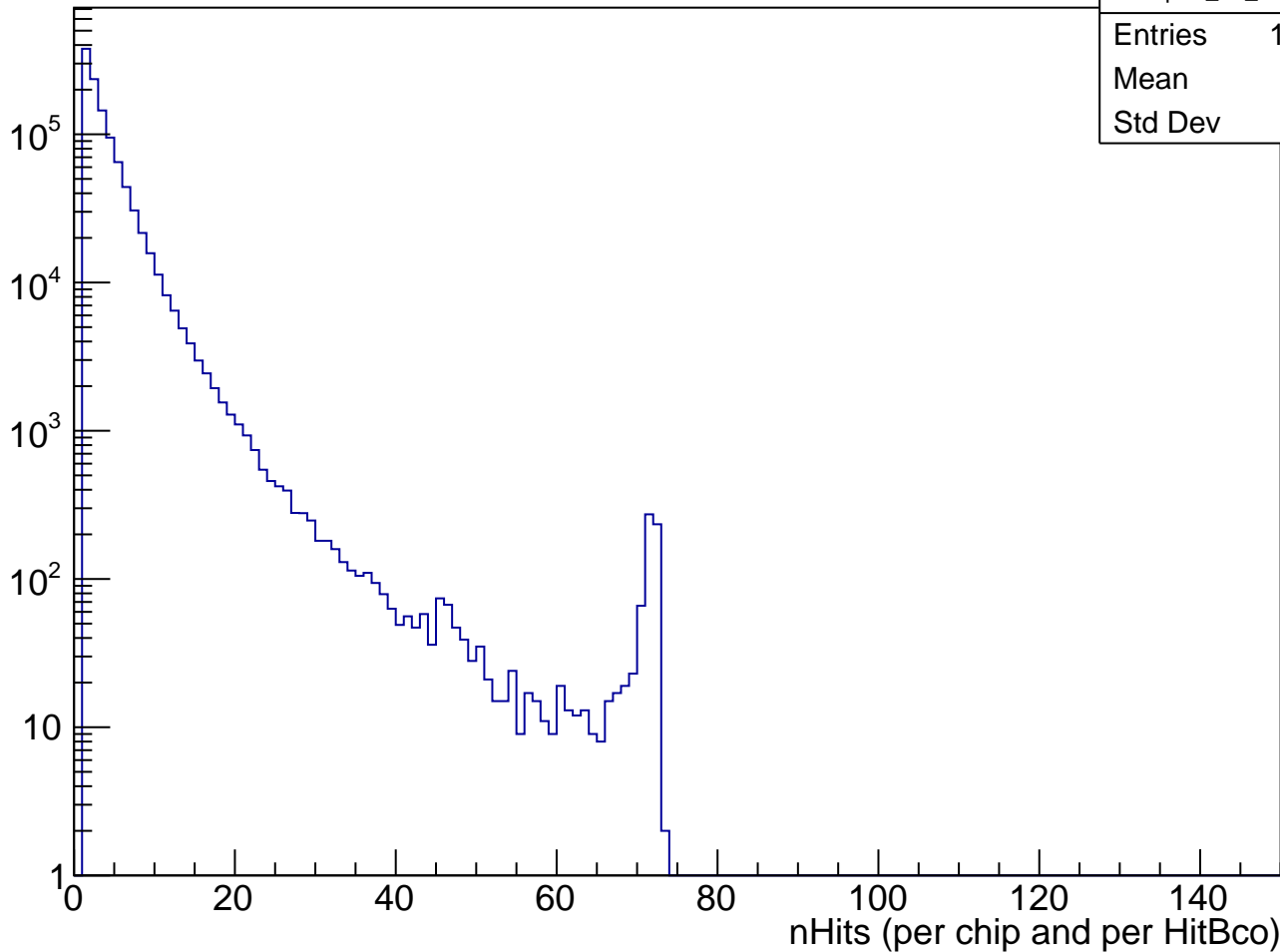


nChipHit_F3_Fch7_Chip7	
Entries	1085587
Mean	3.296
Std Dev	3.841

nHits (per chip and per HitBco)

nChipHit_F3_Fch7_Chip8

Entries



nChipHit_F3_Fch7_Chip8

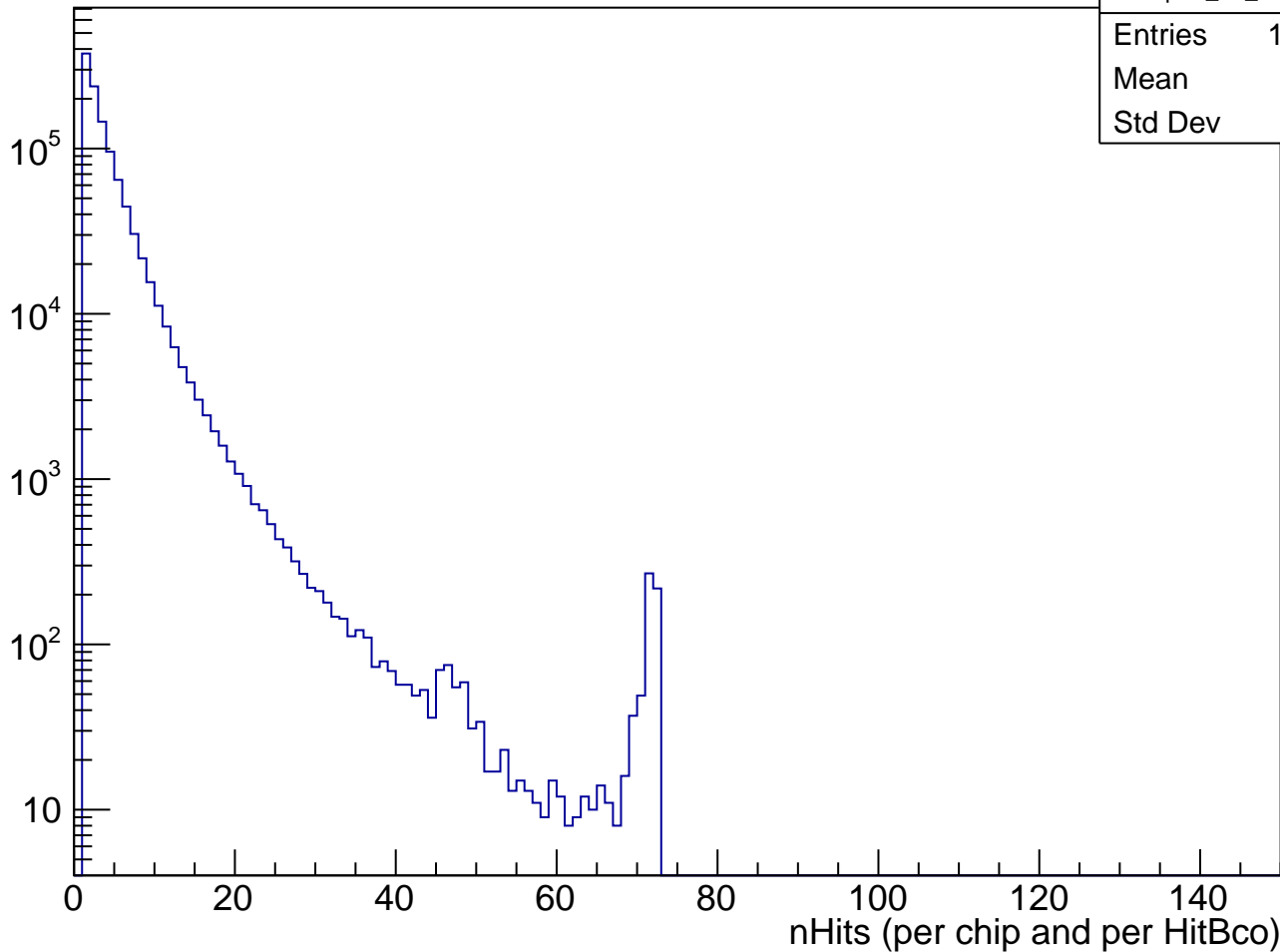
Entries 1082700

Mean 3.322

Std Dev 3.887

nChipHit_F3_Fch7_Chip9

Entries



nChipHit_F3_Fch7_Chip9

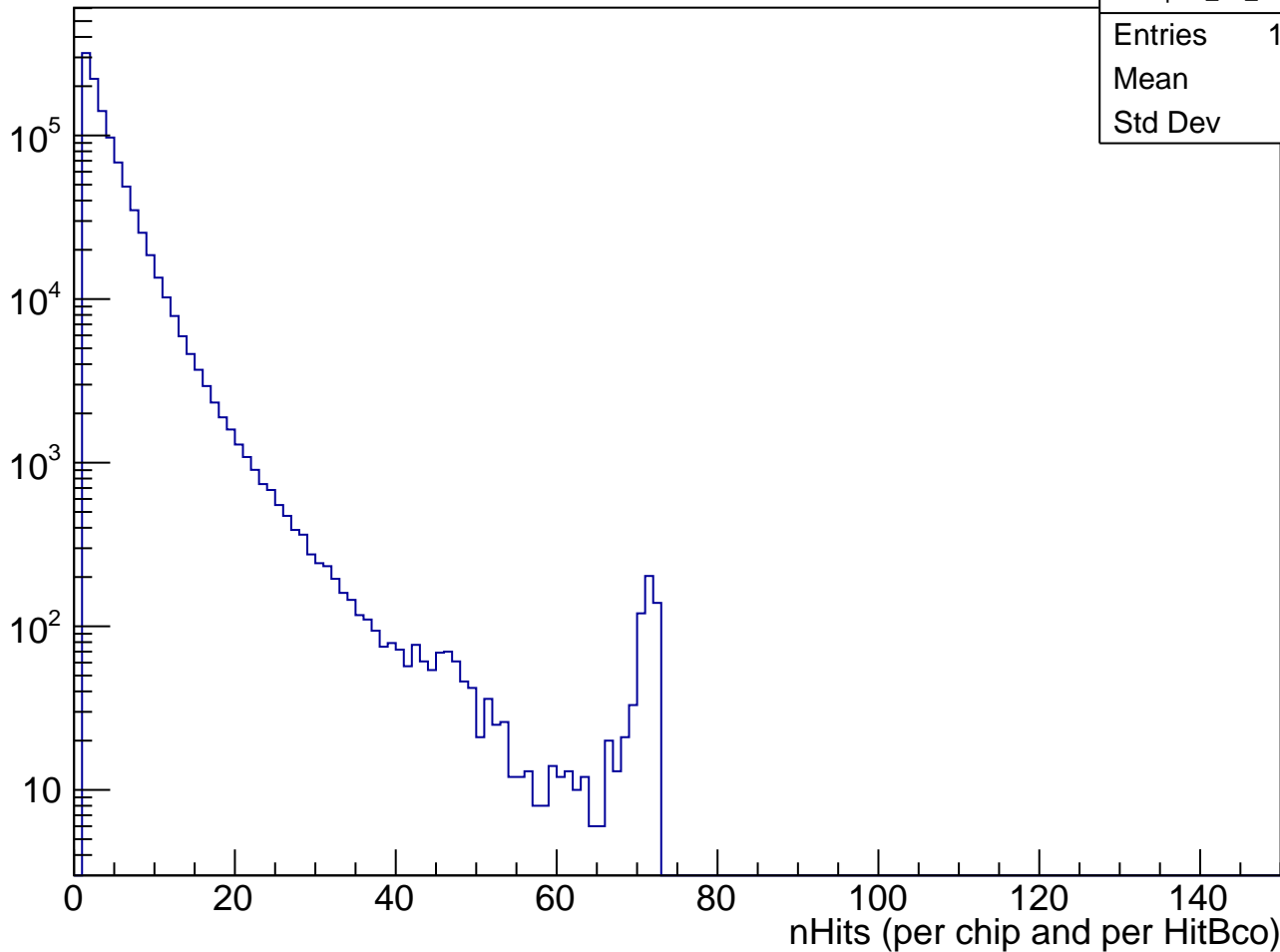
Entries 1084268

Mean 3.322

Std Dev 3.872

nChipHit_F3_Fch8_Chip0

Entries

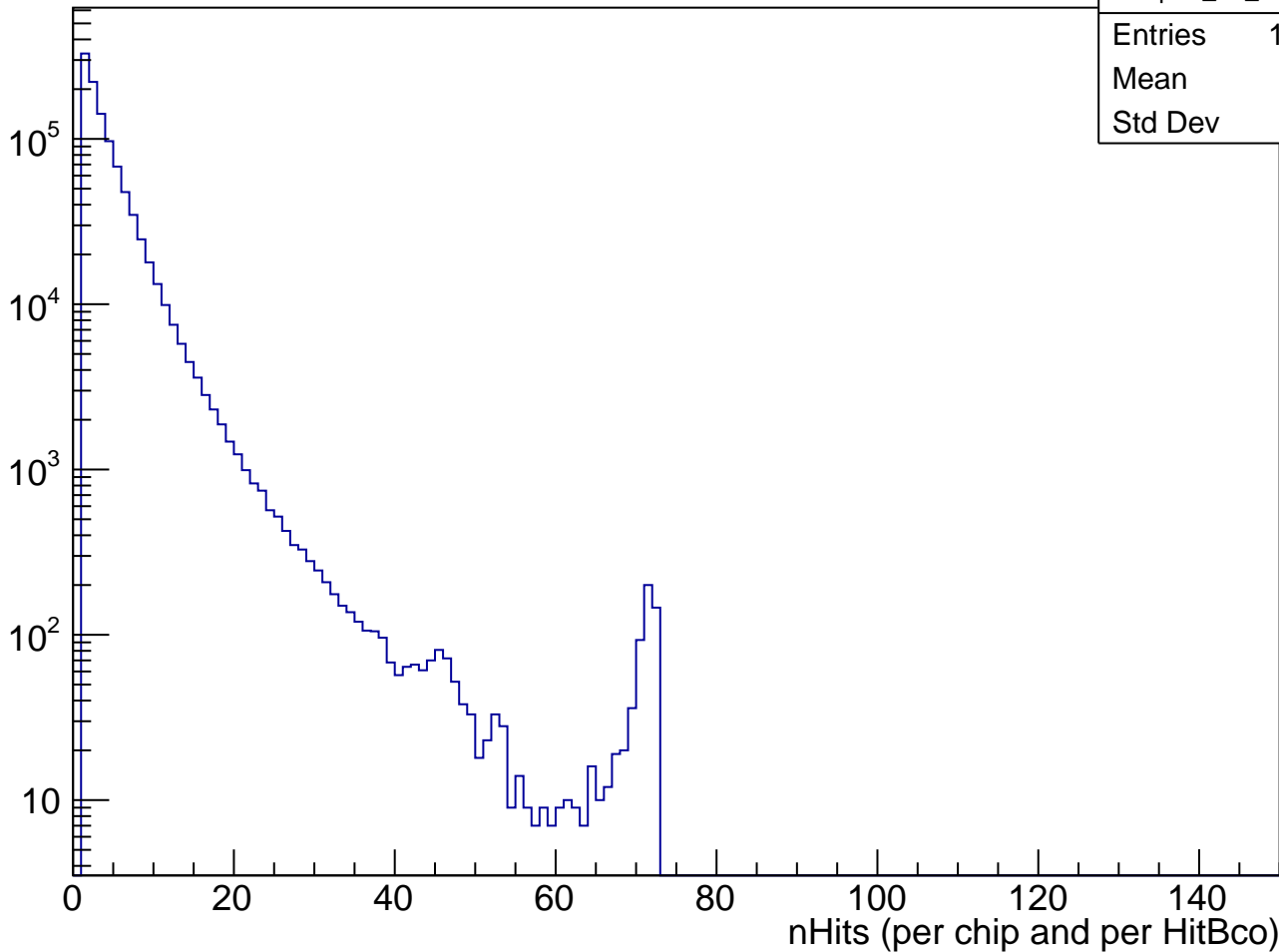


nChipHit_F3_Fch8_Chip0	
Entries	1039029
Mean	3.654
Std Dev	4.103

nHits (per chip and per HitBco)

nChipHit_F3_Fch8_Chip1

Entries

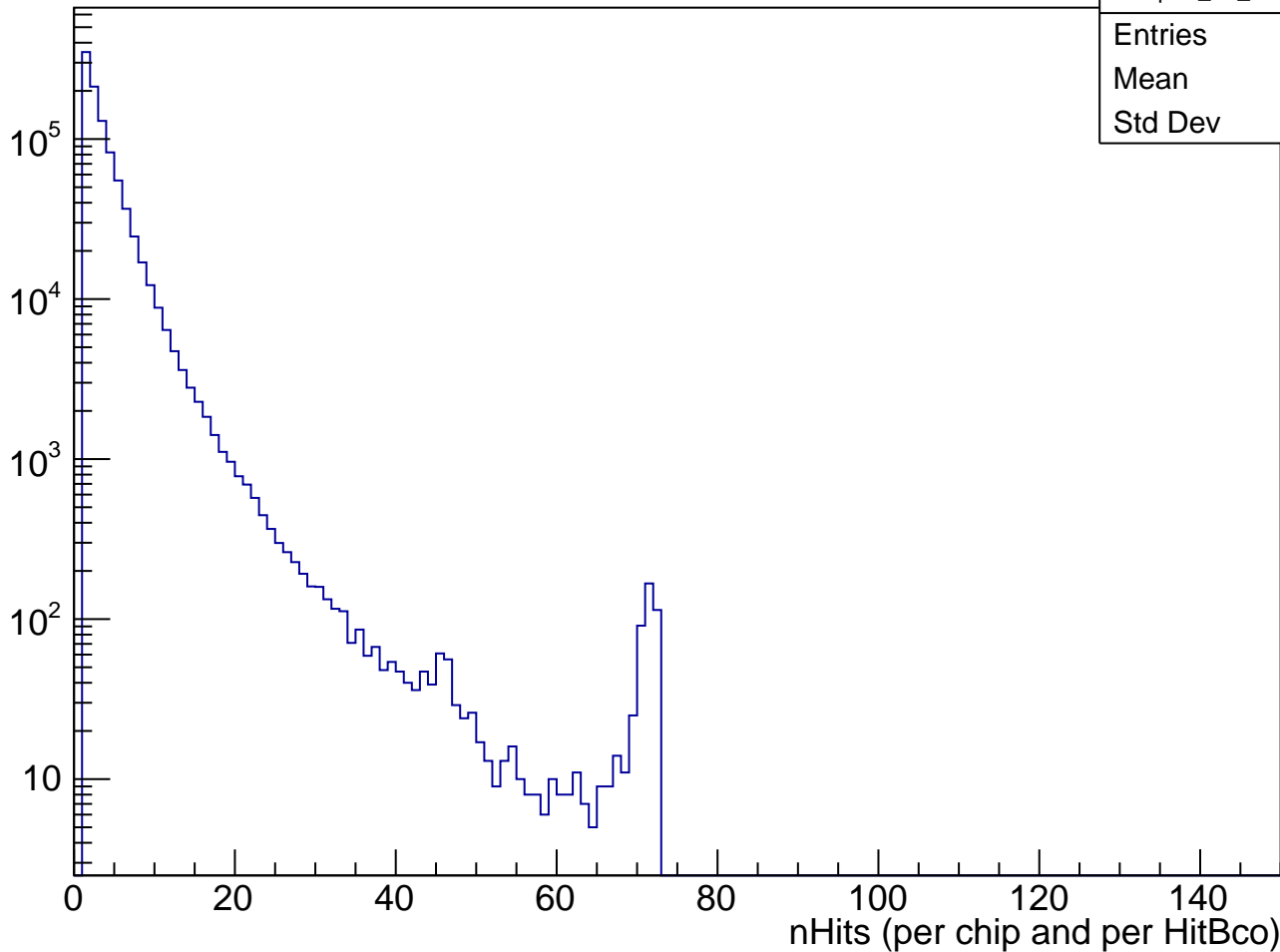


nChipHit_F3_Fch8_Chip1	
Entries	1043373
Mean	3.593
Std Dev	4.035

nHits (per chip and per HitBco)

nChipHit_F3_Fch8_Chip10

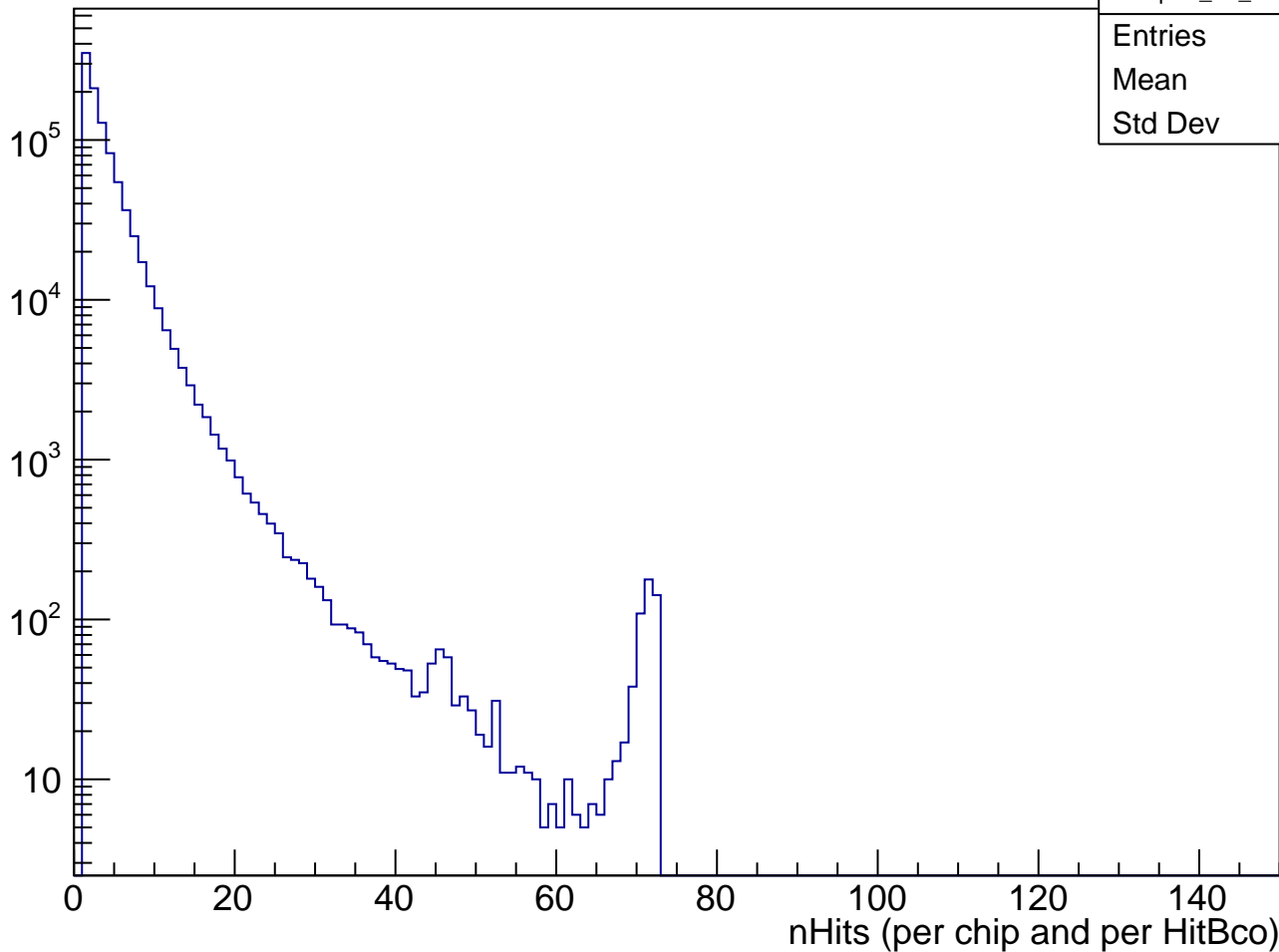
Entries



nChipHit_F3_Fch8_Chip10	
Entries	959975
Mean	3.14
Std Dev	3.597

nChipHit_F3_Fch8_Chip11

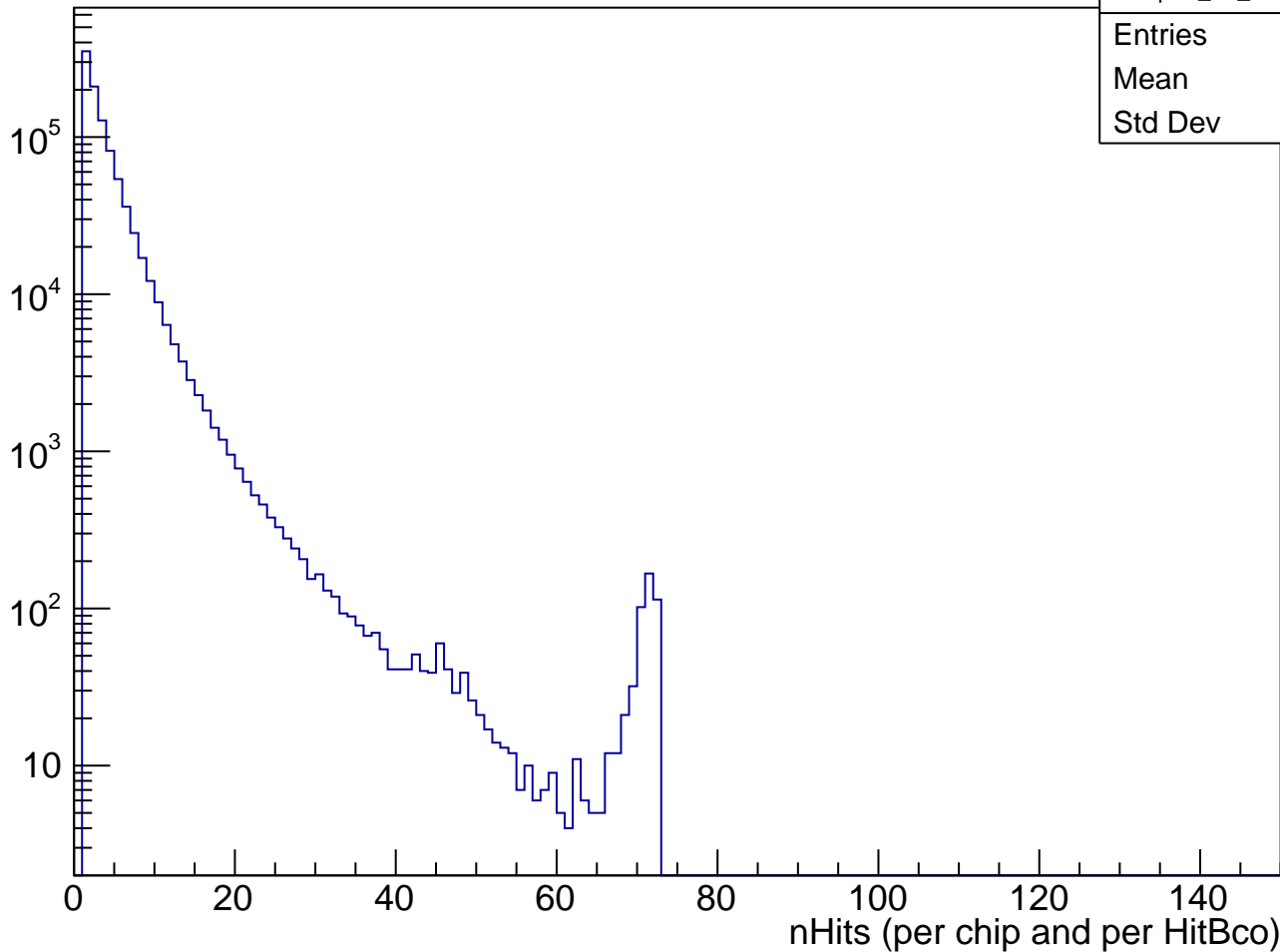
Entries



nChipHit_F3_Fch8_Chip11	
Entries	957708
Mean	3.156
Std Dev	3.673

nChipHit_F3_Fch8_Chip12

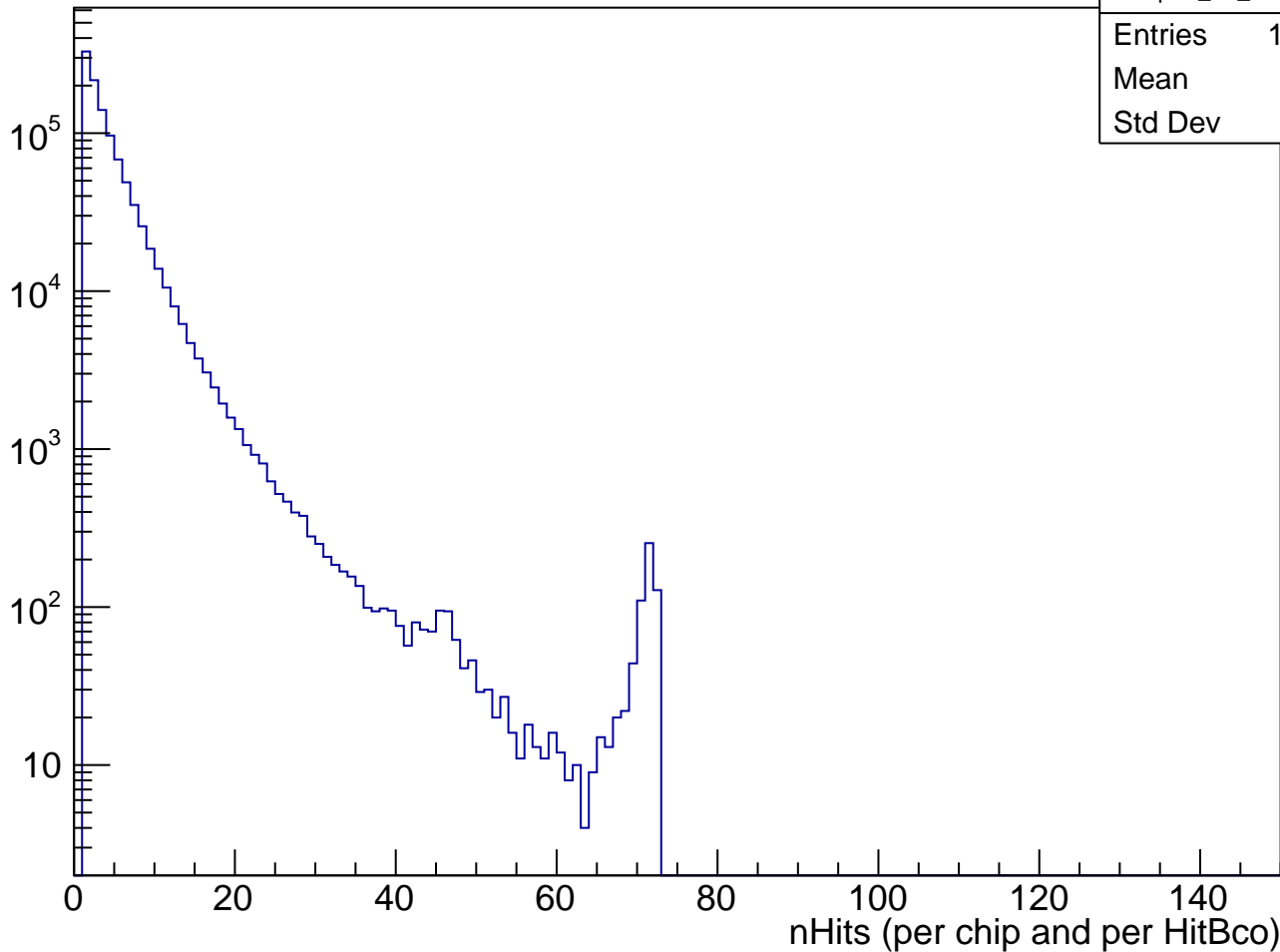
Entries



nChipHit_F3_Fch8_Chip12	
Entries	954617
Mean	3.14
Std Dev	3.627

nChipHit_F3_Fch8_Chip13

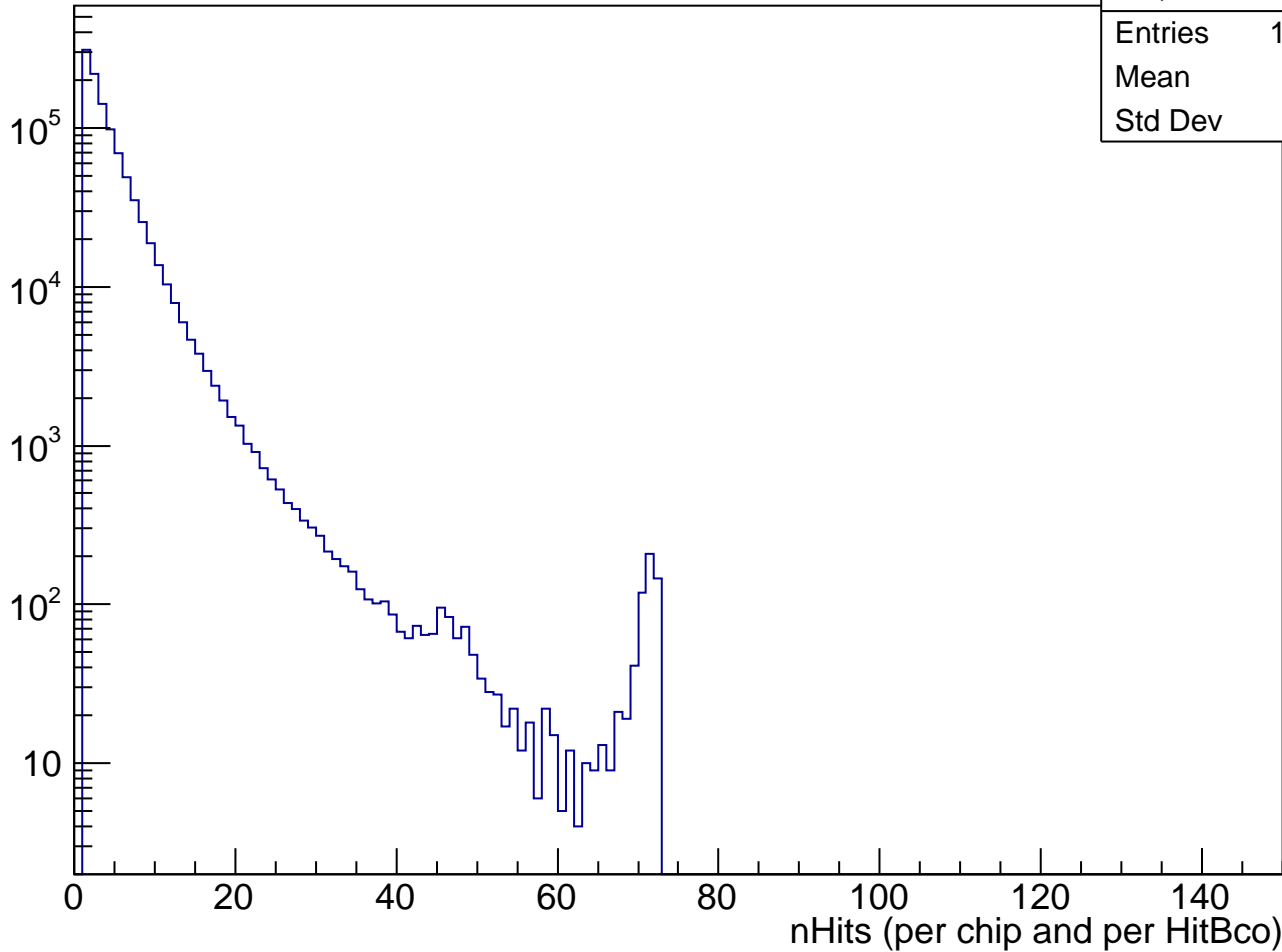
Entries



nChipHit_F3_Fch8_Chip13	
Entries	1044364
Mean	3.661
Std Dev	4.165

nChipHit_F3_Fch8_Chip14

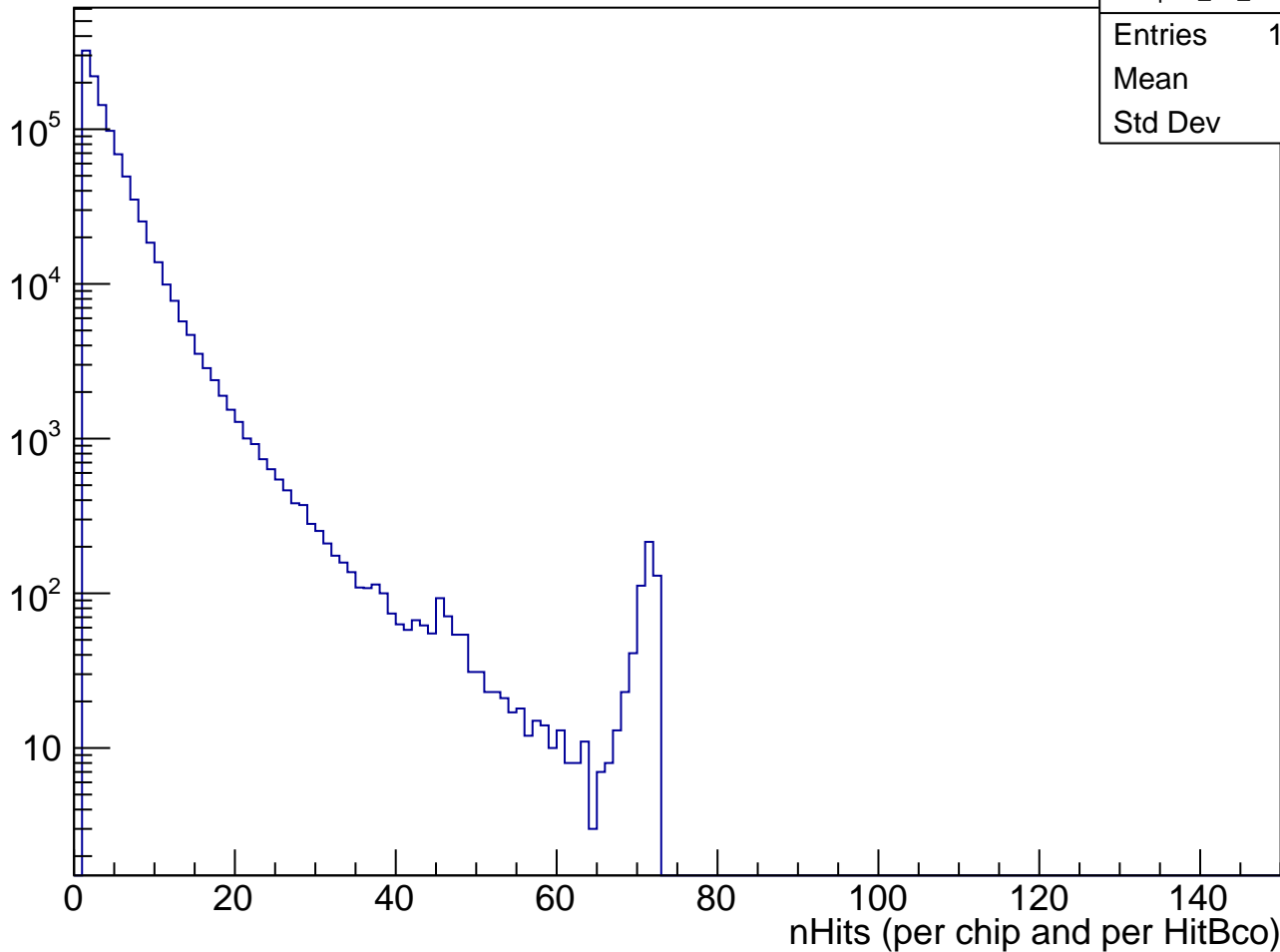
Entries



nChipHit_F3_Fch8_Chip14	
Entries	1032268
Mean	3.697
Std Dev	4.15

nChipHit_F3_Fch8_Chip15

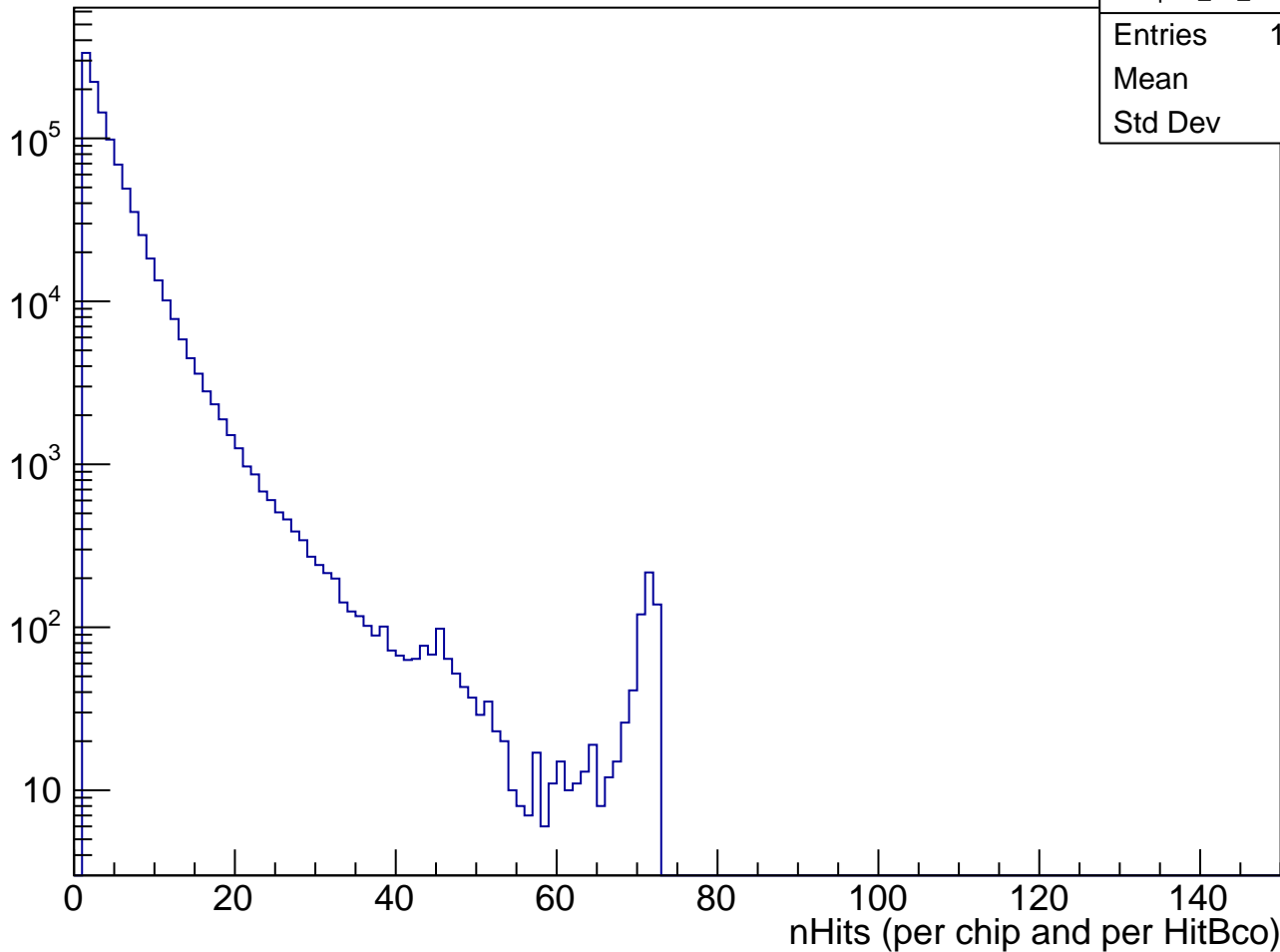
Entries



nChipHit_F3_Fch8_Chip15	
Entries	1044114
Mean	3.641
Std Dev	4.08

nChipHit_F3_Fch8_Chip16

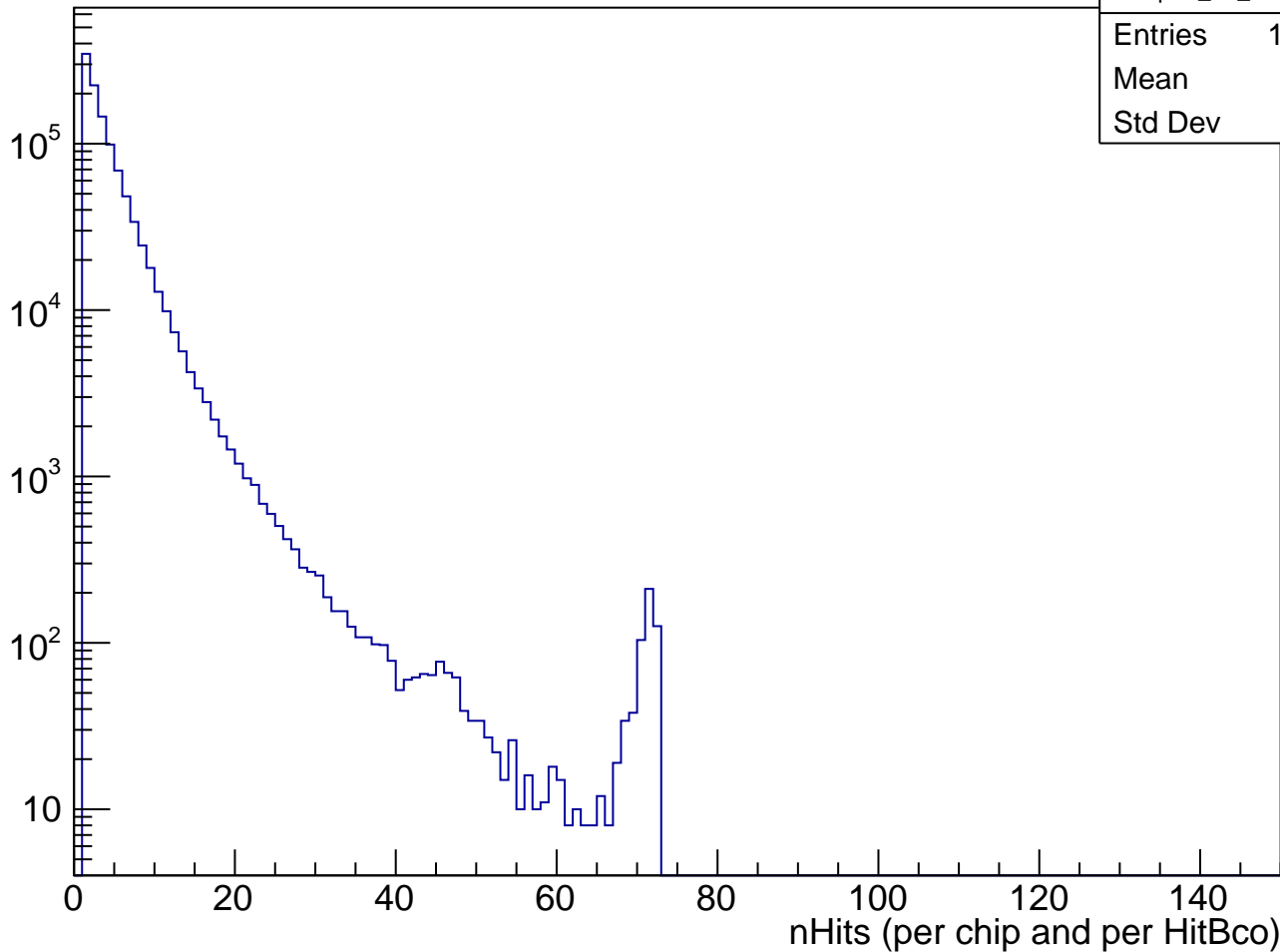
Entries



nChipHit_F3_Fch8_Chip16	
Entries	1059343
Mean	3.601
Std Dev	4.063

nChipHit_F3_Fch8_Chip17

Entries

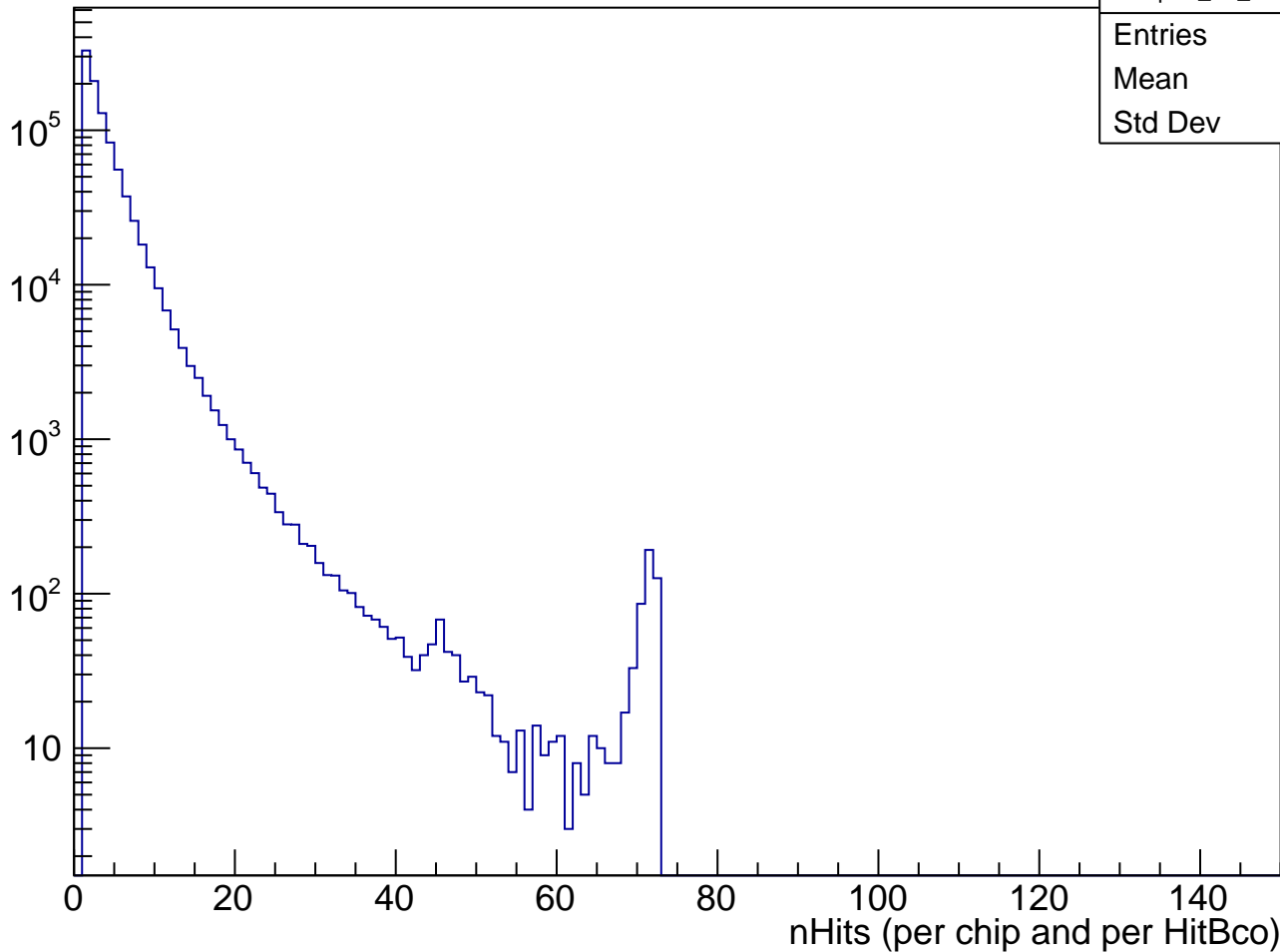


nChipHit_F3_Fch8_Chip17	
Entries	1069208
Mean	3.527
Std Dev	3.987

nHits (per chip and per HitBco)

nChipHit_F3_Fch8_Chip18

Entries

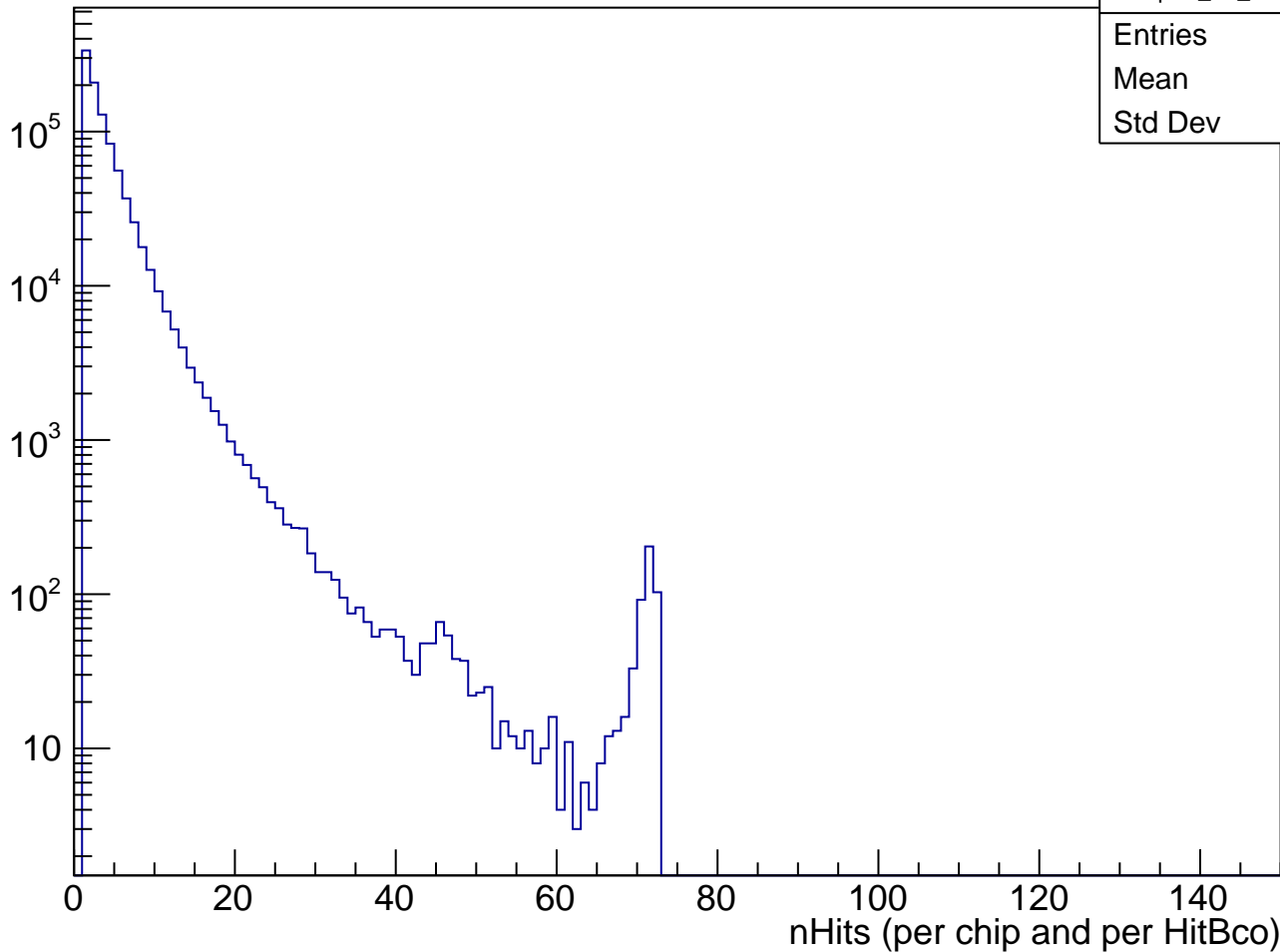


nChipHit_F3_Fch8_Chip18	
Entries	941707
Mean	3.255
Std Dev	3.732

nHits (per chip and per HitBco)

nChipHit_F3_Fch8_Chip19

Entries

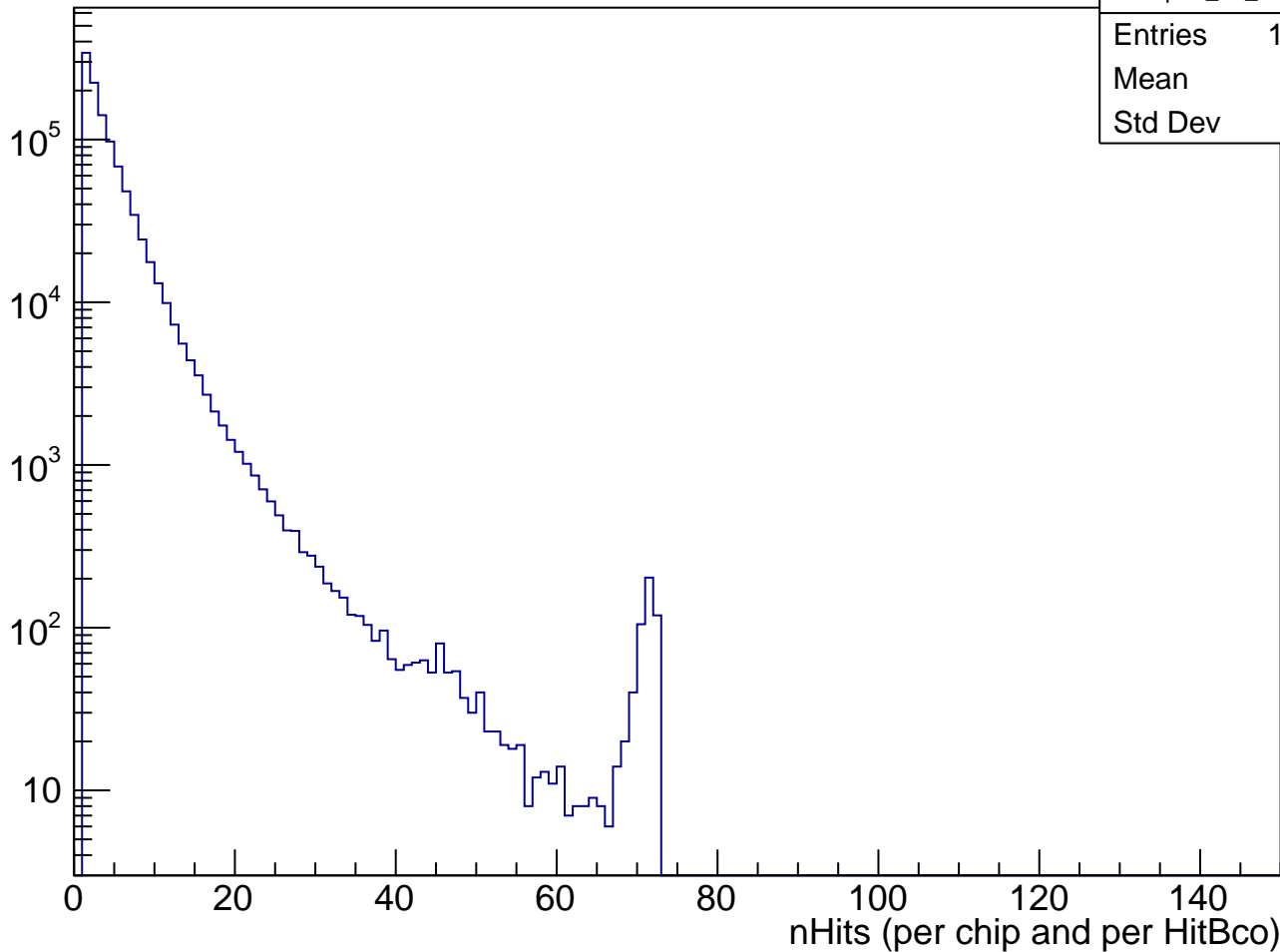


nChipHit_F3_Fch8_Chip19	
Entries	947767
Mean	3.227
Std Dev	3.71

nHits (per chip and per HitBco)

nChipHit_F3_Fch8_Chip2

Entries



nChipHit_F3_Fch8_Chip2

Entries 1056147

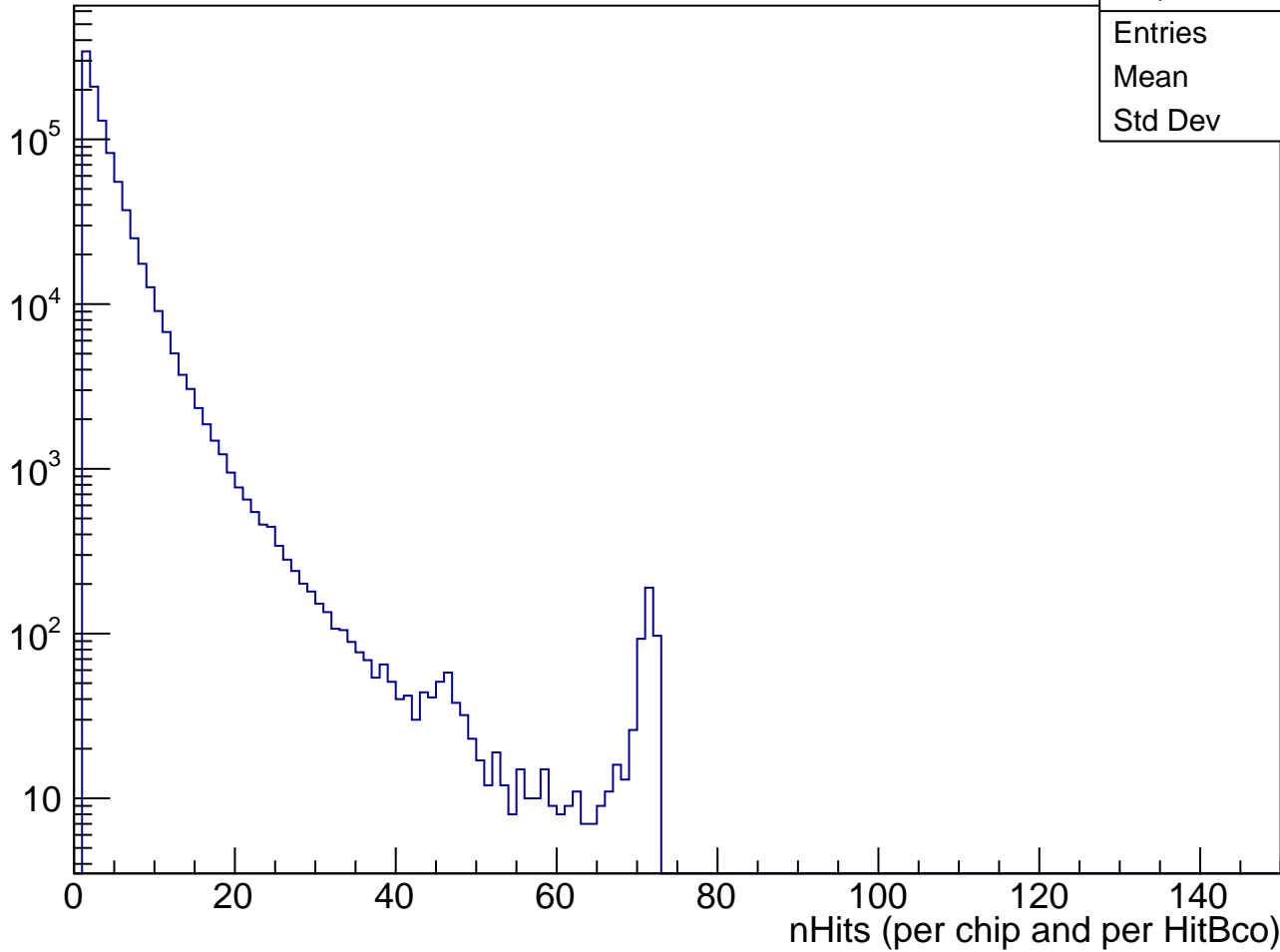
Mean 3.539

Std Dev 3.976

nHits (per chip and per HitBco)

nChipHit_F3_Fch8_Chip20

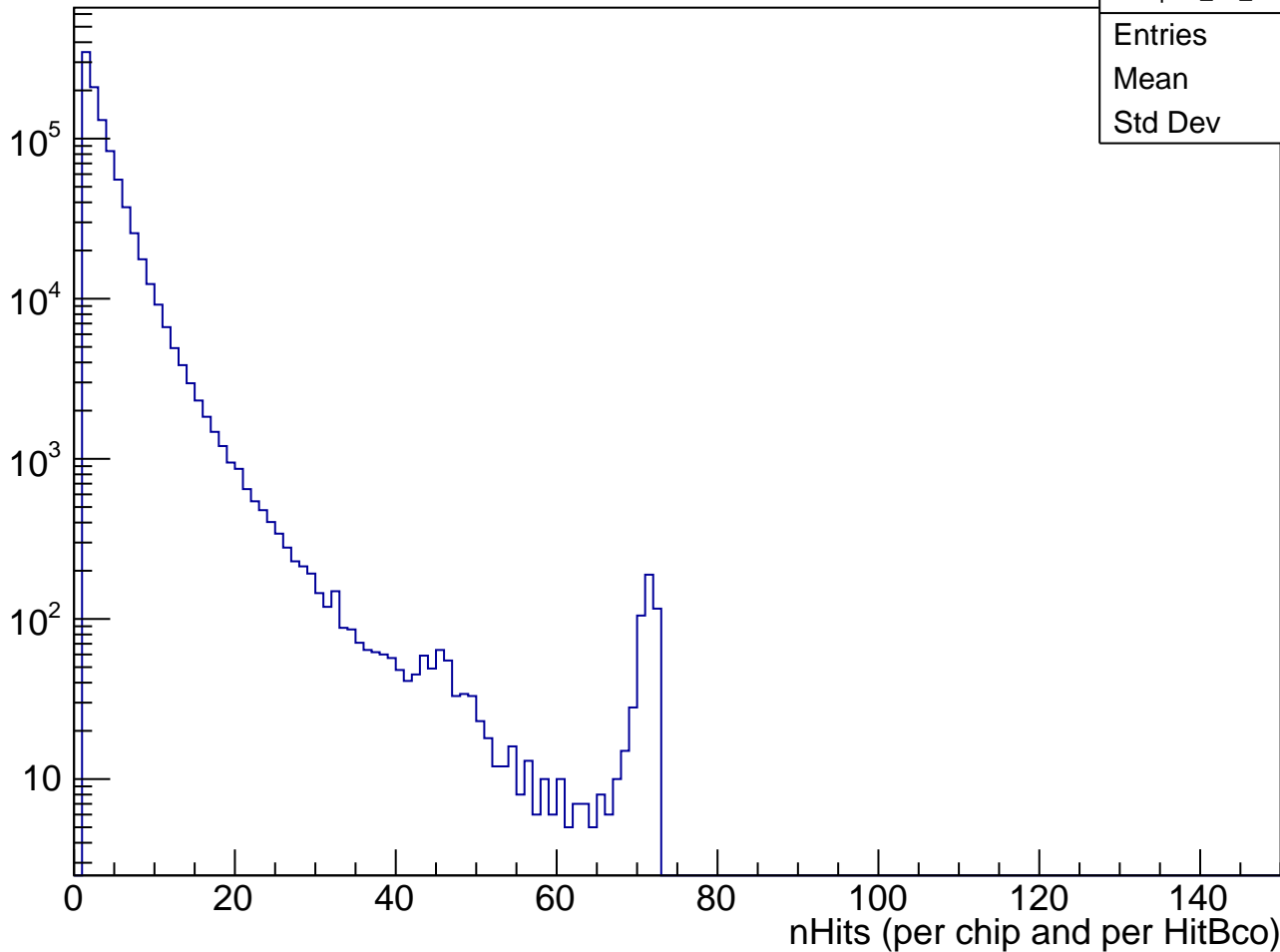
Entries



nChipHit_F3_Fch8_Chip20	
Entries	952508
Mean	3.192
Std Dev	3.659

nChipHit_F3_Fch8_Chip21

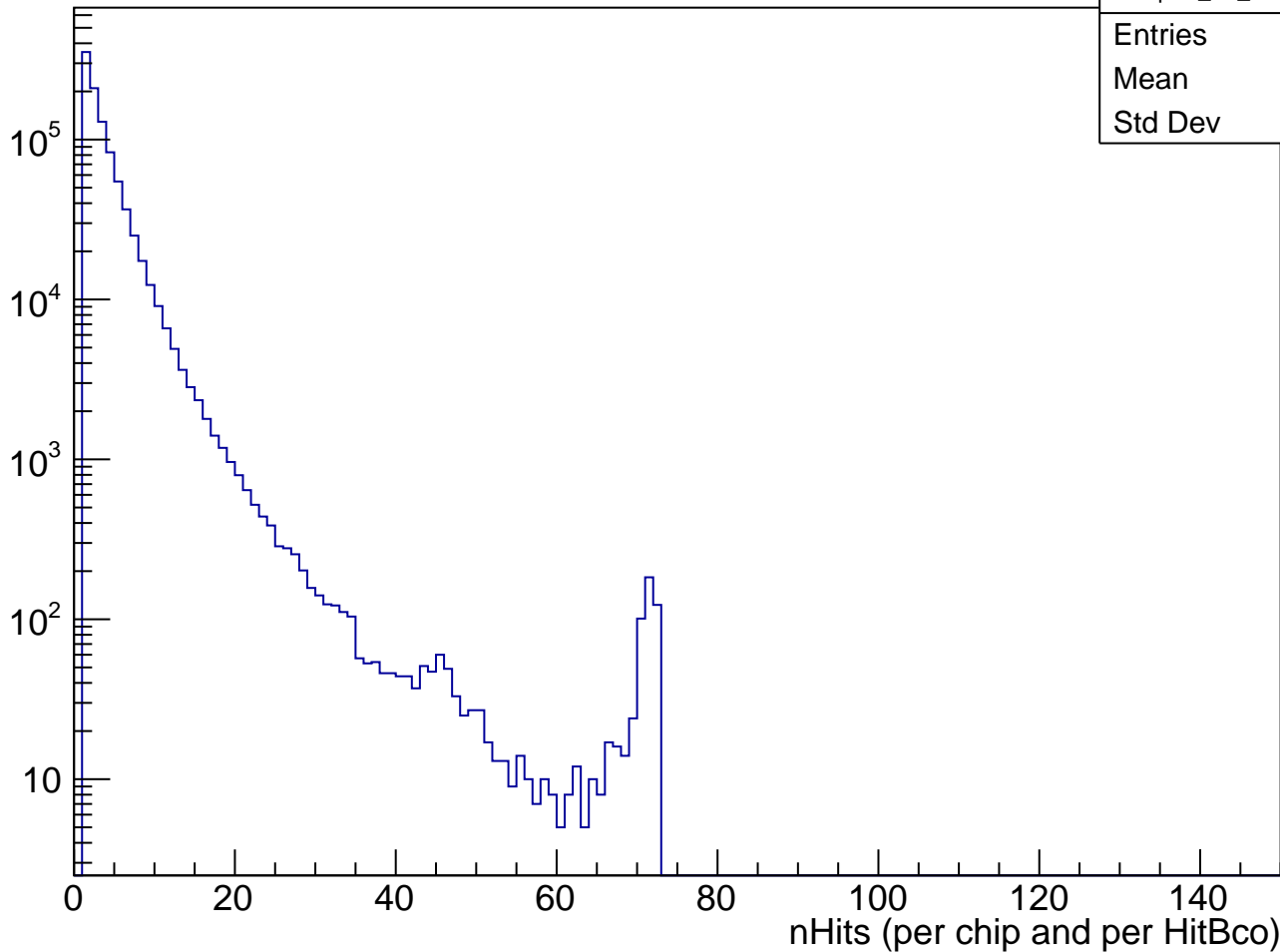
Entries



nChipHit_F3_Fch8_Chip21	
Entries	960662
Mean	3.184
Std Dev	3.671

nChipHit_F3_Fch8_Chip22

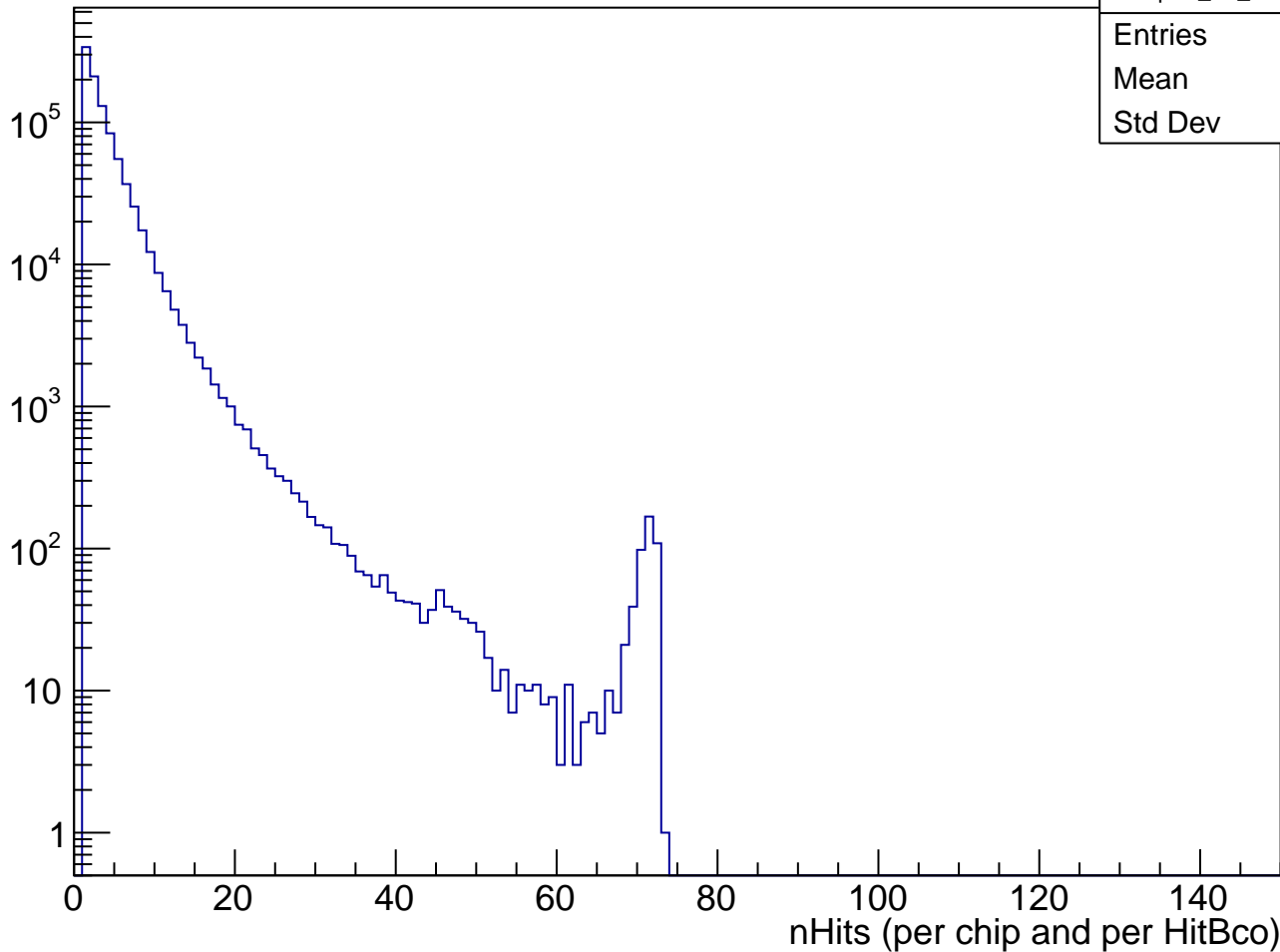
Entries



nChipHit_F3_Fch8_Chip22	
Entries	961466
Mean	3.152
Std Dev	3.639

nChipHit_F3_Fch8_Chip23

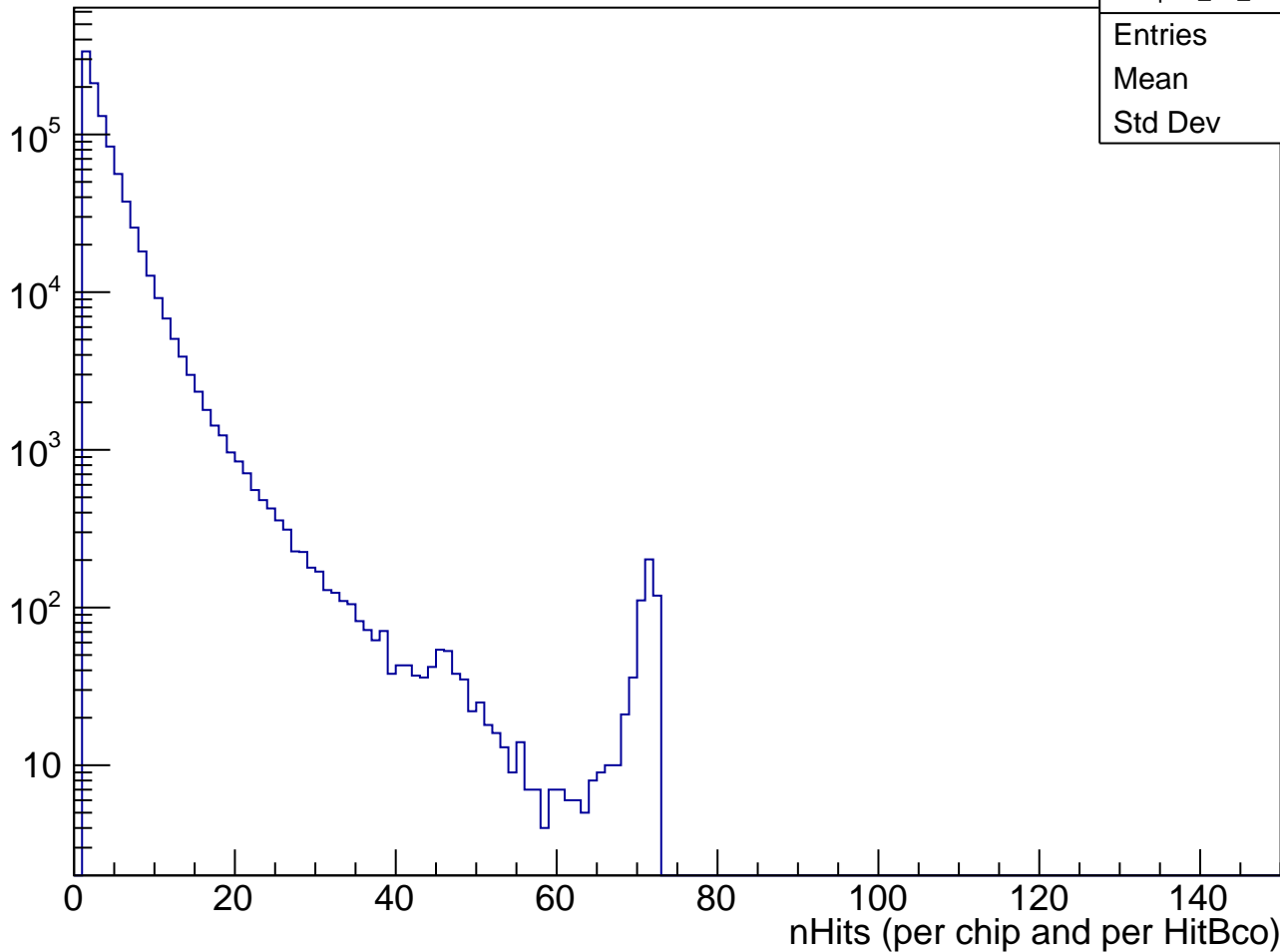
Entries



nChipHit_F3_Fch8_Chip23	
Entries	950978
Mean	3.179
Std Dev	3.624

nChipHit_F3_Fch8_Chip24

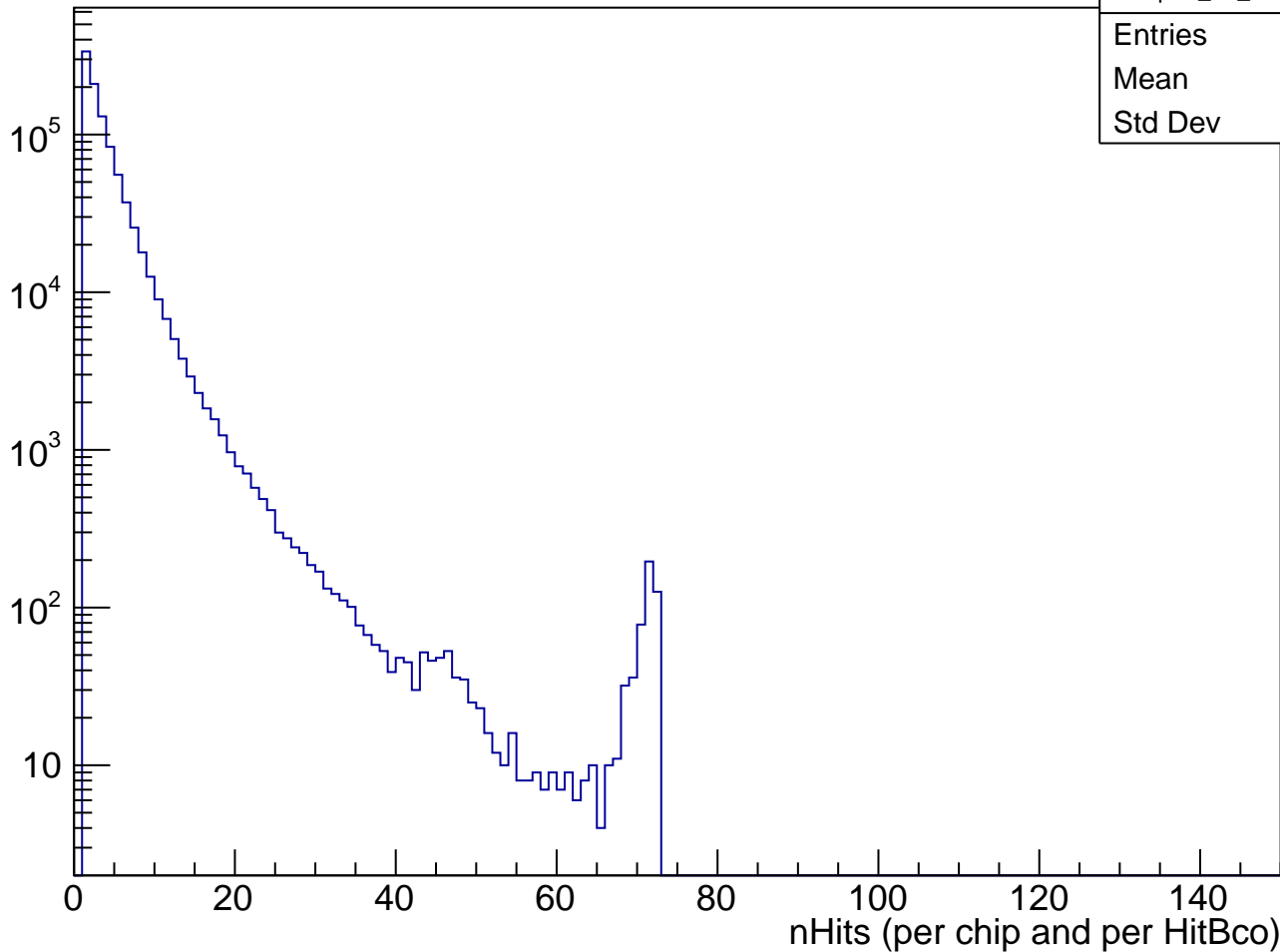
Entries



nChipHit_F3_Fch8_Chip24	
Entries	954418
Mean	3.221
Std Dev	3.707

nChipHit_F3_Fch8_Chip25

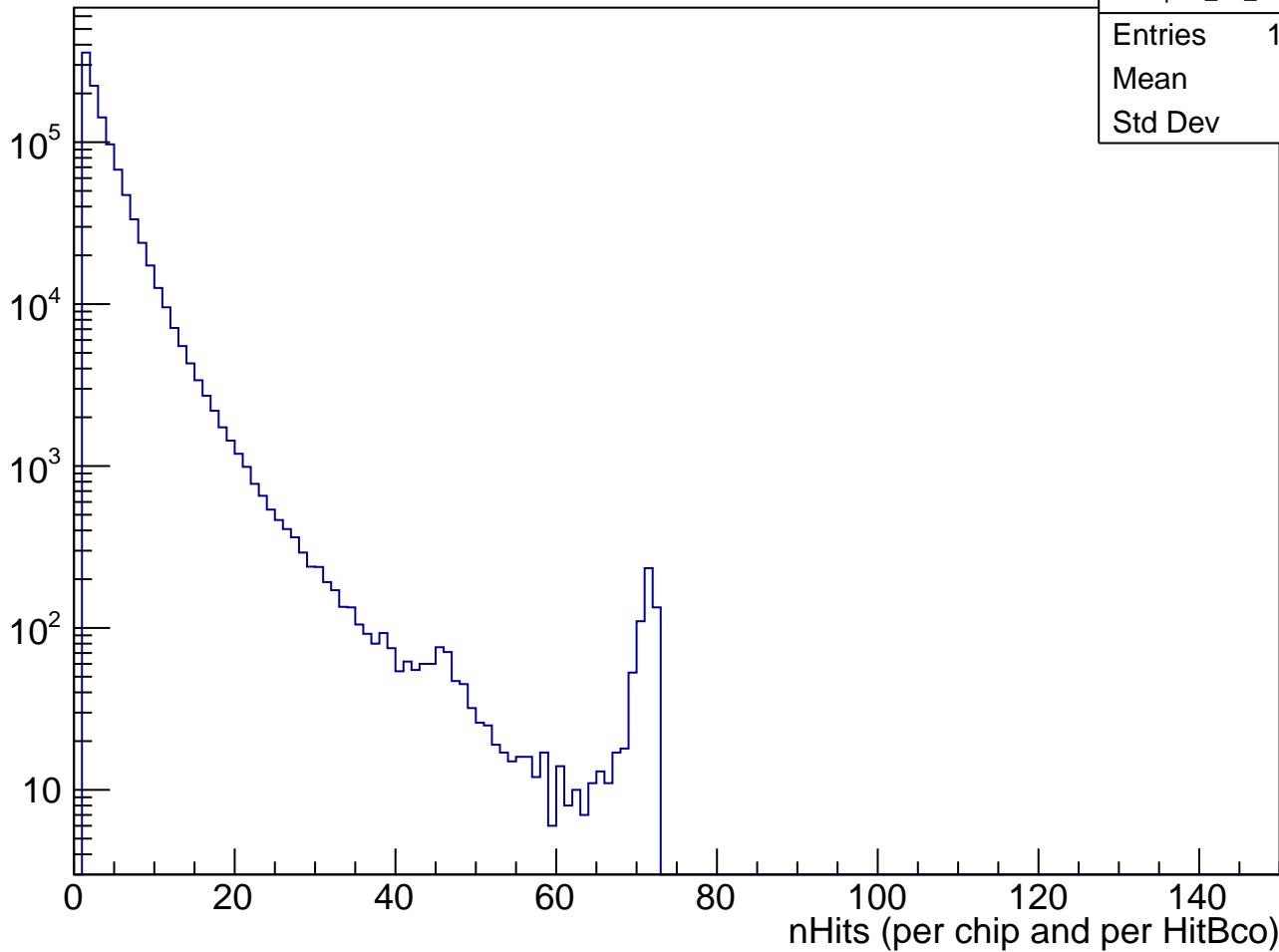
Entries



nChipHit_F3_Fch8_Chip25	
Entries	950477
Mean	3.213
Std Dev	3.691

nChipHit_F3_Fch8_Chip3

Entries



nChipHit_F3_Fch8_Chip3

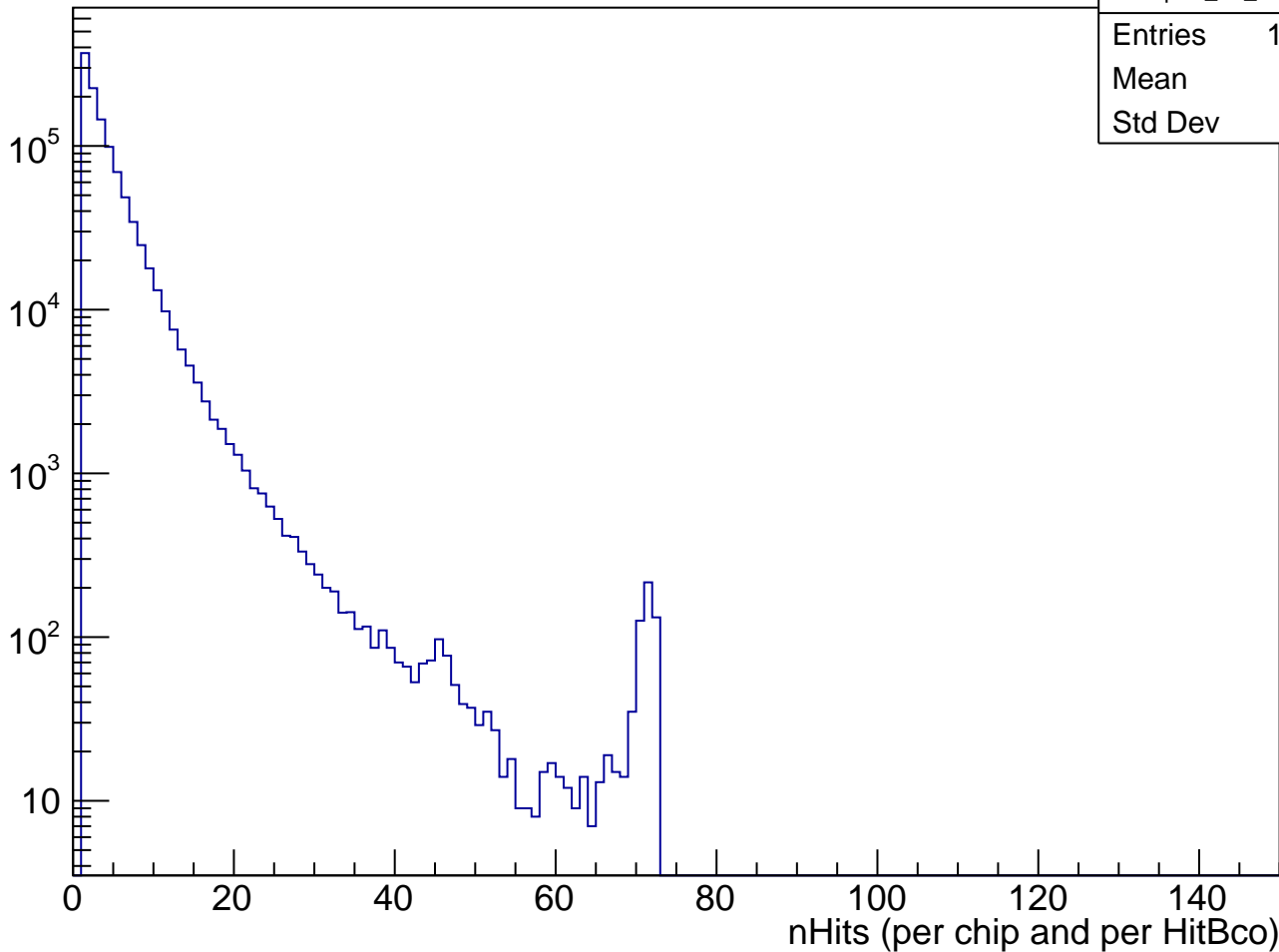
Entries 1068259

Mean 3.48

Std Dev 3.975

nChipHit_F3_Fch8_Chip4

Entries



nChipHit_F3_Fch8_Chip4

Entries 1095387

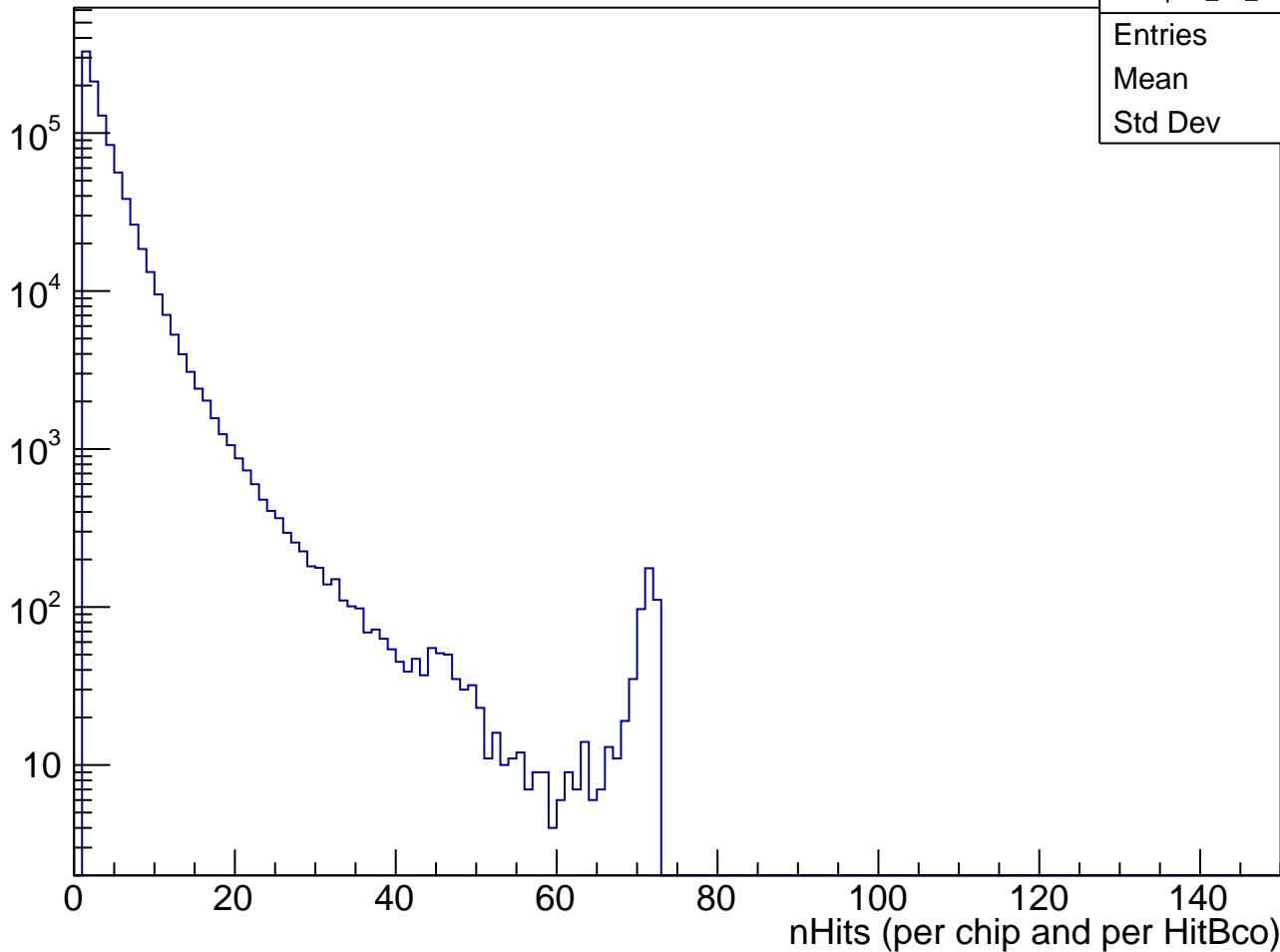
Mean 3.498

Std Dev 4.011

nHits (per chip and per HitBco)

nChipHit_F3_Fch8_Chip5

Entries



nChipHit_F3_Fch8_Chip5

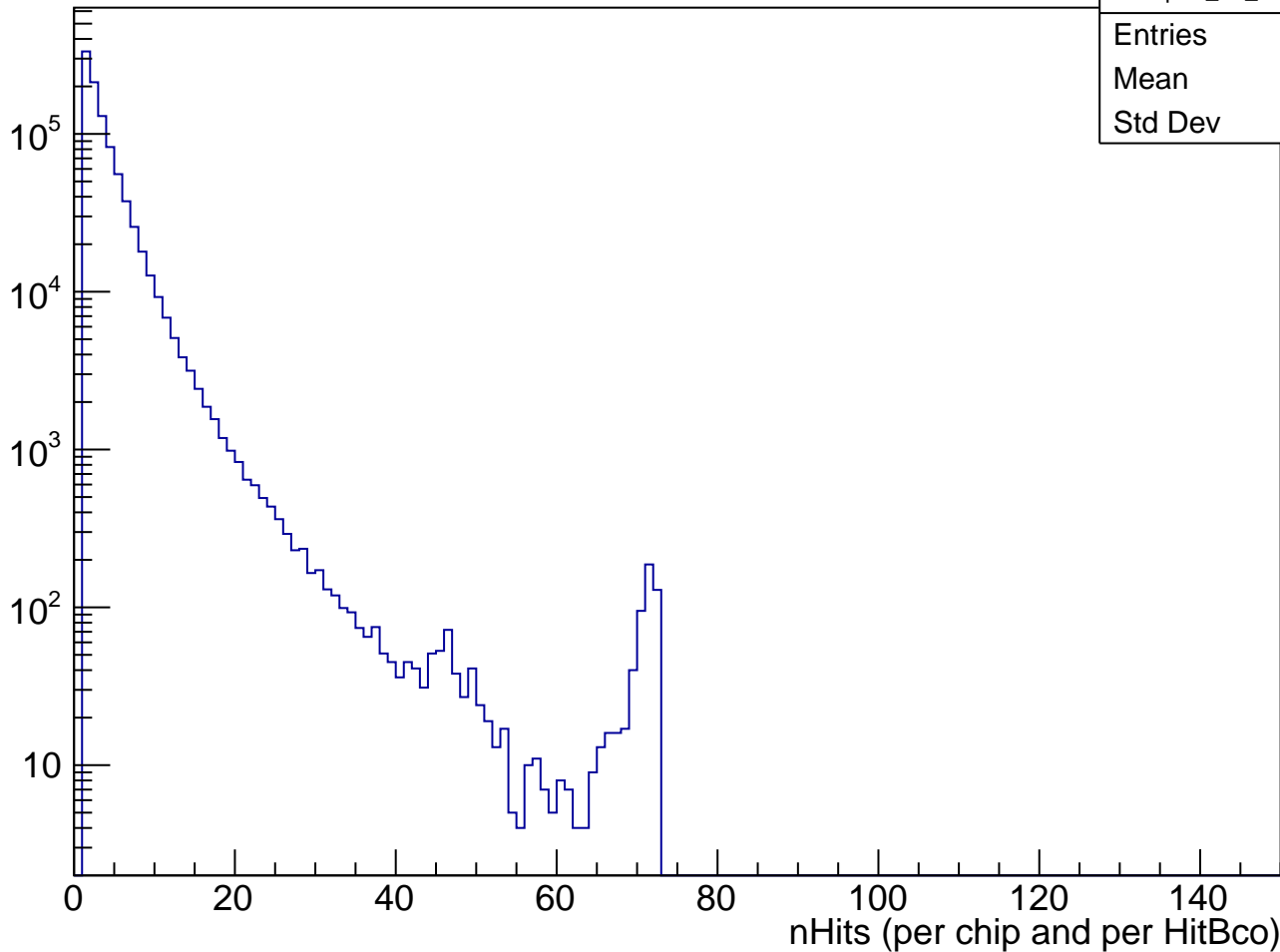
Entries 949453

Mean 3.268

Std Dev 3.729

nChipHit_F3_Fch8_Chip6

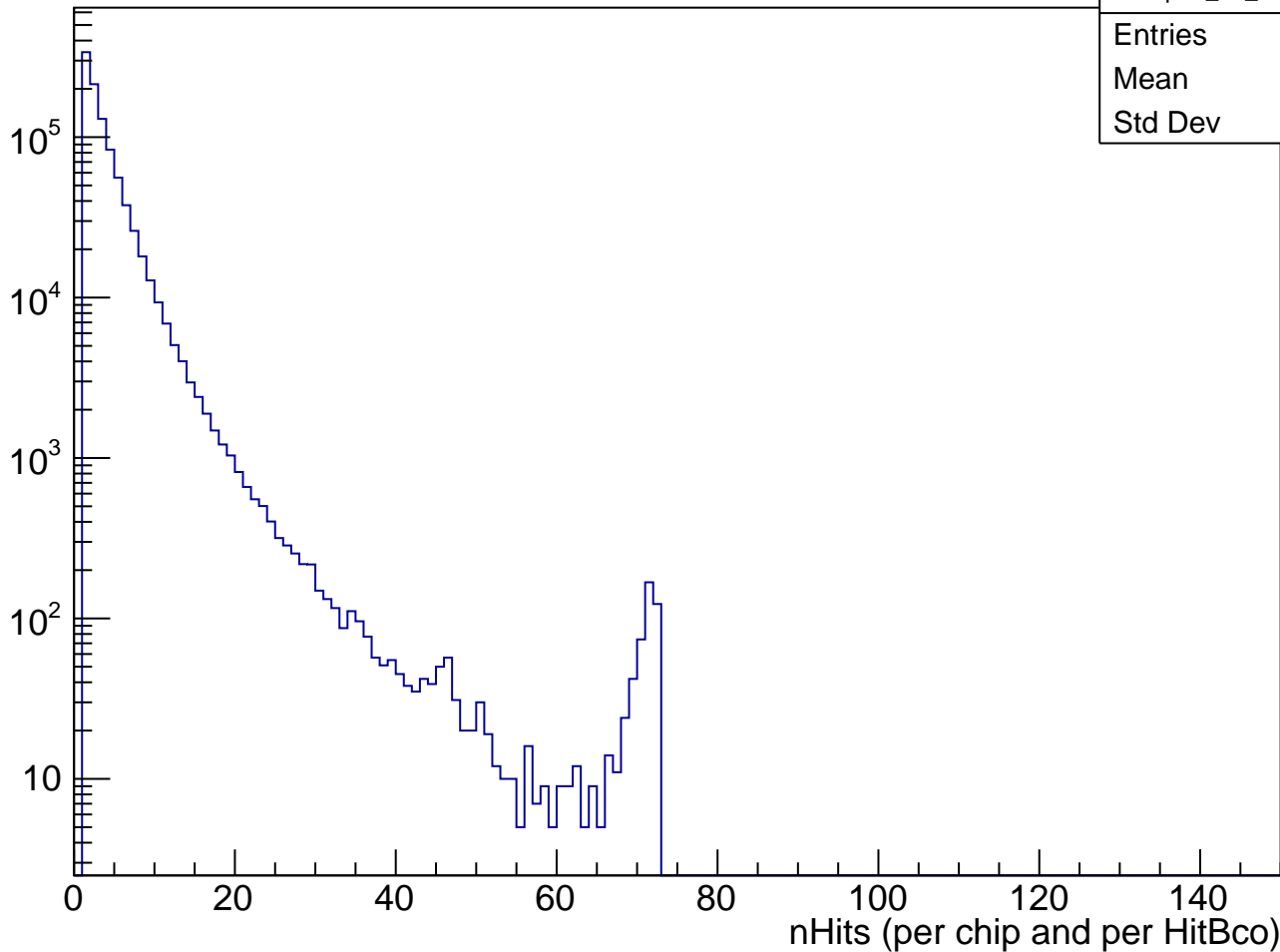
Entries



nChipHit_F3_Fch8_Chip6	
Entries	950116
Mean	3.229
Std Dev	3.718

nChipHit_F3_Fch8_Chip7

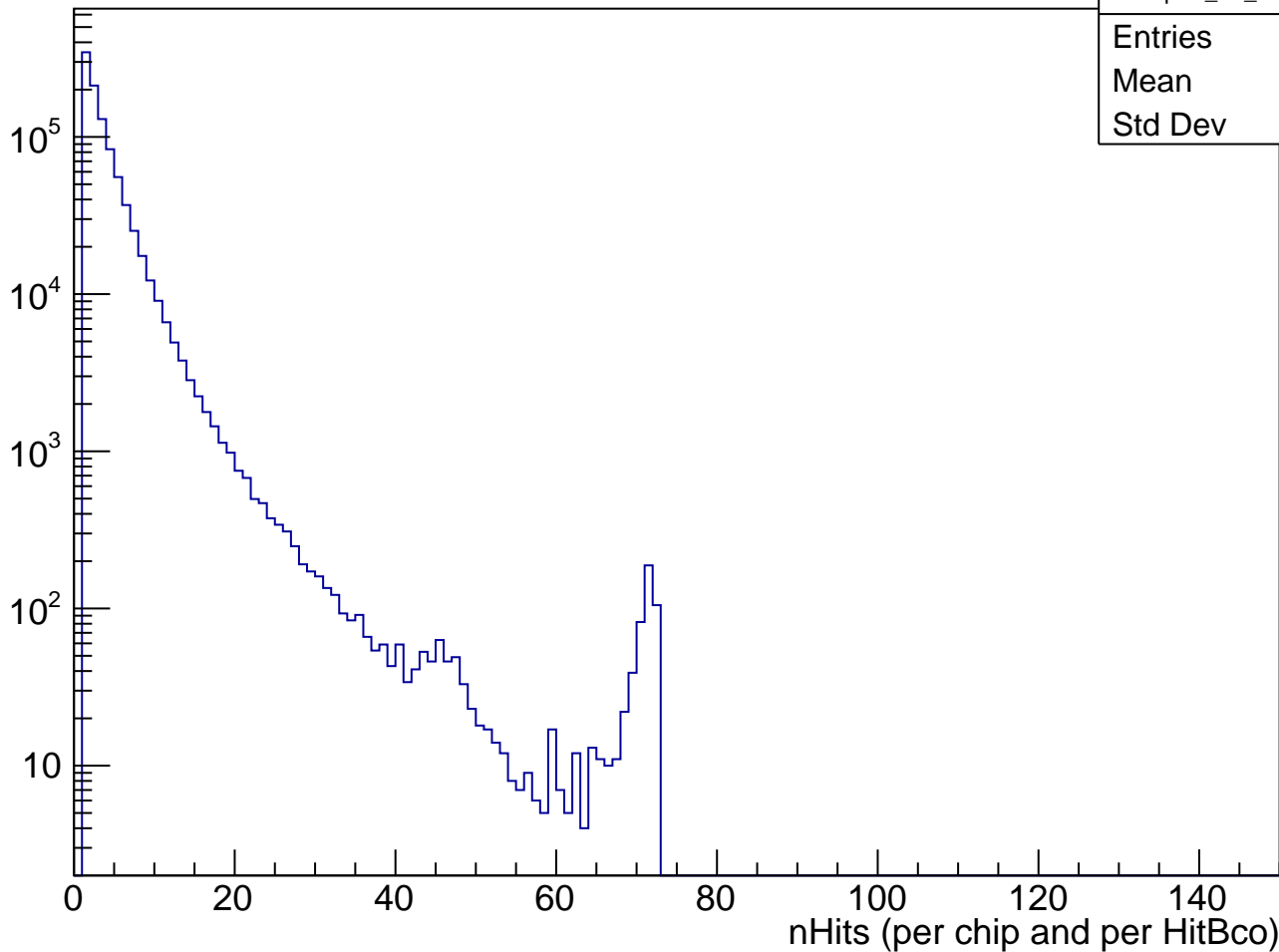
Entries



nChipHit_F3_Fch8_Chip7	
Entries	958886
Mean	3.214
Std Dev	3.668

nChipHit_F3_Fch8_Chip8

Entries



nChipHit_F3_Fch8_Chip8

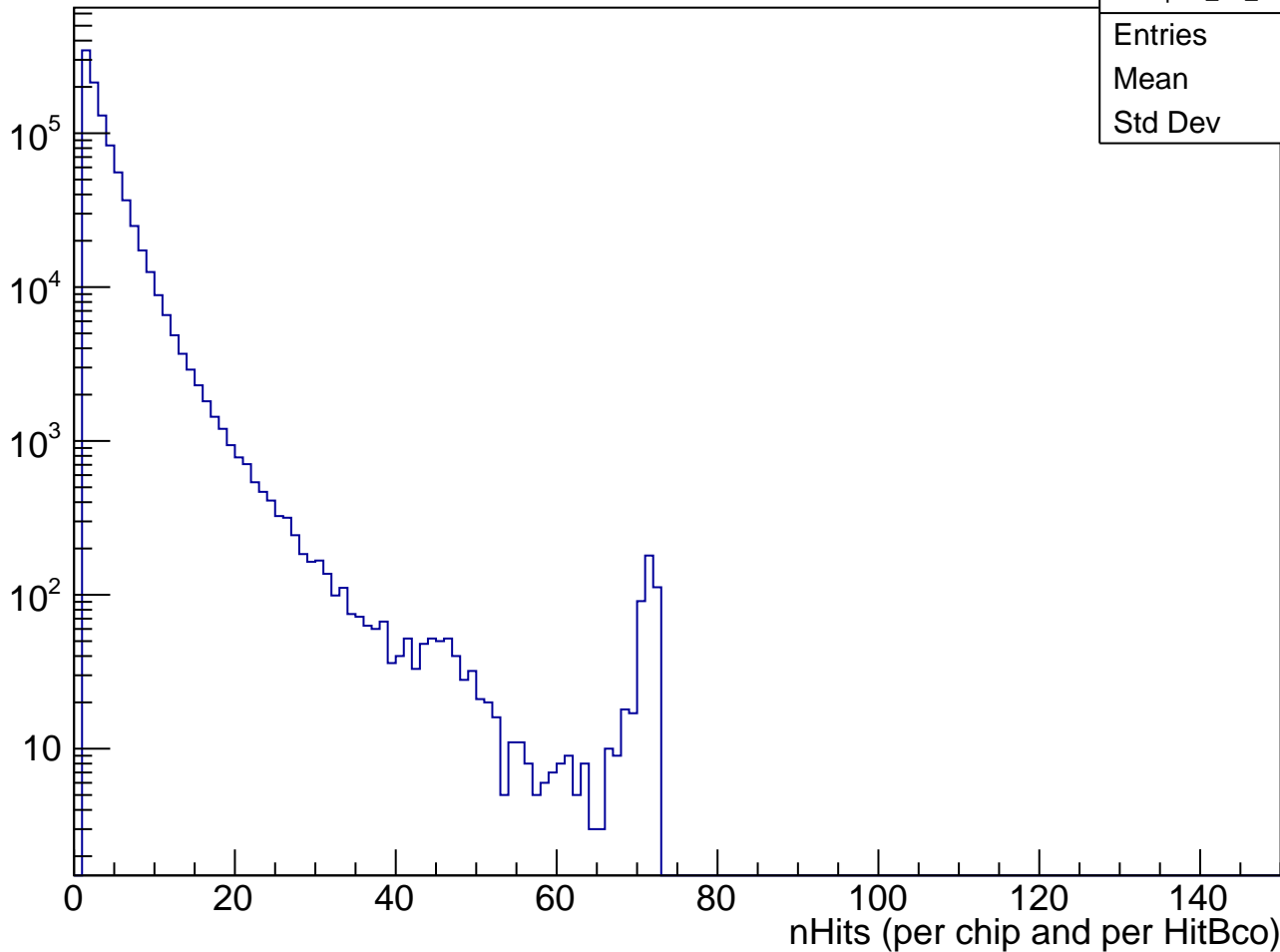
Entries 959521

Mean 3.17

Std Dev 3.643

nChipHit_F3_Fch8_Chip9

Entries



nChipHit_F3_Fch8_Chip9

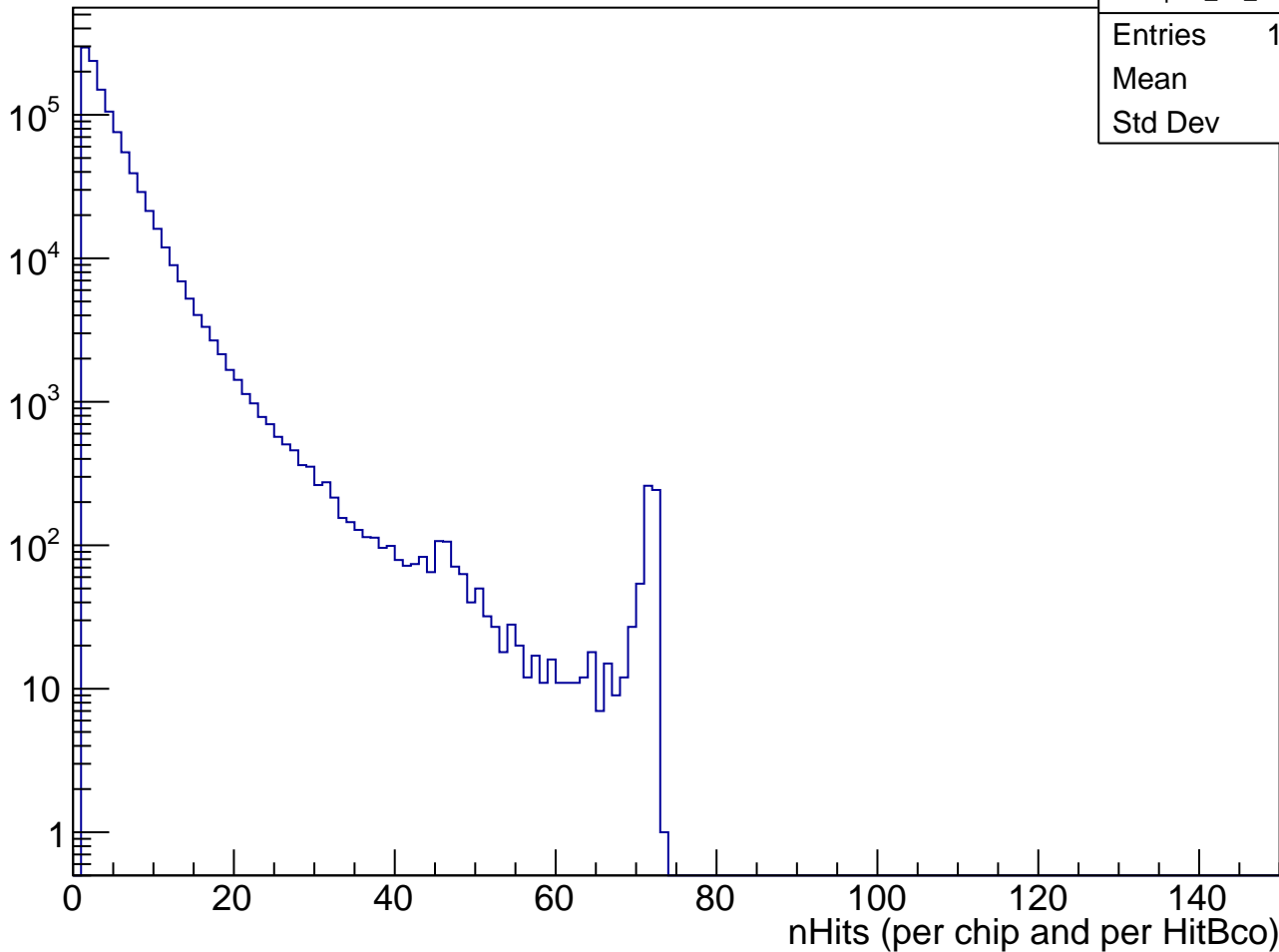
Entries 960862

Mean 3.164

Std Dev 3.615

nChipHit_F3_Fch9_Chip0

Entries



nChipHit_F3_Fch9_Chip0

Entries 1080927

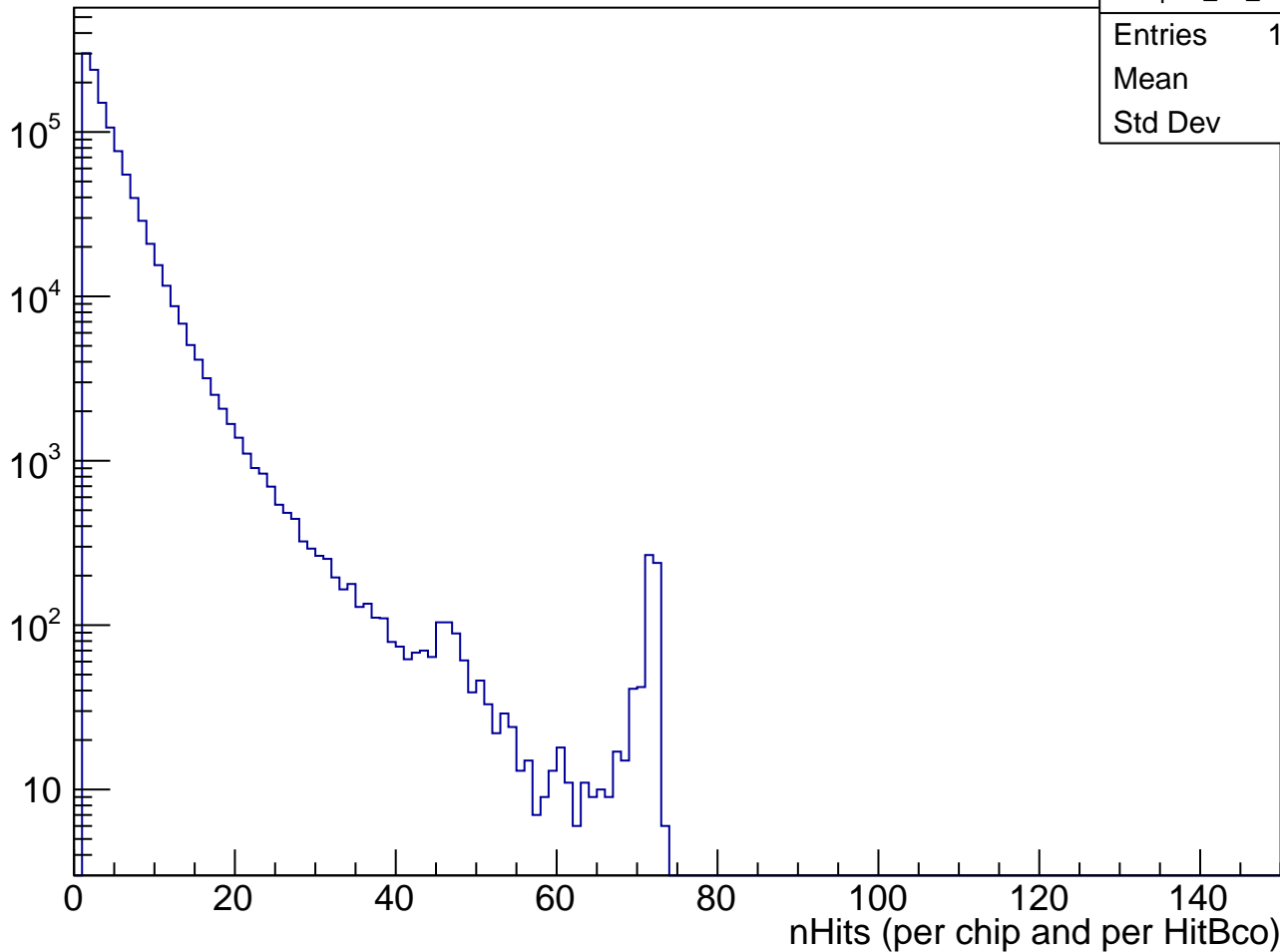
Mean 3.844

Std Dev 4.234

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip1

Entries

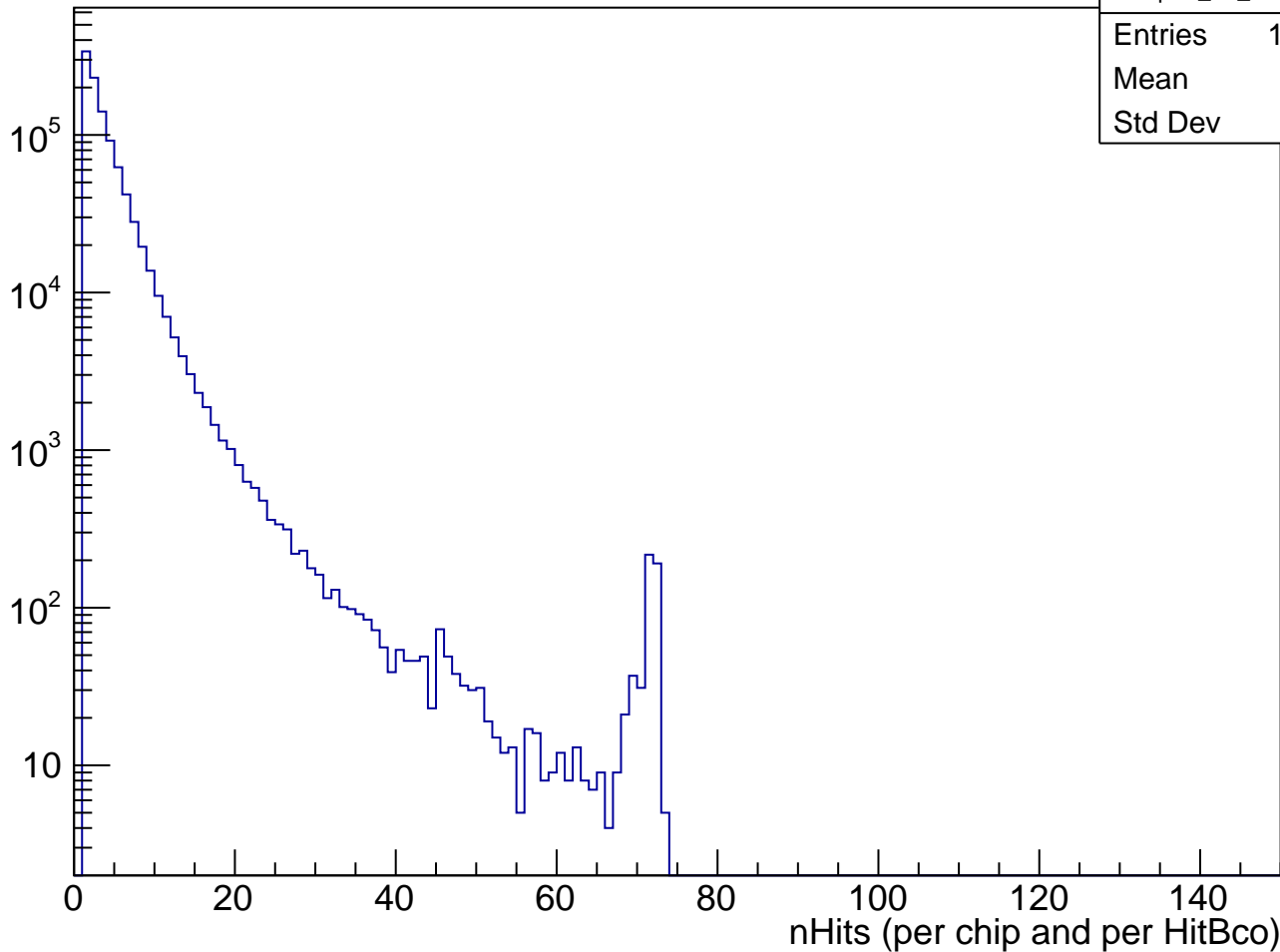


nChipHit_F3_Fch9_Chip1	
Entries	1089715
Mean	3.805
Std Dev	4.192

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip10

Entries

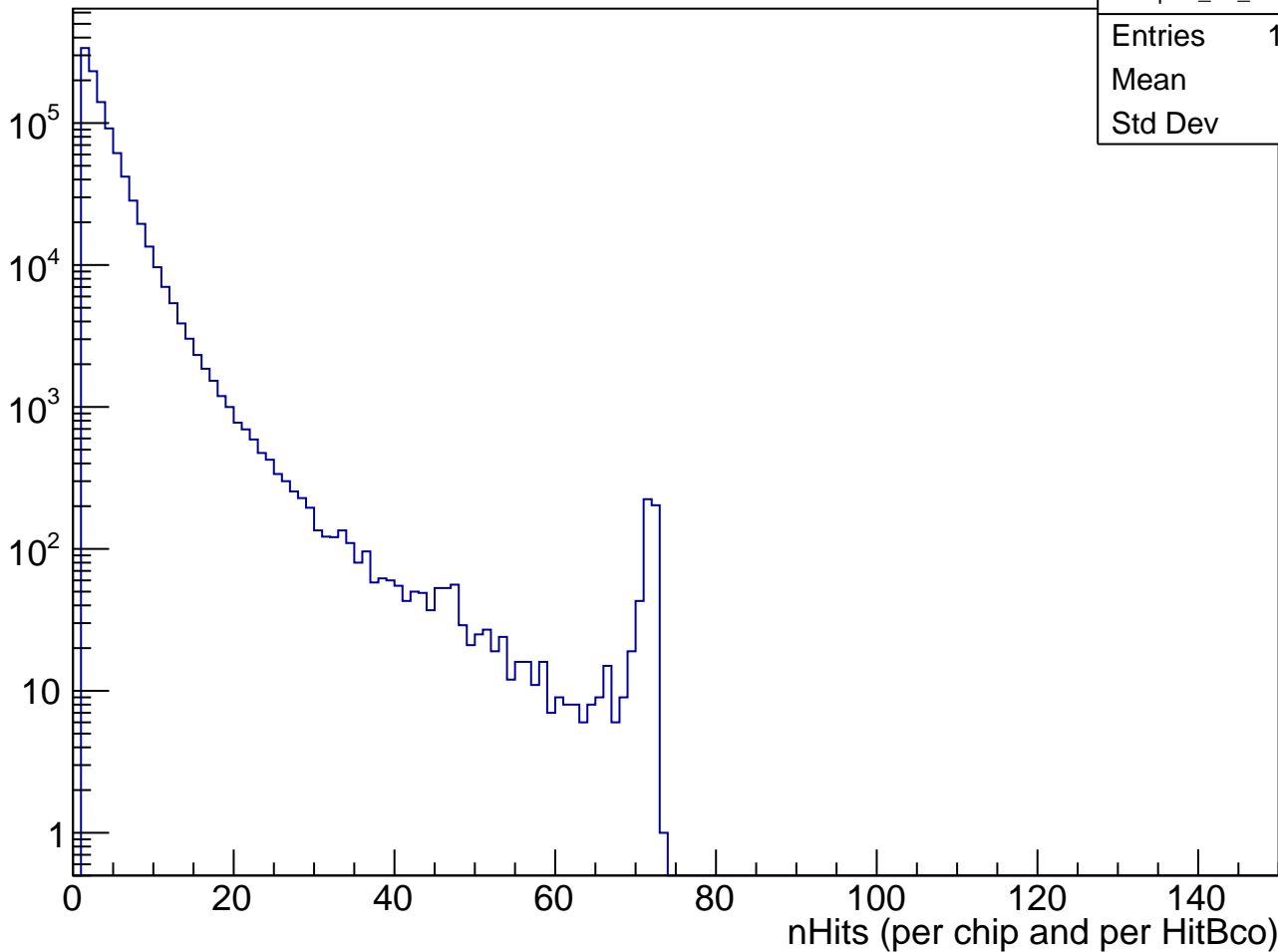


nChipHit_F3_Fch9_Chip10	
Entries	1011135
Mean	3.248
Std Dev	3.658

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip11

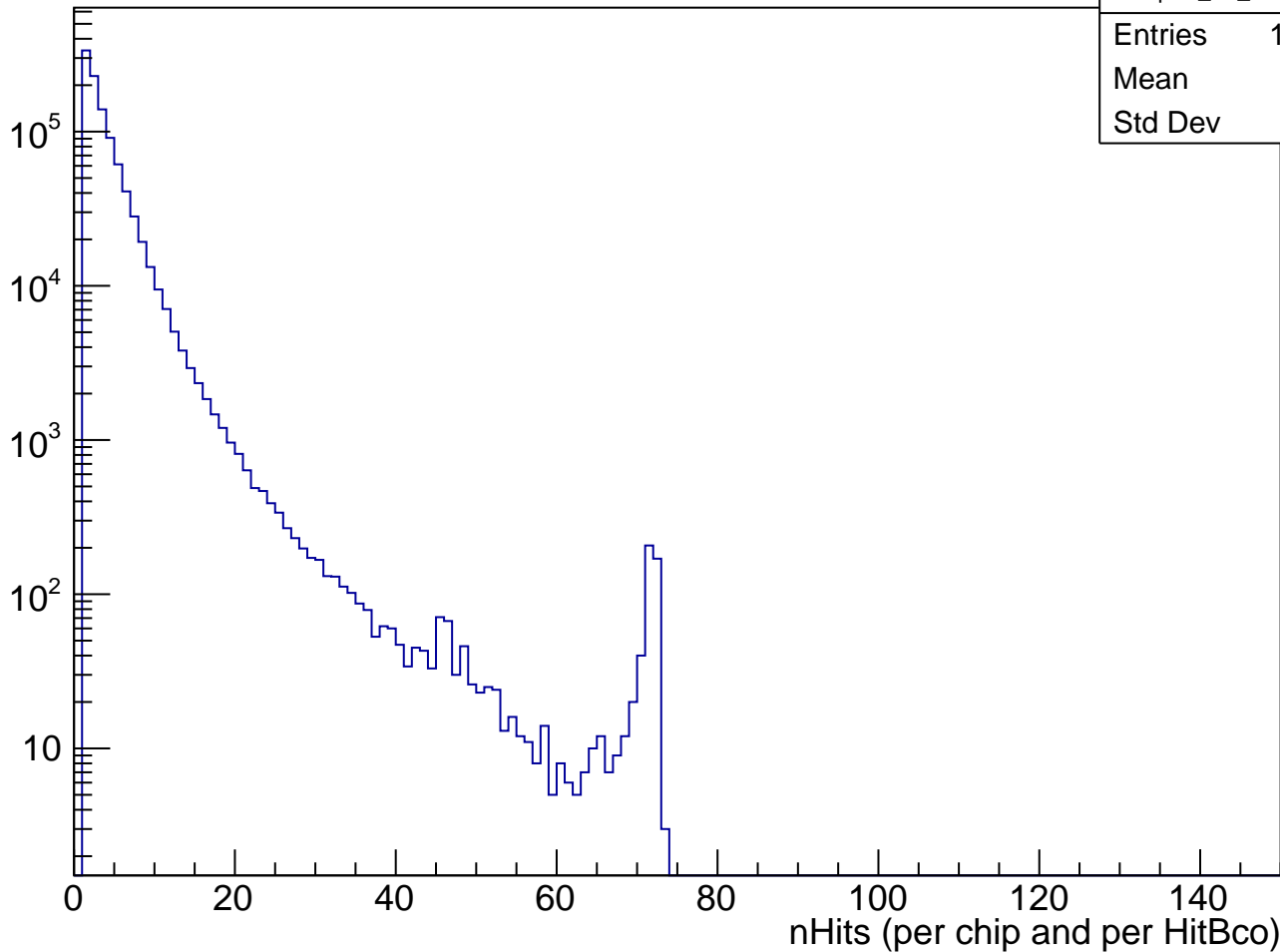
Entries



nChipHit_F3_Fch9_Chip11	
Entries	1009790
Mean	3.256
Std Dev	3.688

nChipHit_F3_Fch9_Chip12

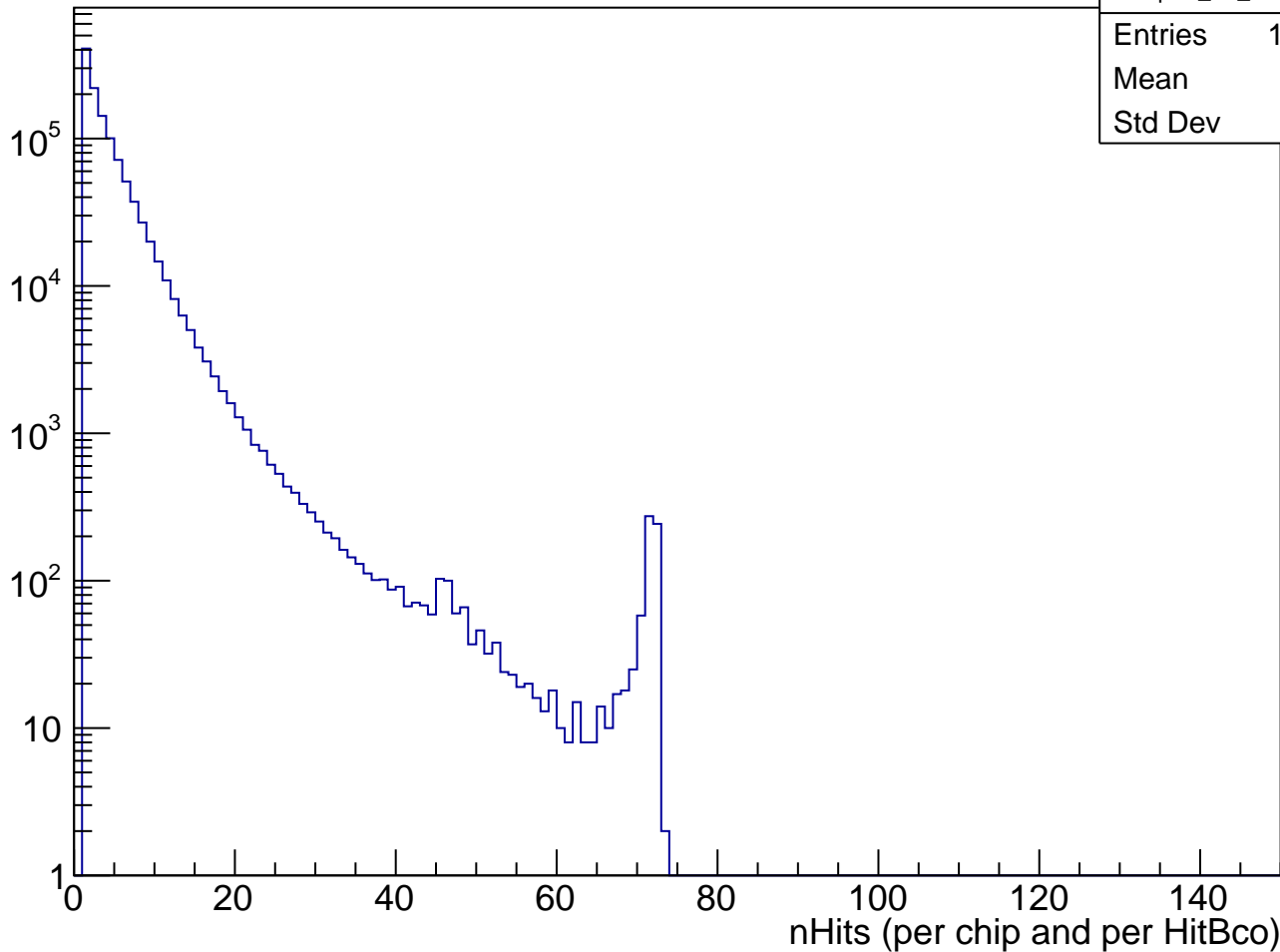
Entries



nChipHit_F3_Fch9_Chip12	
Entries	1001050
Mean	3.239
Std Dev	3.631

nChipHit_F3_Fch9_Chip13

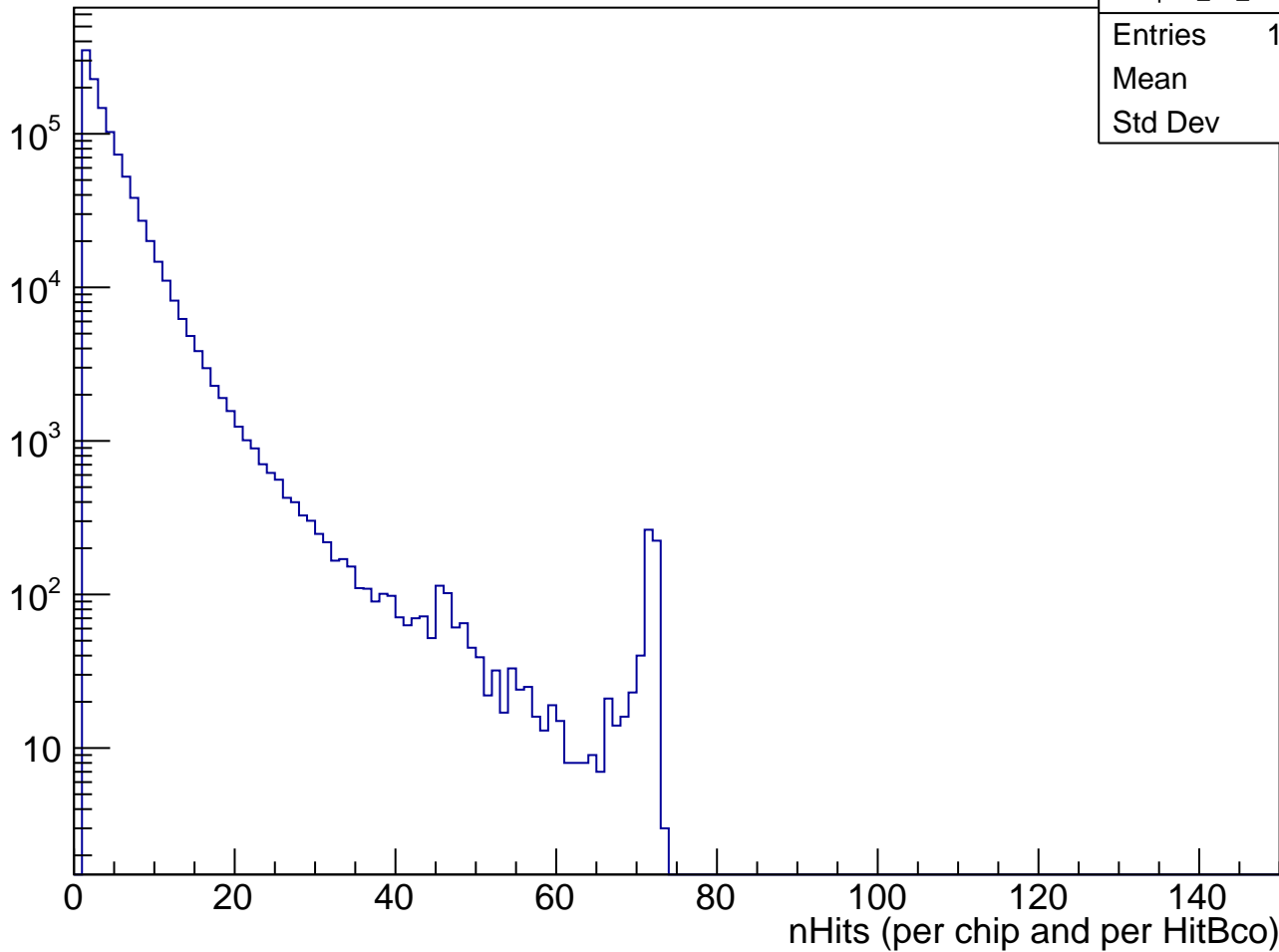
Entries



nChipHit_F3_Fch9_Chip13	
Entries	1145385
Mean	3.513
Std Dev	4.103

nChipHit_F3_Fch9_Chip14

Entries



nChipHit_F3_Fch9_Chip14

Entries 1105709

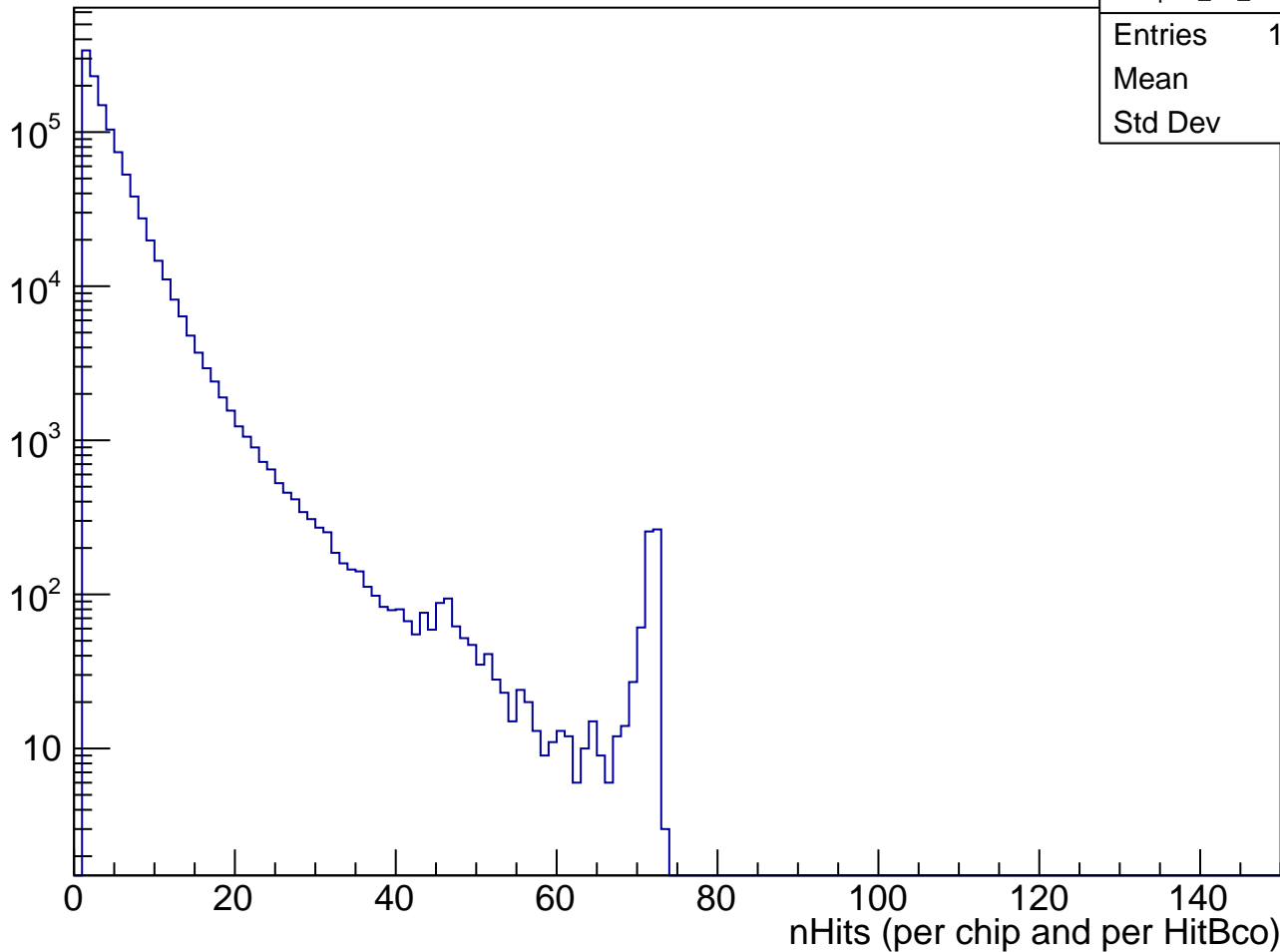
Mean 3.634

Std Dev 4.102

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip15

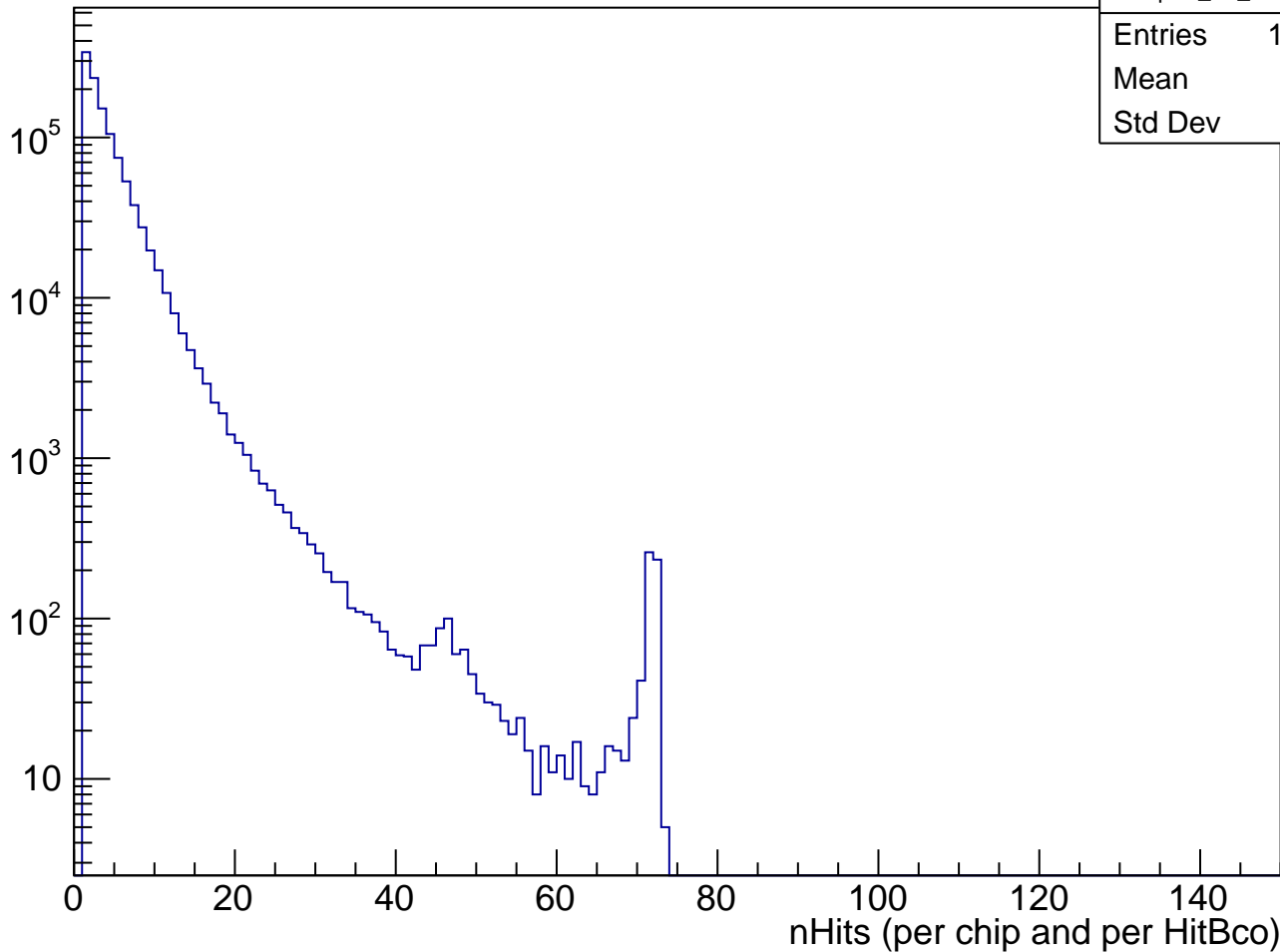
Entries



nChipHit_F3_Fch9_Chip15	
Entries	1102626
Mean	3.661
Std Dev	4.121

nChipHit_F3_Fch9_Chip16

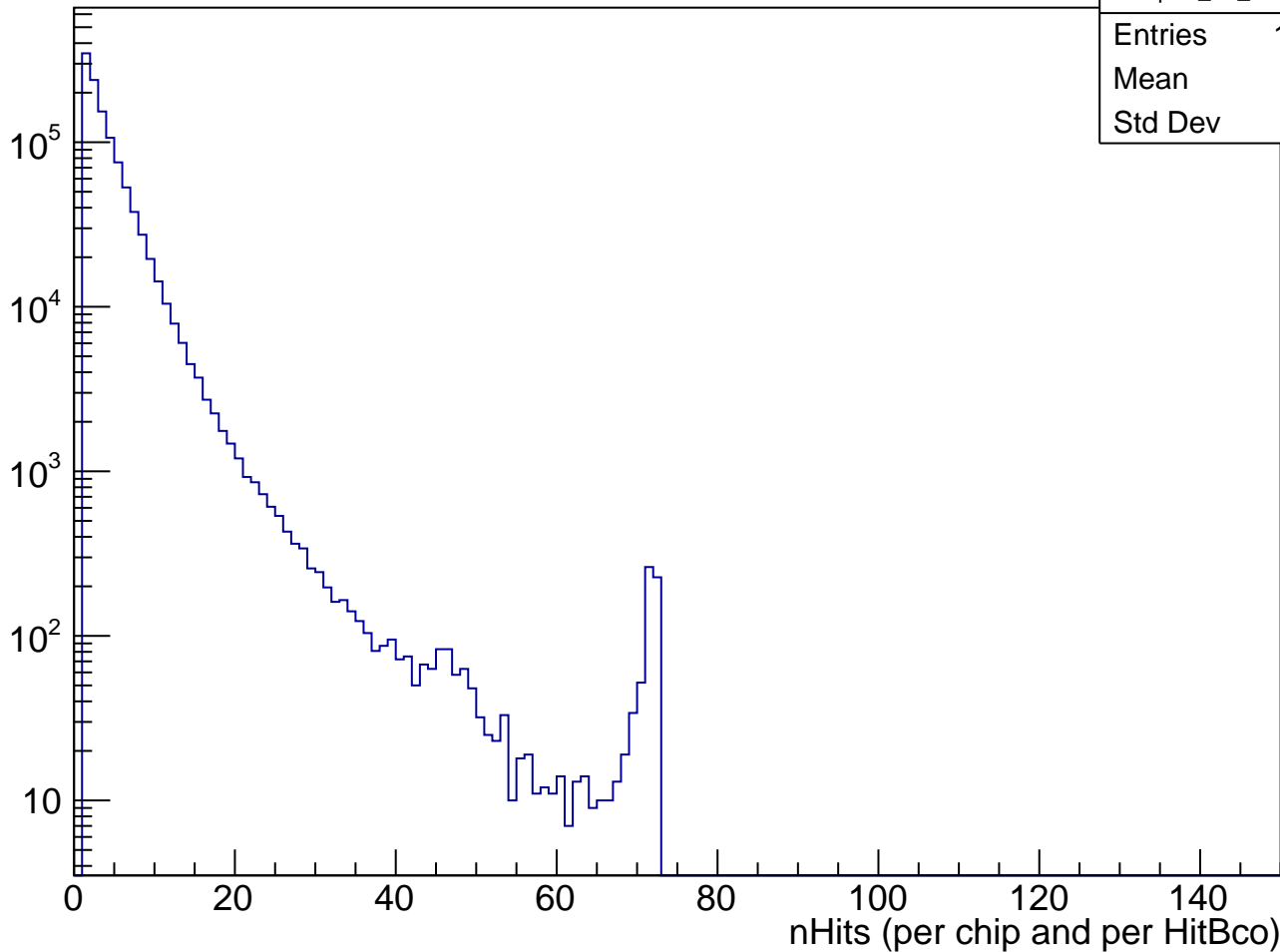
Entries



nChipHit_F3_Fch9_Chip16	
Entries	1110802
Mean	3.628
Std Dev	4.045

nChipHit_F3_Fch9_Chip17

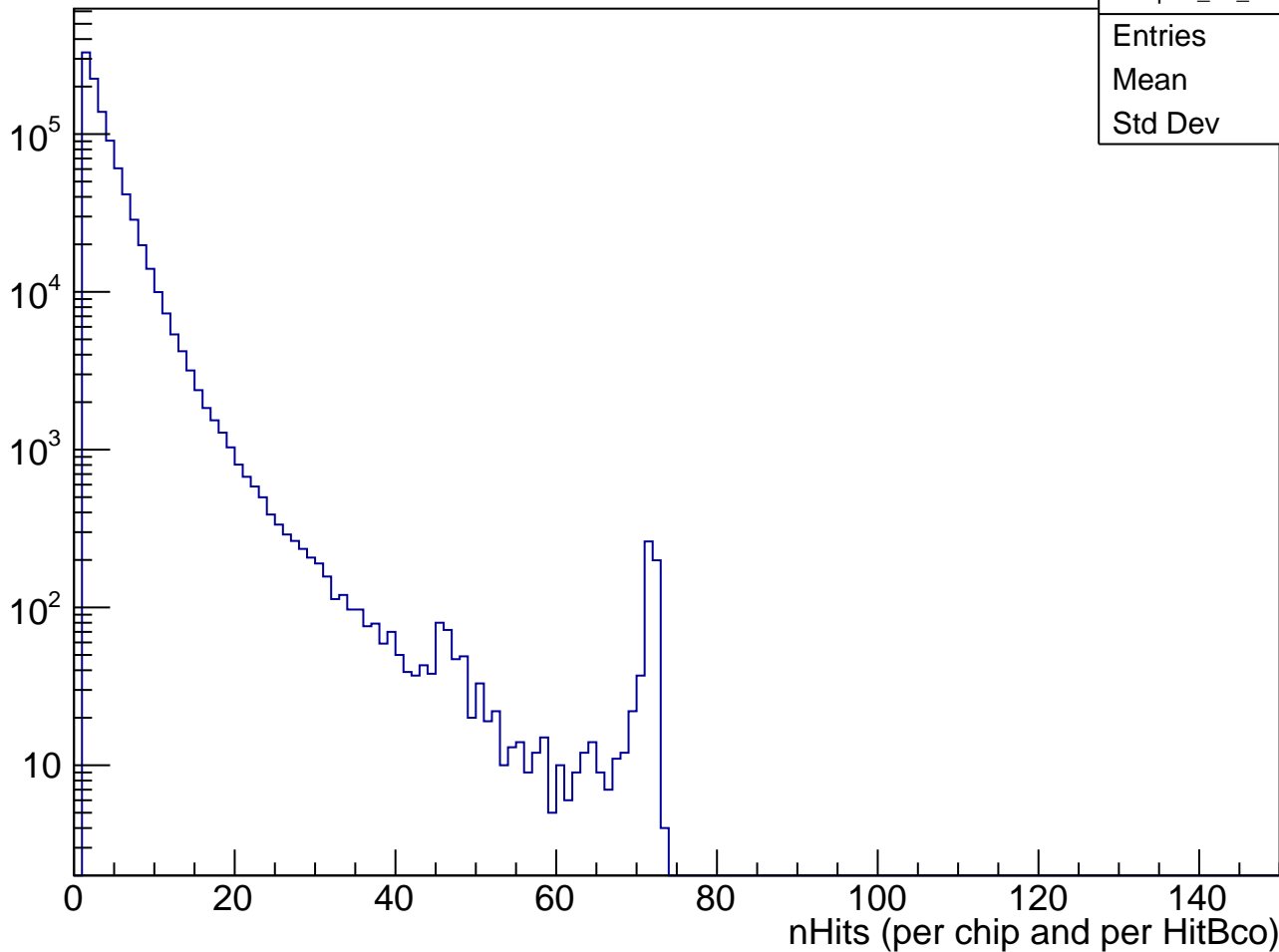
Entries



nChipHit_F3_Fch9_Chip17	
Entries	1123461
Mean	3.593
Std Dev	4.014

nChipHit_F3_Fch9_Chip18

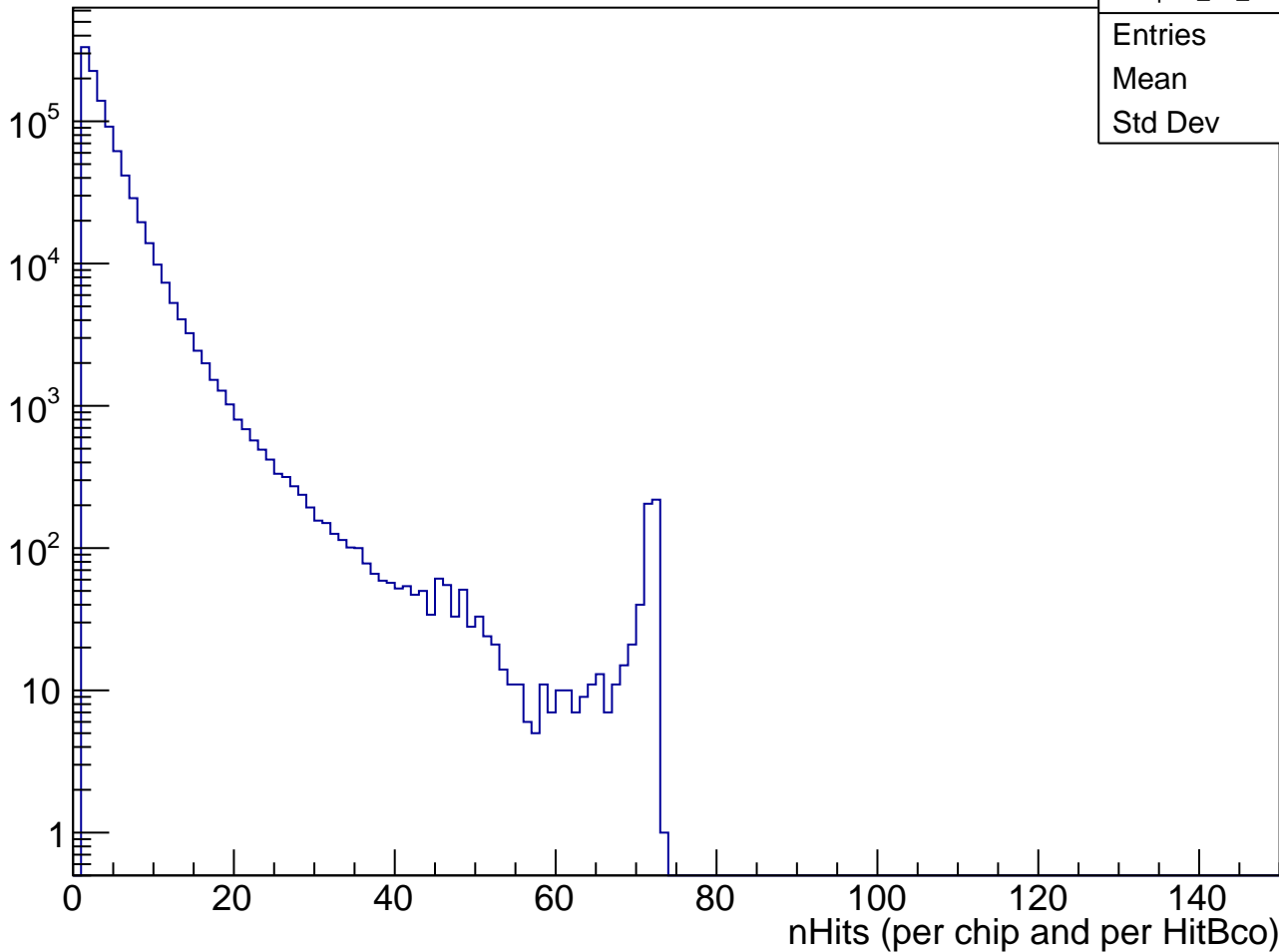
Entries



nChipHit_F3_Fch9_Chip18	
Entries	991994
Mean	3.305
Std Dev	3.768

nChipHit_F3_Fch9_Chip19

Entries

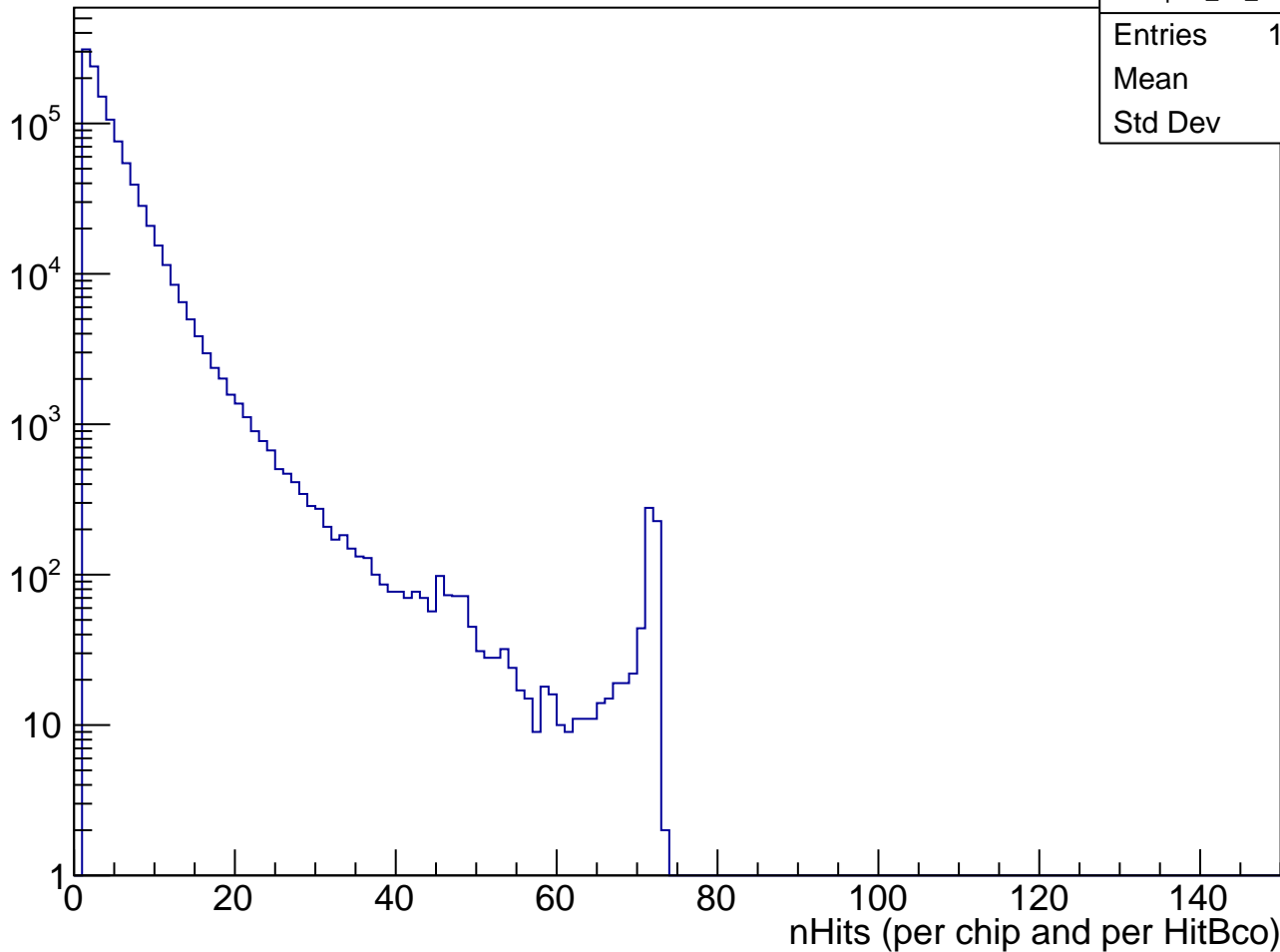


nChipHit_F3_Fch9_Chip19	
Entries	999266
Mean	3.293
Std Dev	3.725

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip2

Entries



nChipHit_F3_Fch9_Chip2

Entries 1095207

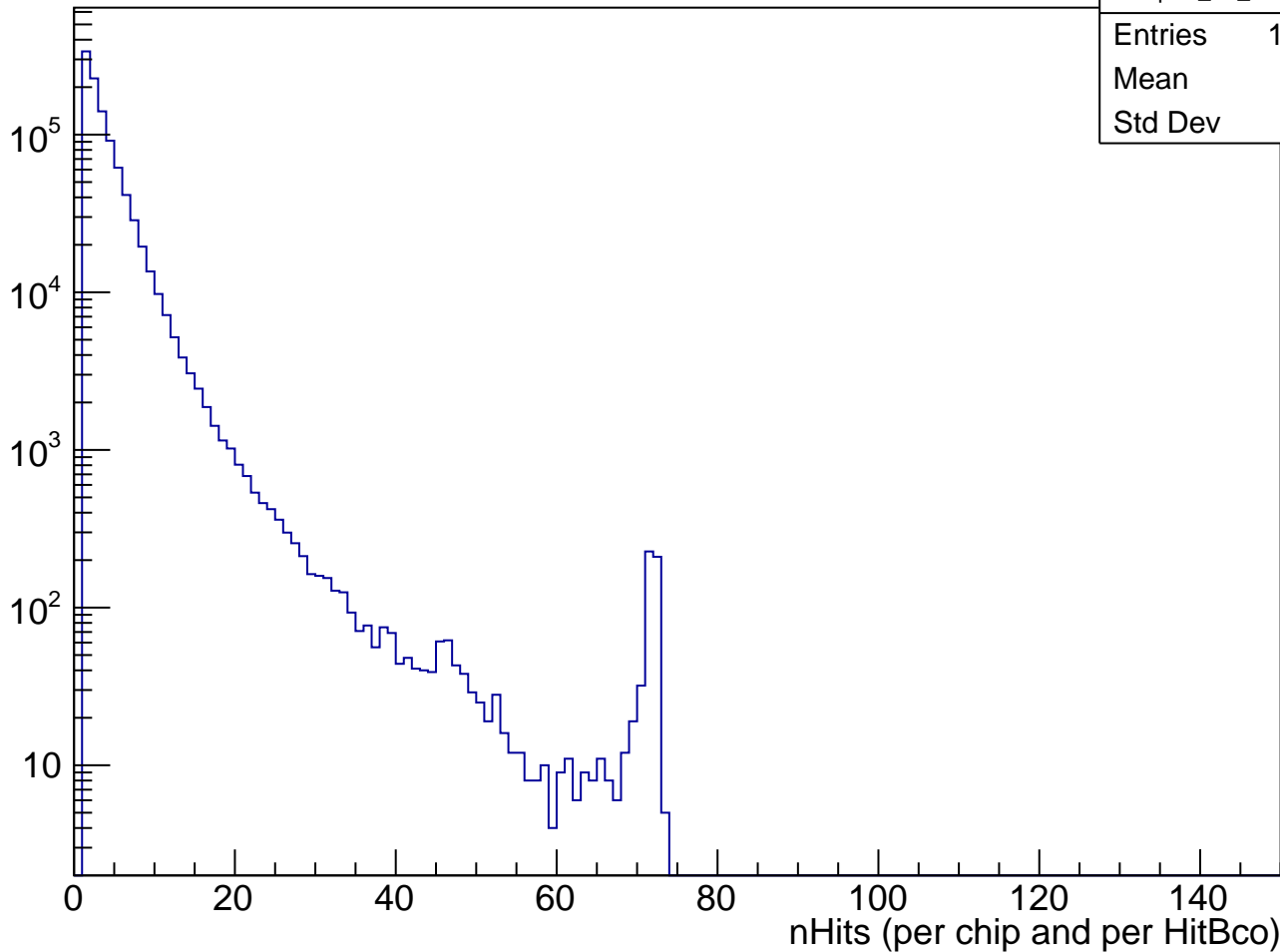
Mean 3.751

Std Dev 4.142

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip20

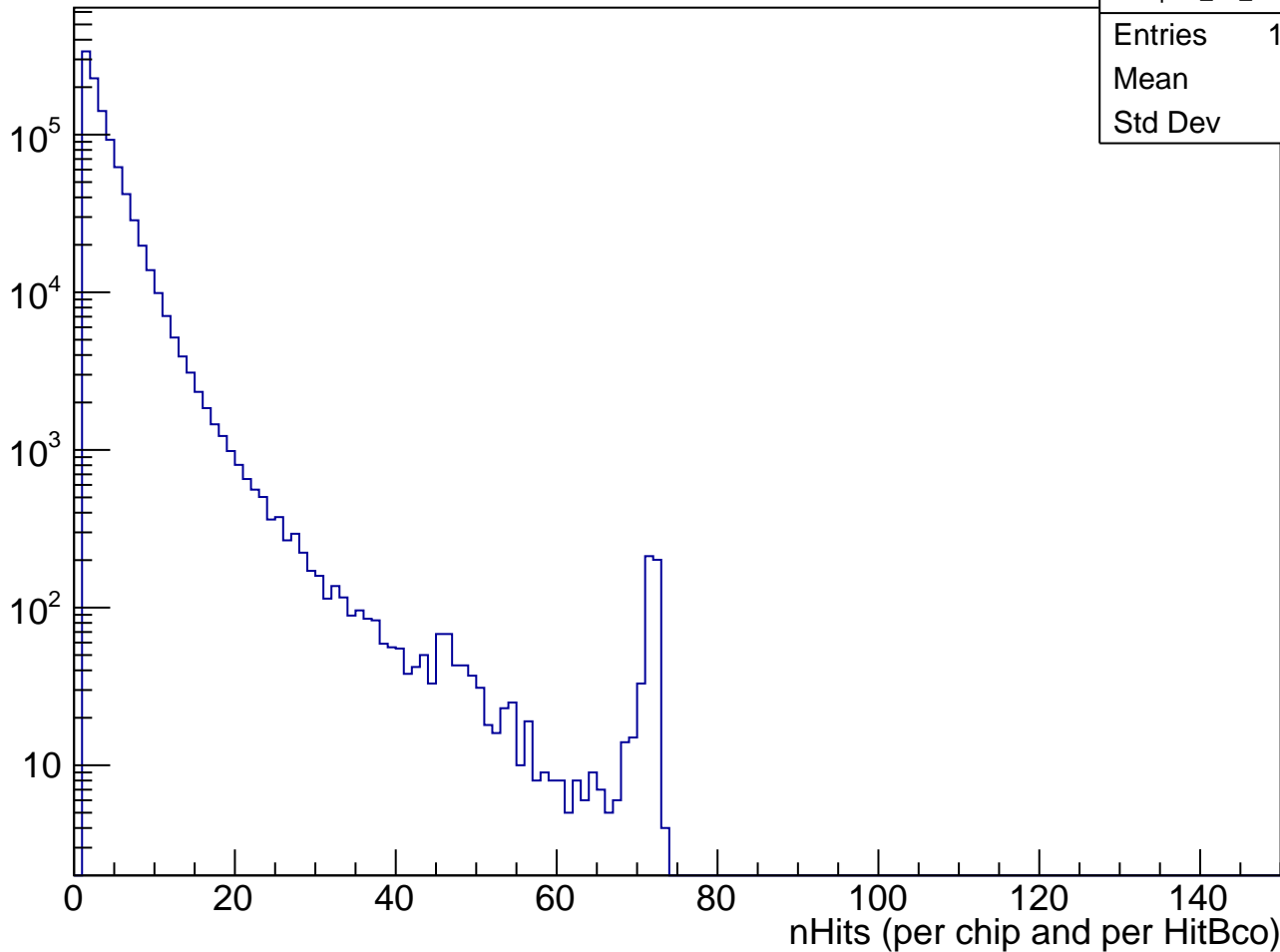
Entries



nChipHit_F3_Fch9_Chip20	
Entries	1004454
Mean	3.261
Std Dev	3.685

nChipHit_F3_Fch9_Chip21

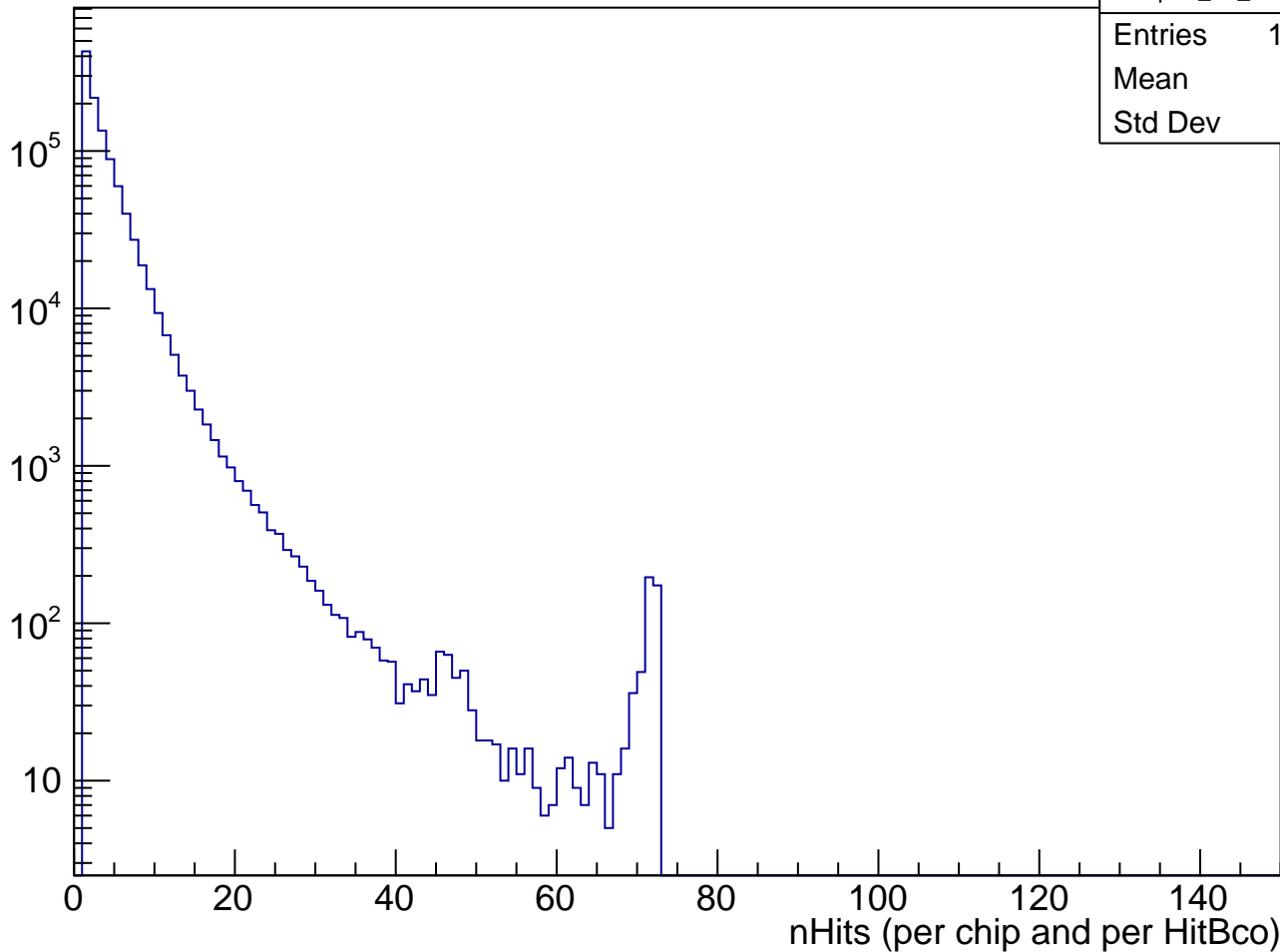
Entries



nChipHit_F3_Fch9_Chip21	
Entries	1008365
Mean	3.267
Std Dev	3.676

nChipHit_F3_Fch9_Chip22

Entries

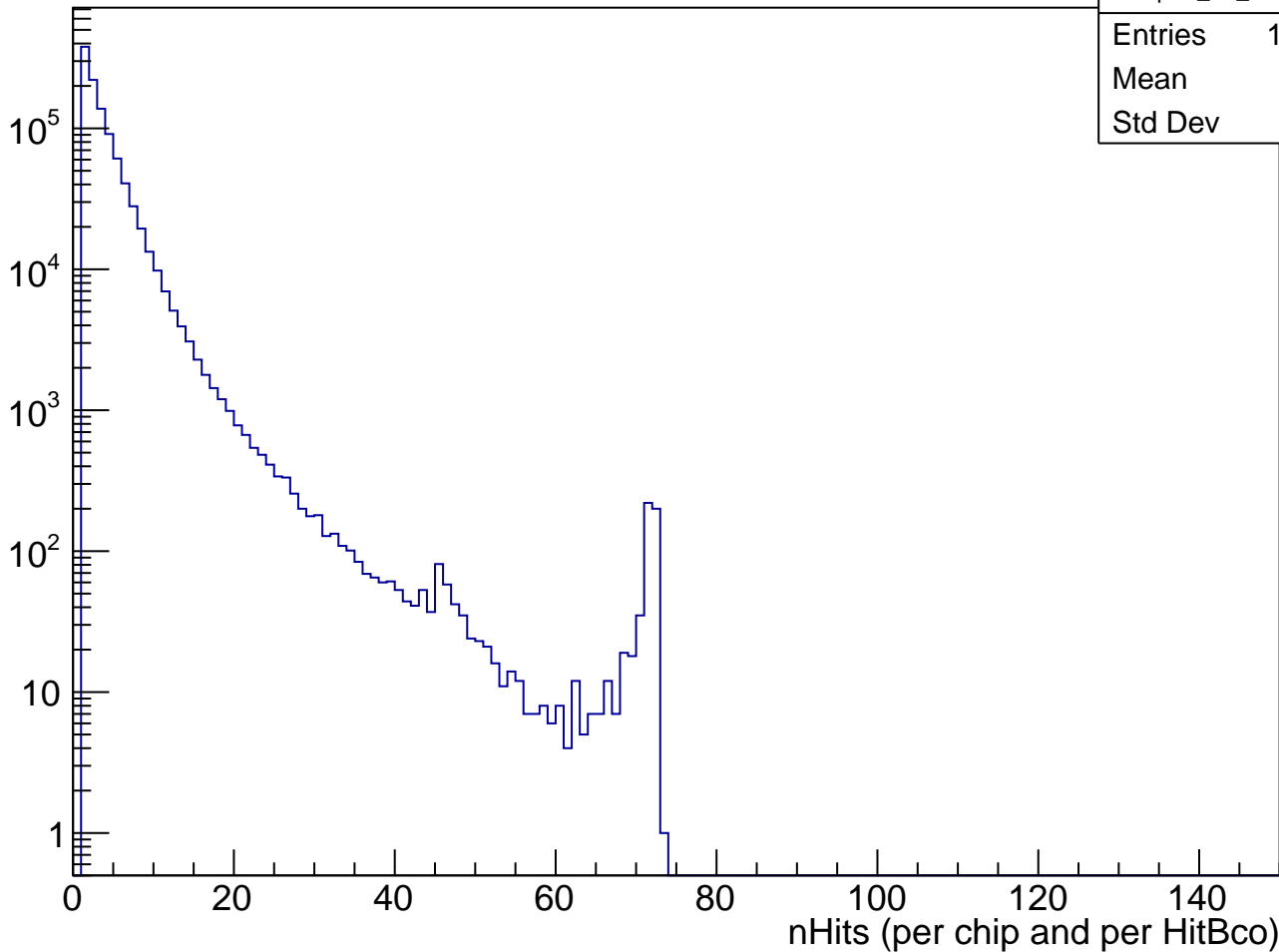


nChipHit_F3_Fch9_Chip22	
Entries	1072009
Mean	3.048
Std Dev	3.579

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip23

Entries

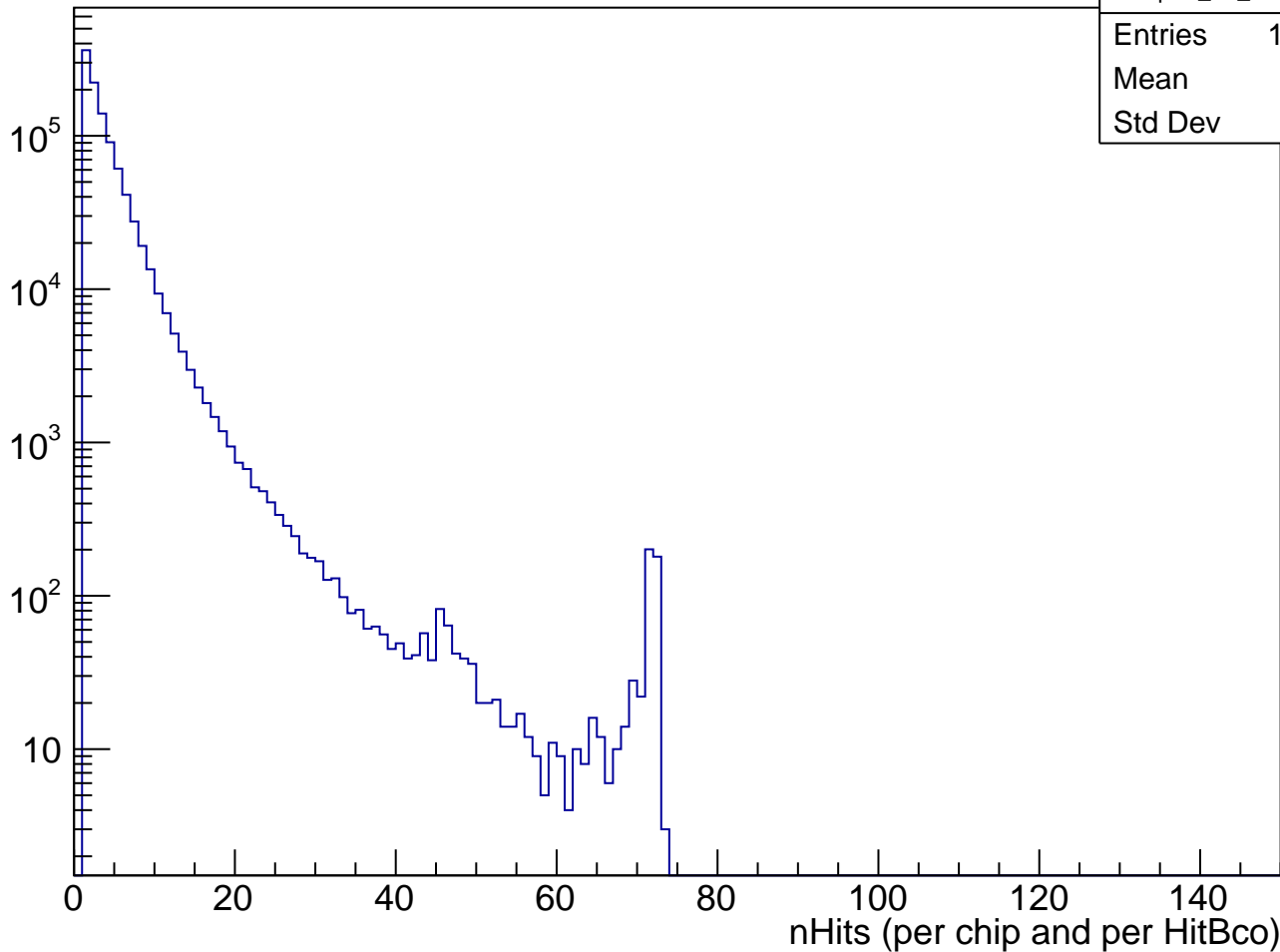


nChipHit_F3_Fch9_Chip23	
Entries	1034514
Mean	3.165
Std Dev	3.635

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip24

Entries

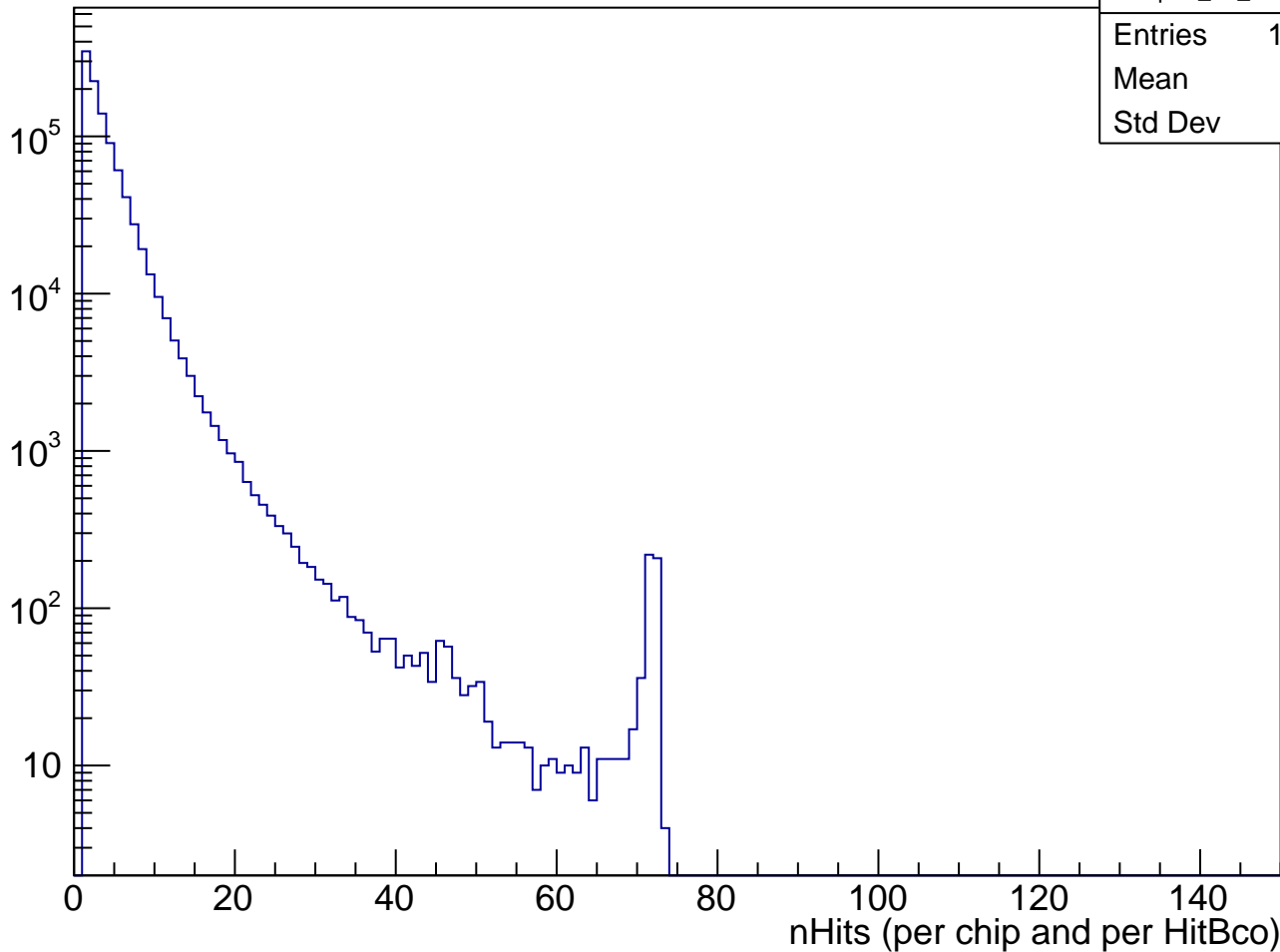


nChipHit_F3_Fch9_Chip24	
Entries	1019728
Mean	3.189
Std Dev	3.616

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip25

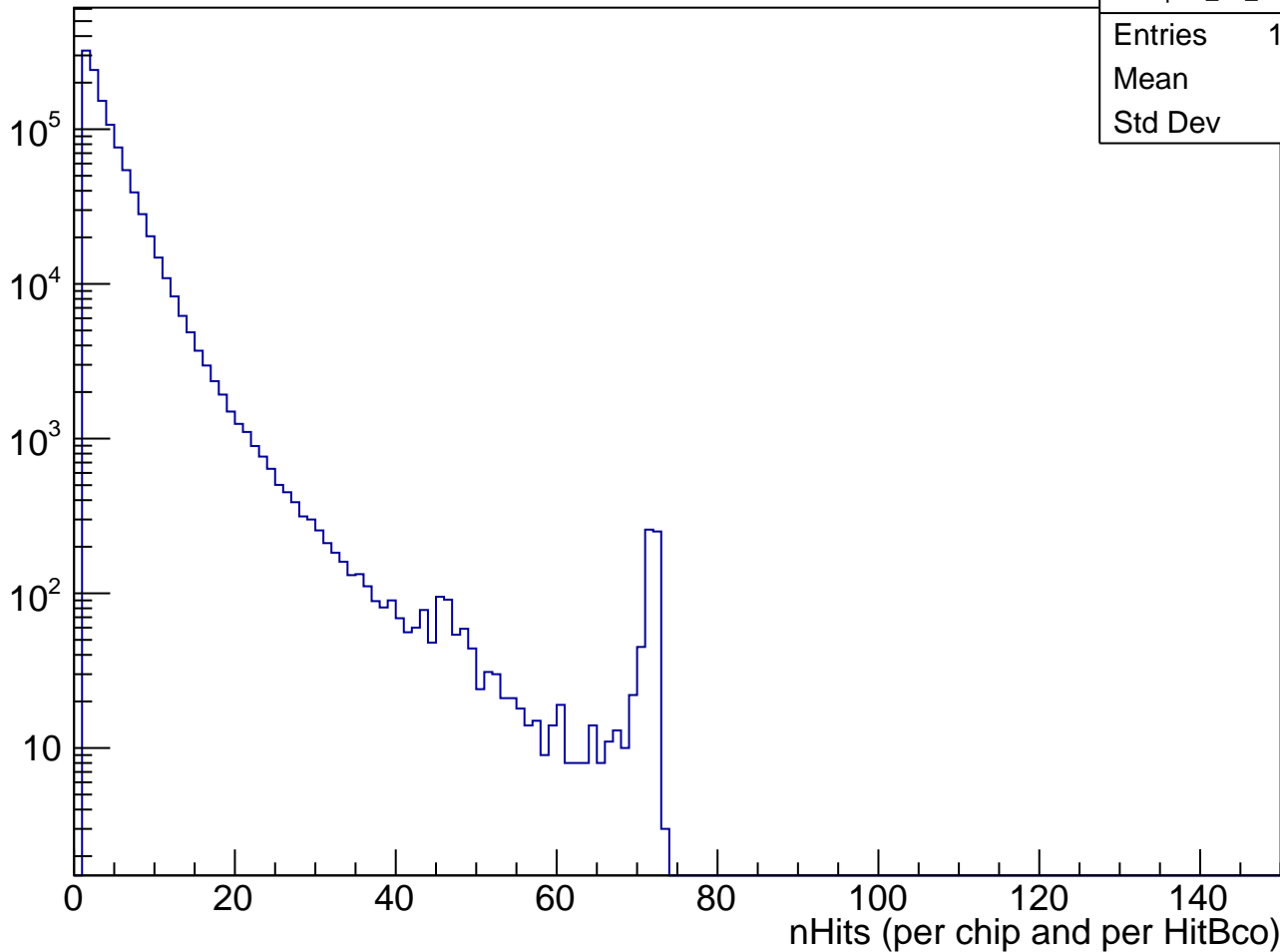
Entries



nChipHit_F3_Fch9_Chip25	
Entries	1006345
Mean	3.219
Std Dev	3.658

nChipHit_F3_Fch9_Chip3

Entries



nChipHit_F3_Fch9_Chip3

Entries 1108686

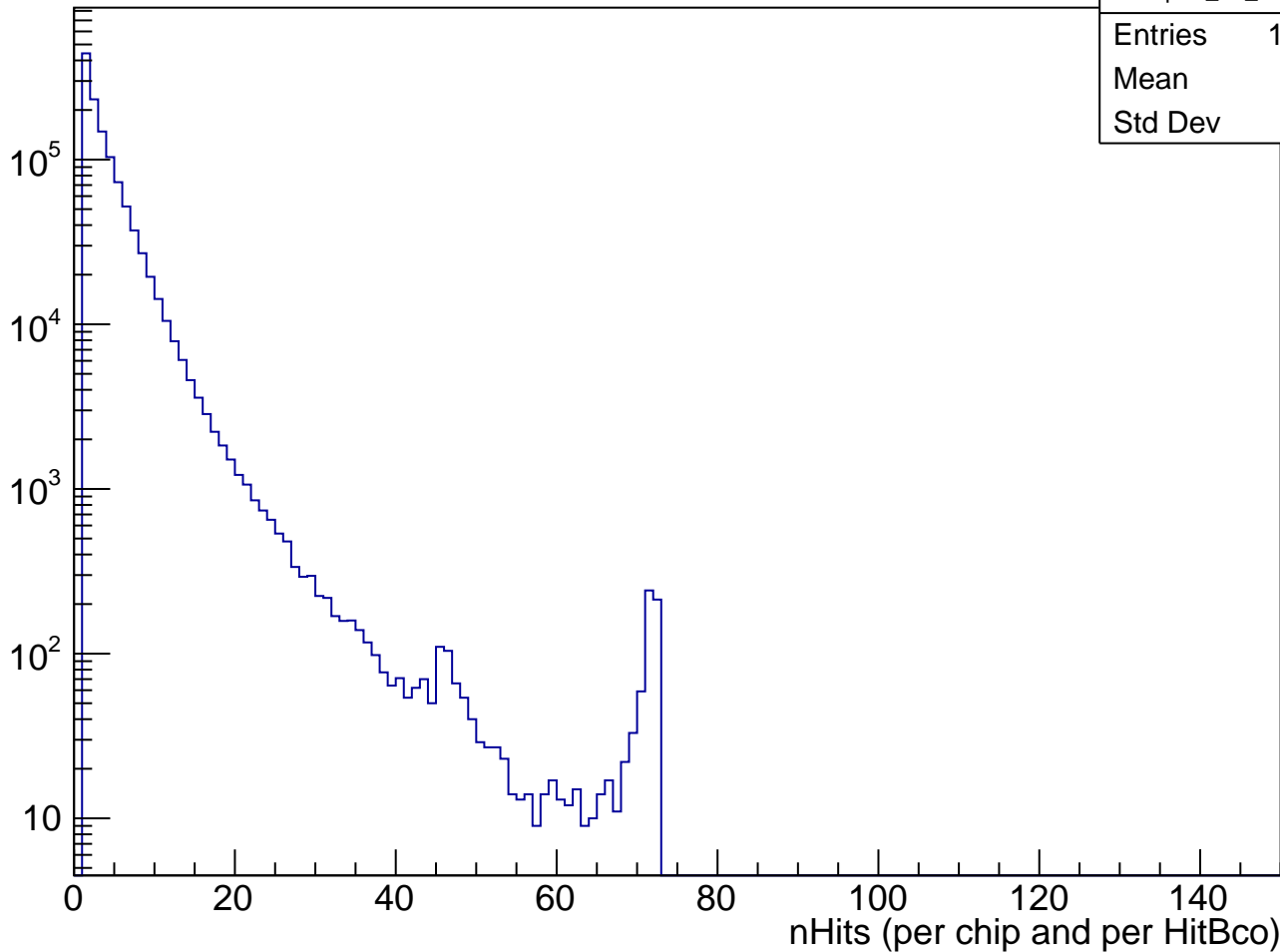
Mean 3.69

Std Dev 4.07

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip4

Entries



nChipHit_F3_Fch9_Chip4

Entries 1198338

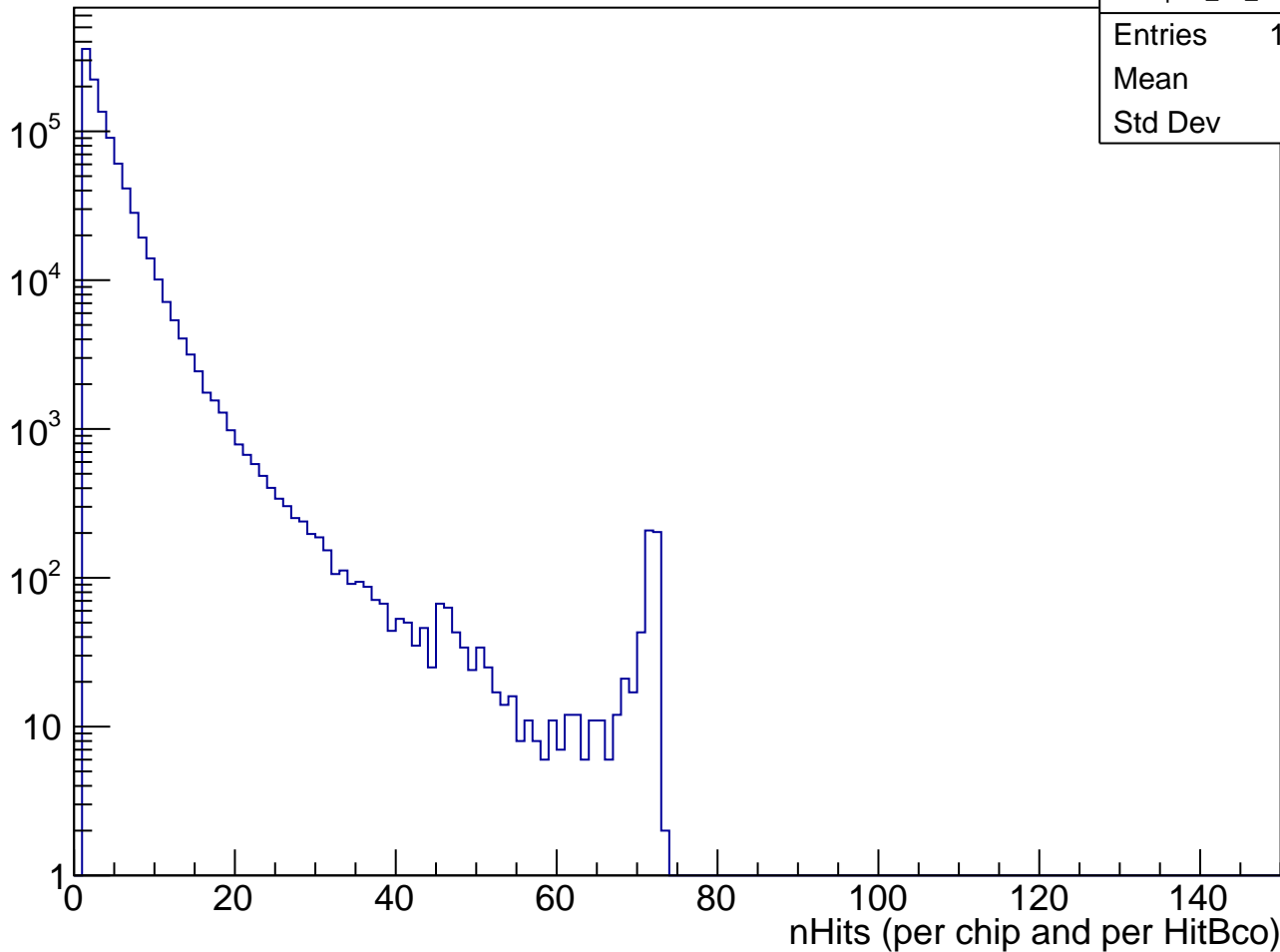
Mean 3.396

Std Dev 3.952

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip5

Entries



nChipHit_F3_Fch9_Chip5

Entries 1014420

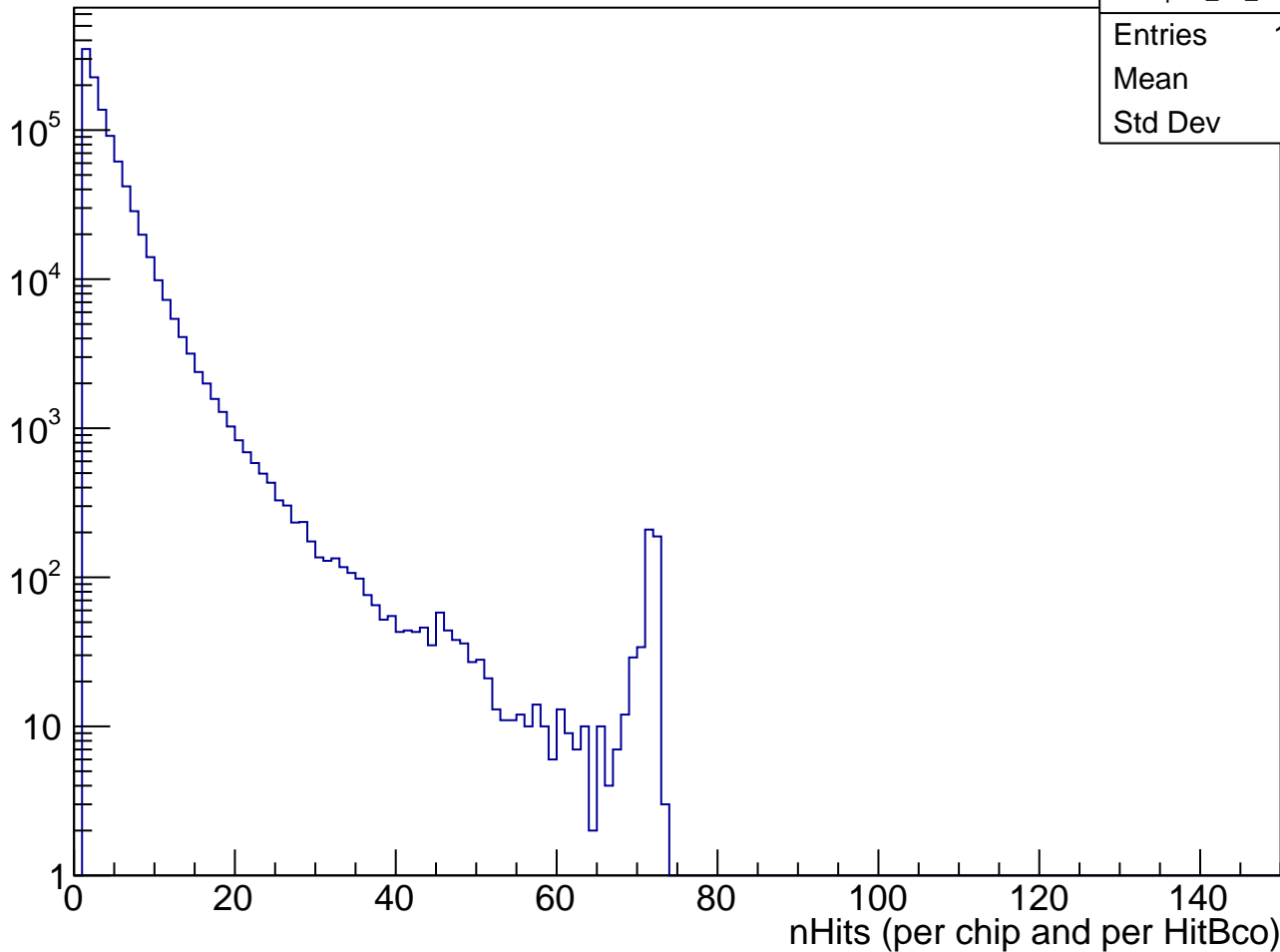
Mean 3.229

Std Dev 3.694

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip6

Entries



nChipHit_F3_Fch9_Chip6

Entries 1014481

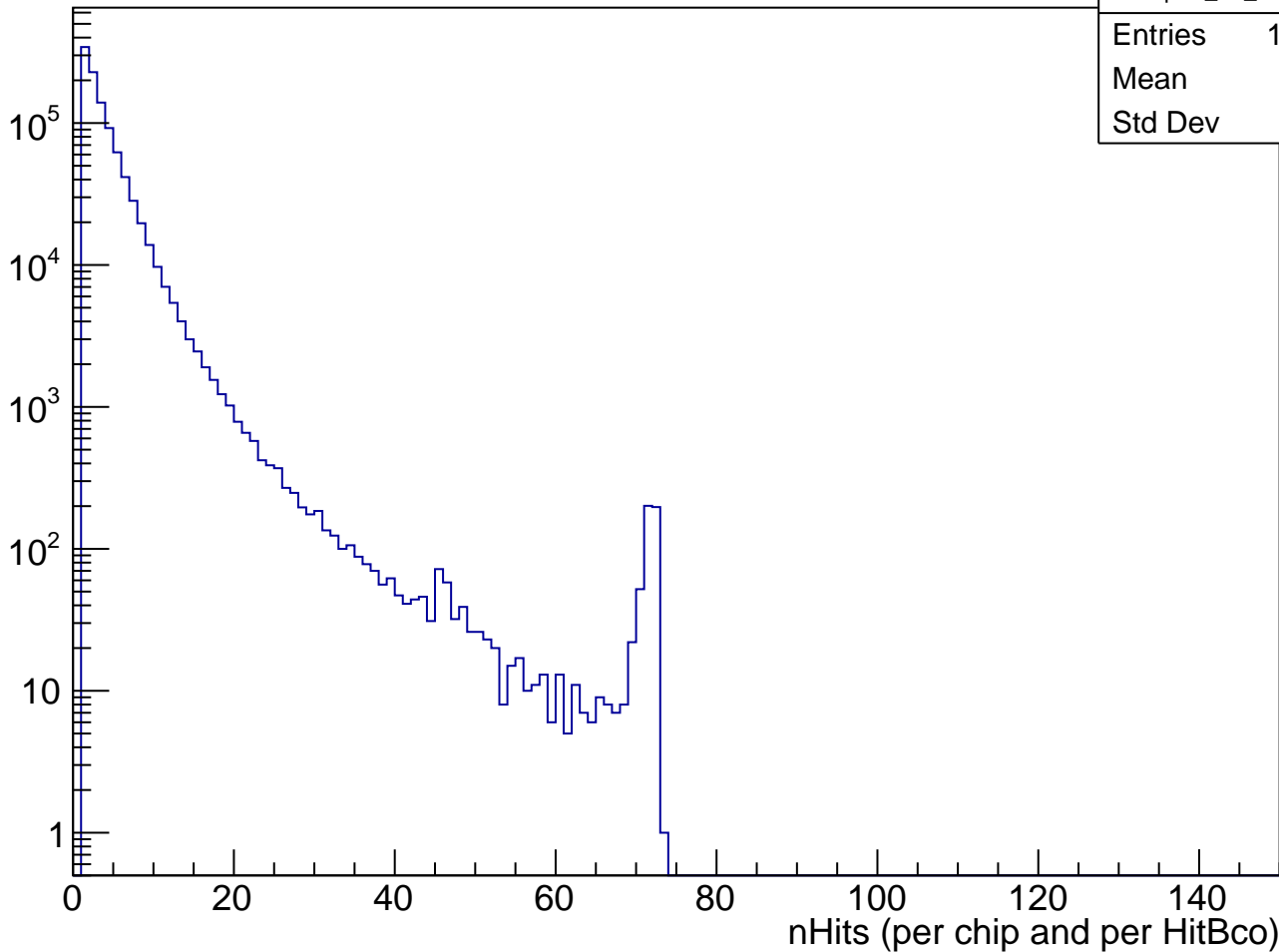
Mean 3.249

Std Dev 3.666

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip7

Entries



nChipHit_F3_Fch9_Chip7

Entries 1012289

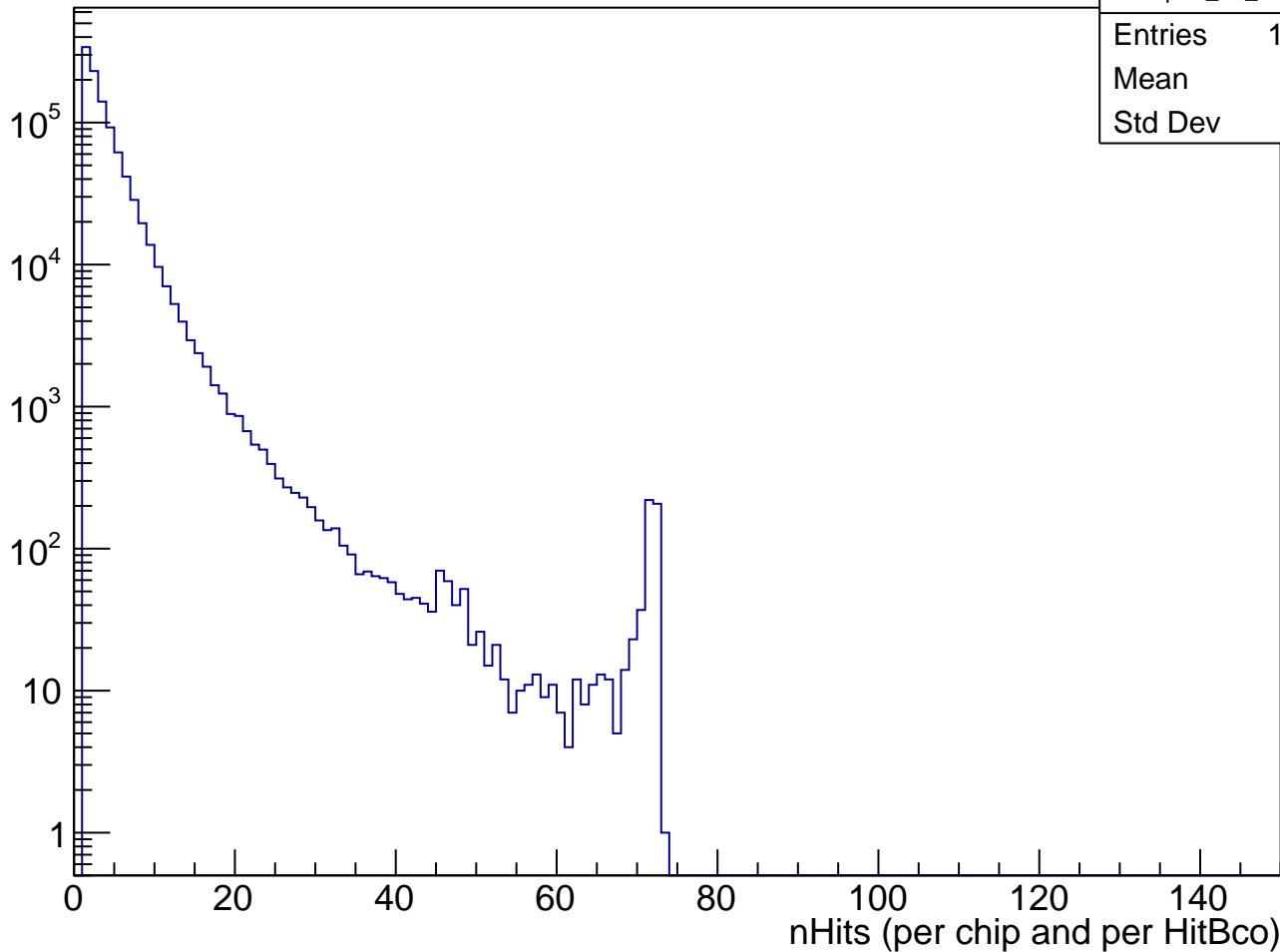
Mean 3.25

Std Dev 3.662

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip8

Entries



nChipHit_F3_Fch9_Chip8

Entries 1012128

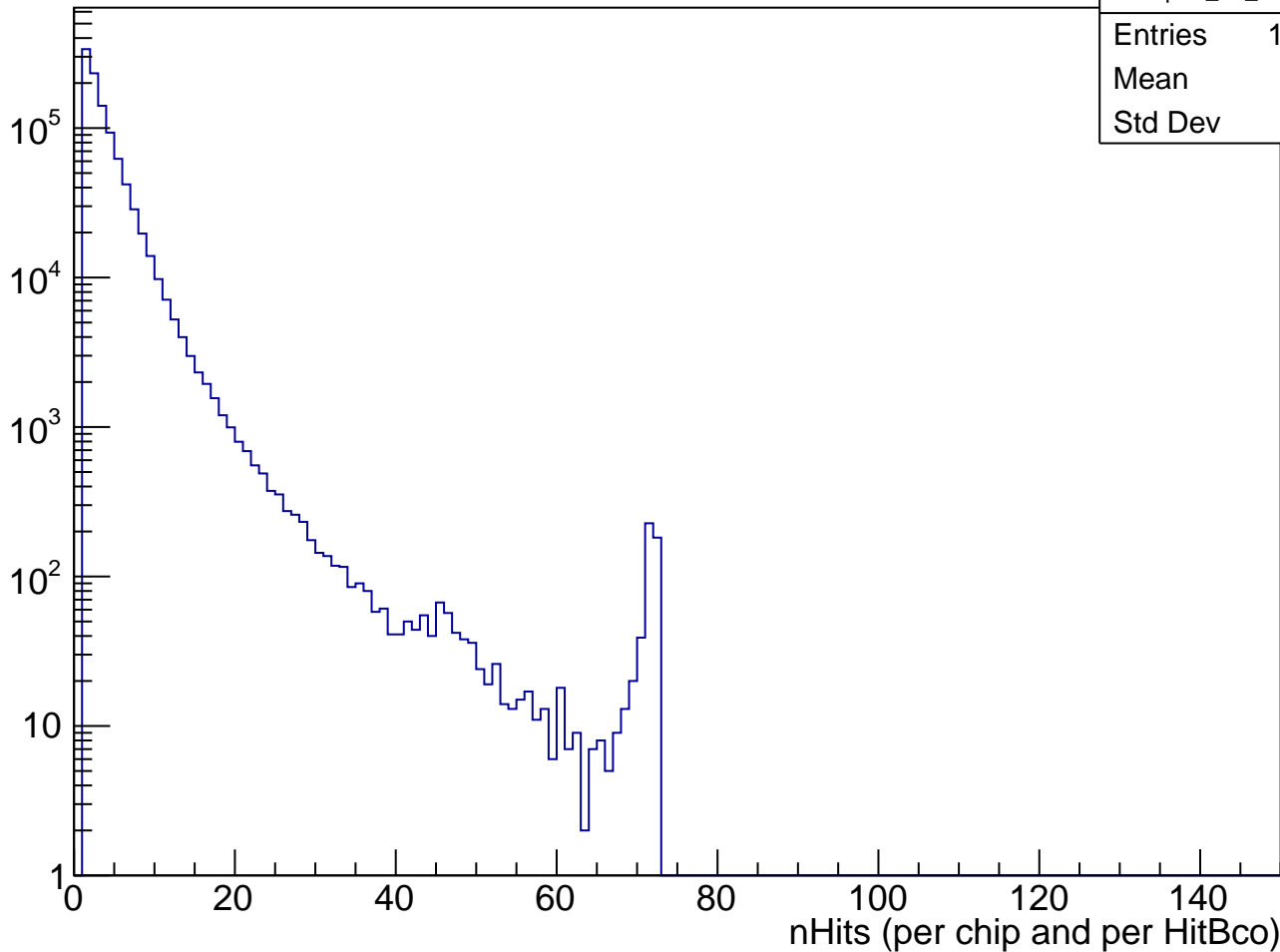
Mean 3.249

Std Dev 3.665

nHits (per chip and per HitBco)

nChipHit_F3_Fch9_Chip9

Entries



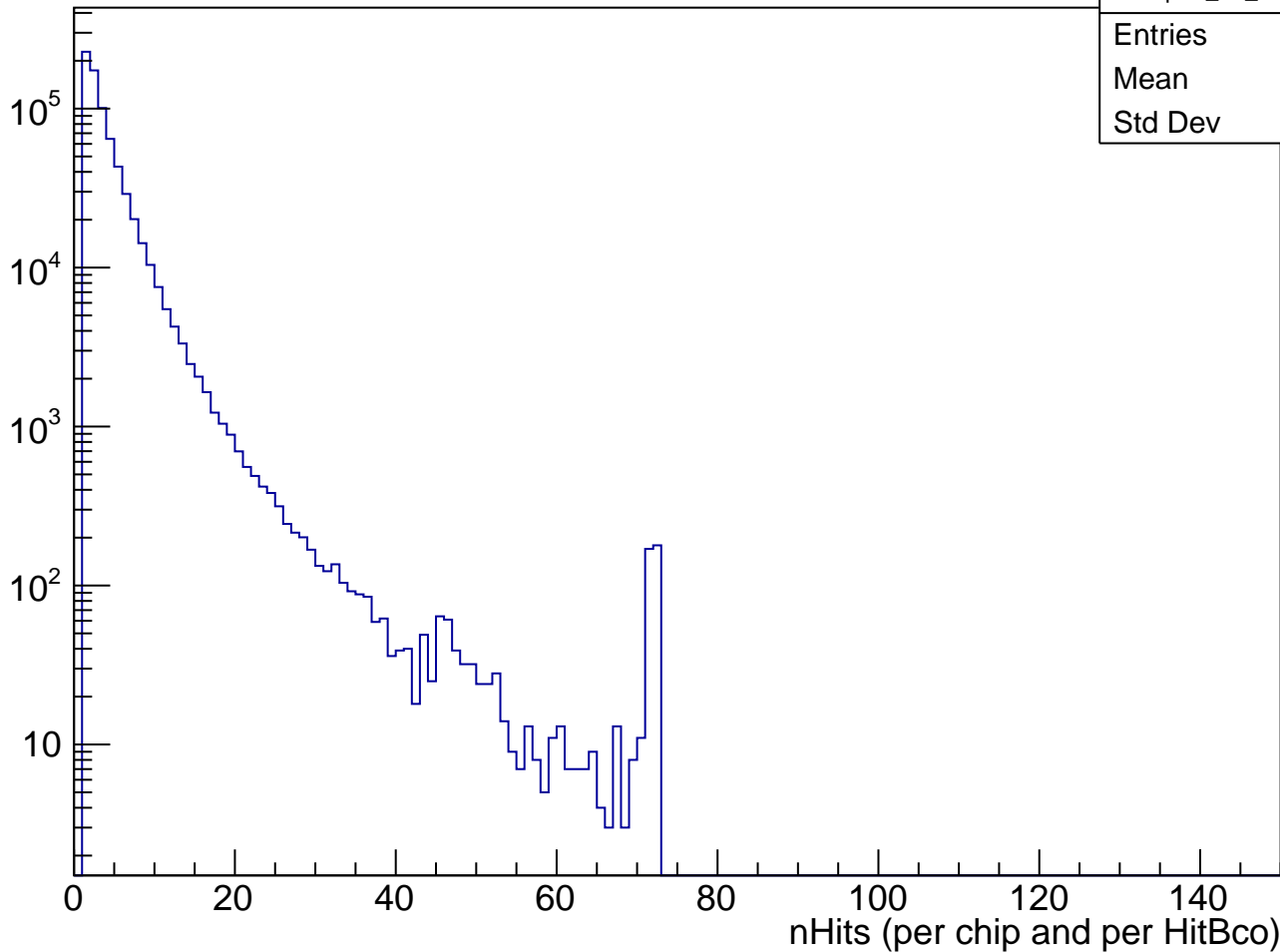
nChipHit_F3_Fch9_Chip9

Entries	1014048
Mean	3.261
Std Dev	3.661

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip0

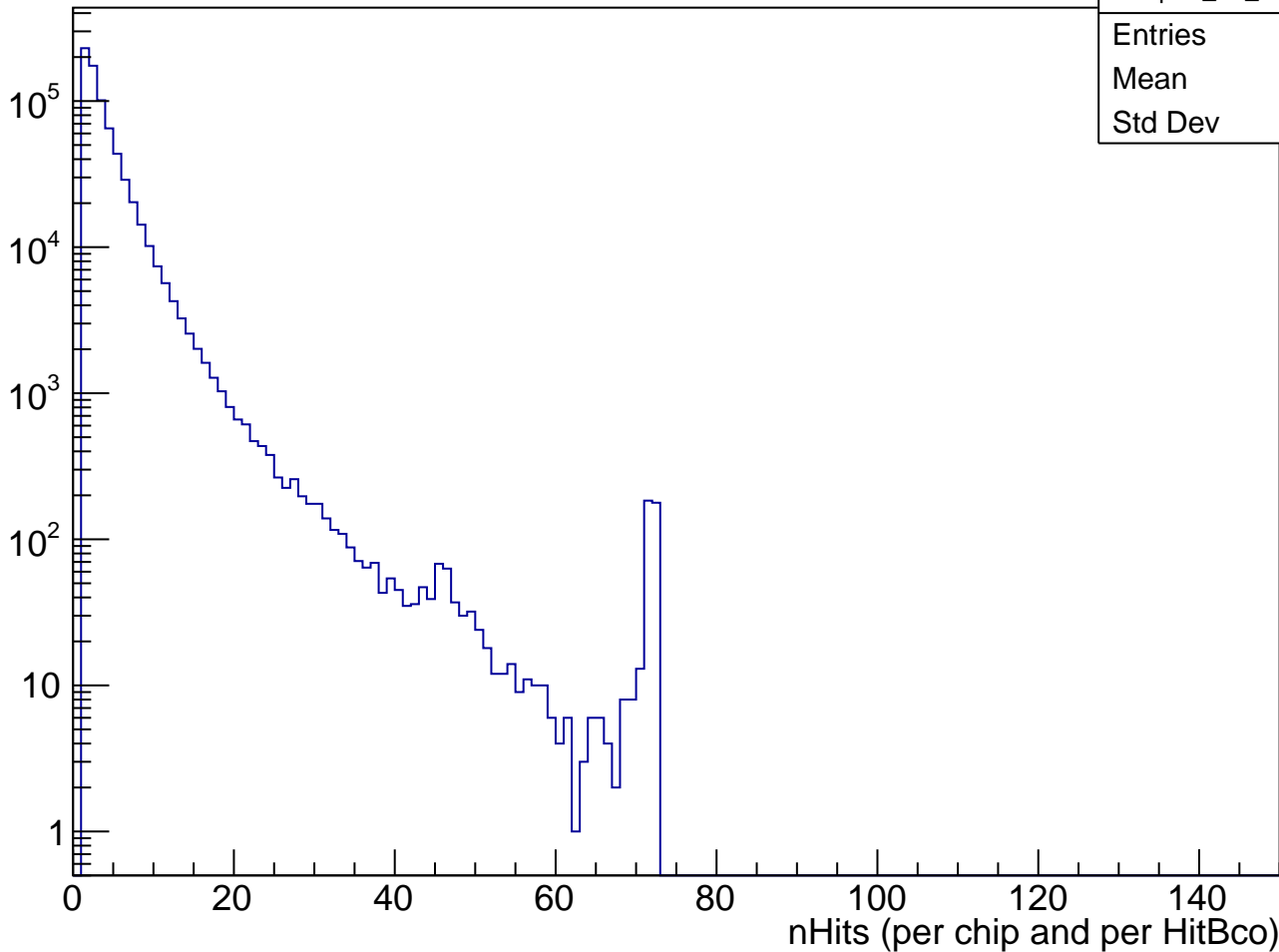
Entries



nChipHit_F4_Fch0_Chip0	
Entries	719712
Mean	3.371
Std Dev	3.898

nChipHit_F4_Fch0_Chip1

Entries

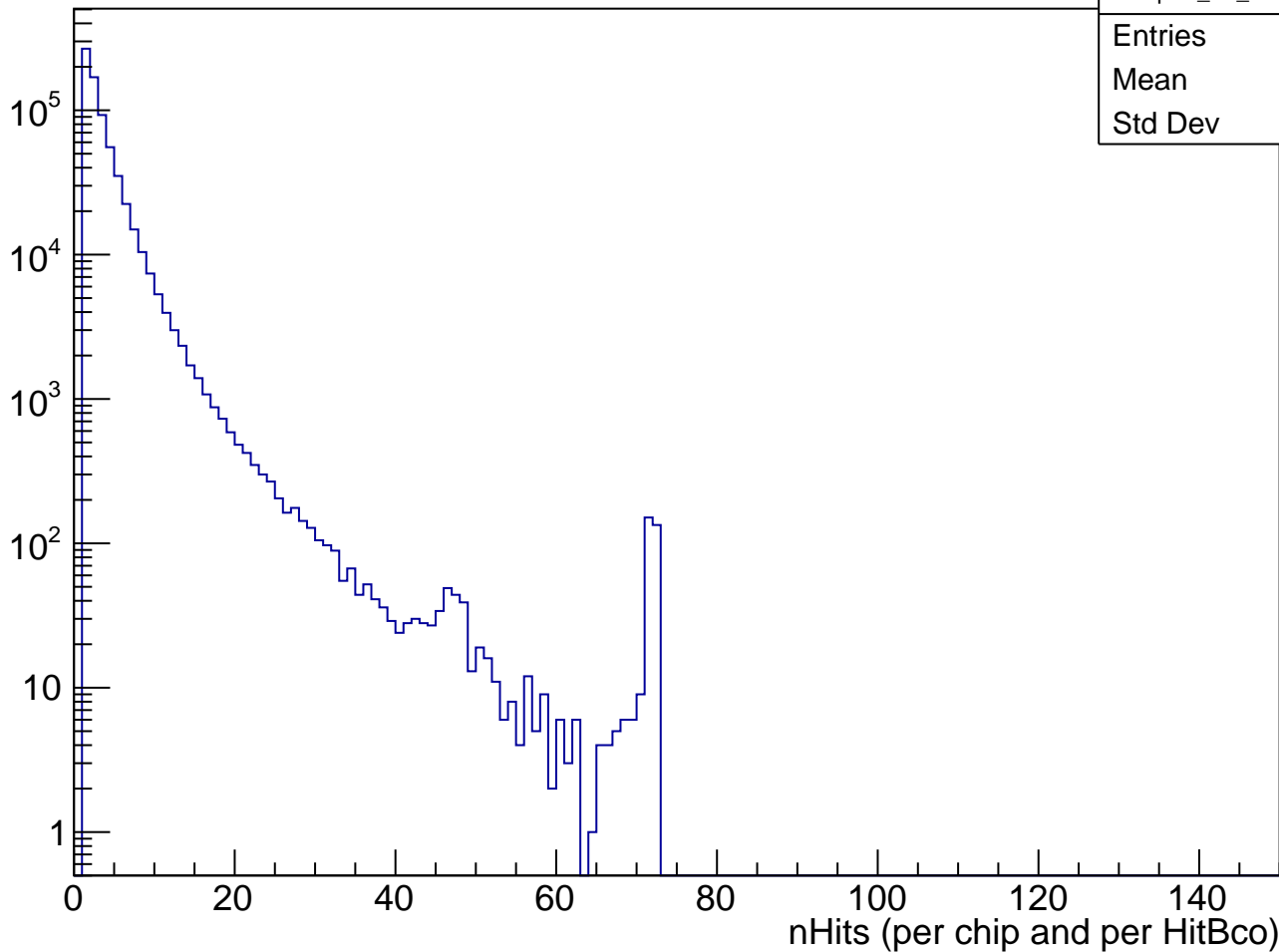


nChipHit_F4_Fch0_Chip1	
Entries	723538
Mean	3.361
Std Dev	3.884

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip10

Entries

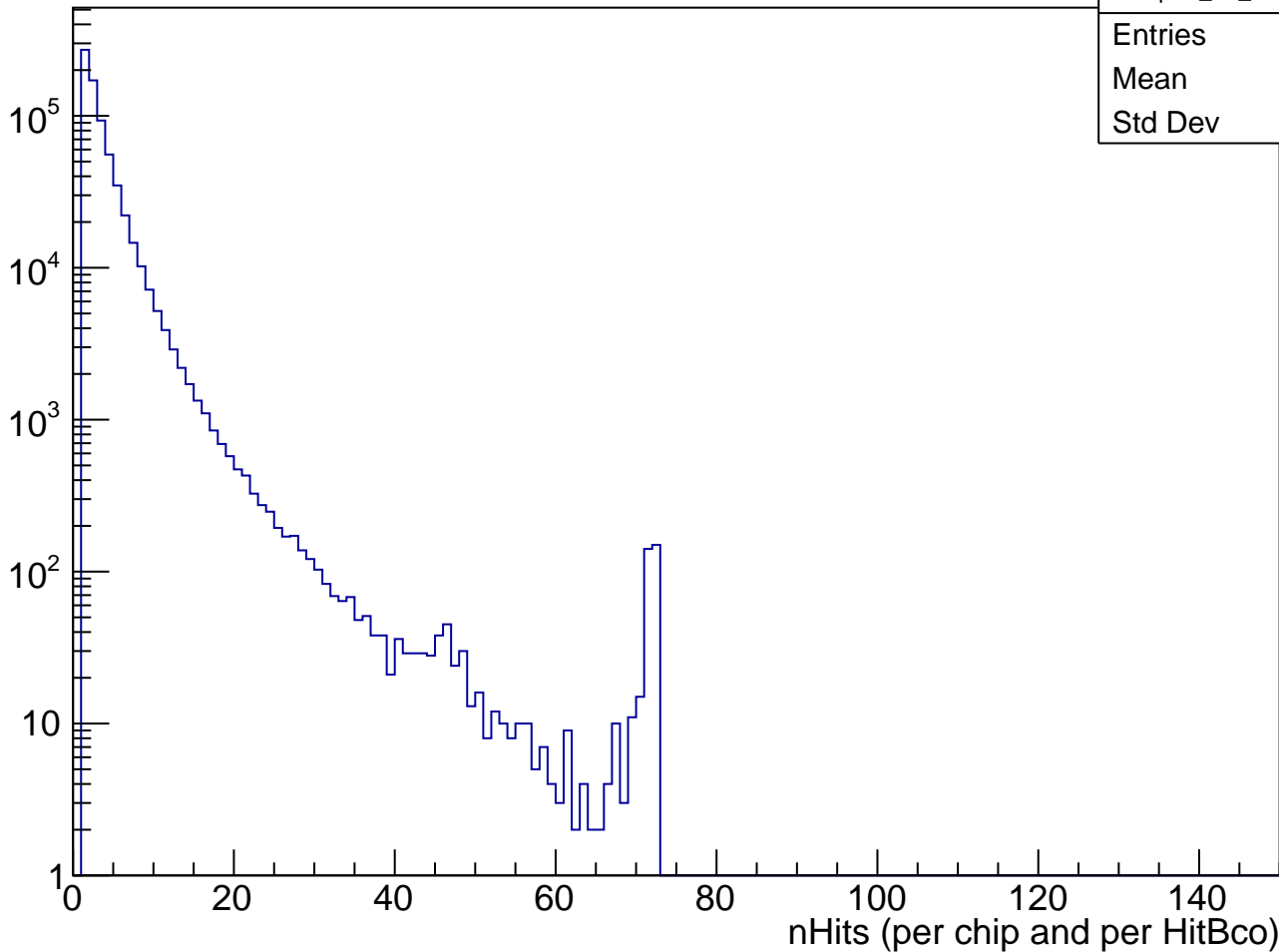


nChipHit_F4_Fch0_Chip10	
Entries	699785
Mean	2.942
Std Dev	3.477

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip11

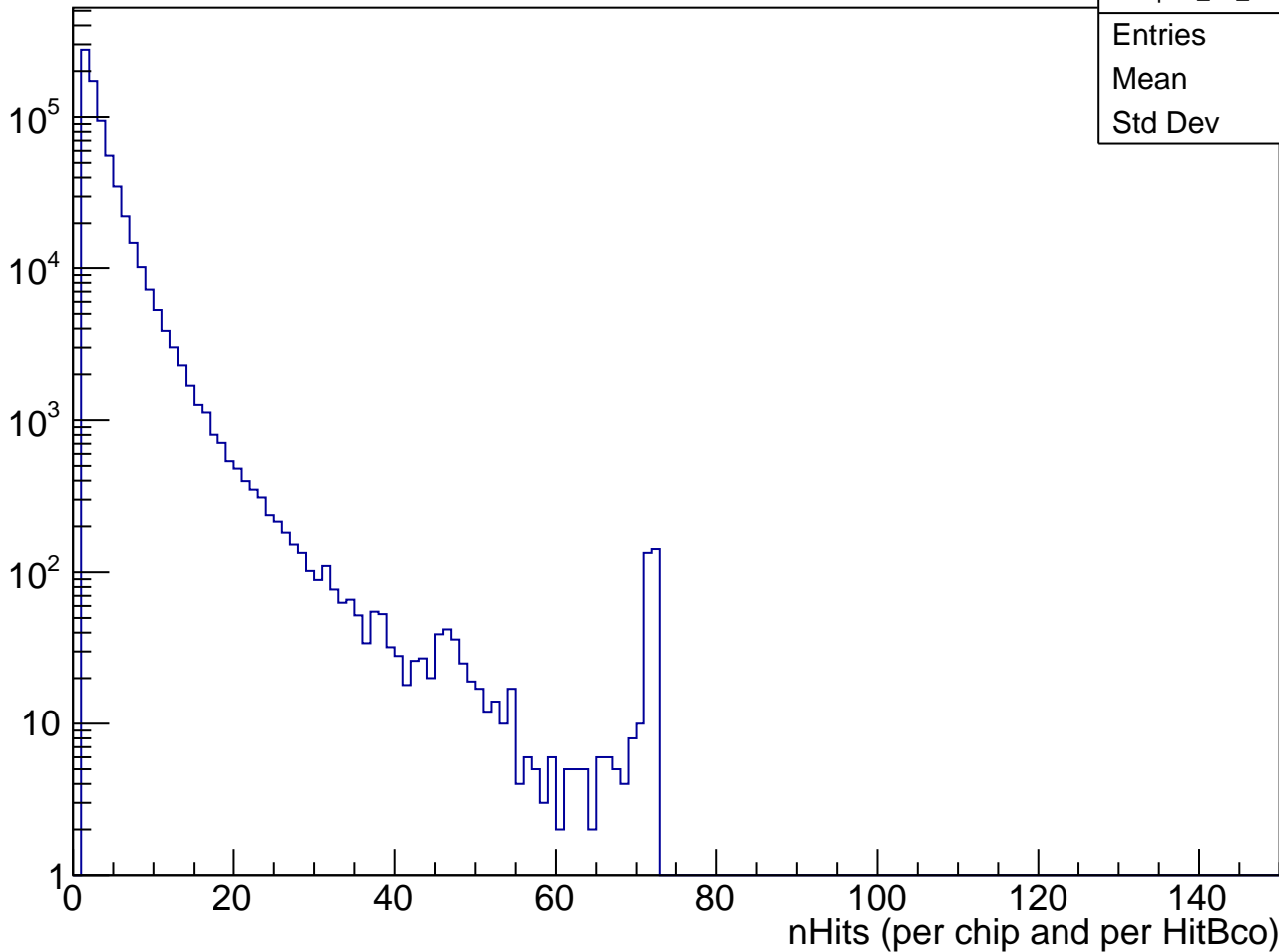
Entries



nChipHit_F4_Fch0_Chip11	
Entries	705164
Mean	2.908
Std Dev	3.449

nChipHit_F4_Fch0_Chip12

Entries

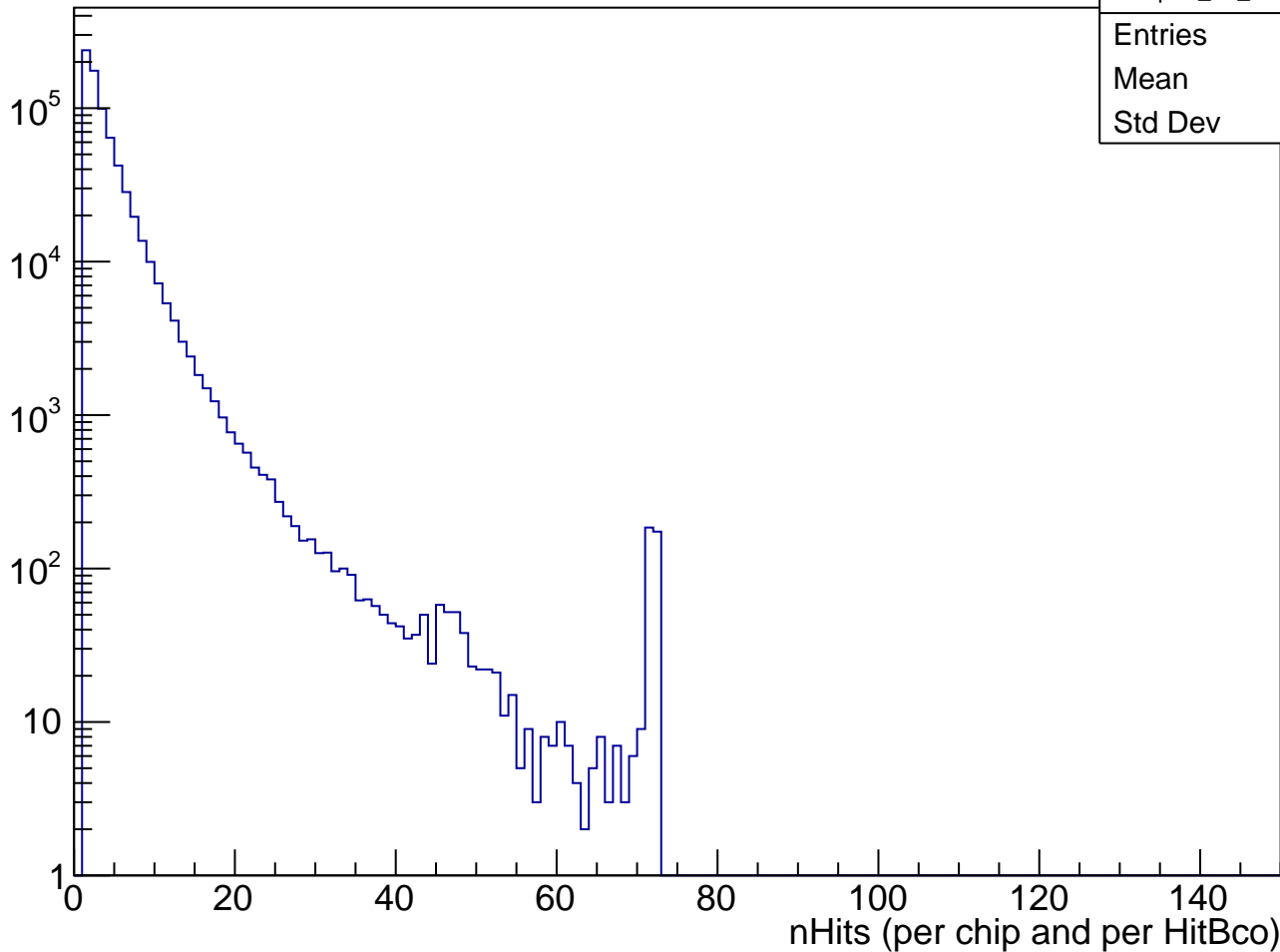


nChipHit_F4_Fch0_Chip12	
Entries	712526
Mean	2.897
Std Dev	3.42

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip13

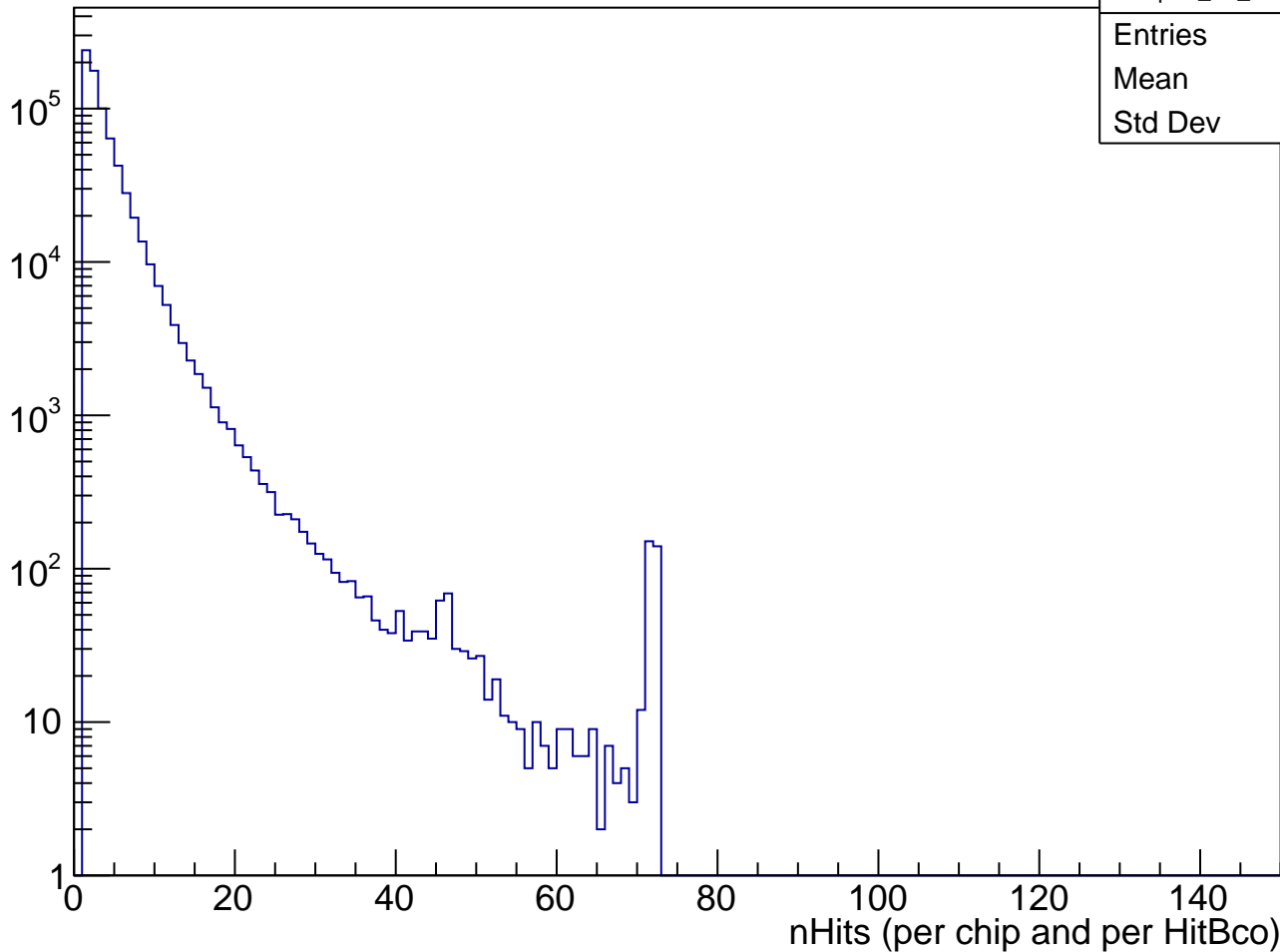
Entries



nChipHit_F4_Fch0_Chip13	
Entries	724692
Mean	3.285
Std Dev	3.807

nChipHit_F4_Fch0_Chip14

Entries

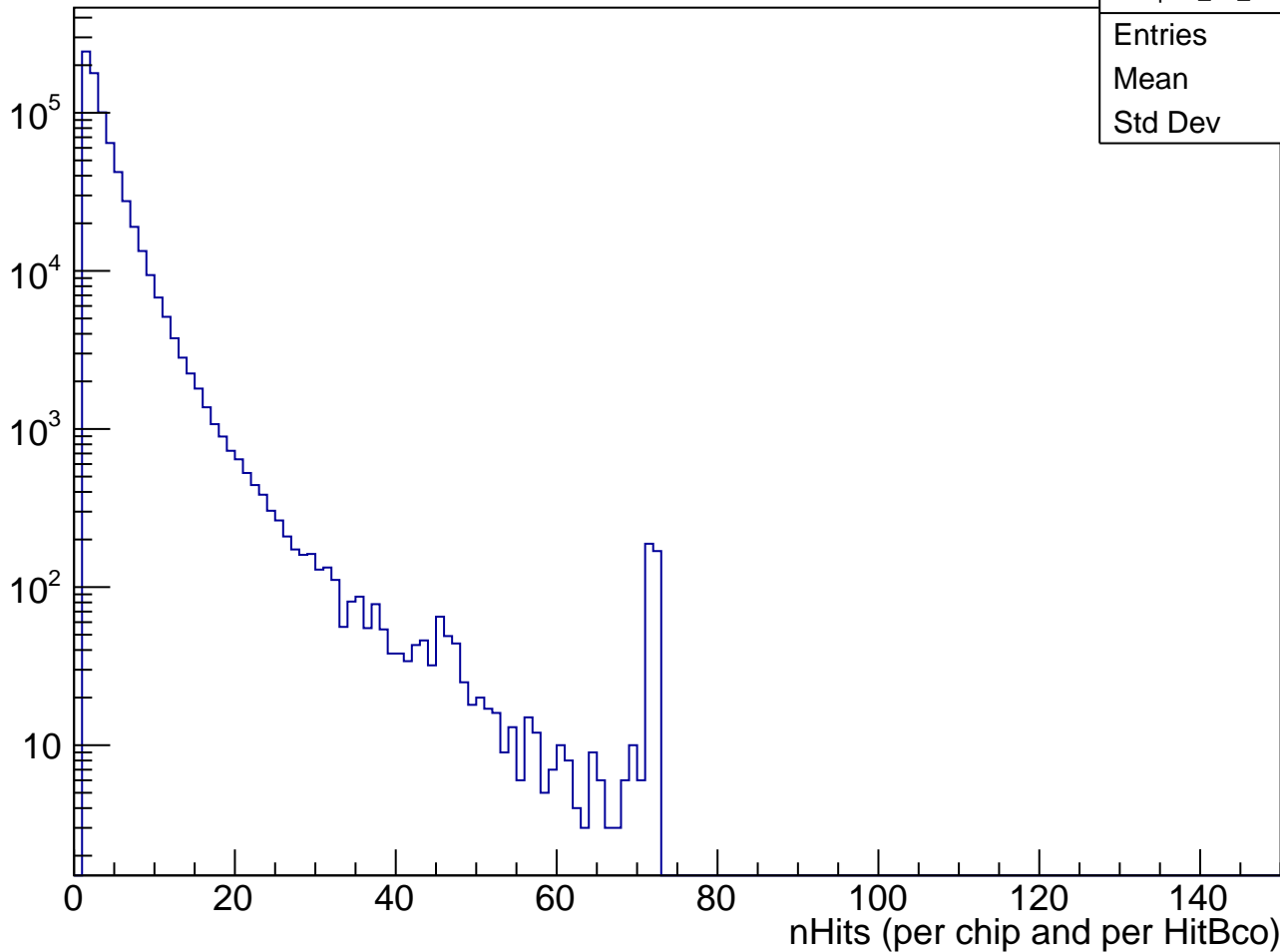


nChipHit_F4_Fch0_Chip14	
Entries	726566
Mean	3.249
Std Dev	3.71

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip15

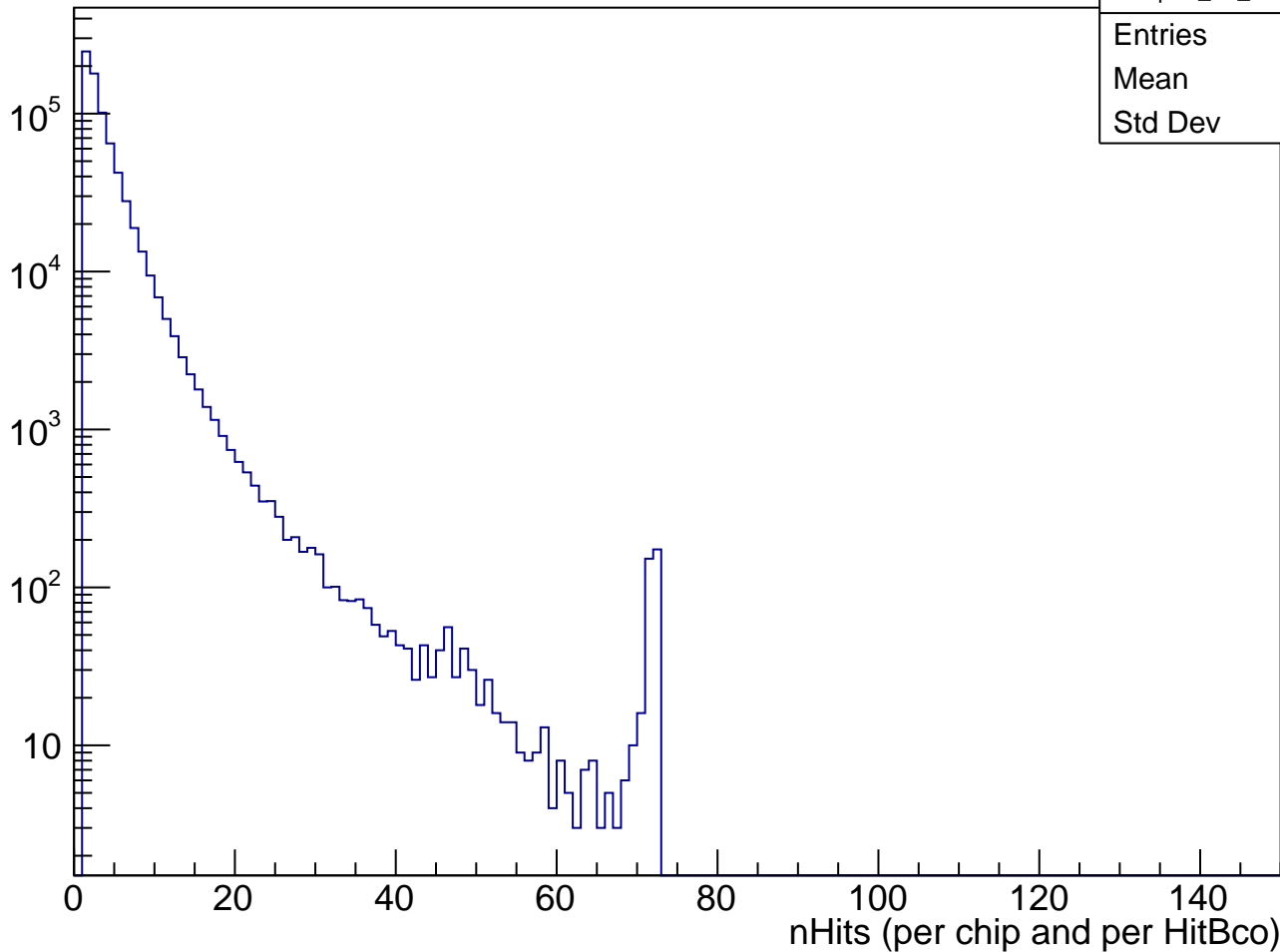
Entries



nChipHit_F4_Fch0_Chip15	
Entries	730437
Mean	3.221
Std Dev	3.742

nChipHit_F4_Fch0_Chip16

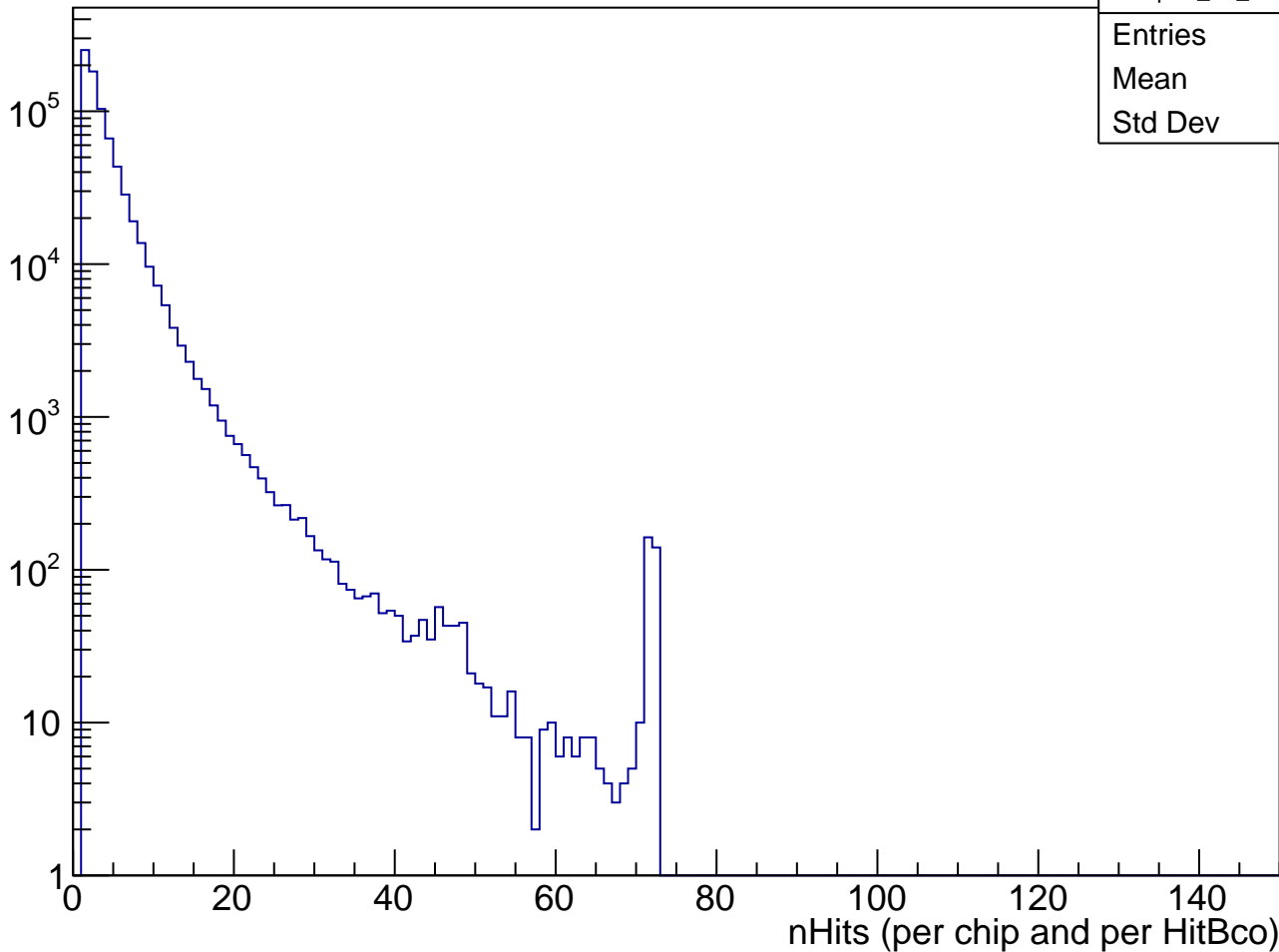
Entries



nChipHit_F4_Fch0_Chip16	
Entries	736945
Mean	3.214
Std Dev	3.727

nChipHit_F4_Fch0_Chip17

Entries

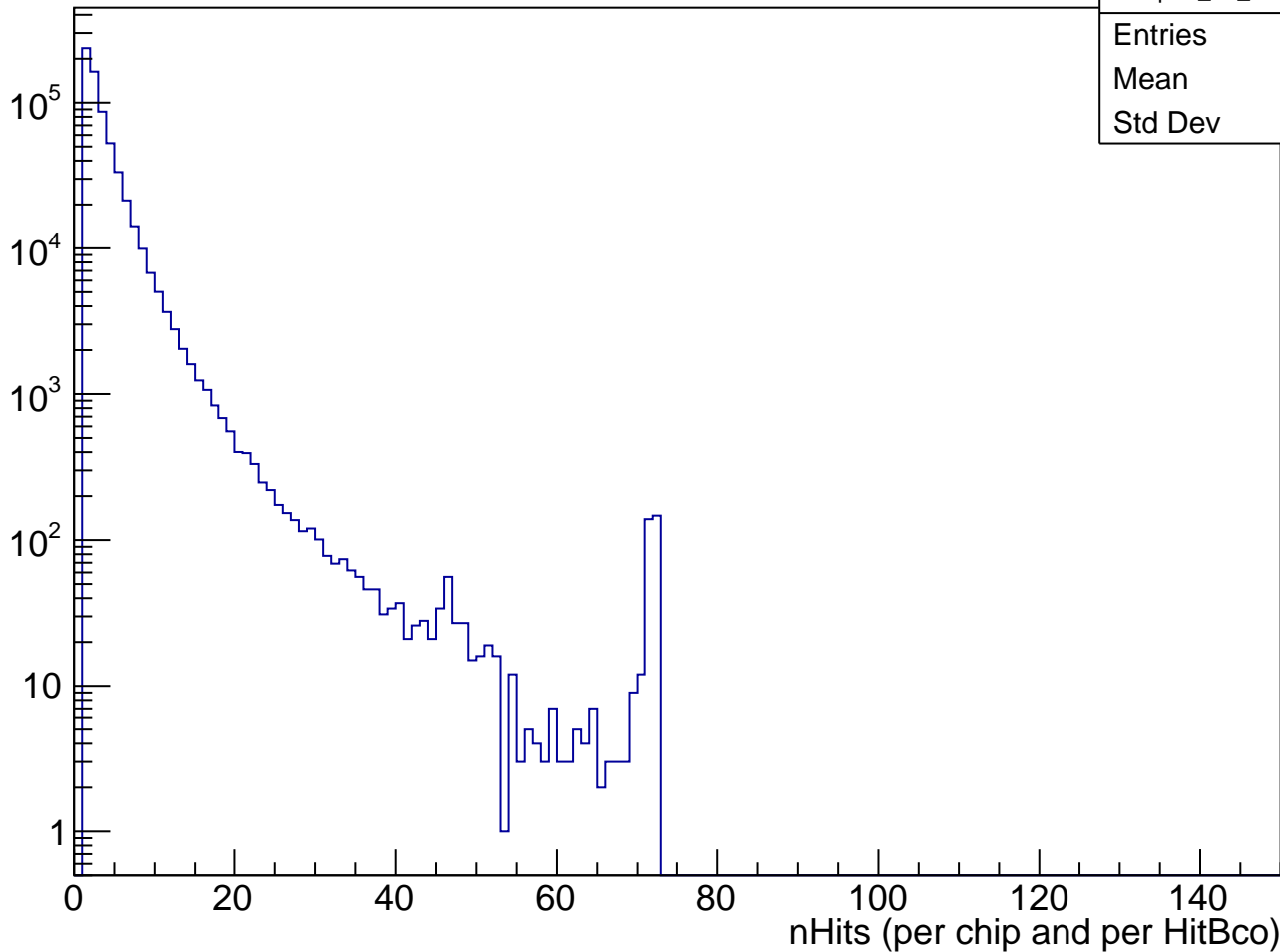


nChipHit_F4_Fch0_Chip17	
Entries	751060
Mean	3.223
Std Dev	3.704

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip18

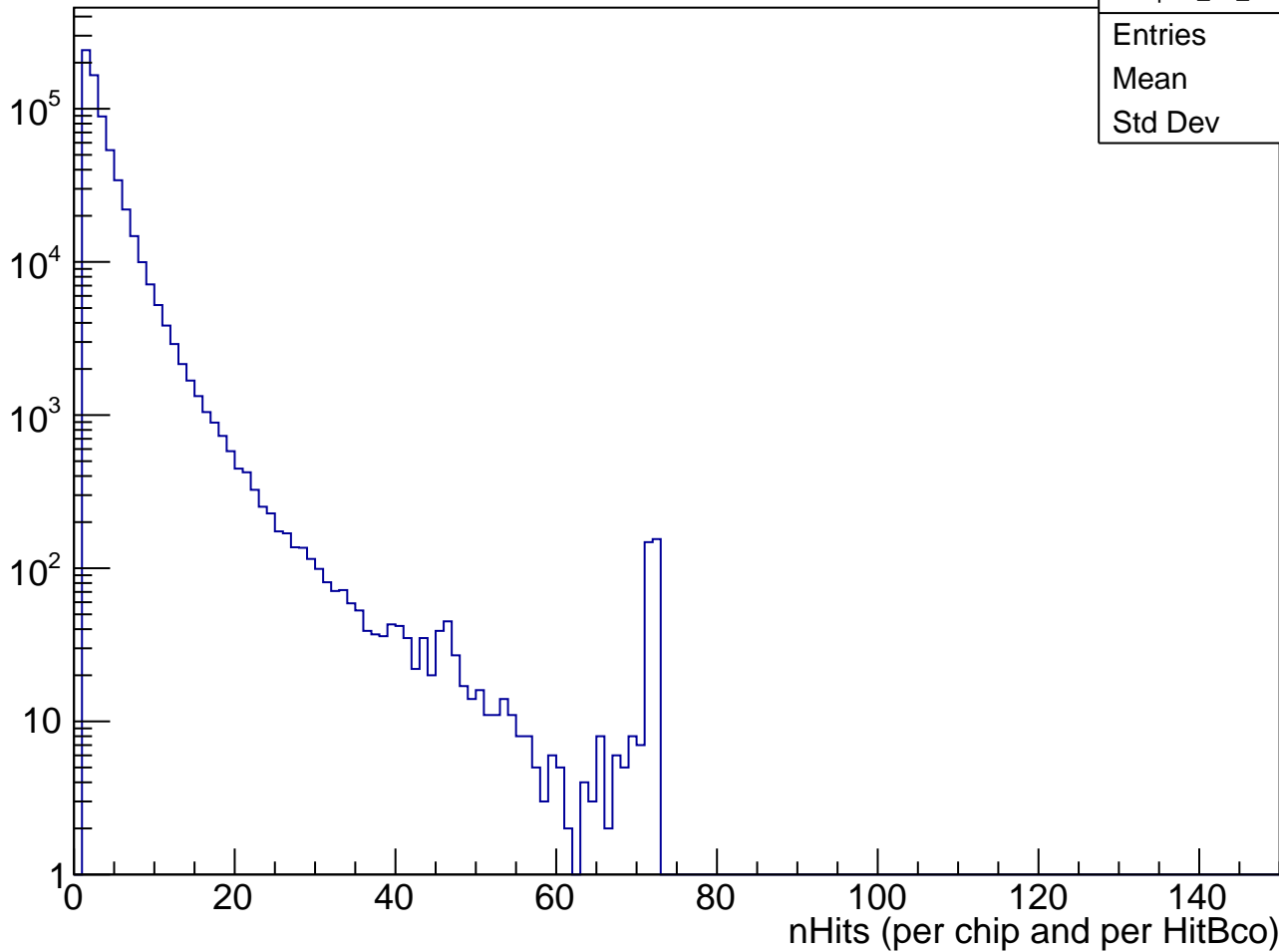
Entries



nChipHit_F4_Fch0_Chip18	
Entries	647520
Mean	2.978
Std Dev	3.497

nChipHit_F4_Fch0_Chip19

Entries



nChipHit_F4_Fch0_Chip19

Entries 661957

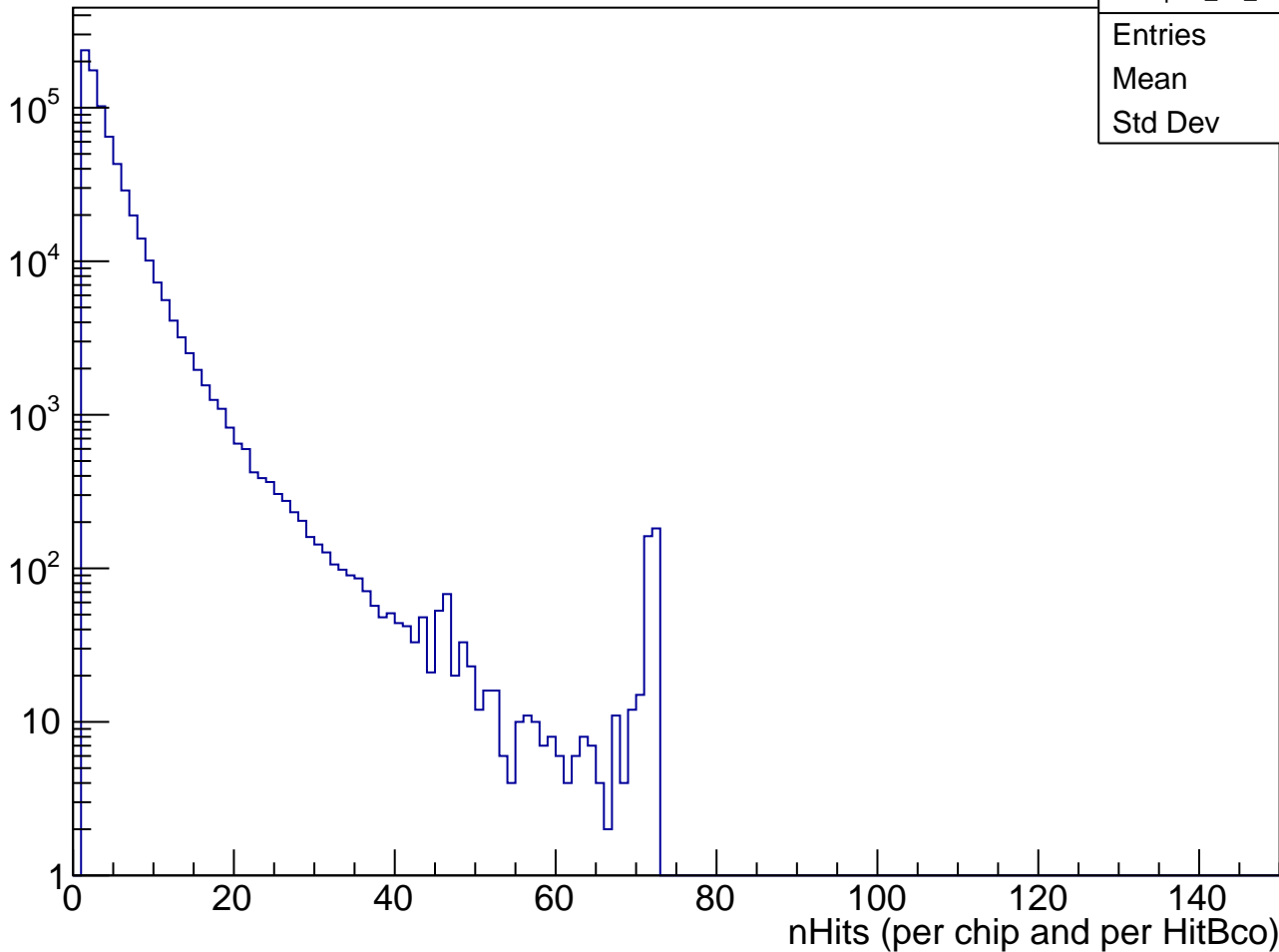
Mean 2.991

Std Dev 3.517

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip2

Entries



nChipHit_F4_Fch0_Chip2

Entries 729292

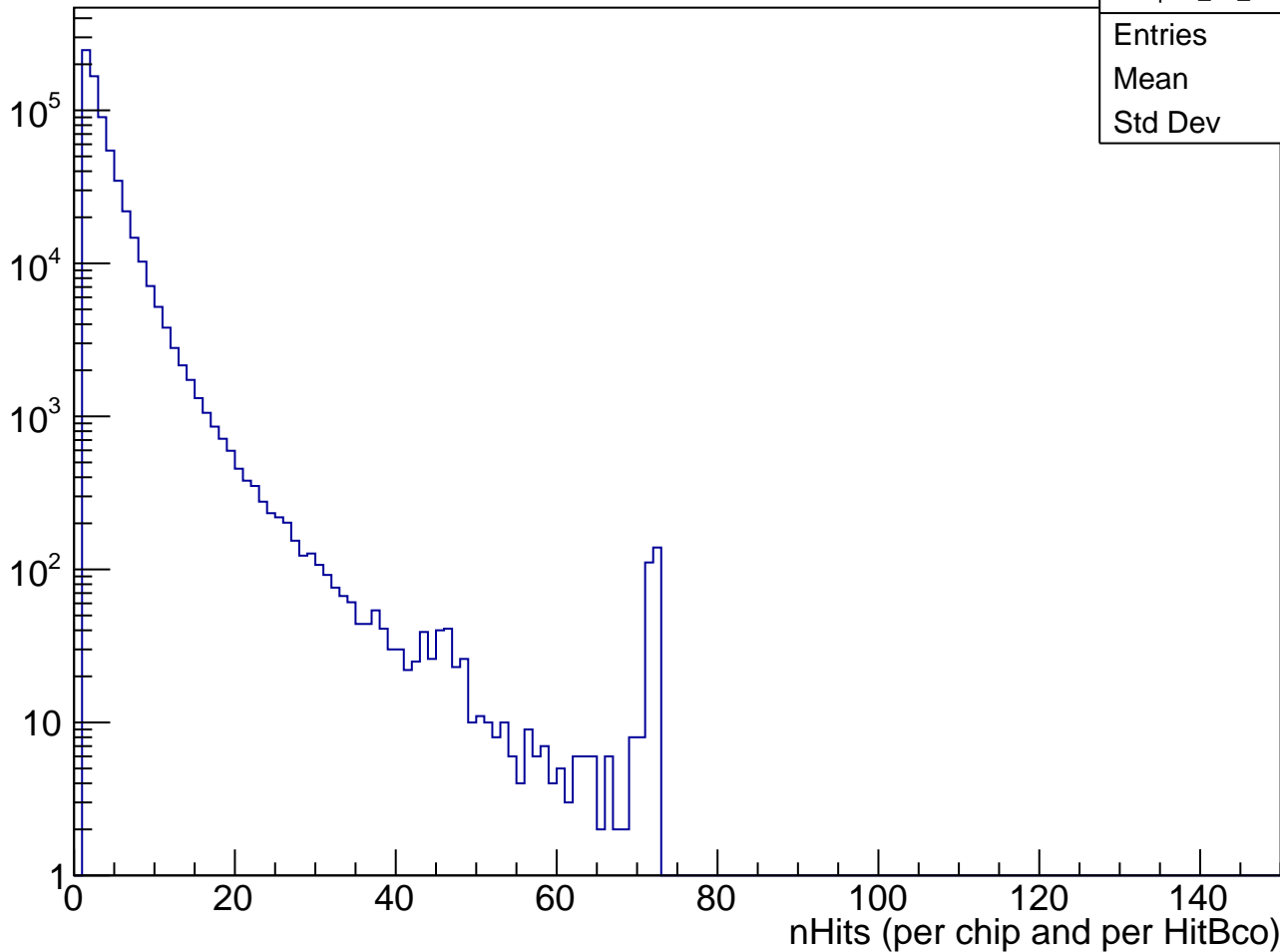
Mean 3.318

Std Dev 3.826

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip20

Entries

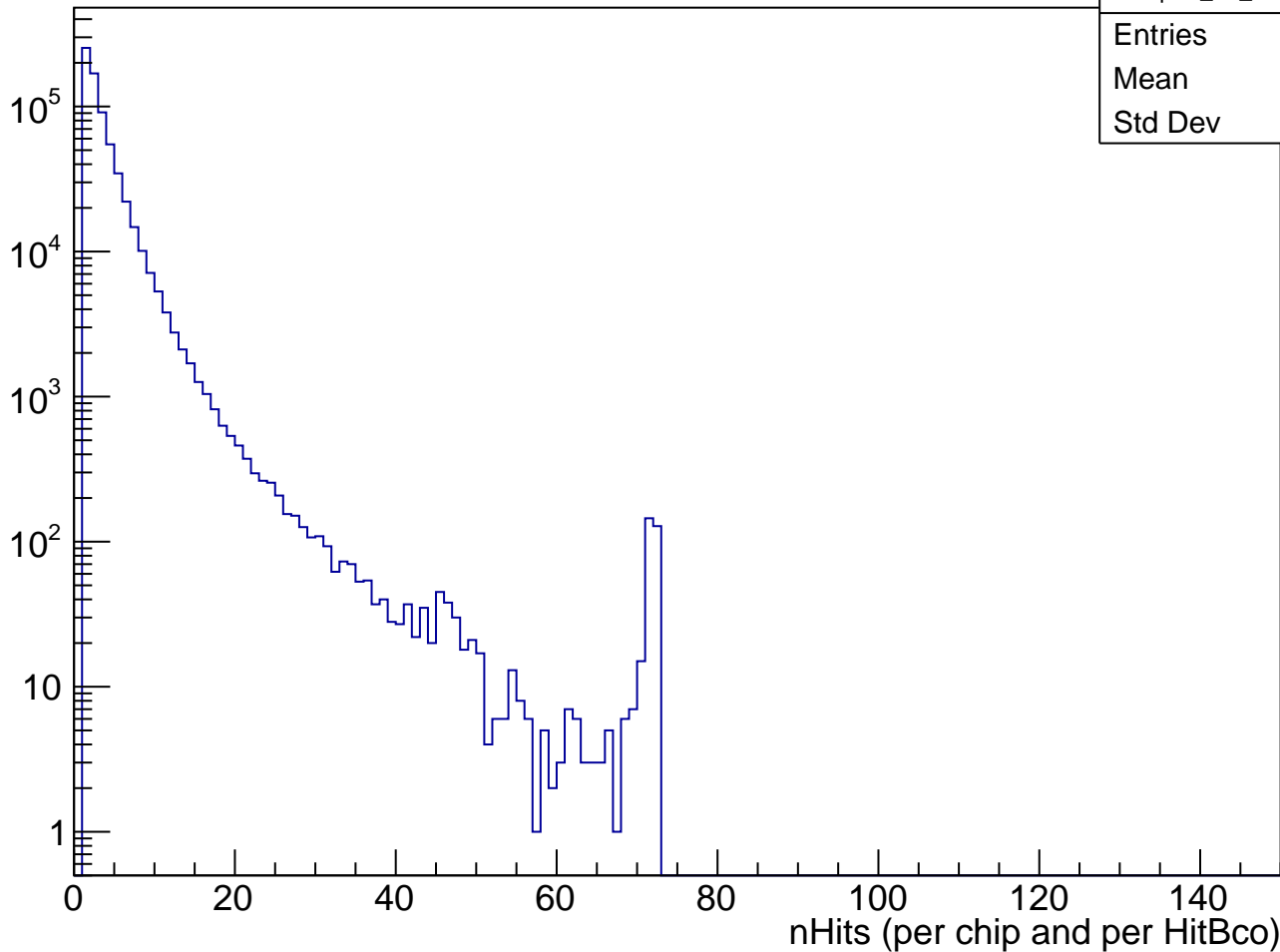


nChipHit_F4_Fch0_Chip20	
Entries	671989
Mean	2.971
Std Dev	3.448

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip21

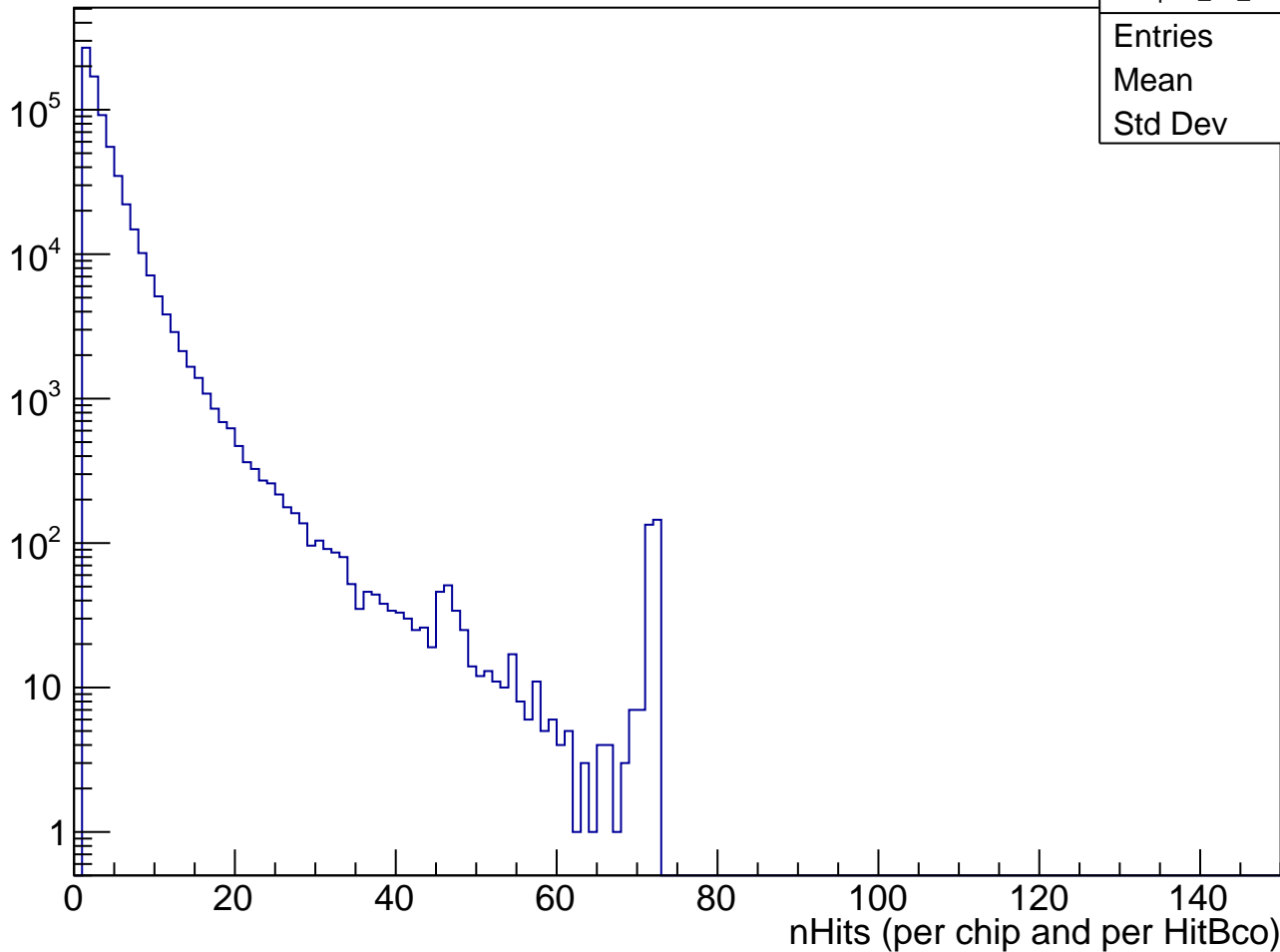
Entries



nChipHit_F4_Fch0_Chip21	
Entries	680816
Mean	2.945
Std Dev	3.433

nChipHit_F4_Fch0_Chip22

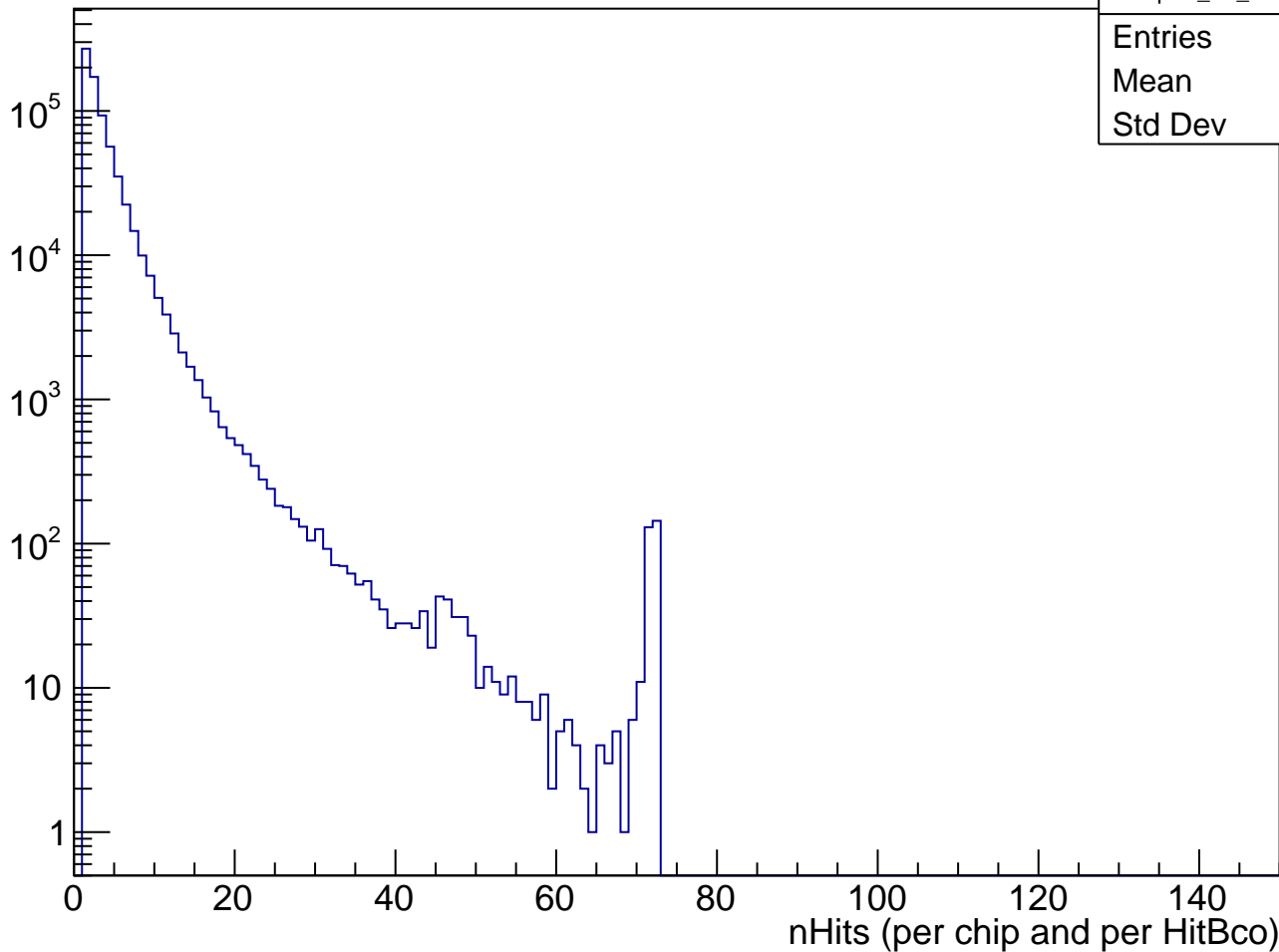
Entries



nChipHit_F4_Fch0_Chip22	
Entries	698821
Mean	2.914
Std Dev	3.434

nChipHit_F4_Fch0_Chip23

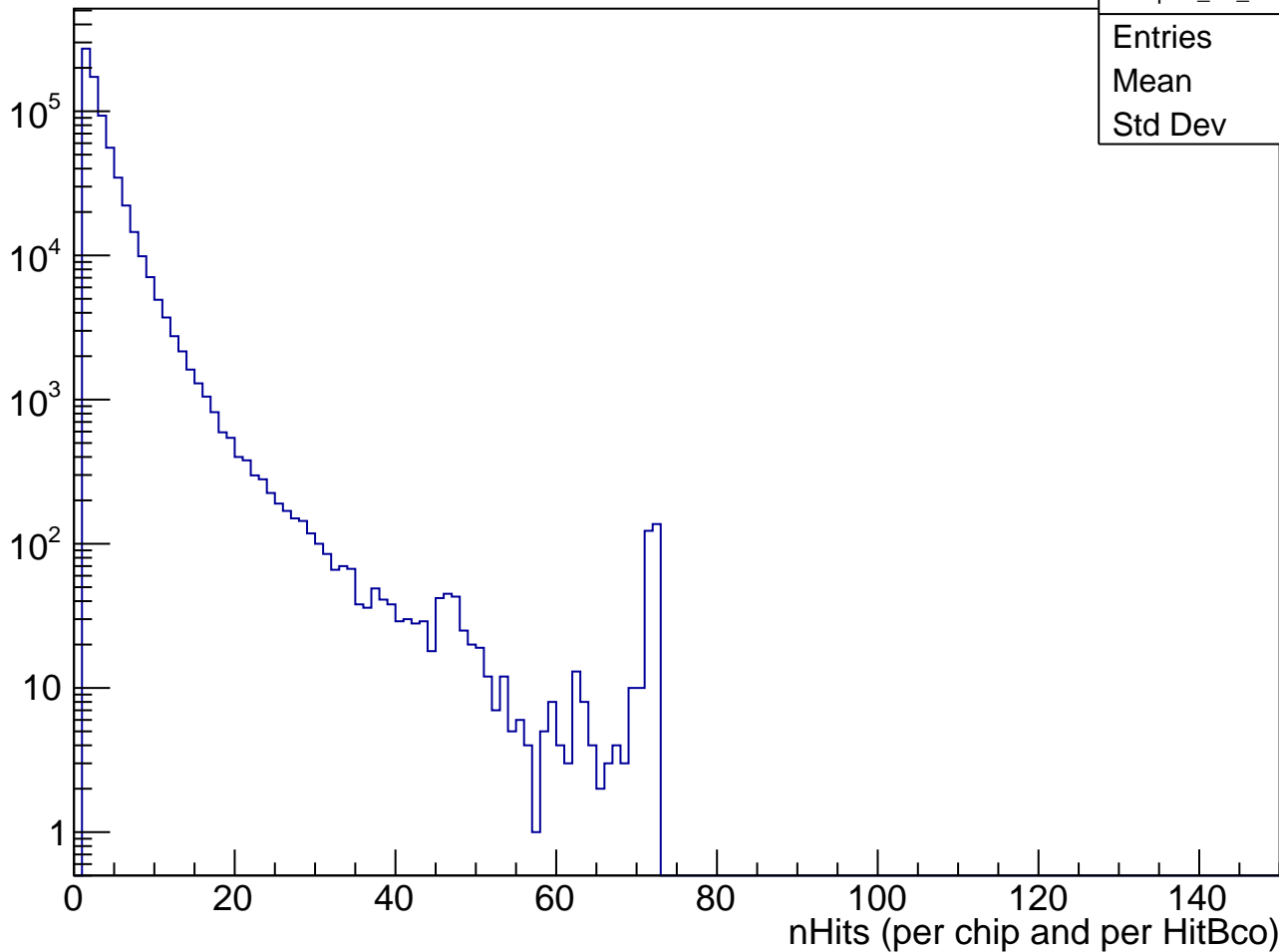
Entries



nChipHit_F4_Fch0_Chip23	
Entries	705211
Mean	2.905
Std Dev	3.41

nChipHit_F4_Fch0_Chip24

Entries

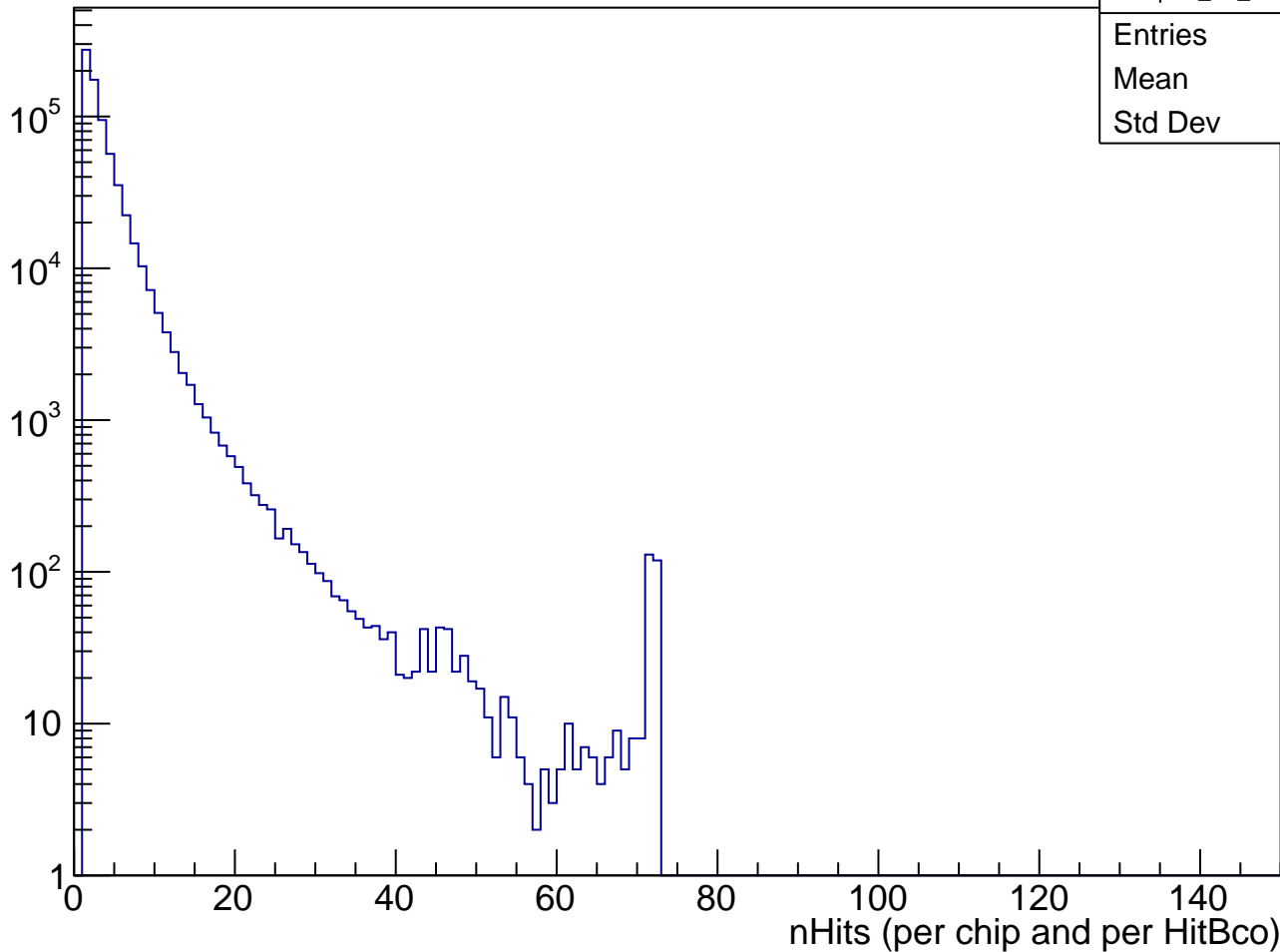


nChipHit_F4_Fch0_Chip24	
Entries	705261
Mean	2.881
Std Dev	3.377

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip25

Entries

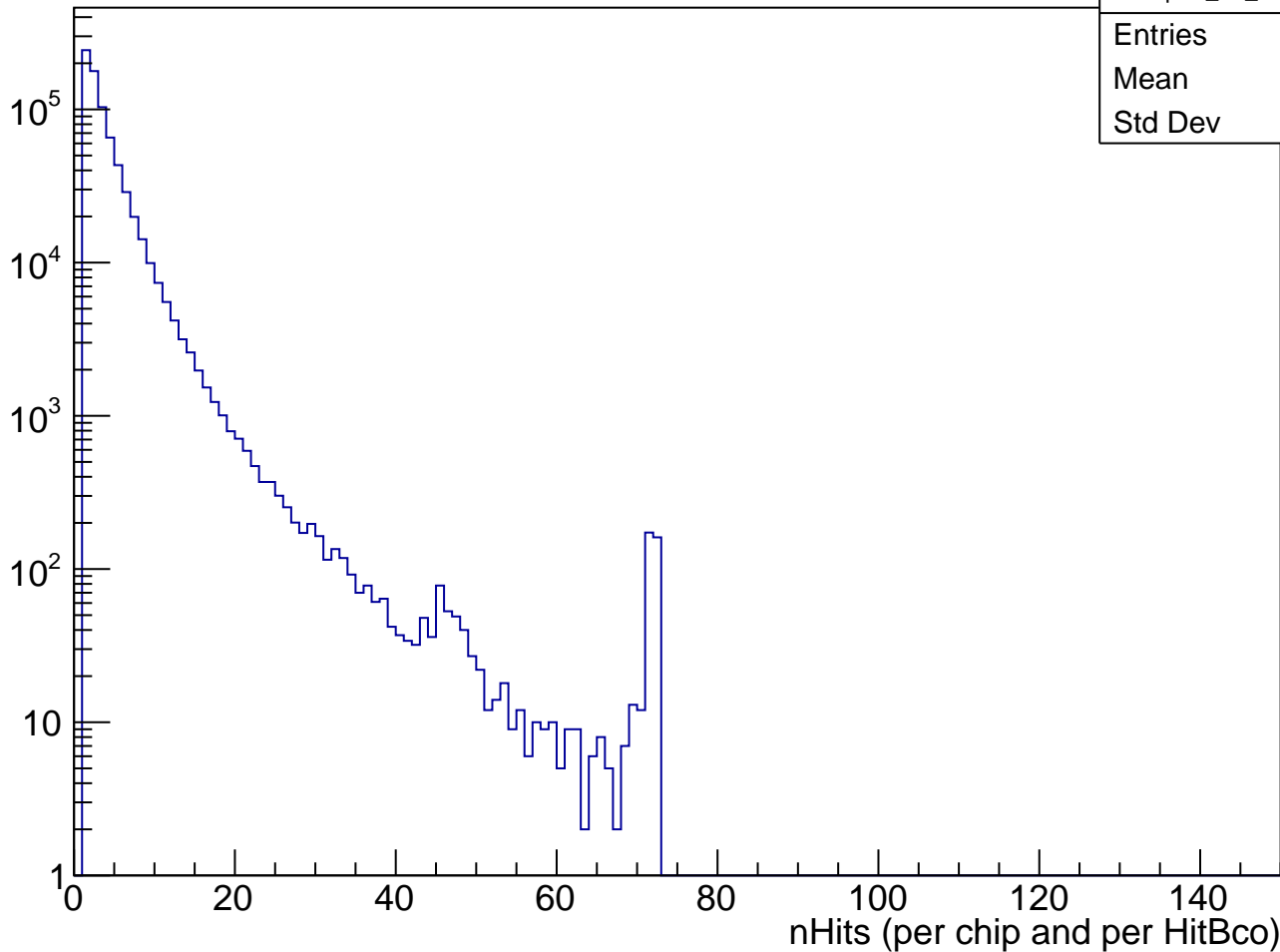


nChipHit_F4_Fch0_Chip25	
Entries	714597
Mean	2.885
Std Dev	3.366

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip3

Entries



nChipHit_F4_Fch0_Chip3

Entries 741276

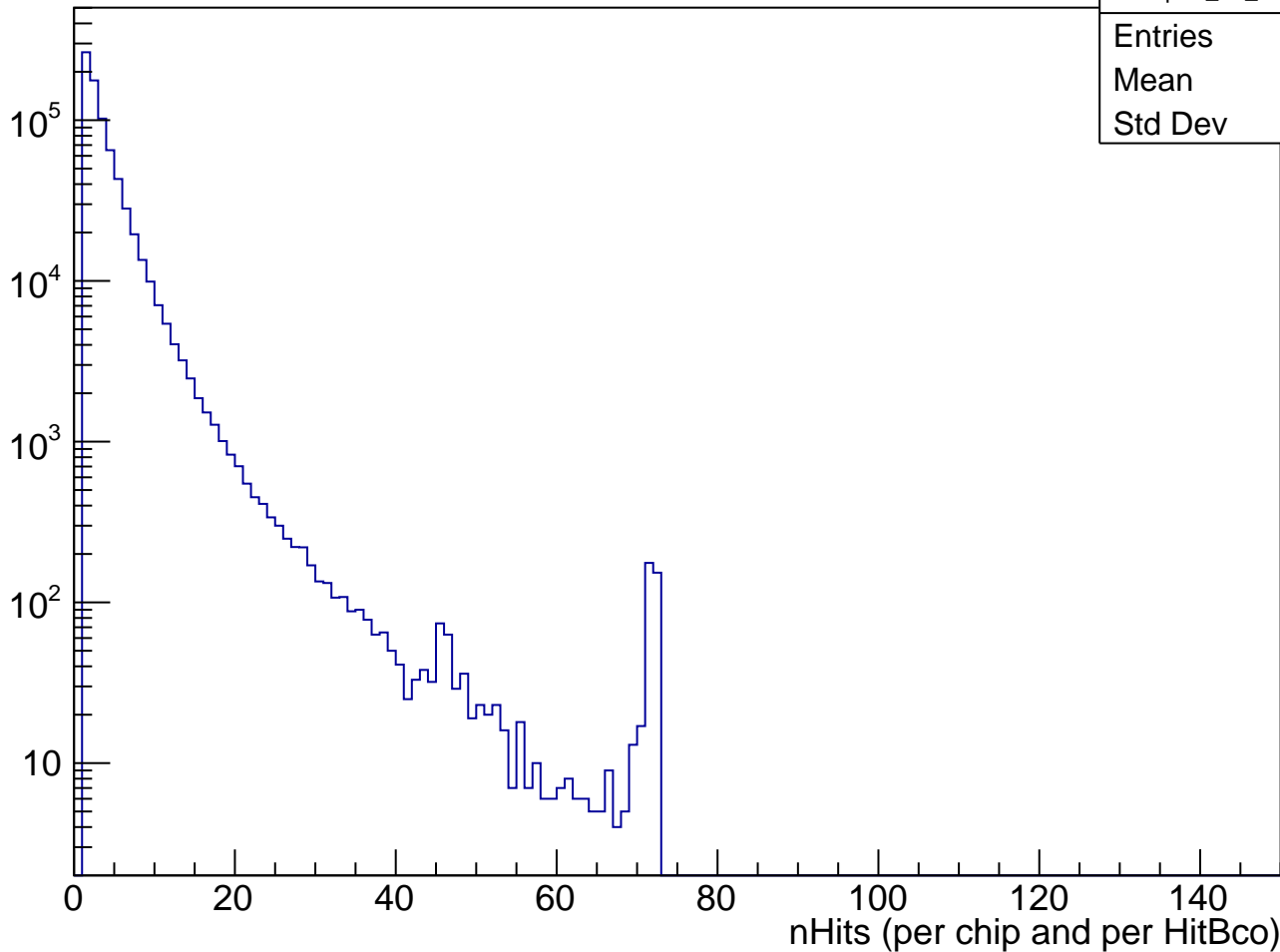
Mean 3.297

Std Dev 3.825

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip4

Entries



nChipHit_F4_Fch0_Chip4

Entries 756991

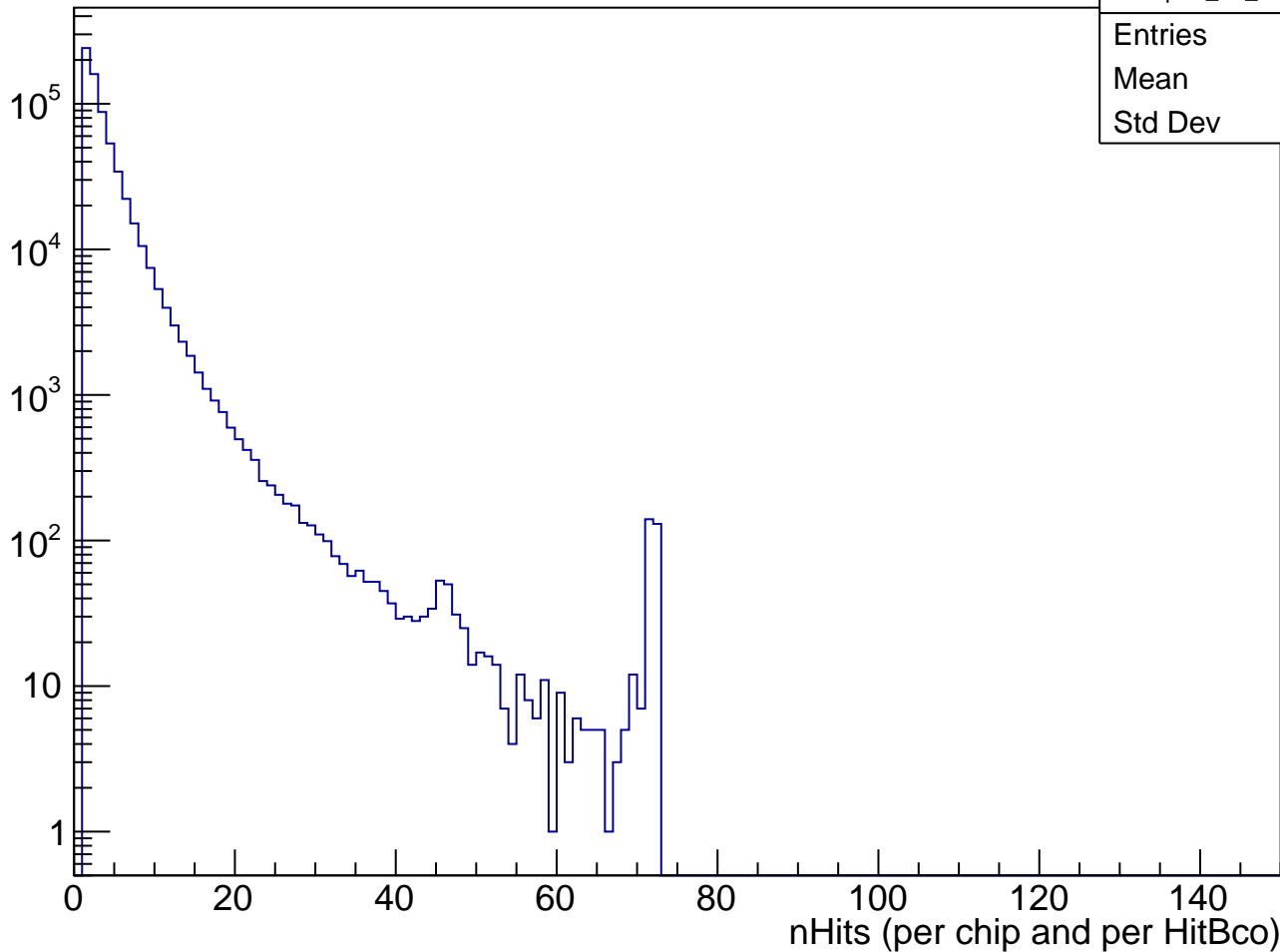
Mean 3.216

Std Dev 3.782

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip5

Entries



nChipHit_F4_Fch0_Chip5

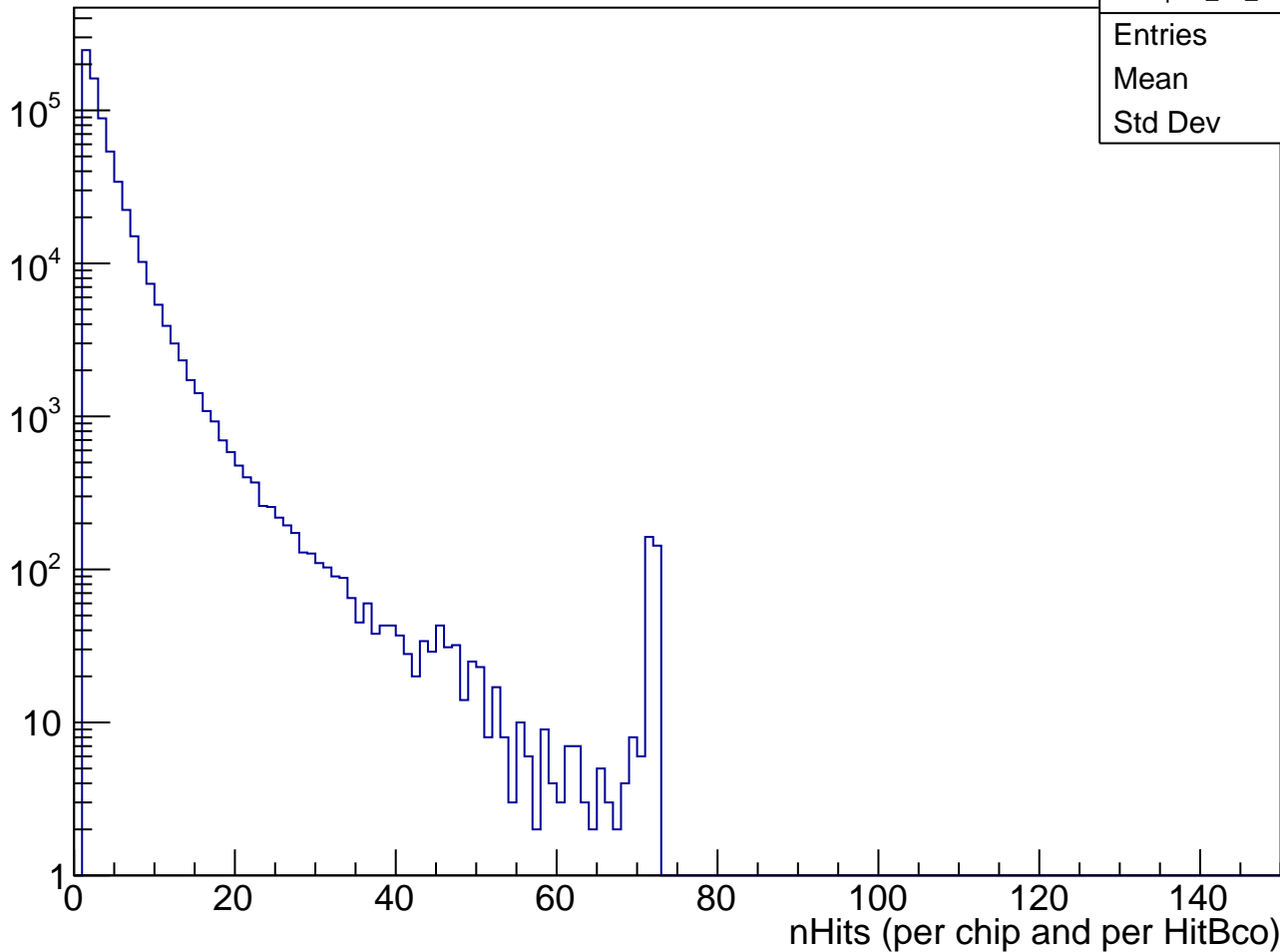
Entries 657623

Mean 3.032

Std Dev 3.575

nChipHit_F4_Fch0_Chip6

Entries



nChipHit_F4_Fch0_Chip6

Entries 665875

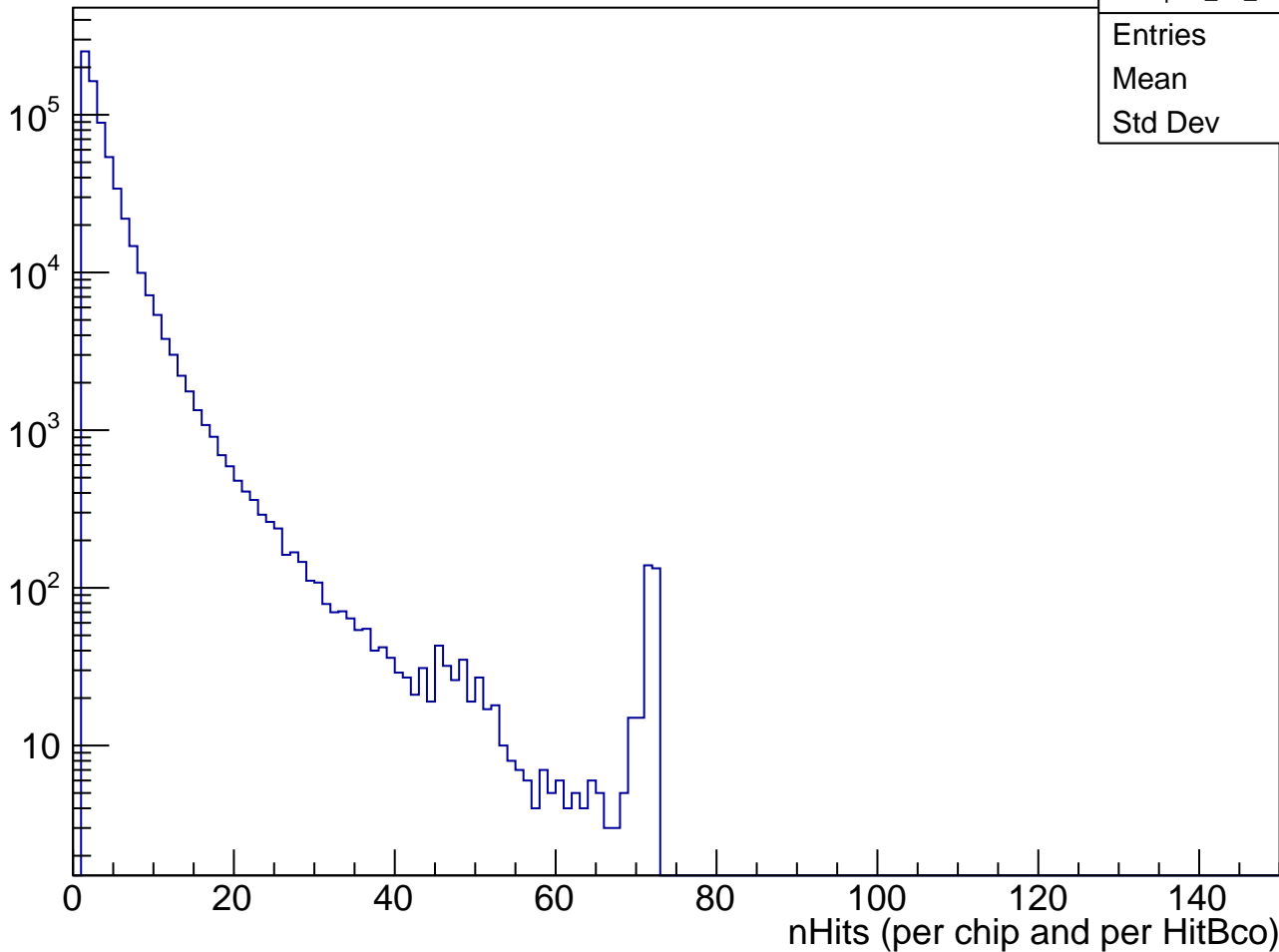
Mean 3.006

Std Dev 3.566

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip7

Entries

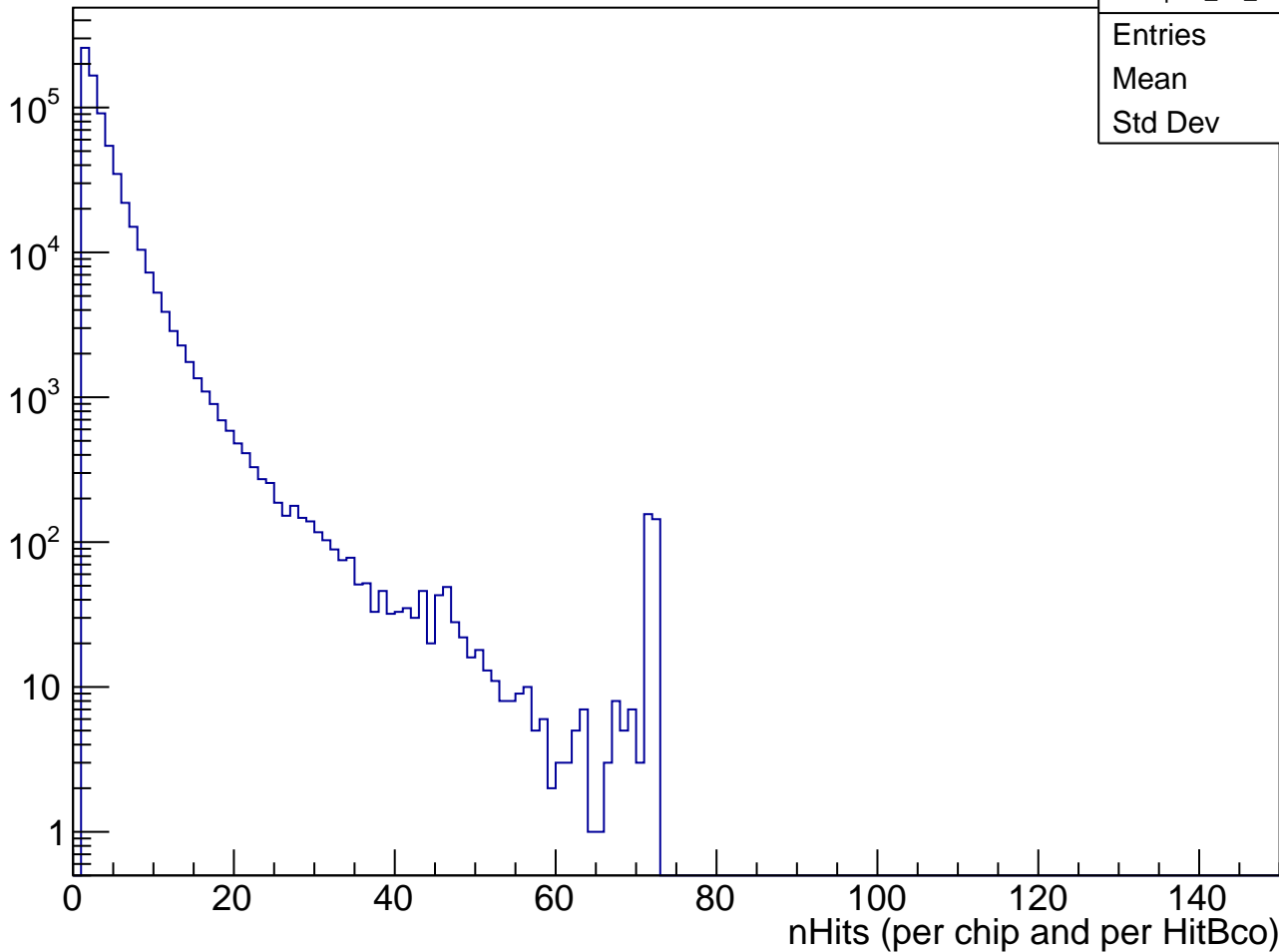


nChipHit_F4_Fch0_Chip7	
Entries	671400
Mean	2.974
Std Dev	3.526

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip8

Entries



nChipHit_F4_Fch0_Chip8

Entries 683992

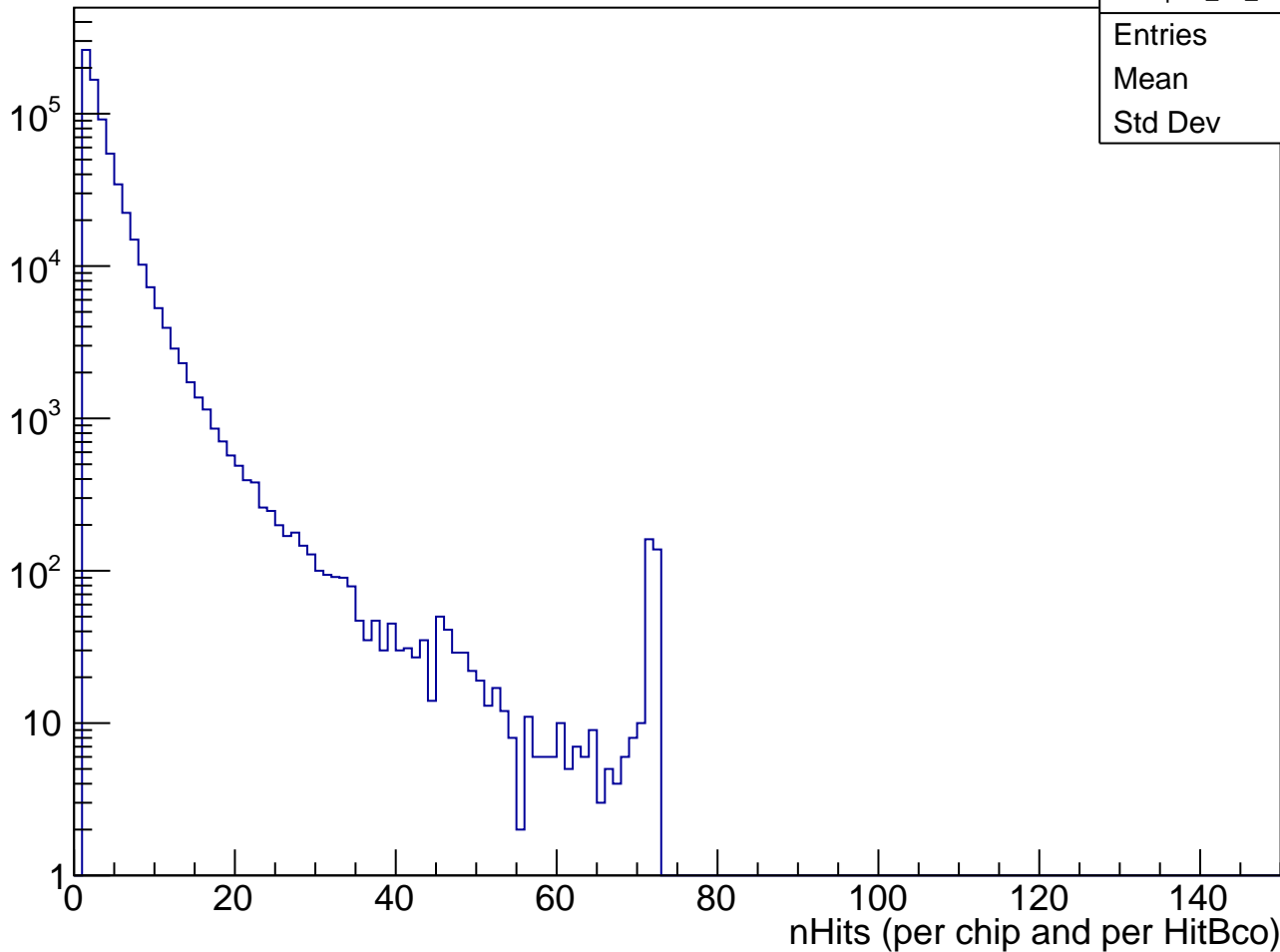
Mean 2.964

Std Dev 3.516

nHits (per chip and per HitBco)

nChipHit_F4_Fch0_Chip9

Entries

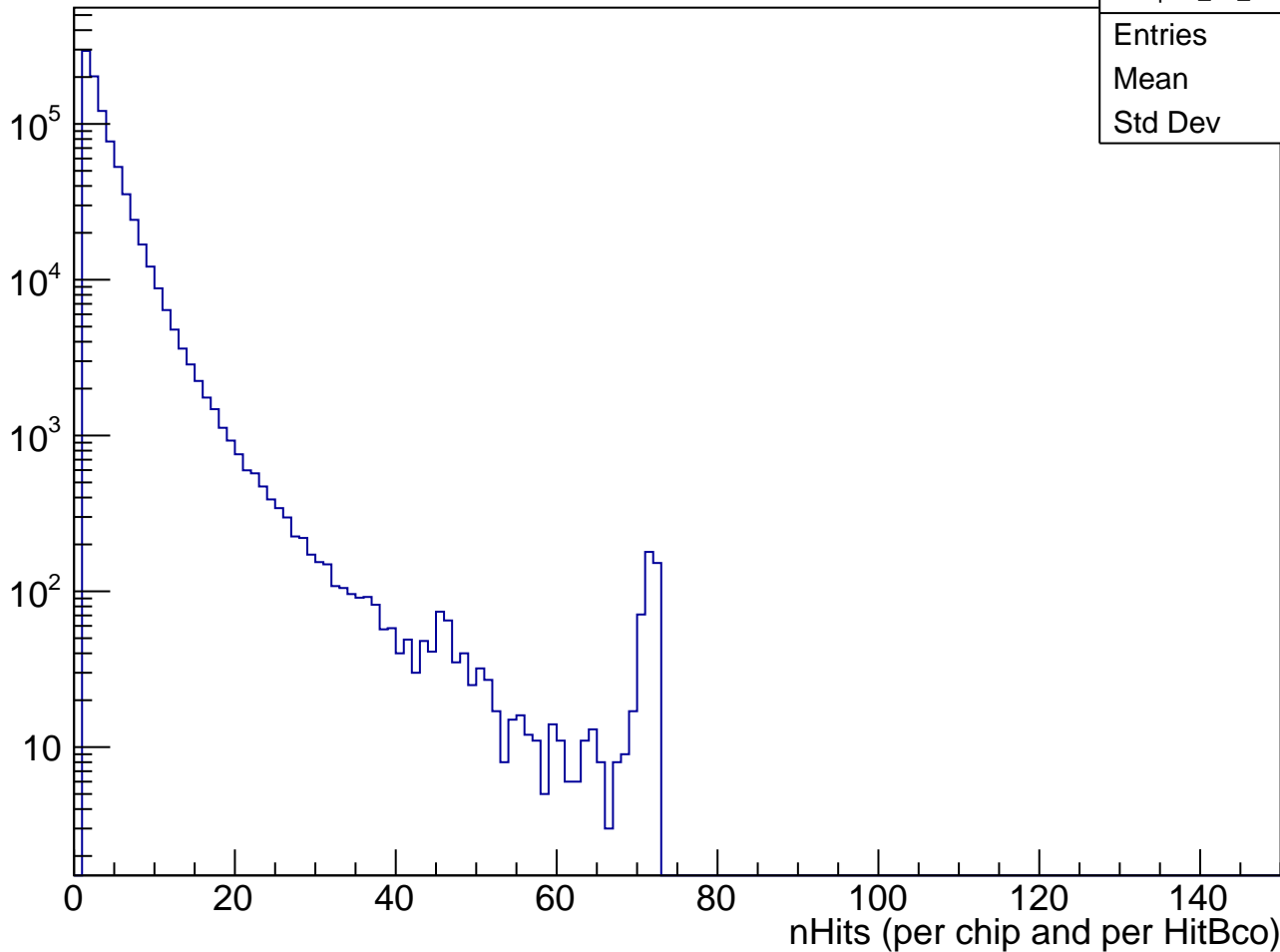


nChipHit_F4_Fch0_Chip9	
Entries	689496
Mean	2.954
Std Dev	3.527

nHits (per chip and per HitBco)

nChipHit_F4_Fch10_Chip0

Entries

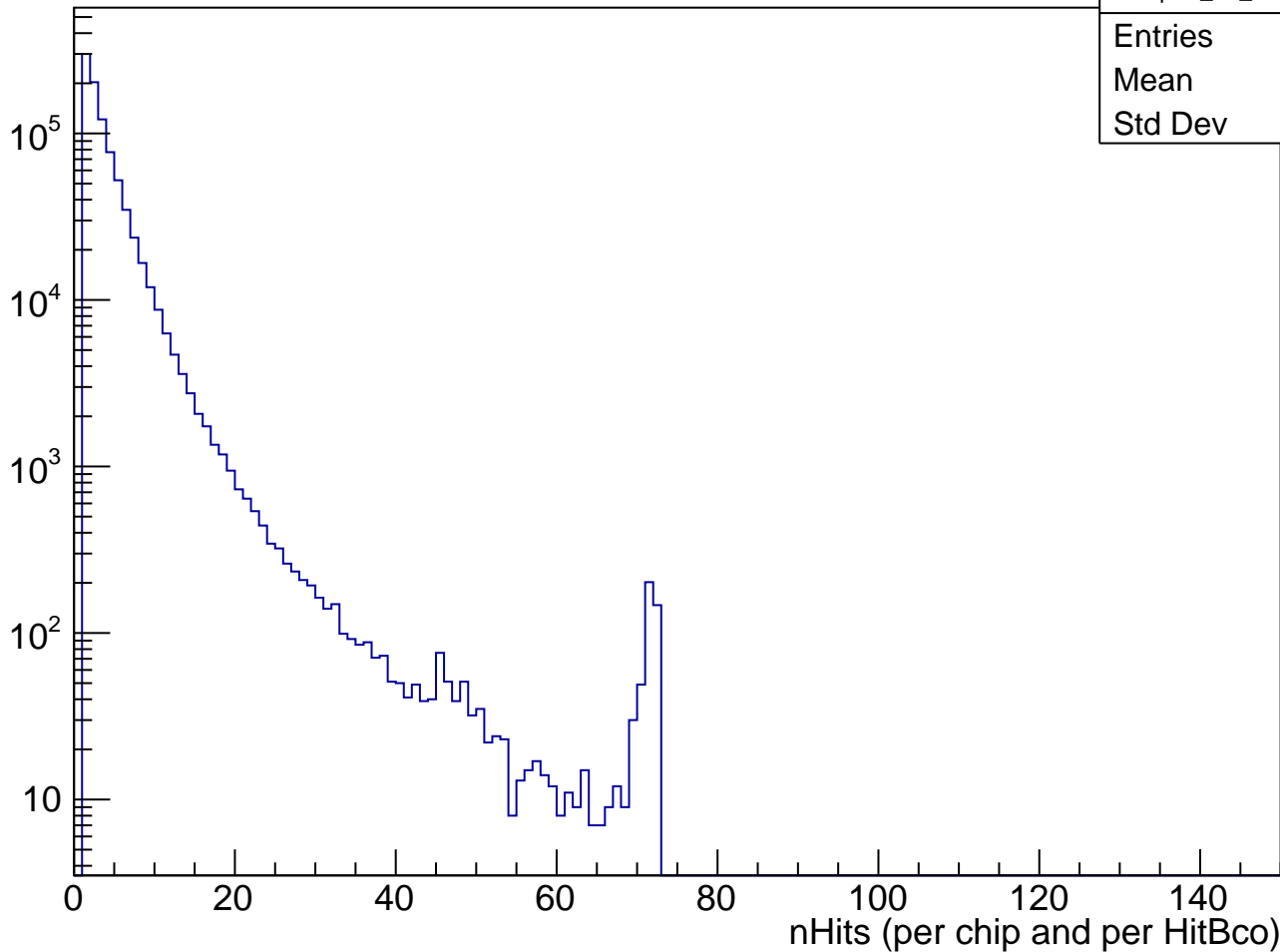


nChipHit_F4_Fch10_Chip0	
Entries	876599
Mean	3.285
Std Dev	3.778

nHits (per chip and per HitBco)

nChipHit_F4_Fch10_Chip1

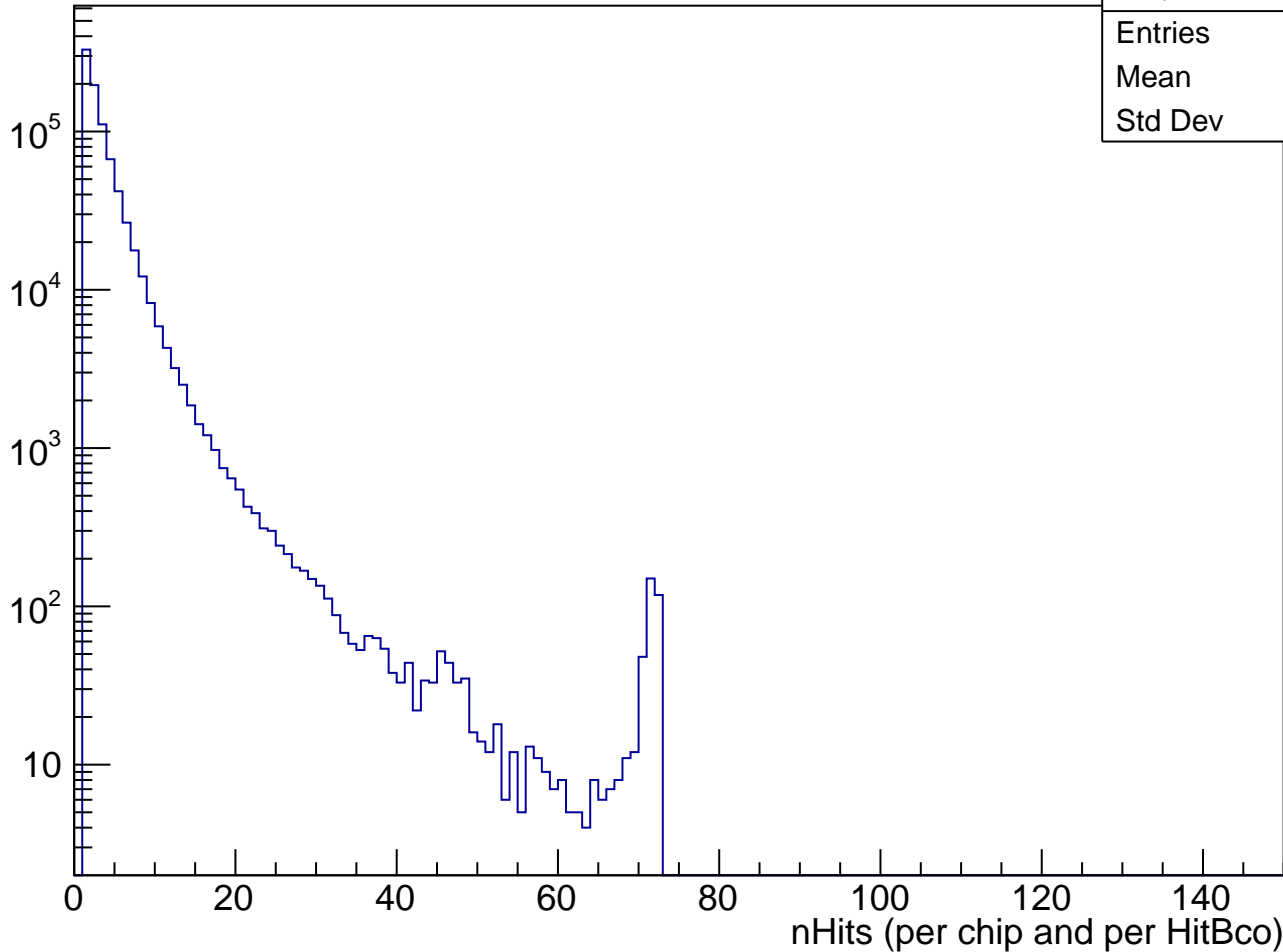
Entries



nChipHit_F4_Fch10_Chip1	
Entries	880911
Mean	3.254
Std Dev	3.783

nChipHit_F4_Fch10_Chip10

Entries

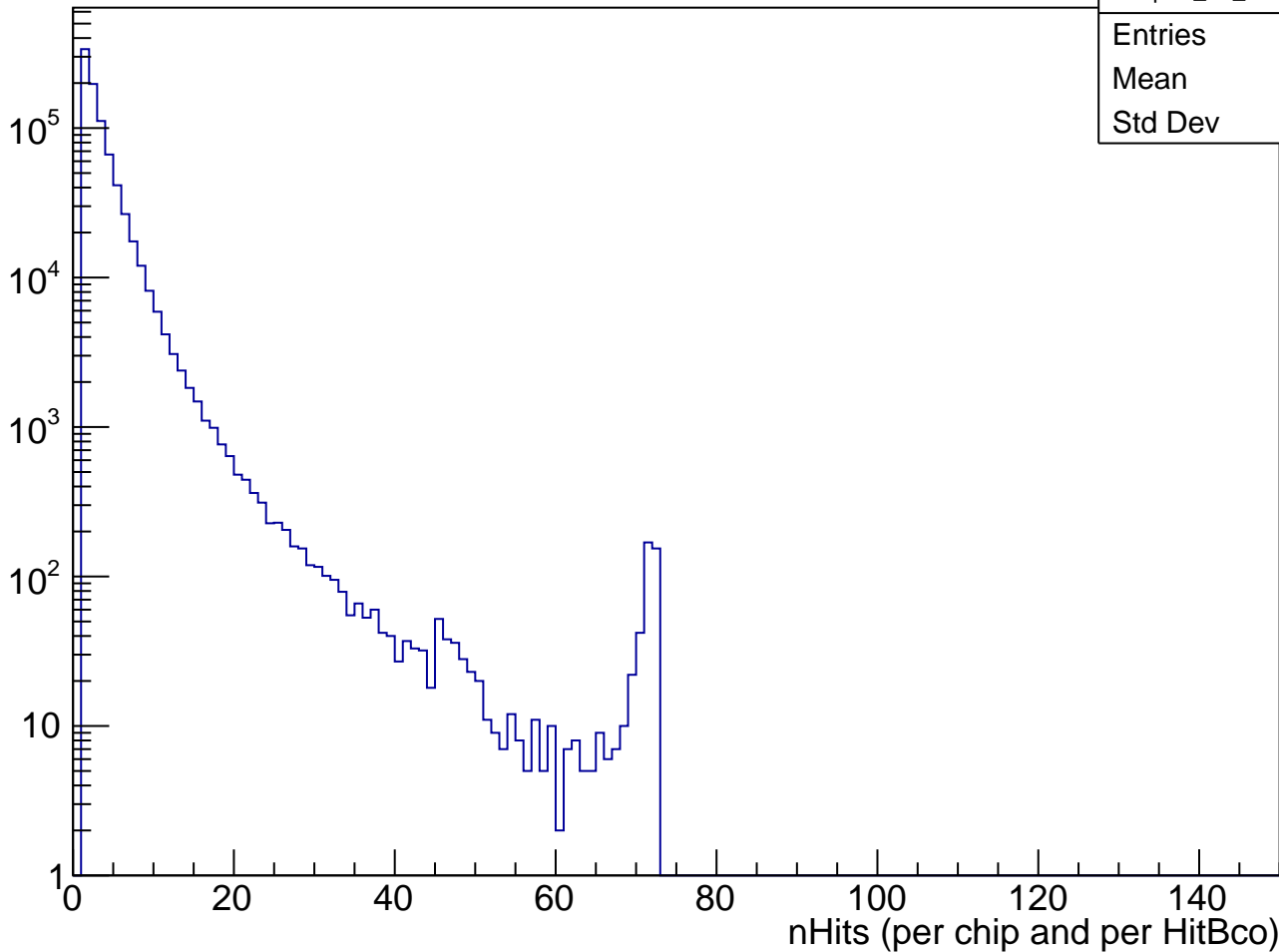


nChipHit_F4_Fch10_Chip10	
Entries	837277
Mean	2.883
Std Dev	3.393

nHits (per chip and per HitBco)

nChipHit_F4_Fch10_Chip11

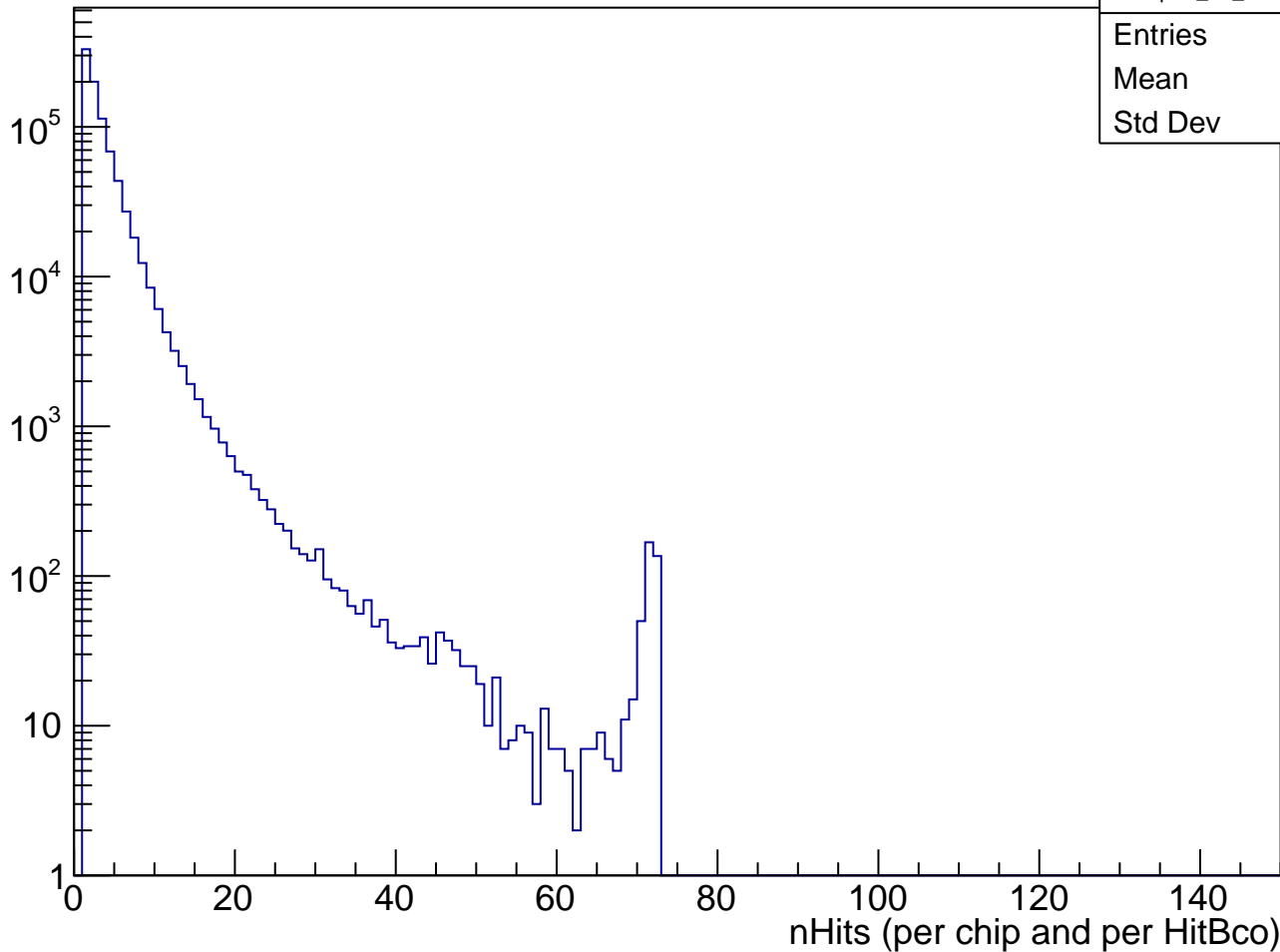
Entries



nChipHit_F4_Fch10_Chip11	
Entries	844813
Mean	2.851
Std Dev	3.387

nChipHit_F4_Fch10_Chip12

Entries



nChipHit_F4_Fch10_Chip12

Entries 850064

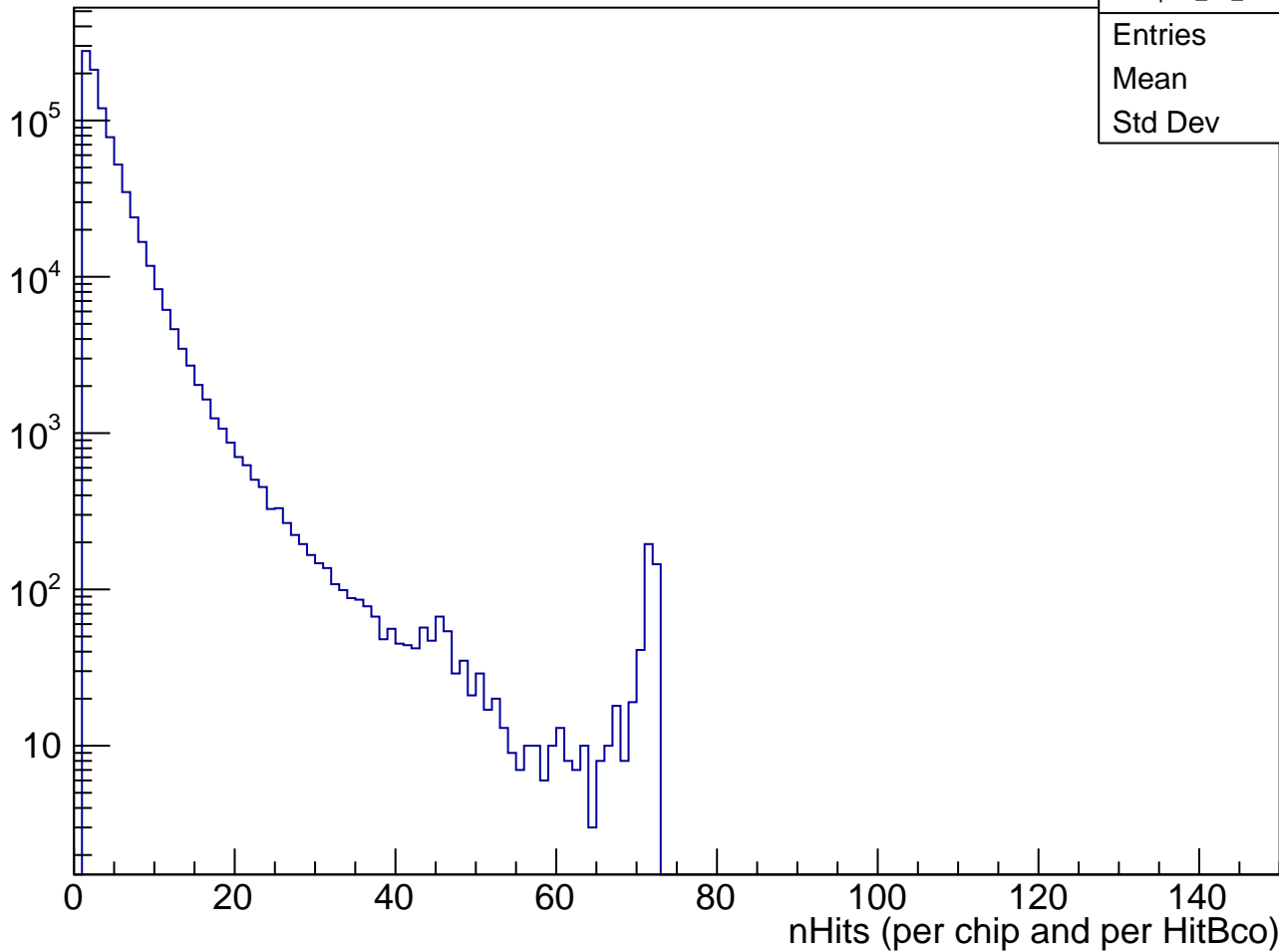
Mean 2.891

Std Dev 3.391

nHits (per chip and per HitBco)

nChipHit_F4_Fch10_Chip13

Entries



nChipHit_F4_Fch10_Chip13

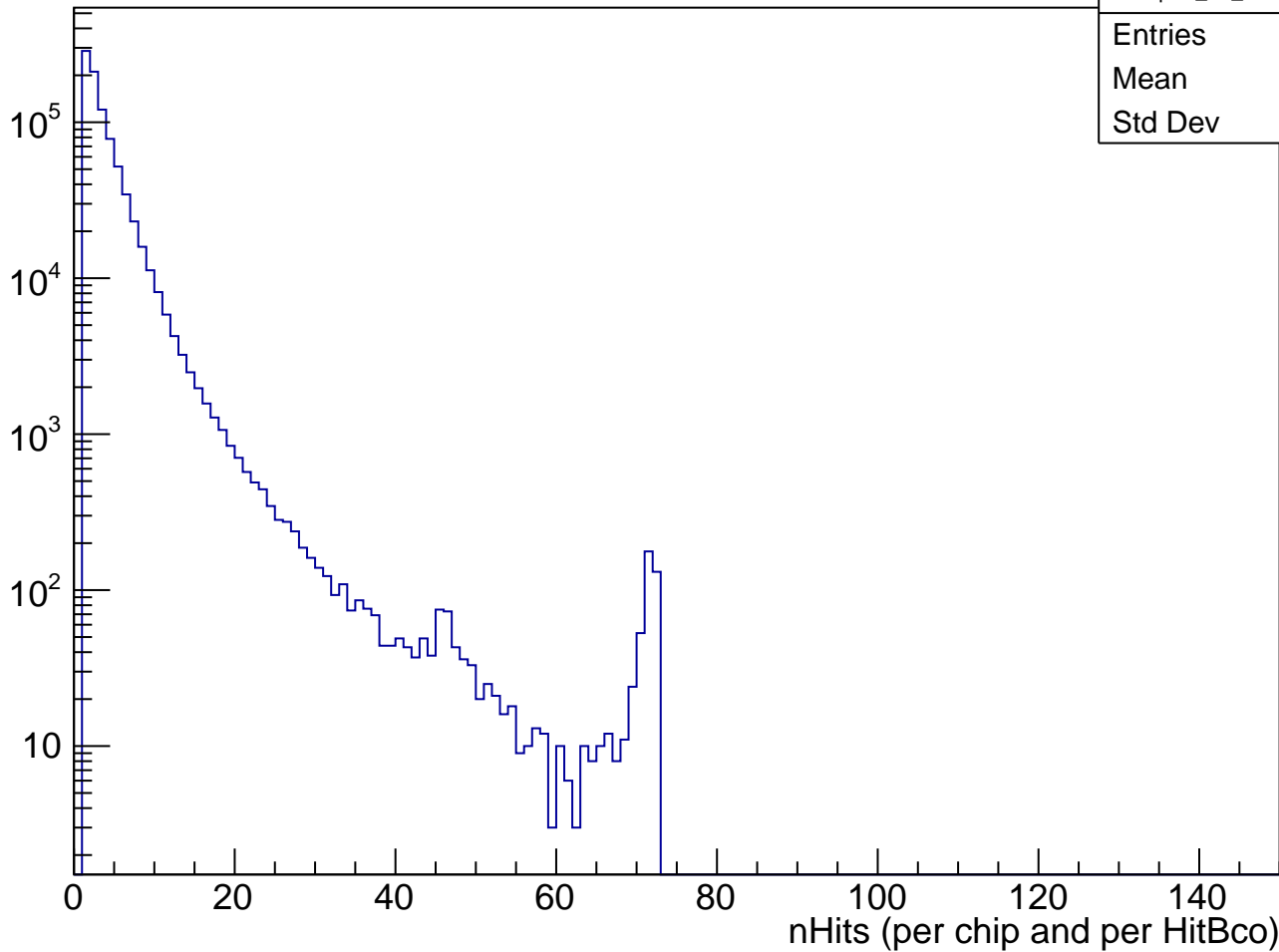
Entries 864625

Mean 3.275

Std Dev 3.711

nChipHit_F4_Fch10_Chip14

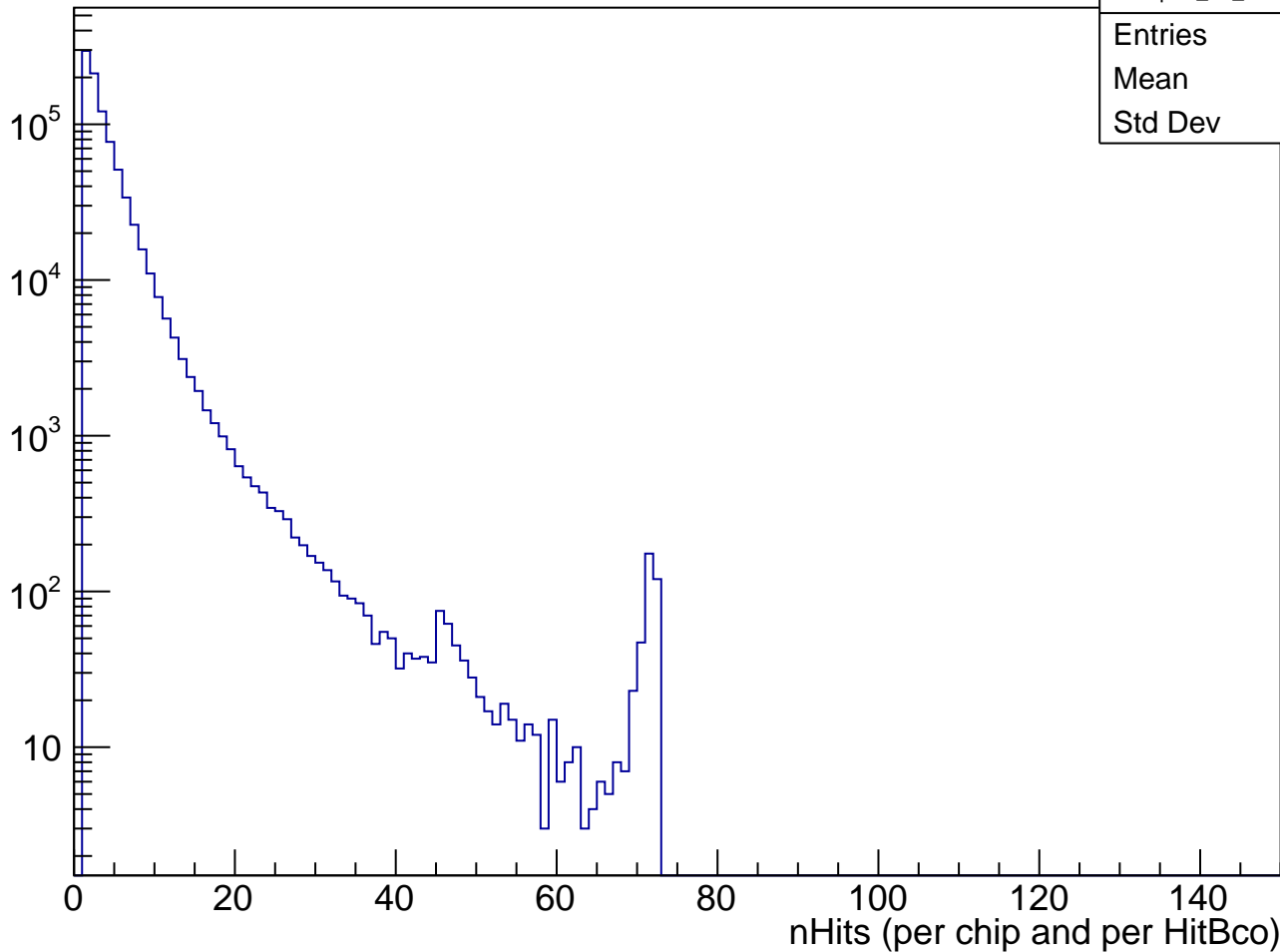
Entries



nChipHit_F4_Fch10_Chip14	
Entries	869433
Mean	3.222
Std Dev	3.669

nChipHit_F4_Fch10_Chip15

Entries



nChipHit_F4_Fch10_Chip15

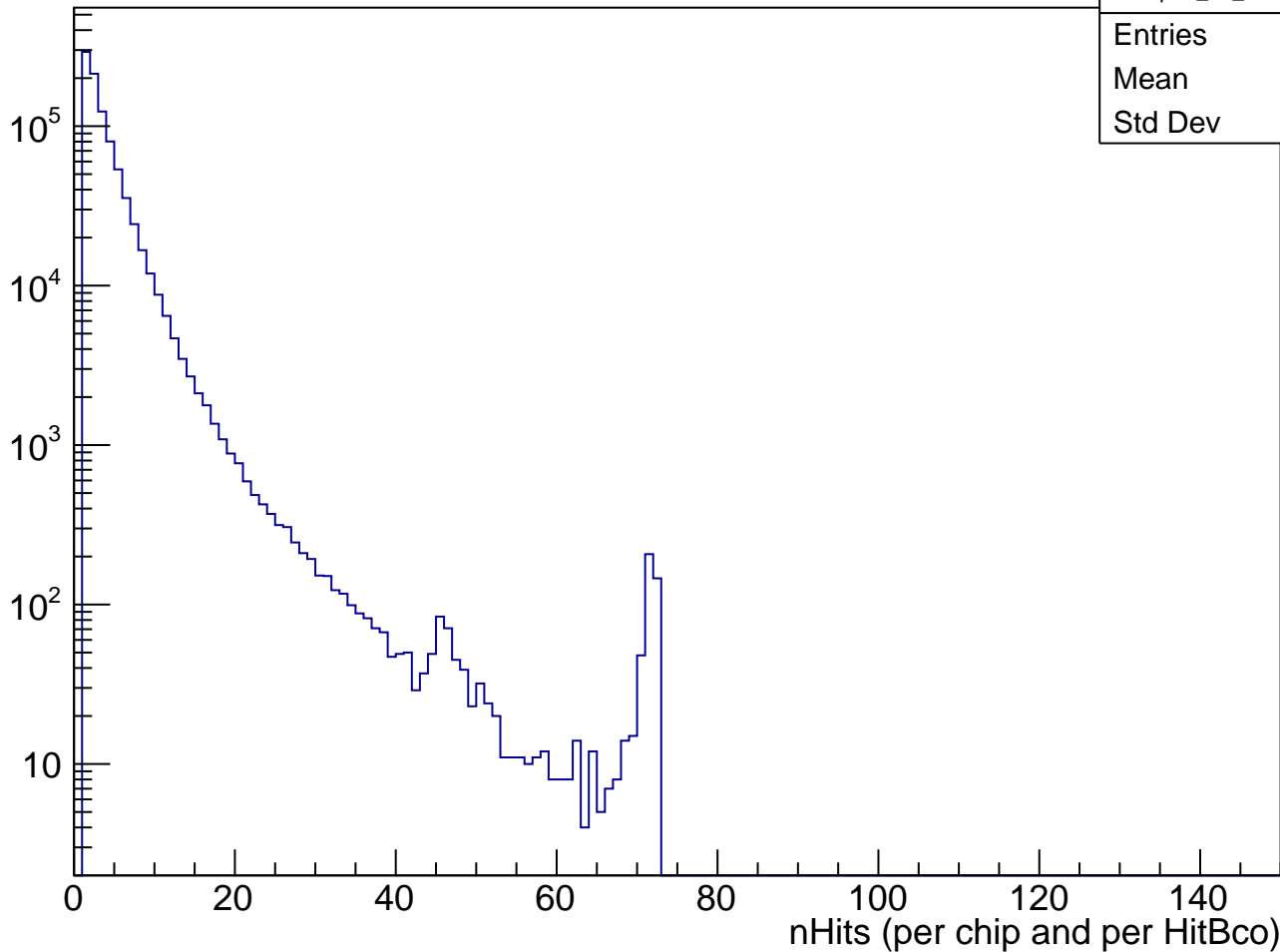
Entries 876085

Mean 3.169

Std Dev 3.603

nChipHit_F4_Fch10_Chip16

Entries



nChipHit_F4_Fch10_Chip16

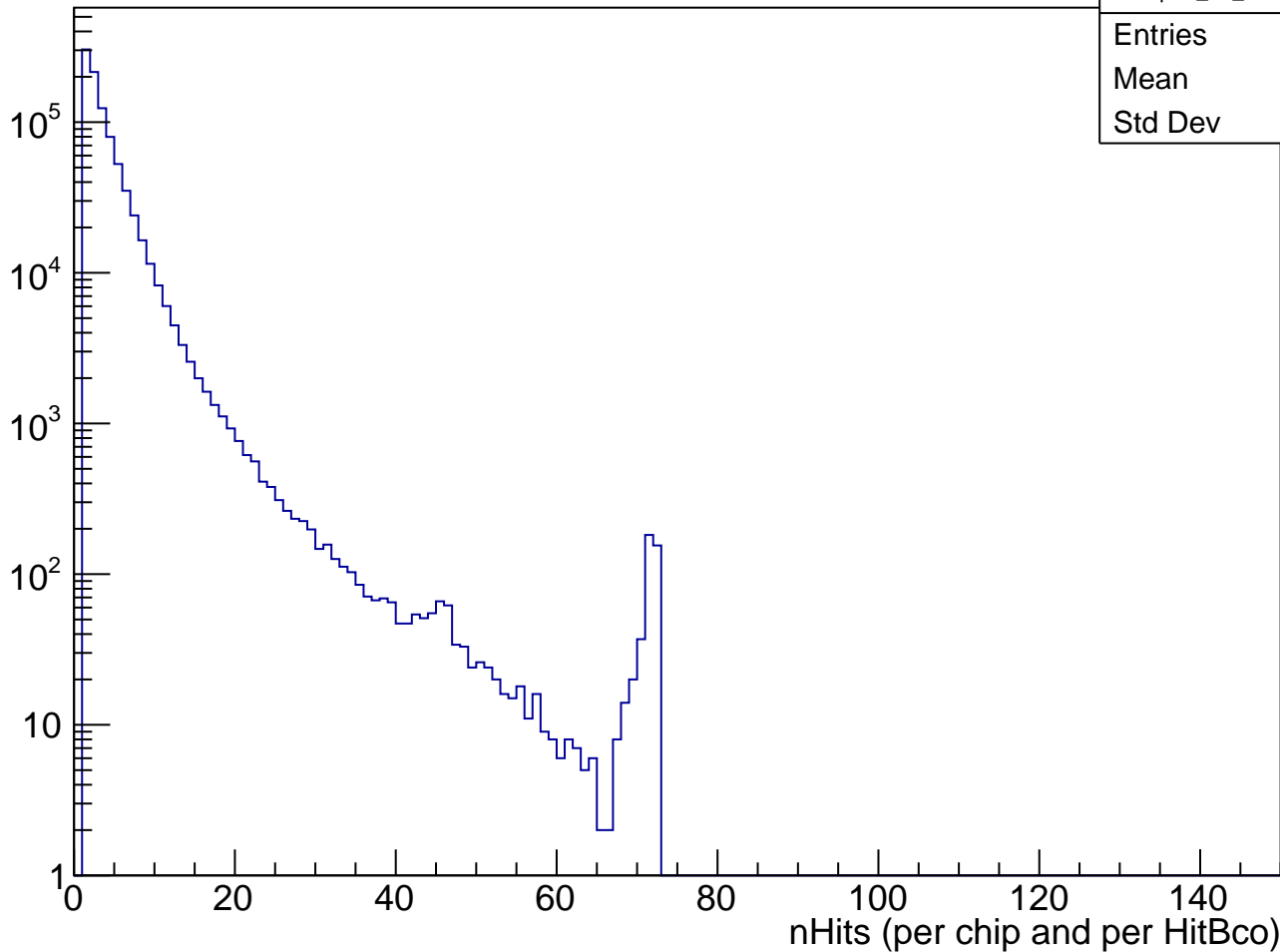
Entries 890180

Mean 3.265

Std Dev 3.734

nChipHit_F4_Fch10_Chip17

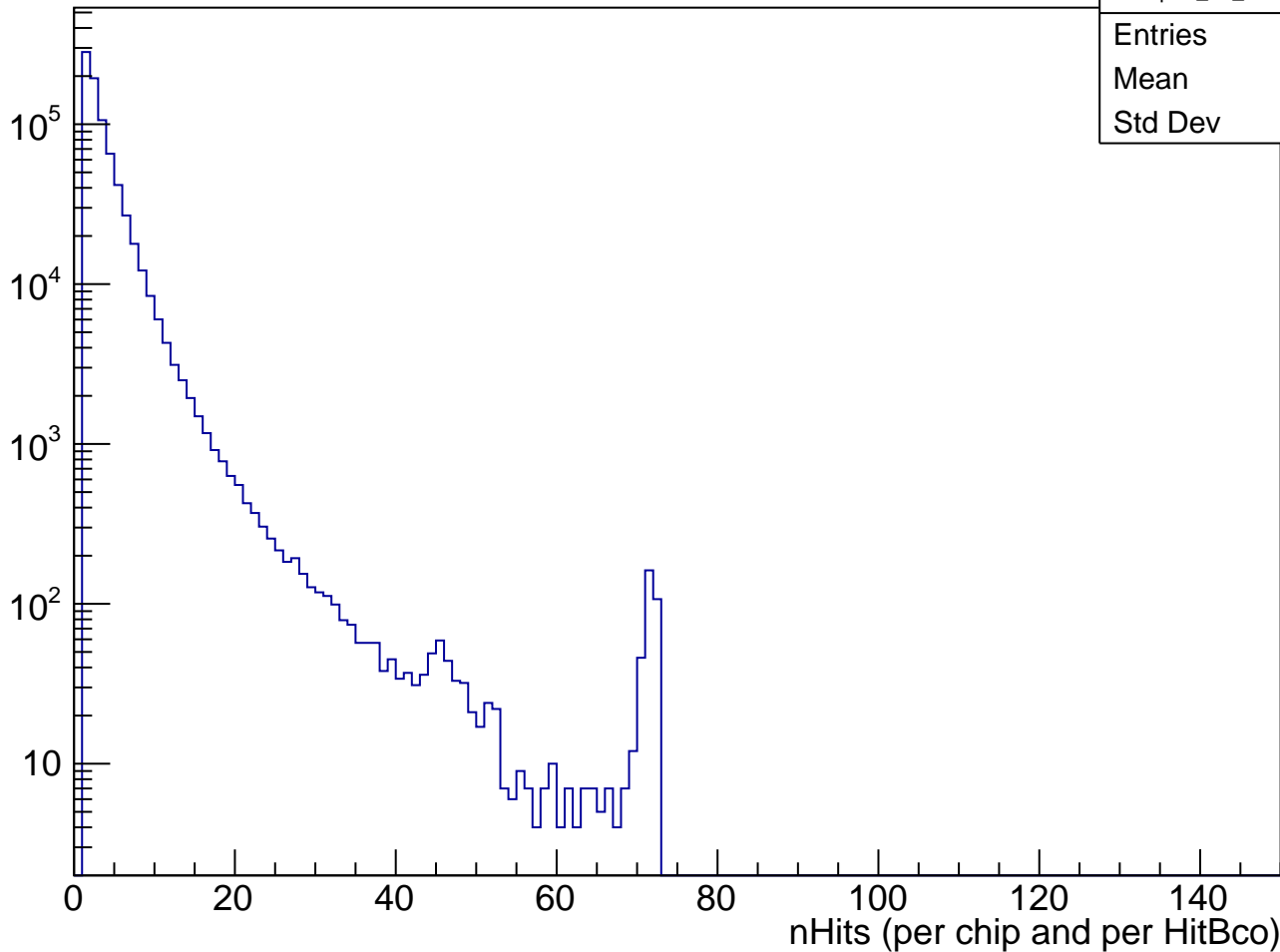
Entries



nChipHit_F4_Fch10_Chip17	
Entries	899773
Mean	3.208
Std Dev	3.679

nChipHit_F4_Fch10_Chip18

Entries



nChipHit_F4_Fch10_Chip18

Entries 782530

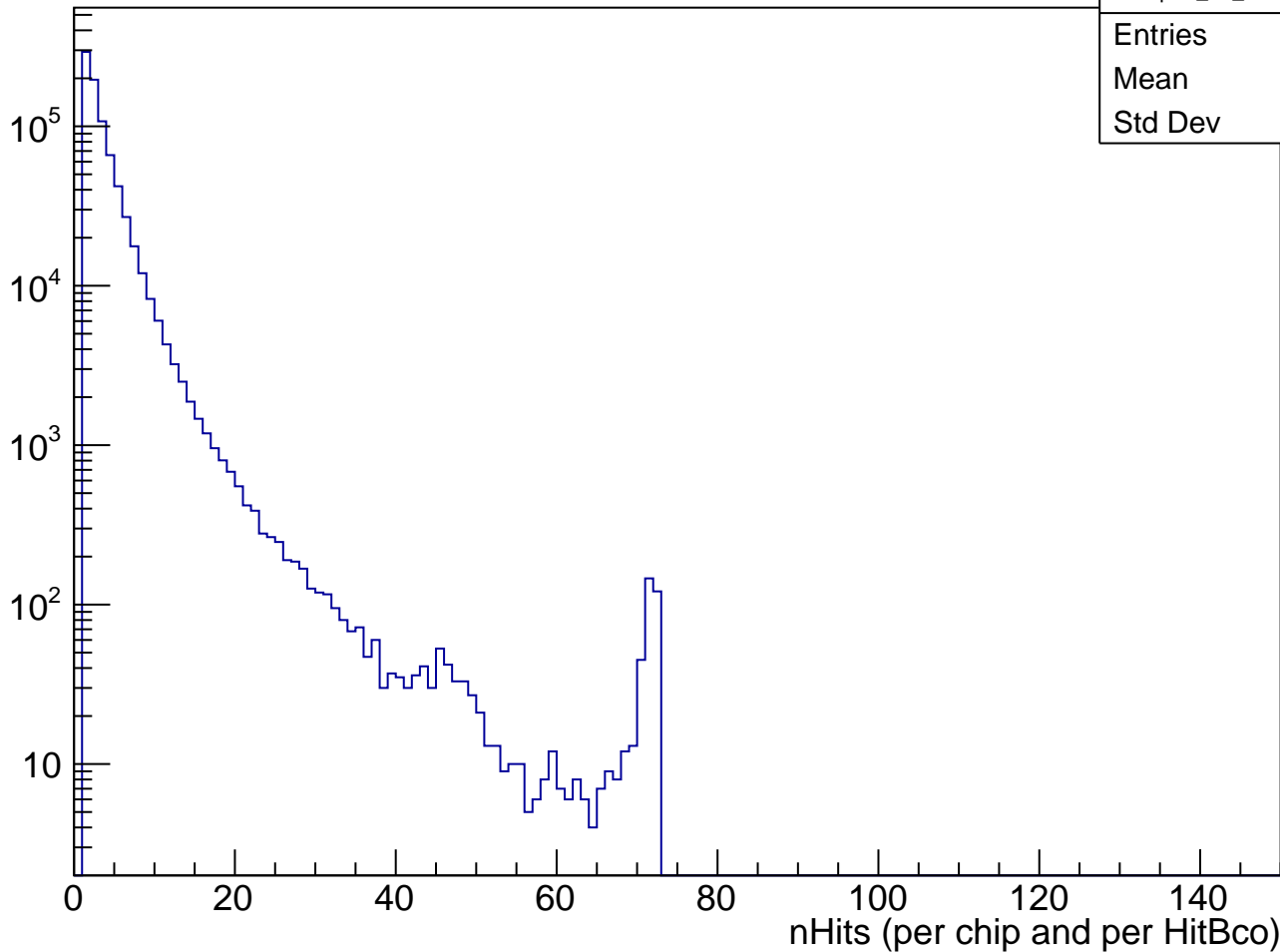
Mean 2.996

Std Dev 3.47

nHits (per chip and per HitBco)

nChipHit_F4_Fch10_Chip19

Entries



nChipHit_F4_Fch10_Chip19

Entries 796852

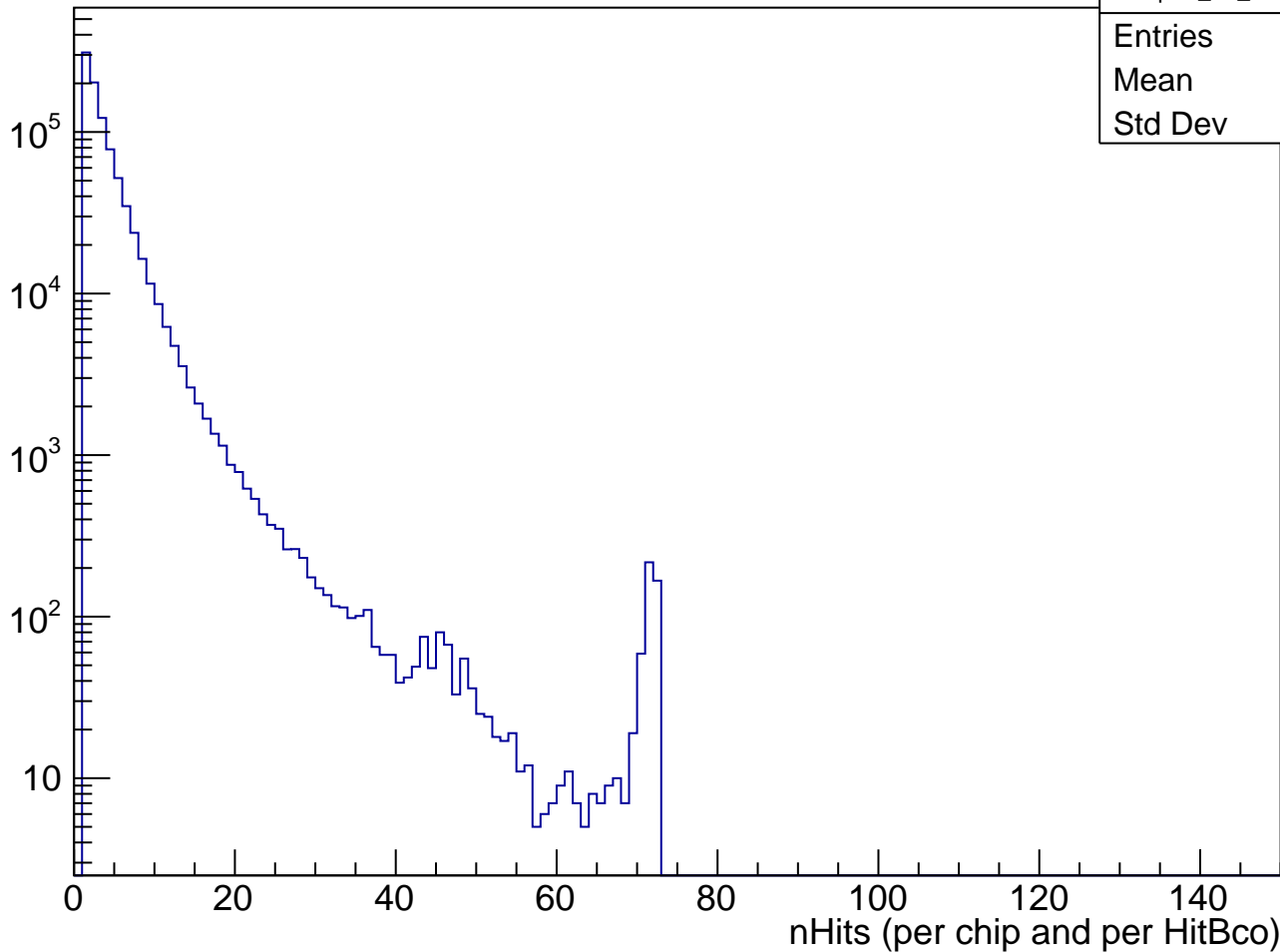
Mean 2.969

Std Dev 3.453

nHits (per chip and per HitBco)

nChipHit_F4_Fch10_Chip2

Entries

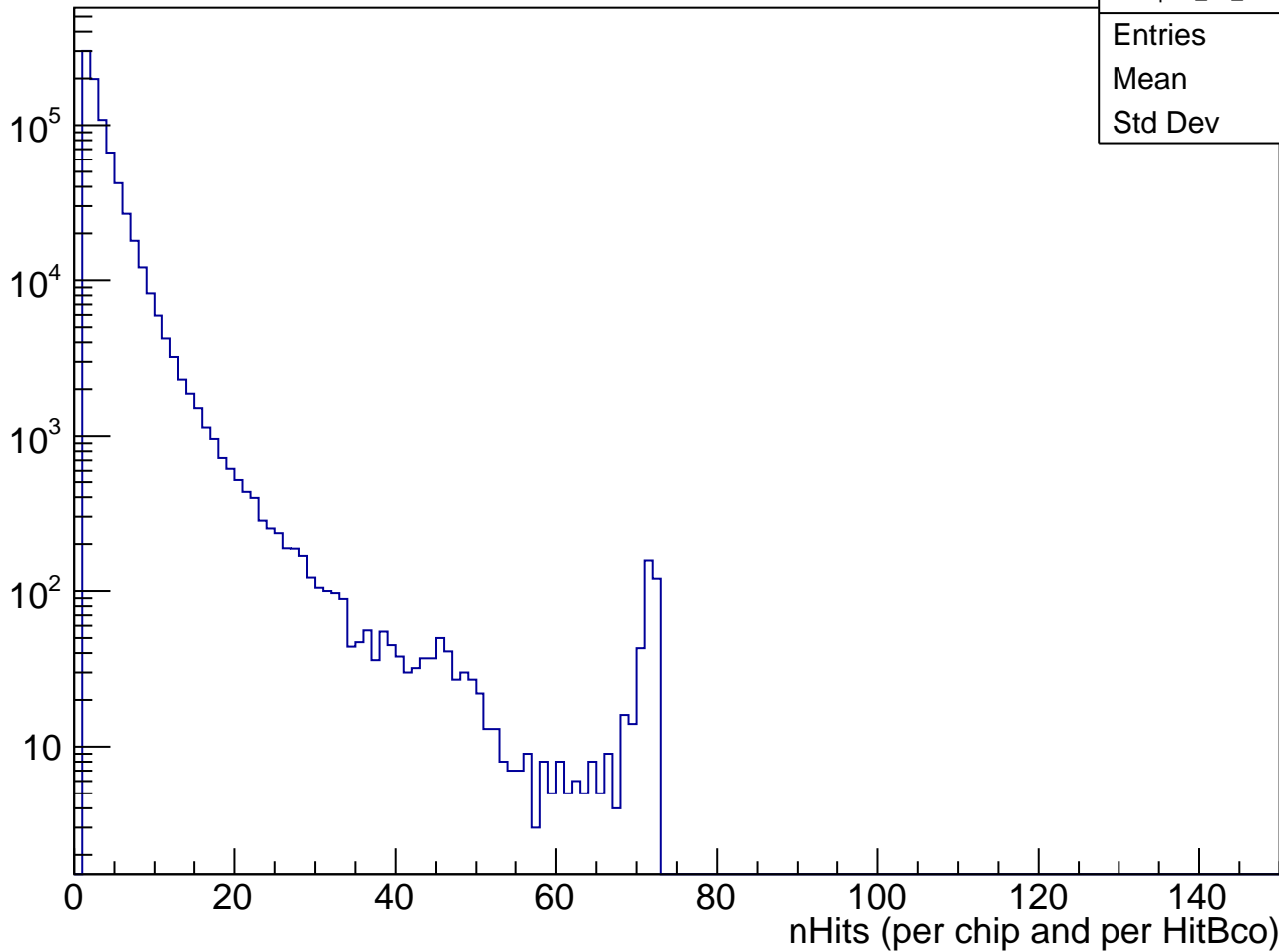


nChipHit_F4_Fch10_Chip2	
Entries	891111
Mean	3.224
Std Dev	3.78

nHits (per chip and per HitBco)

nChipHit_F4_Fch10_Chip20

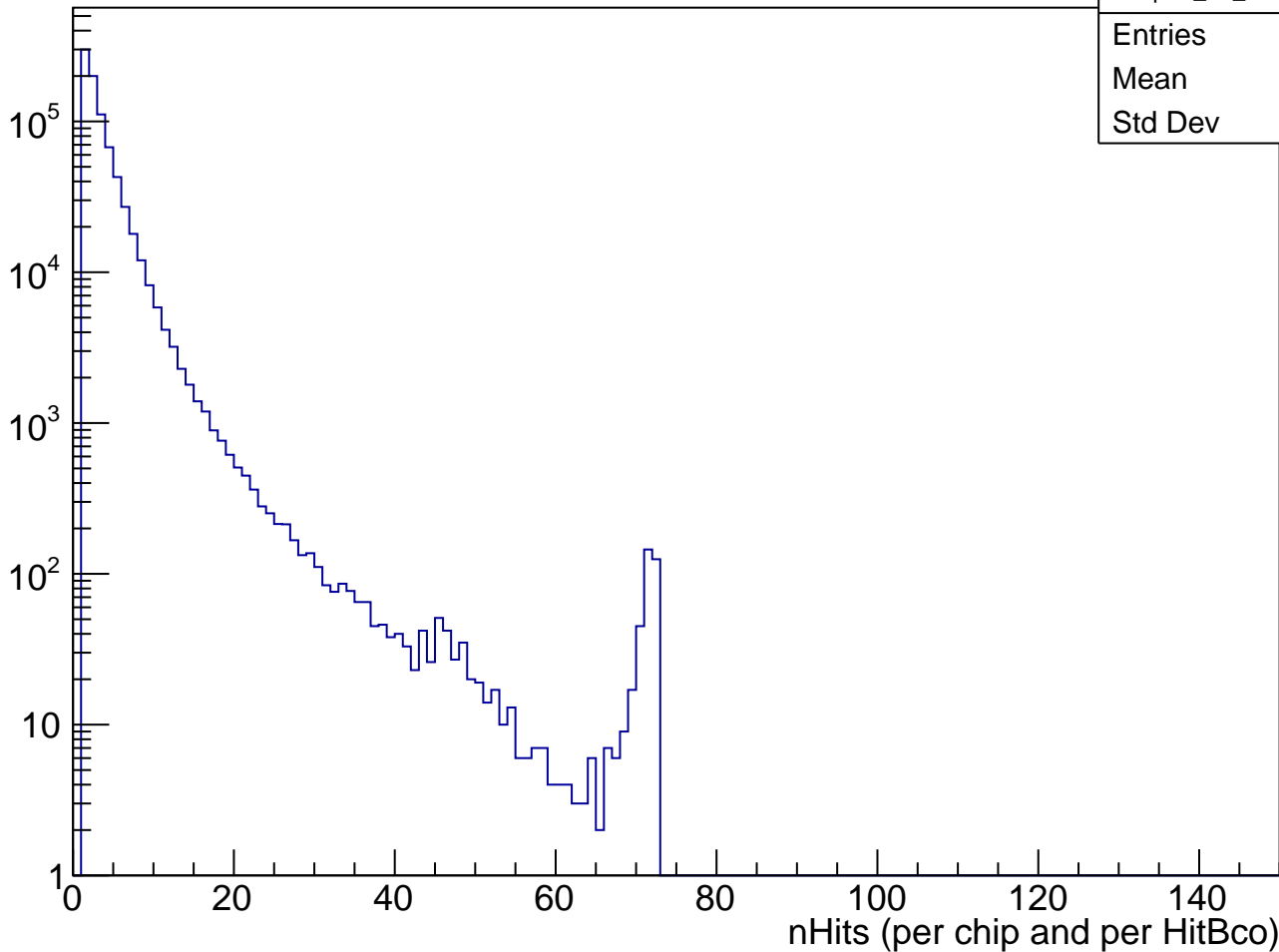
Entries



nChipHit_F4_Fch10_Chip20	
Entries	807963
Mean	2.94
Std Dev	3.412

nChipHit_F4_Fch10_Chip21

Entries

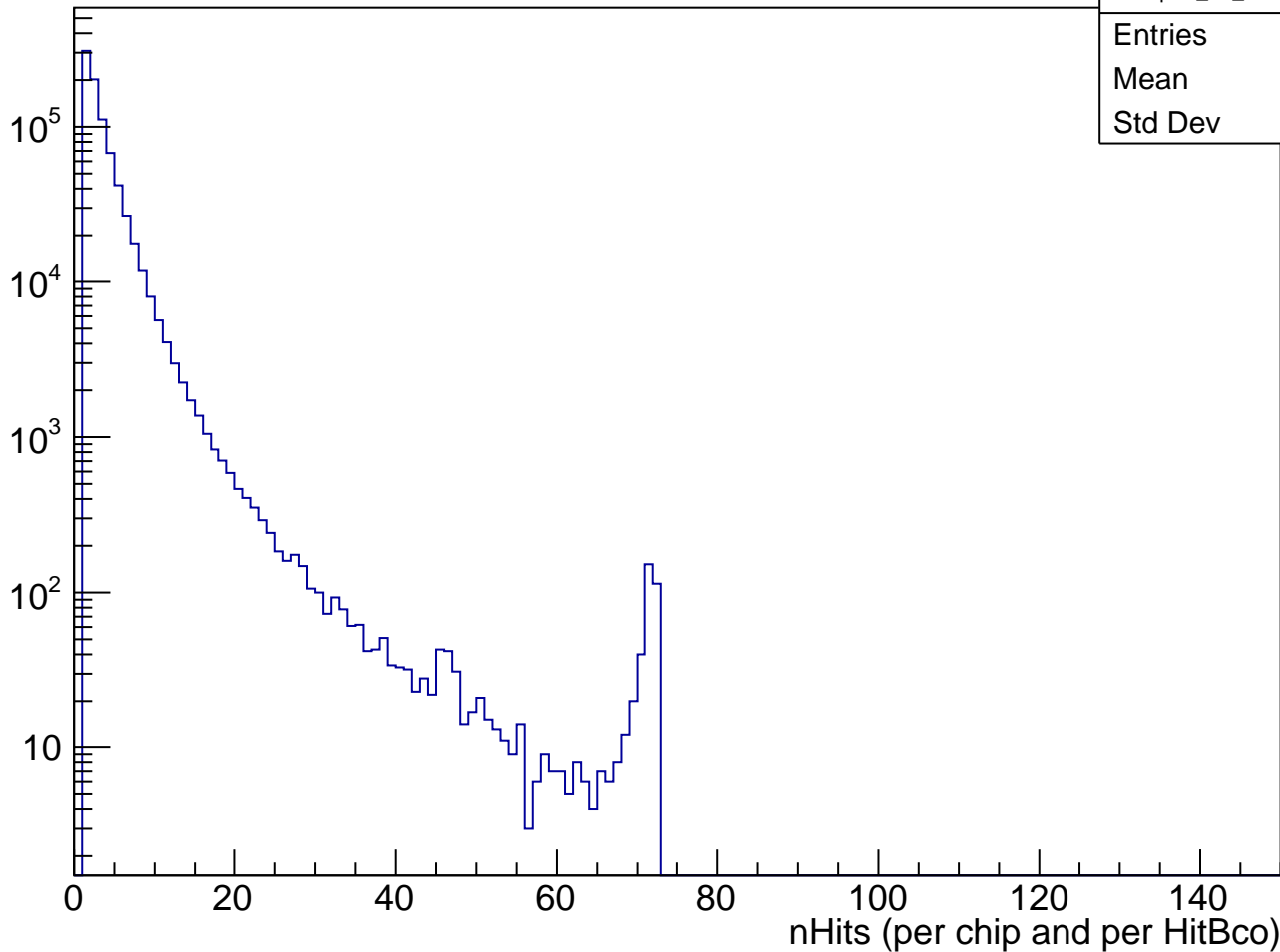


nChipHit_F4_Fch10_Chip21	
Entries	812902
Mean	2.936
Std Dev	3.377

nHits (per chip and per HitBco)

nChipHit_F4_Fch10_Chip22

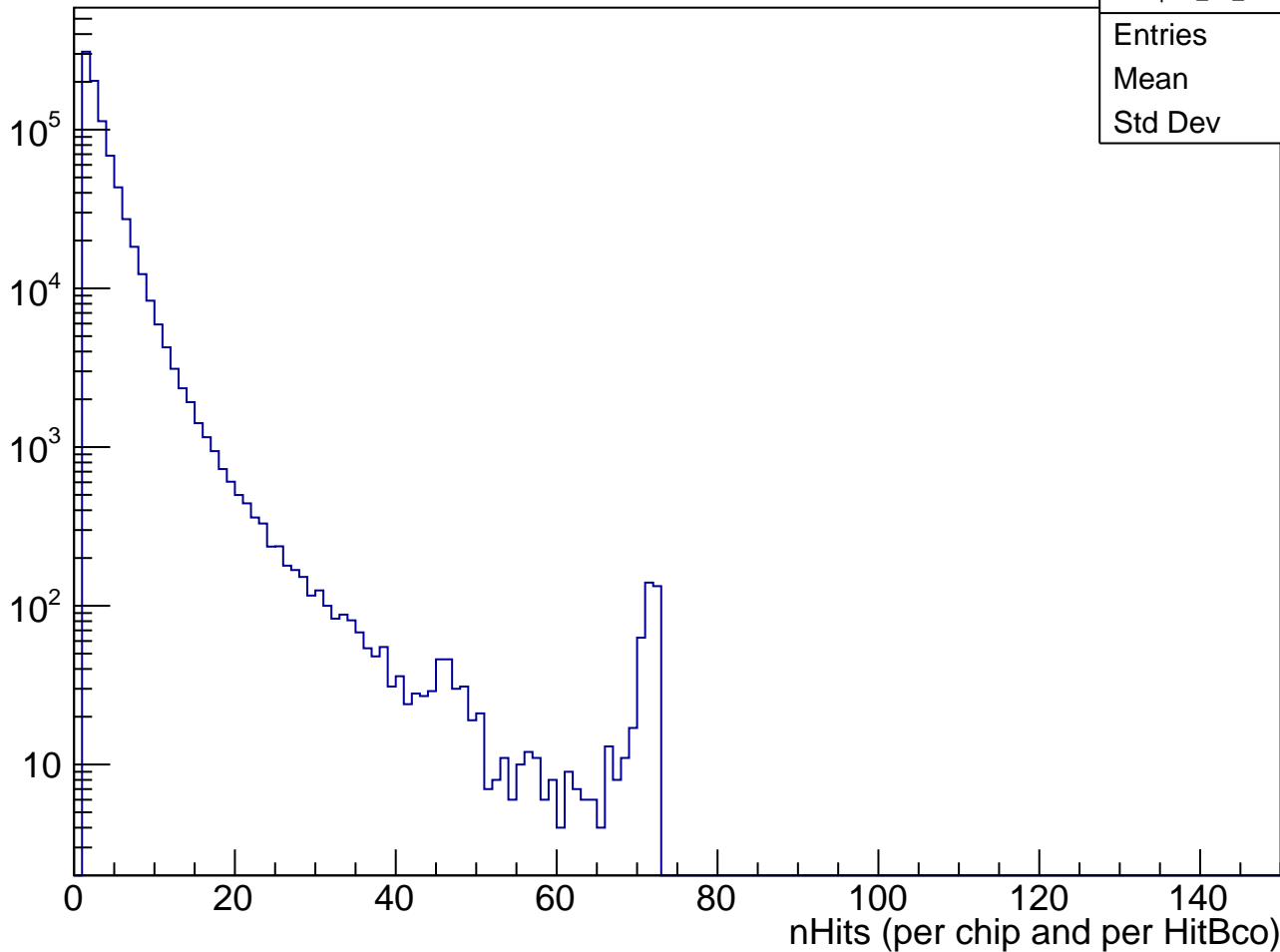
Entries



nChipHit_F4_Fch10_Chip22	
Entries	820800
Mean	2.886
Std Dev	3.31

nChipHit_F4_Fch10_Chip23

Entries



nChipHit_F4_Fch10_Chip23

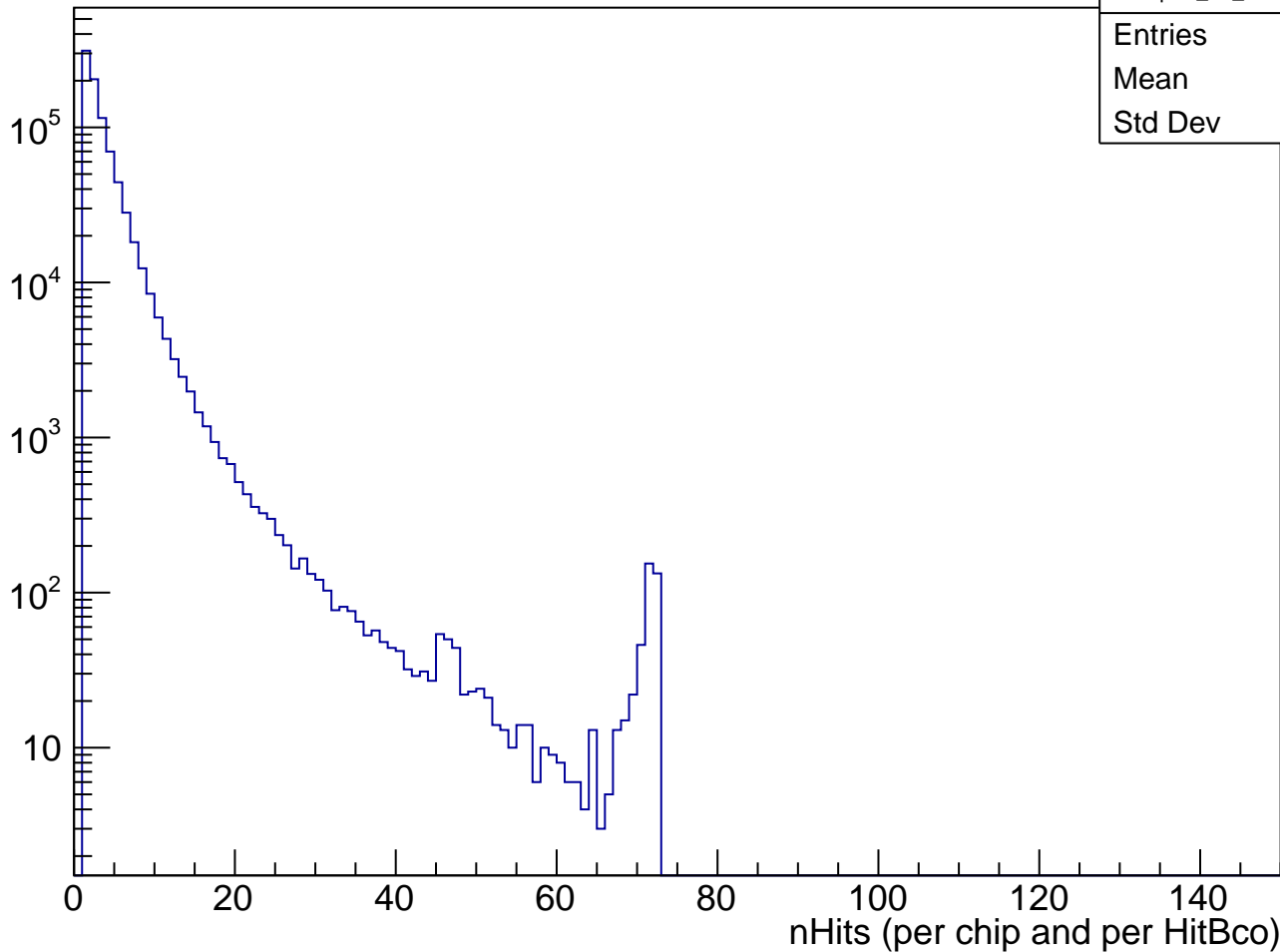
Entries 830672

Mean 2.924

Std Dev 3.388

nChipHit_F4_Fch10_Chip24

Entries



nChipHit_F4_Fch10_Chip24

Entries 840296

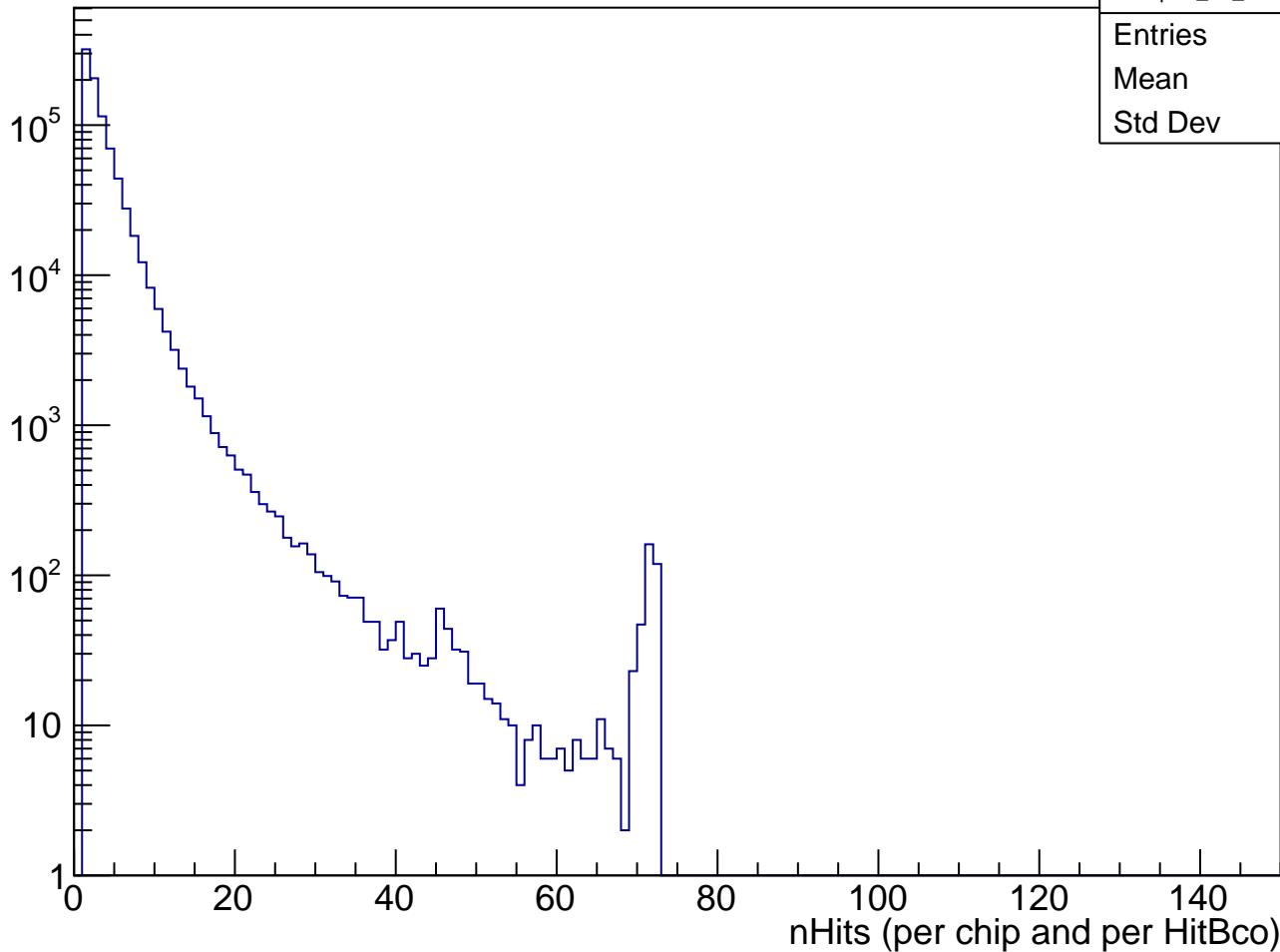
Mean 2.938

Std Dev 3.422

nHits (per chip and per HitBco)

nChipHit_F4_Fch10_Chip25

Entries



nChipHit_F4_Fch10_Chip25

Entries 846720

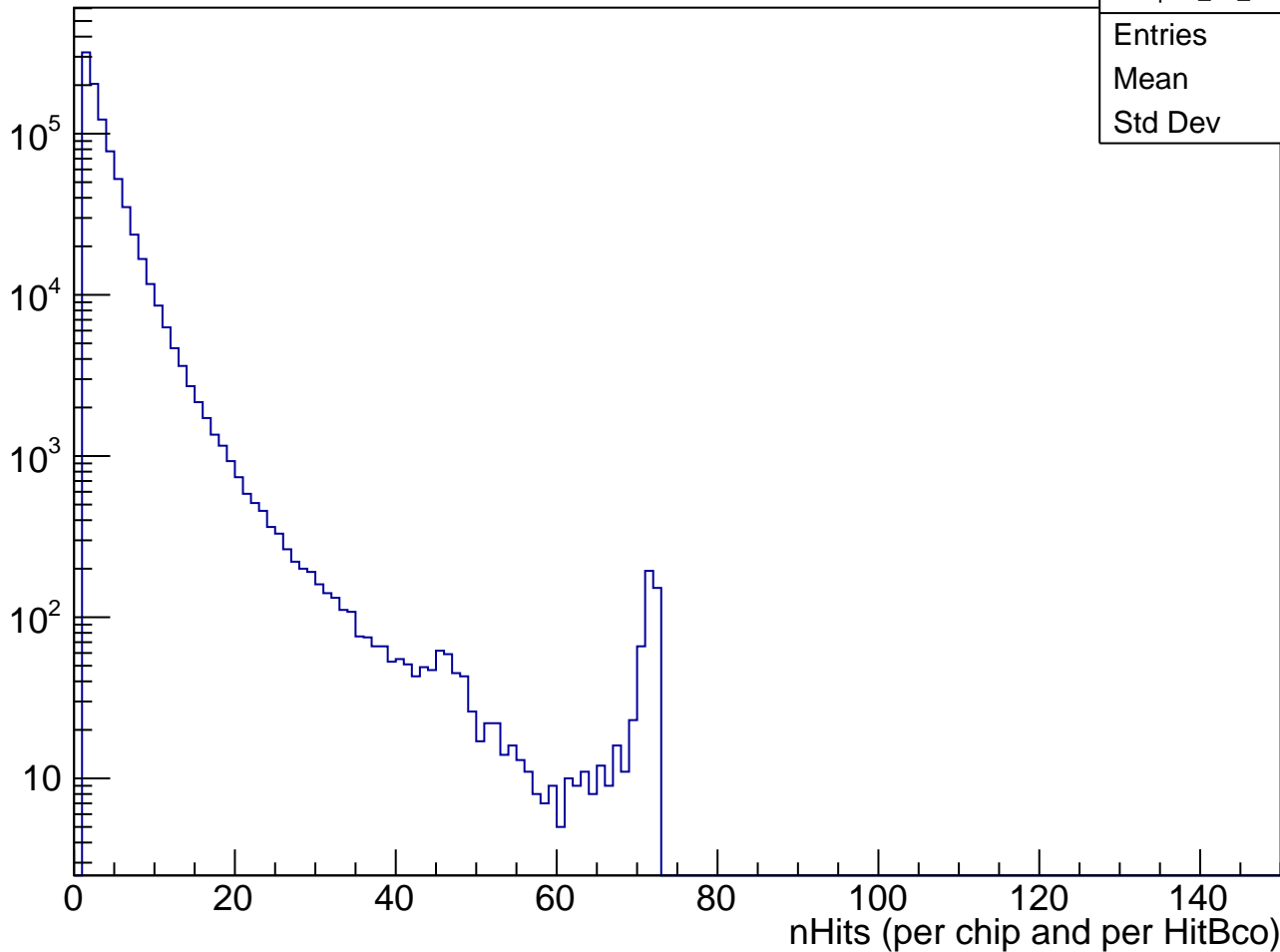
Mean 2.902

Std Dev 3.357

nHits (per chip and per HitBco)

nChipHit_F4_Fch10_Chip3

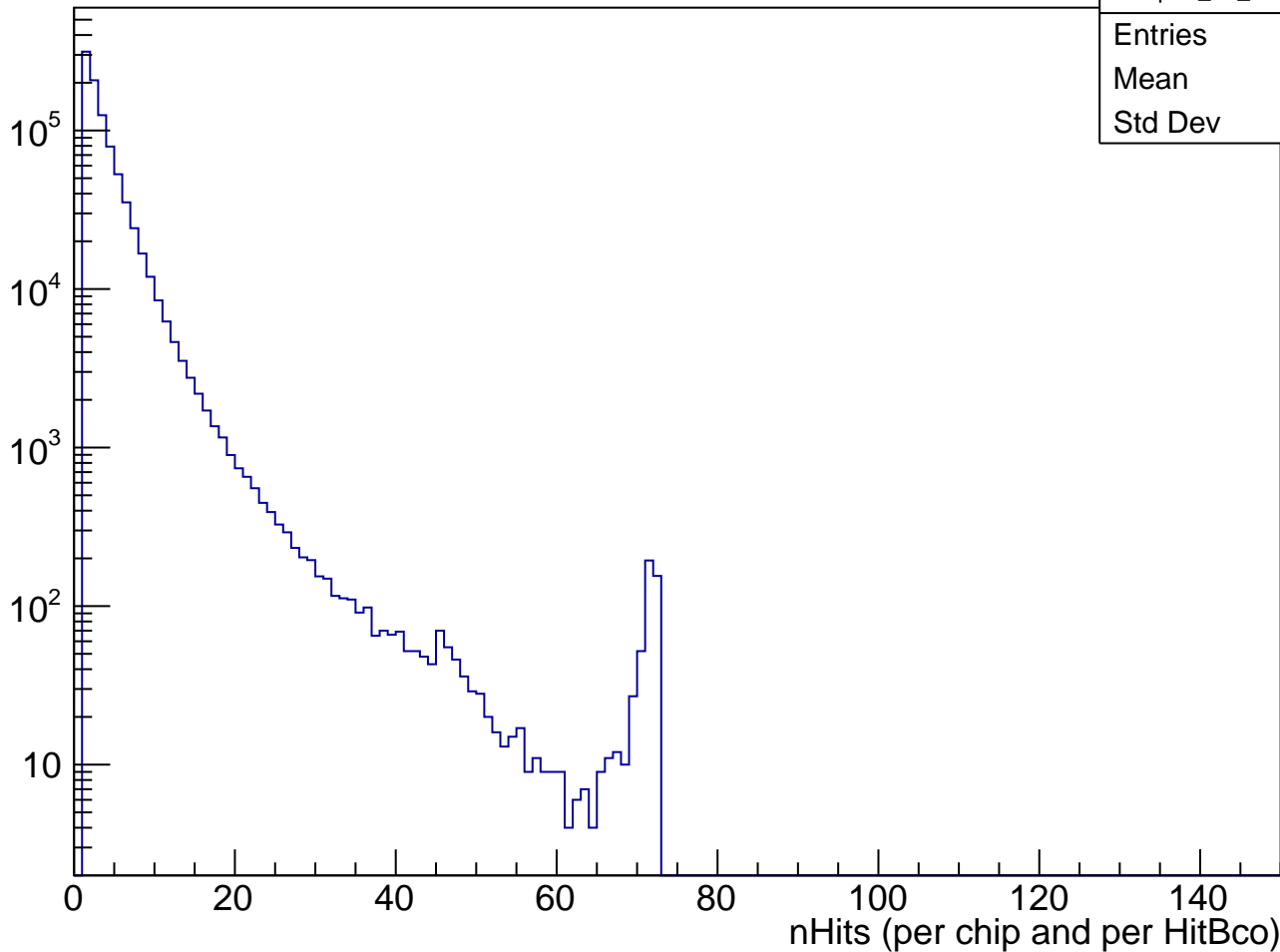
Entries



nChipHit_F4_Fch10_Chip3	
Entries	901814
Mean	3.201
Std Dev	3.737

nChipHit_F4_Fch10_Chip4

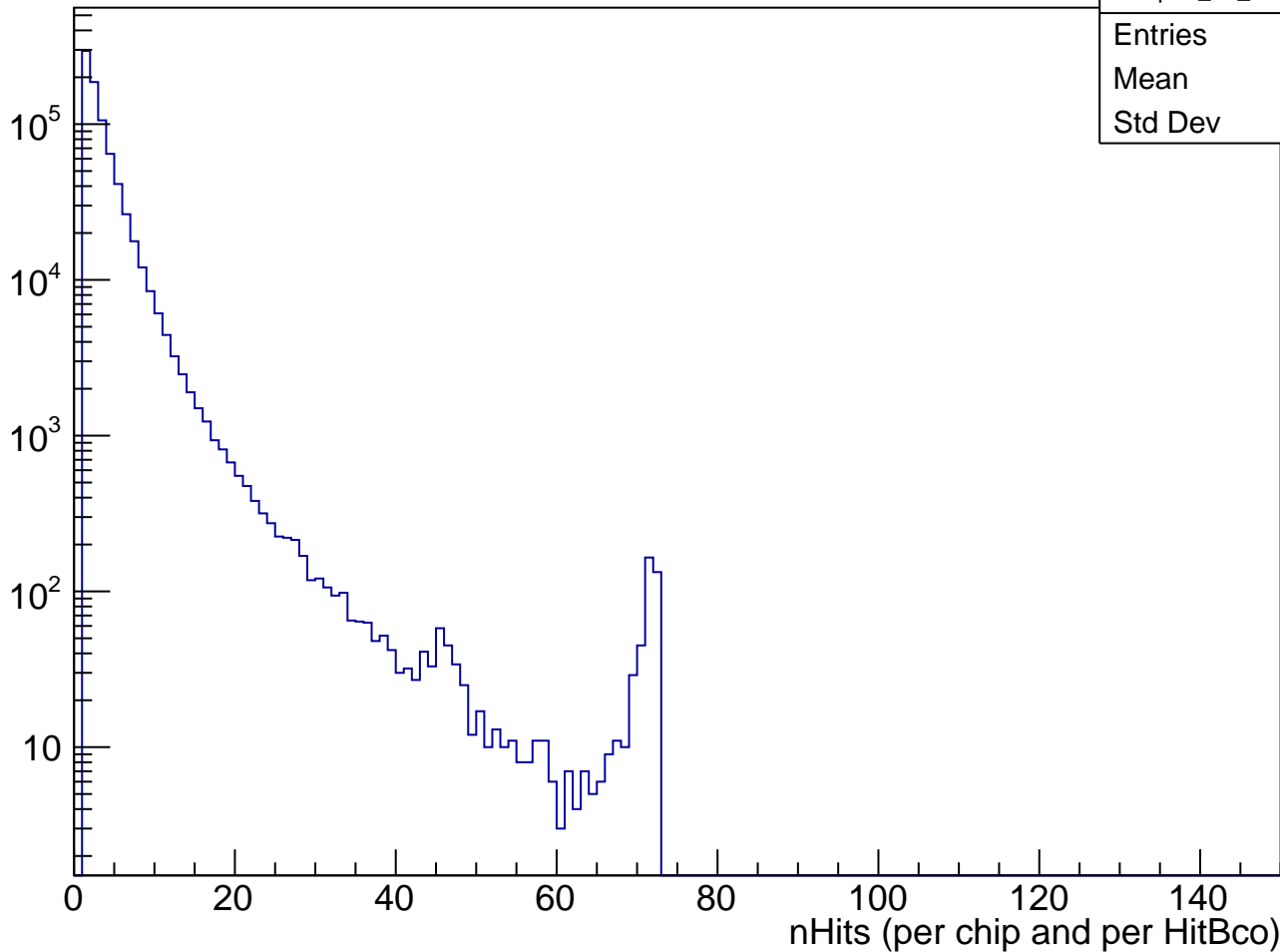
Entries



nChipHit_F4_Fch10_Chip4	
Entries	905896
Mean	3.218
Std Dev	3.736

nChipHit_F4_Fch10_Chip5

Entries

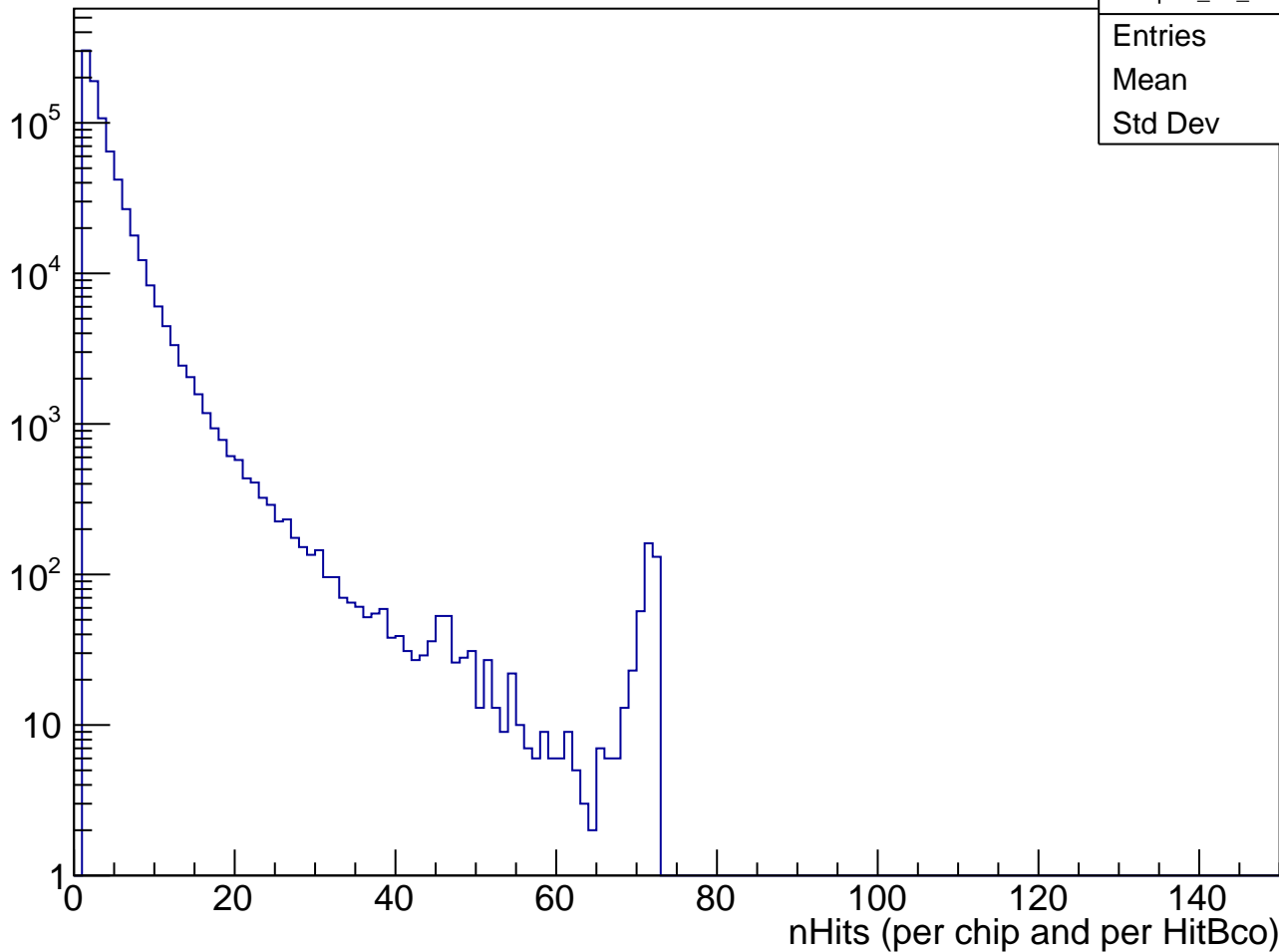


nChipHit_F4_Fch10_Chip5	
Entries	785875
Mean	2.981
Std Dev	3.525

nHits (per chip and per HitBco)

nChipHit_F4_Fch10_Chip6

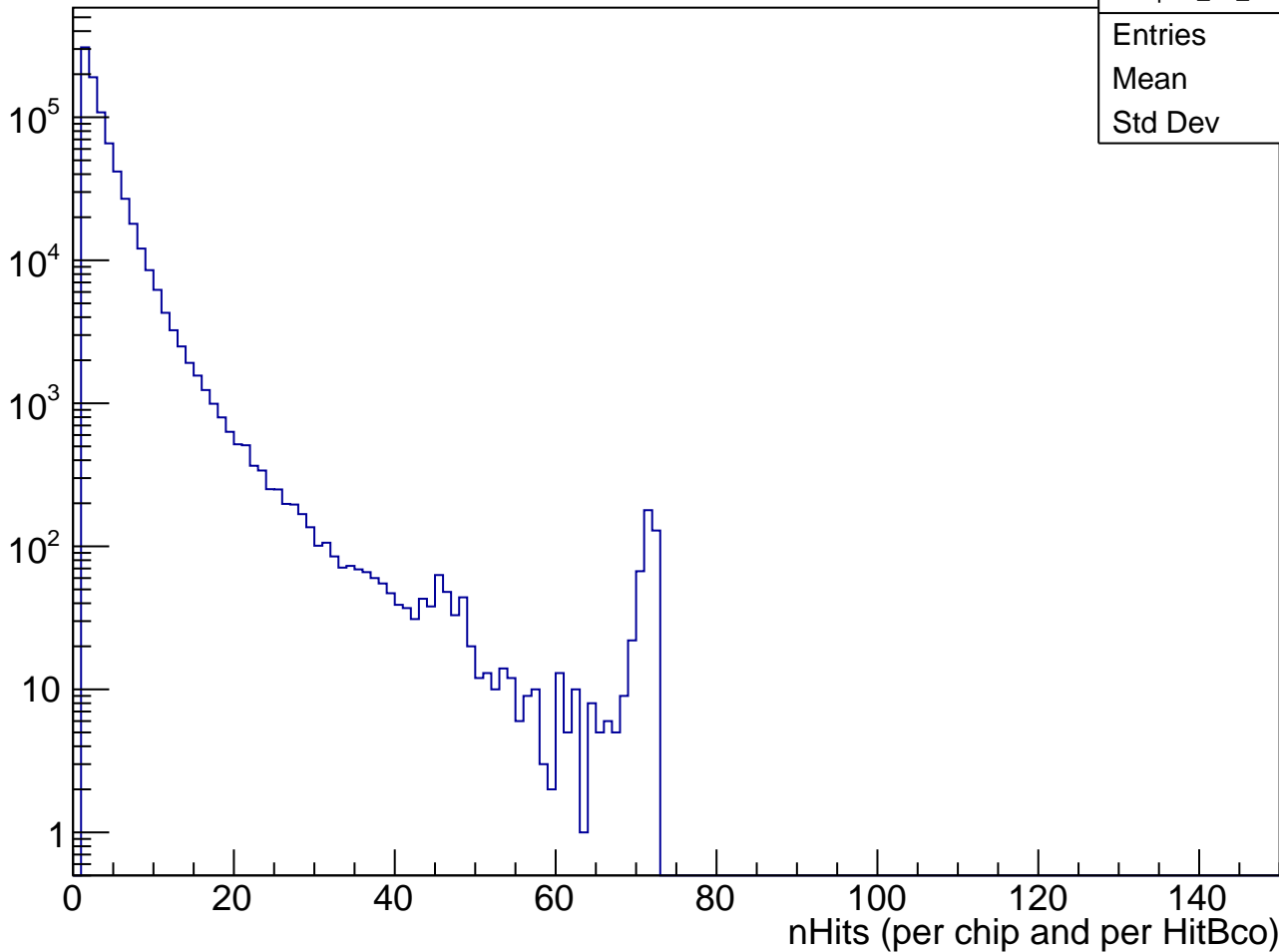
Entries



nChipHit_F4_Fch10_Chip6	
Entries	799159
Mean	2.965
Std Dev	3.508

nChipHit_F4_Fch10_Chip7

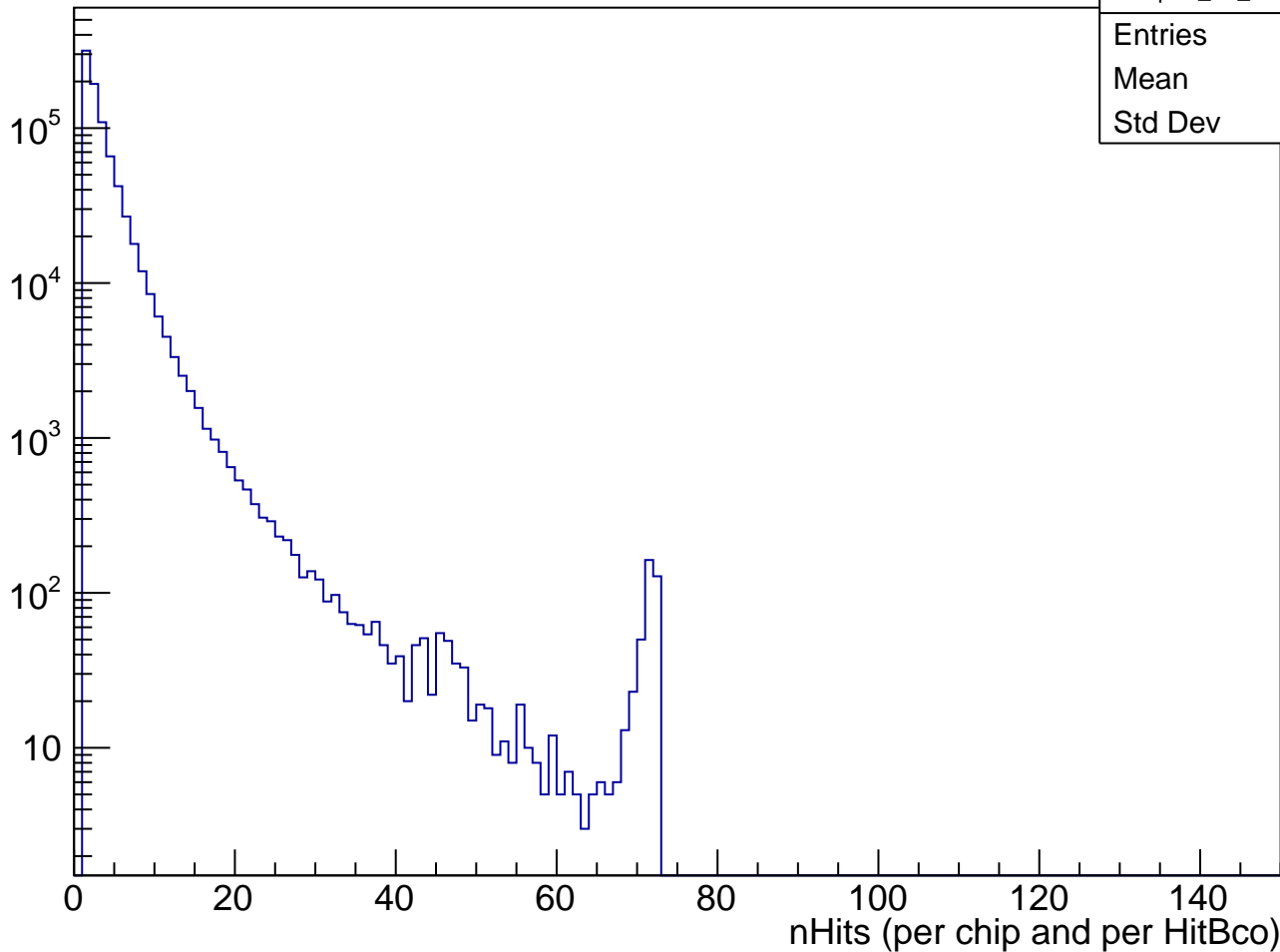
Entries



nChipHit_F4_Fch10_Chip7	
Entries	807689
Mean	2.956
Std Dev	3.518

nChipHit_F4_Fch10_Chip8

Entries

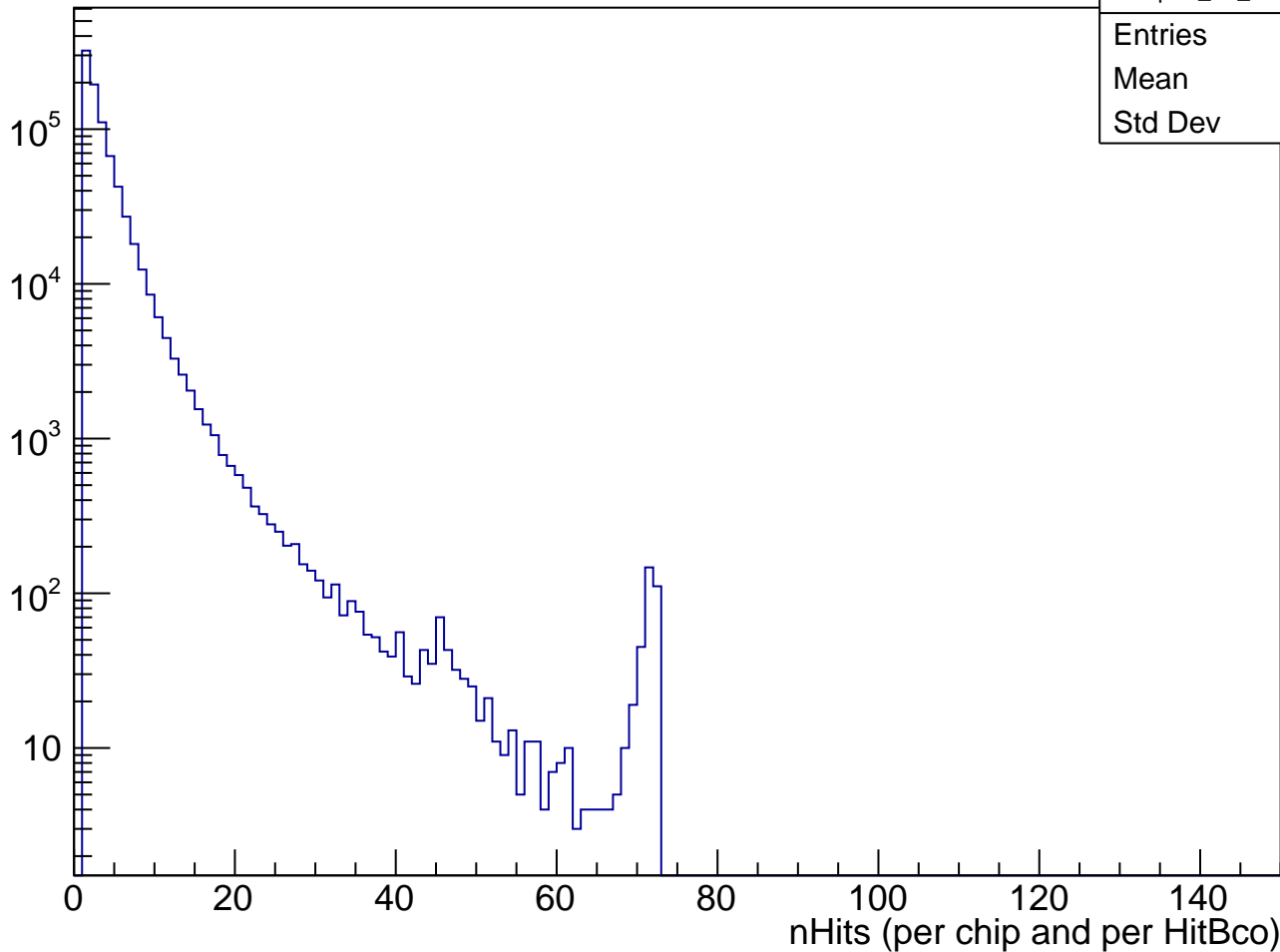


nChipHit_F4_Fch10_Chip8	
Entries	819477
Mean	2.929
Std Dev	3.461

nHits (per chip and per HitBco)

nChipHit_F4_Fch10_Chip9

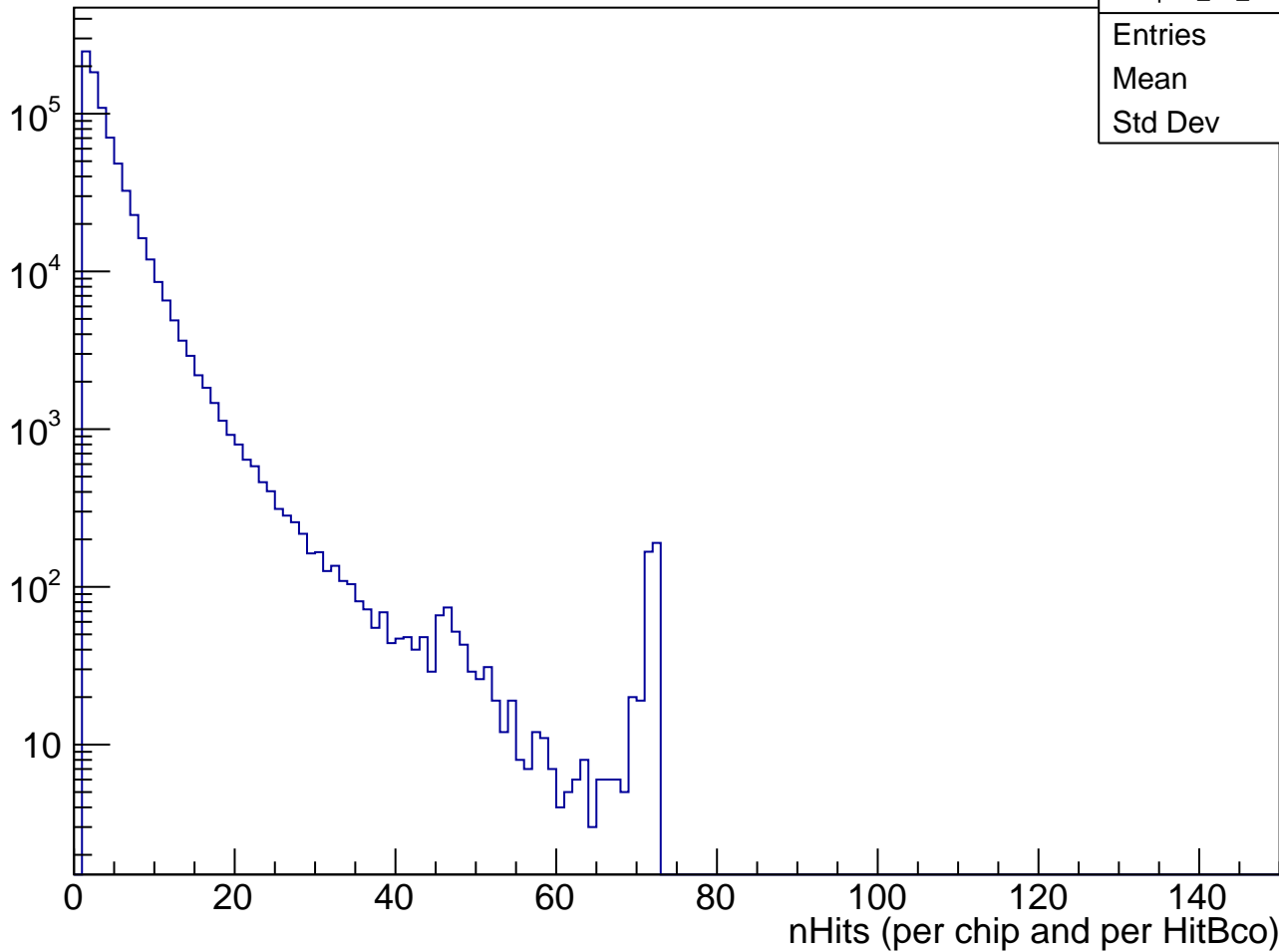
Entries



nChipHit_F4_Fch10_Chip9	
Entries	831398
Mean	2.926
Std Dev	3.43

nChipHit_F4_Fch11_Chip0

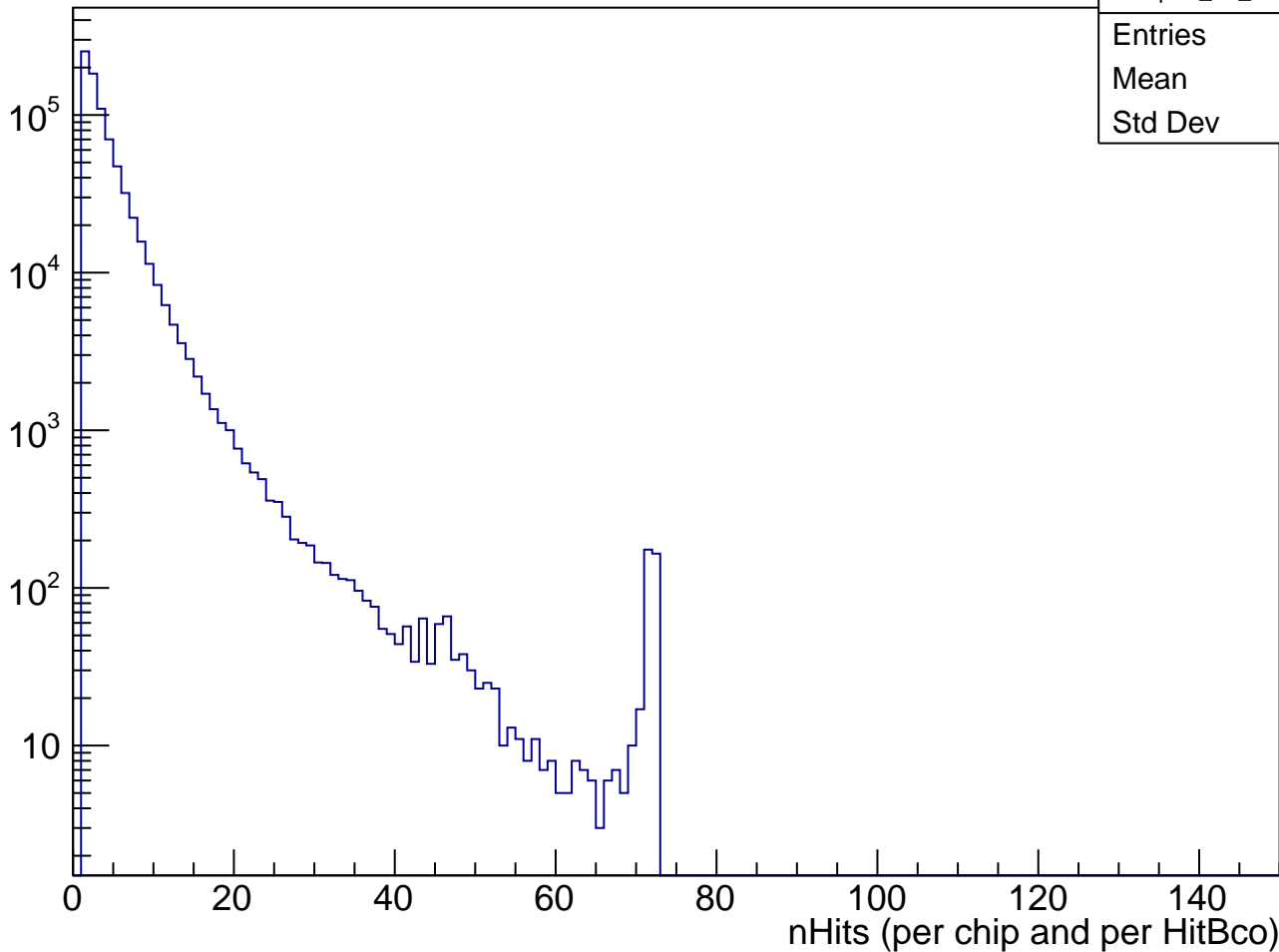
Entries



nChipHit_F4_Fch11_Chip0	
Entries	782918
Mean	3.416
Std Dev	3.914

nChipHit_F4_Fch11_Chip1

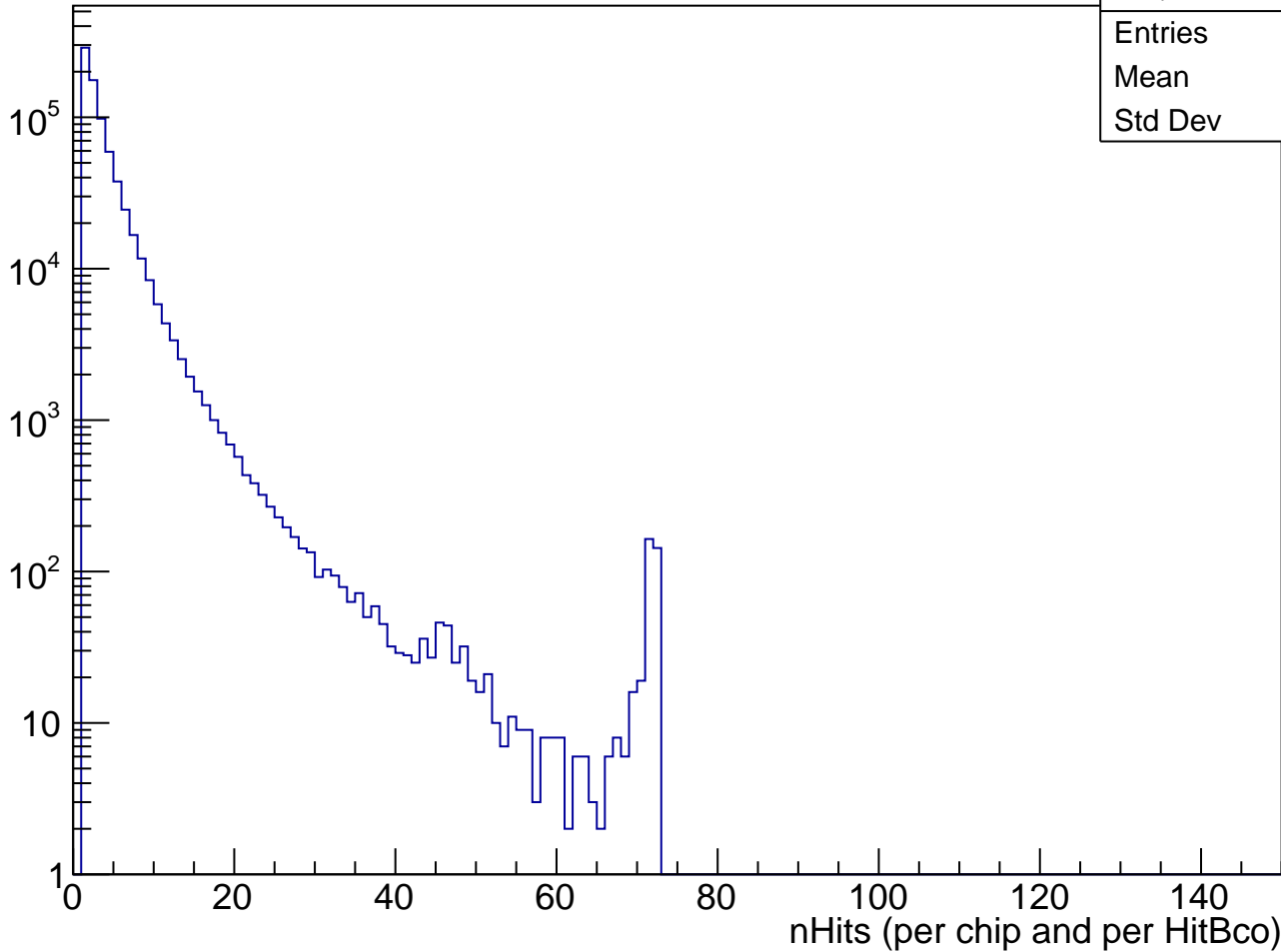
Entries



nChipHit_F4_Fch11_Chip1	
Entries	783472
Mean	3.37
Std Dev	3.864

nChipHit_F4_Fch11_Chip10

Entries



nChipHit_F4_Fch11_Chip10

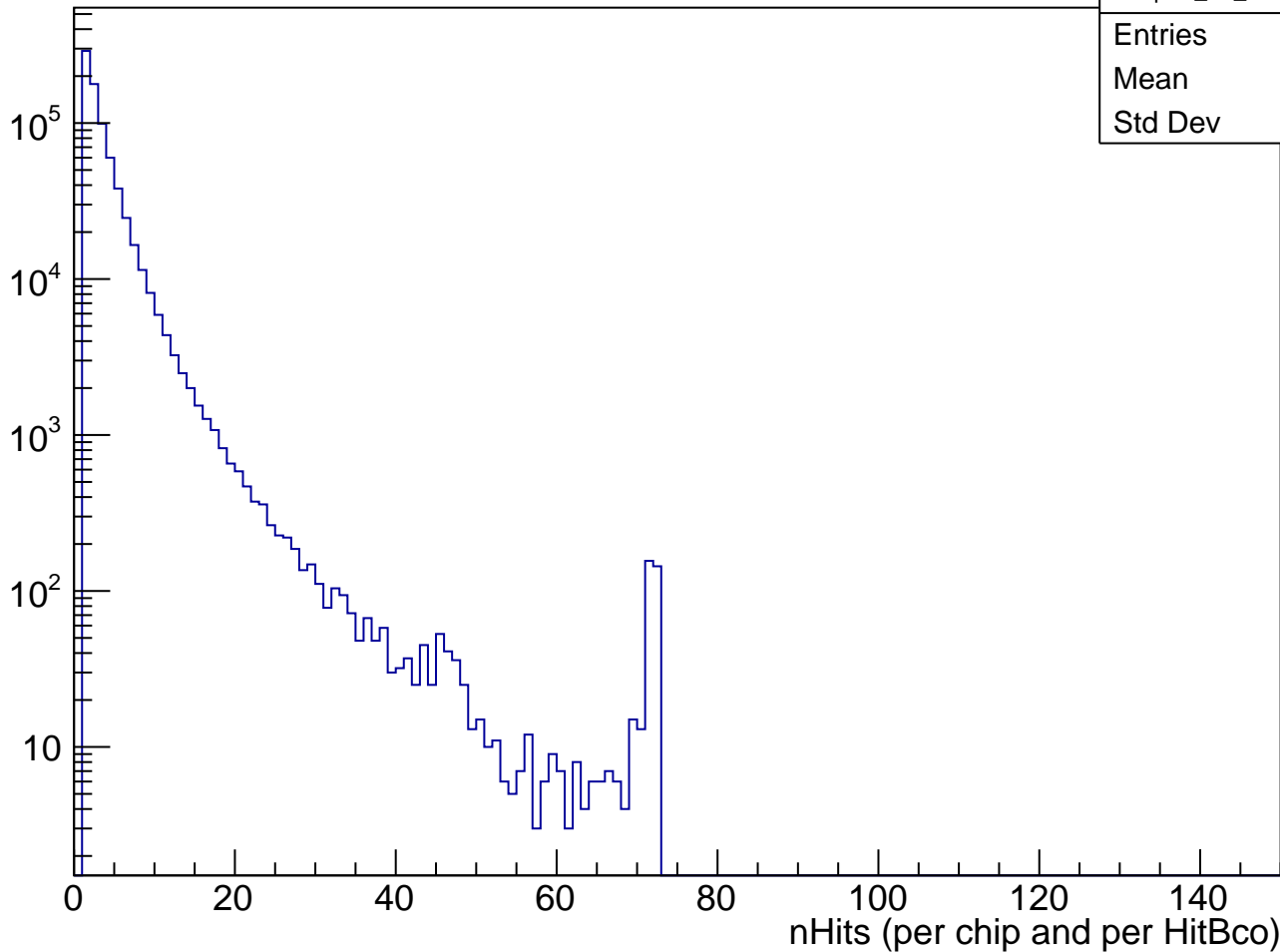
Entries 747868

Mean 2.972

Std Dev 3.534

nChipHit_F4_Fch11_Chip11

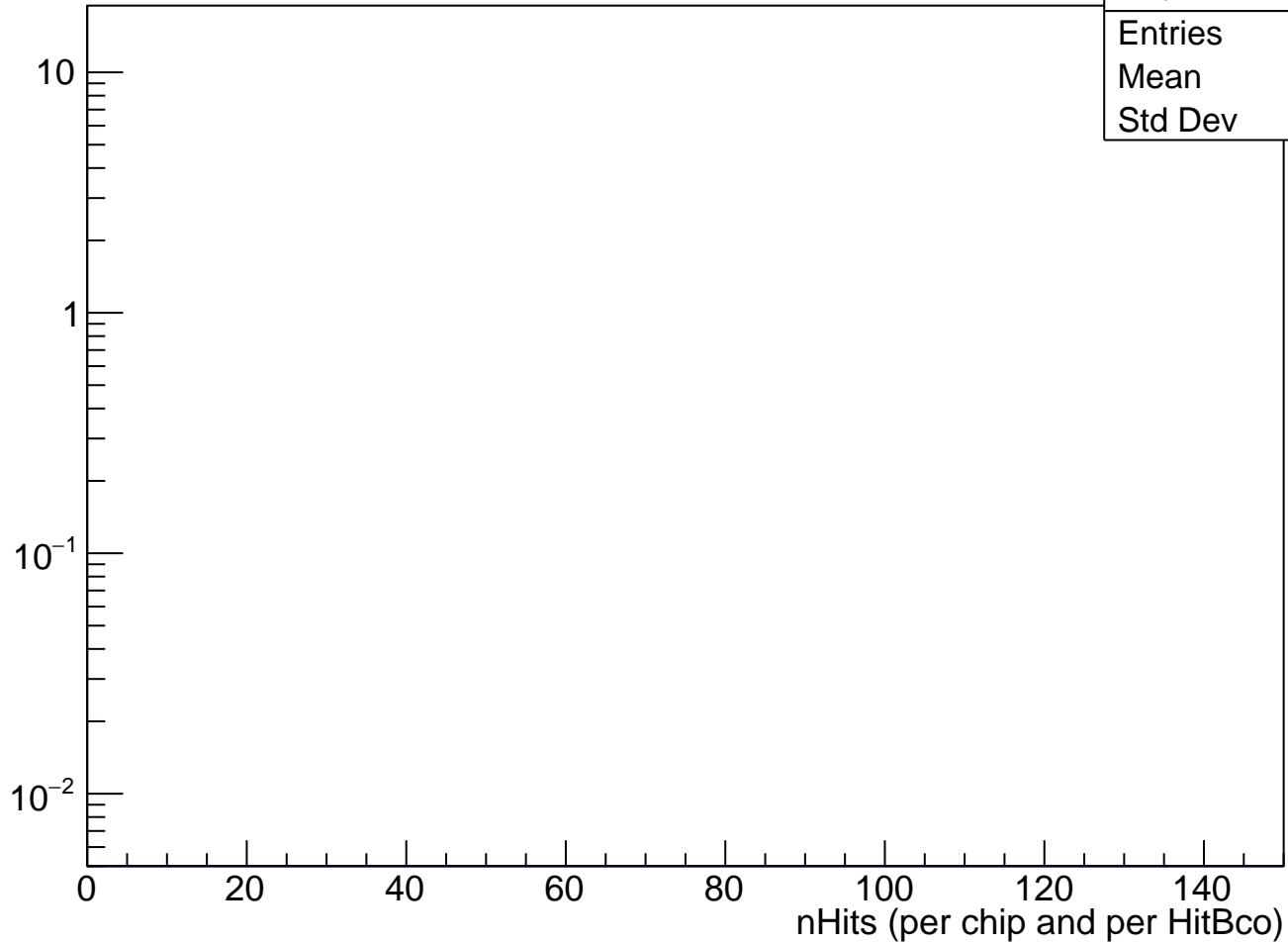
Entries



nChipHit_F4_Fch11_Chip11	
Entries	753492
Mean	2.966
Std Dev	3.522

nChipHit_F4_Fch11_Chip12

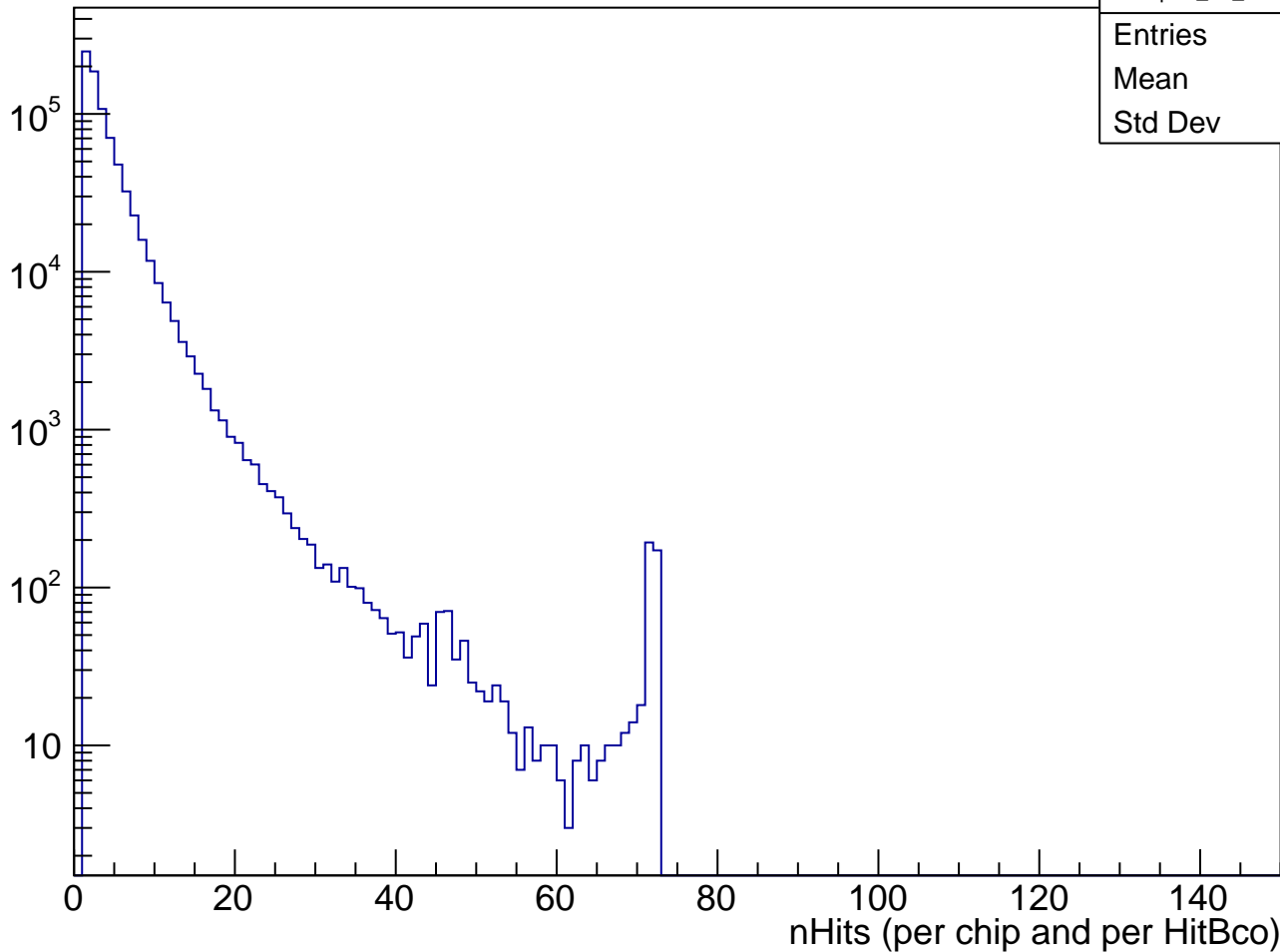
Entries



nChipHit_F4_Fch11_Chip12	
Entries	0
Mean	0
Std Dev	0

nChipHit_F4_Fch11_Chip13

Entries



nChipHit_F4_Fch11_Chip13

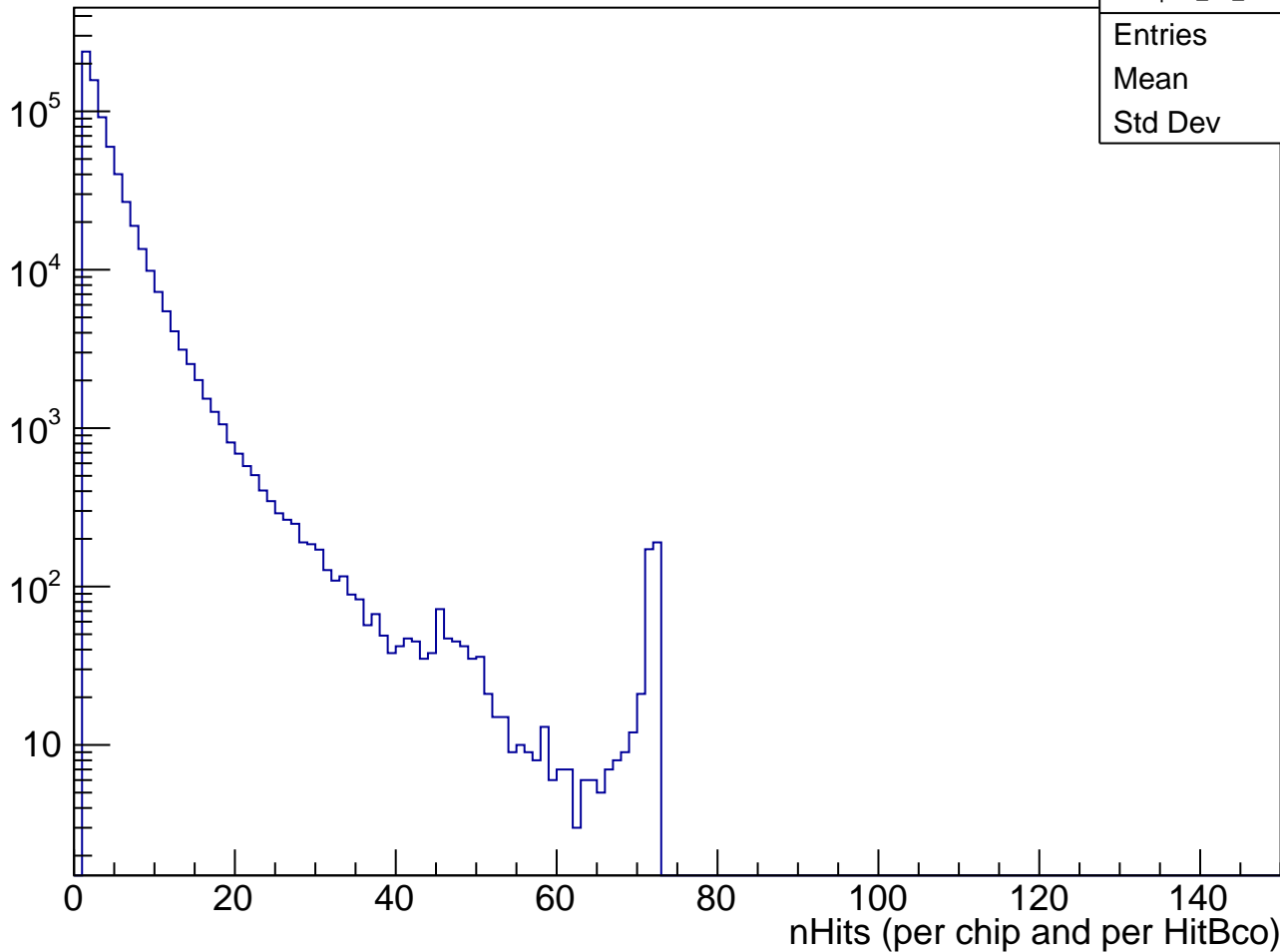
Entries 783165

Mean 3.405

Std Dev 3.928

nChipHit_F4_Fch11_Chip14

Entries



nChipHit_F4_Fch11_Chip14

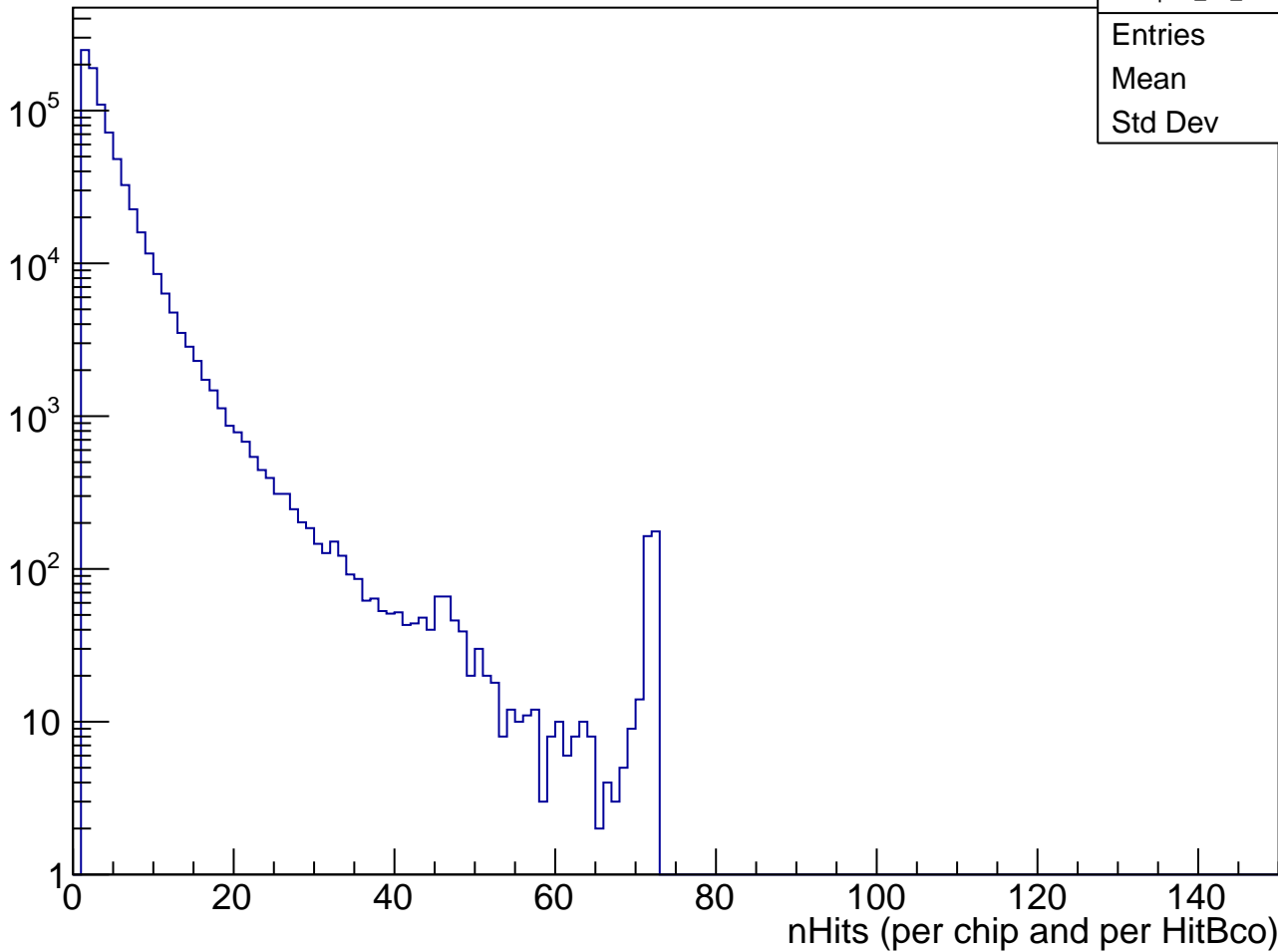
Entries 691062

Mean 3.331

Std Dev 3.985

nChipHit_F4_Fch11_Chip15

Entries



nChipHit_F4_Fch11_Chip15

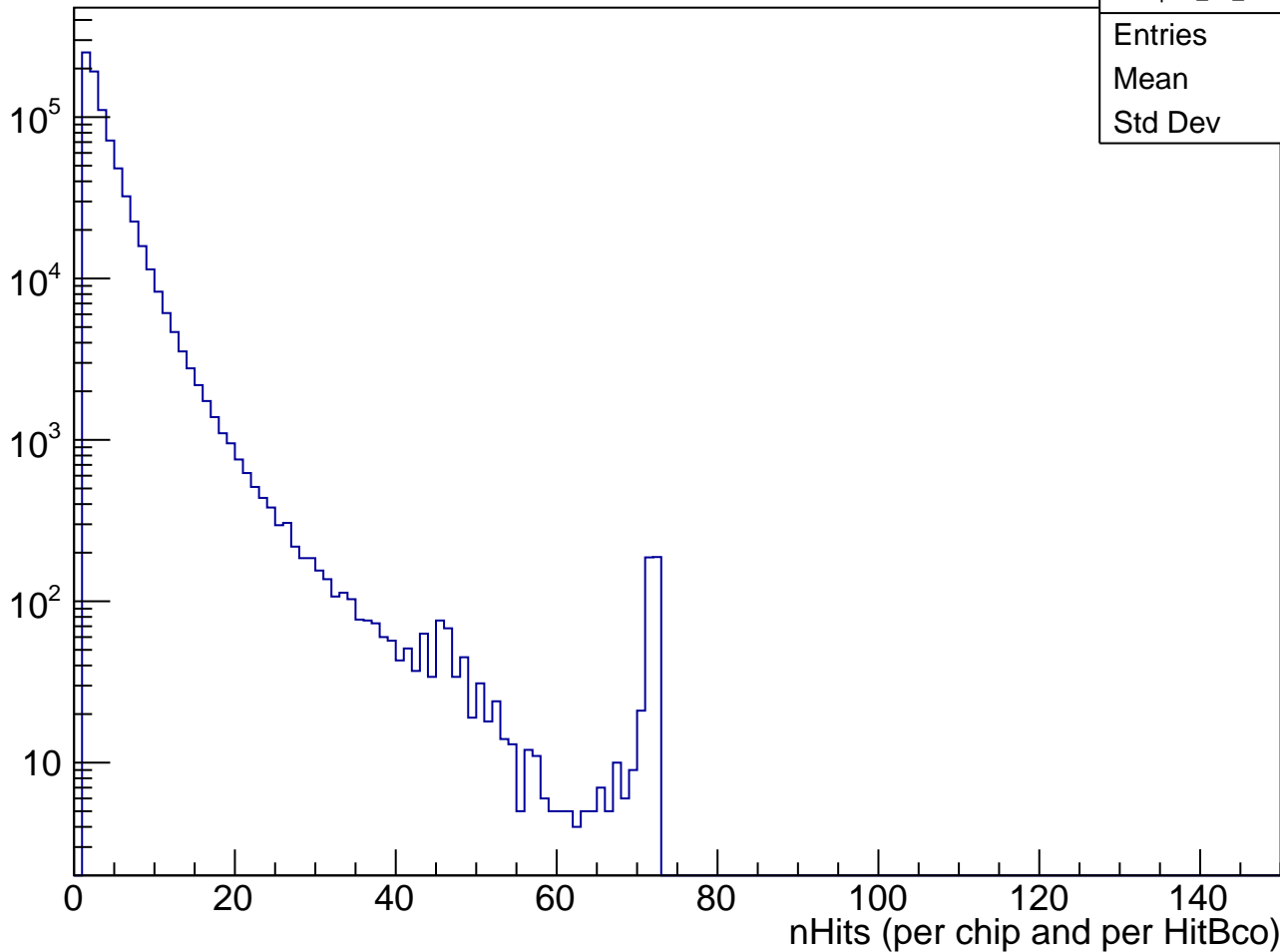
Entries 790114

Mean 3.386

Std Dev 3.851

nChipHit_F4_Fch11_Chip16

Entries



nChipHit_F4_Fch11_Chip16

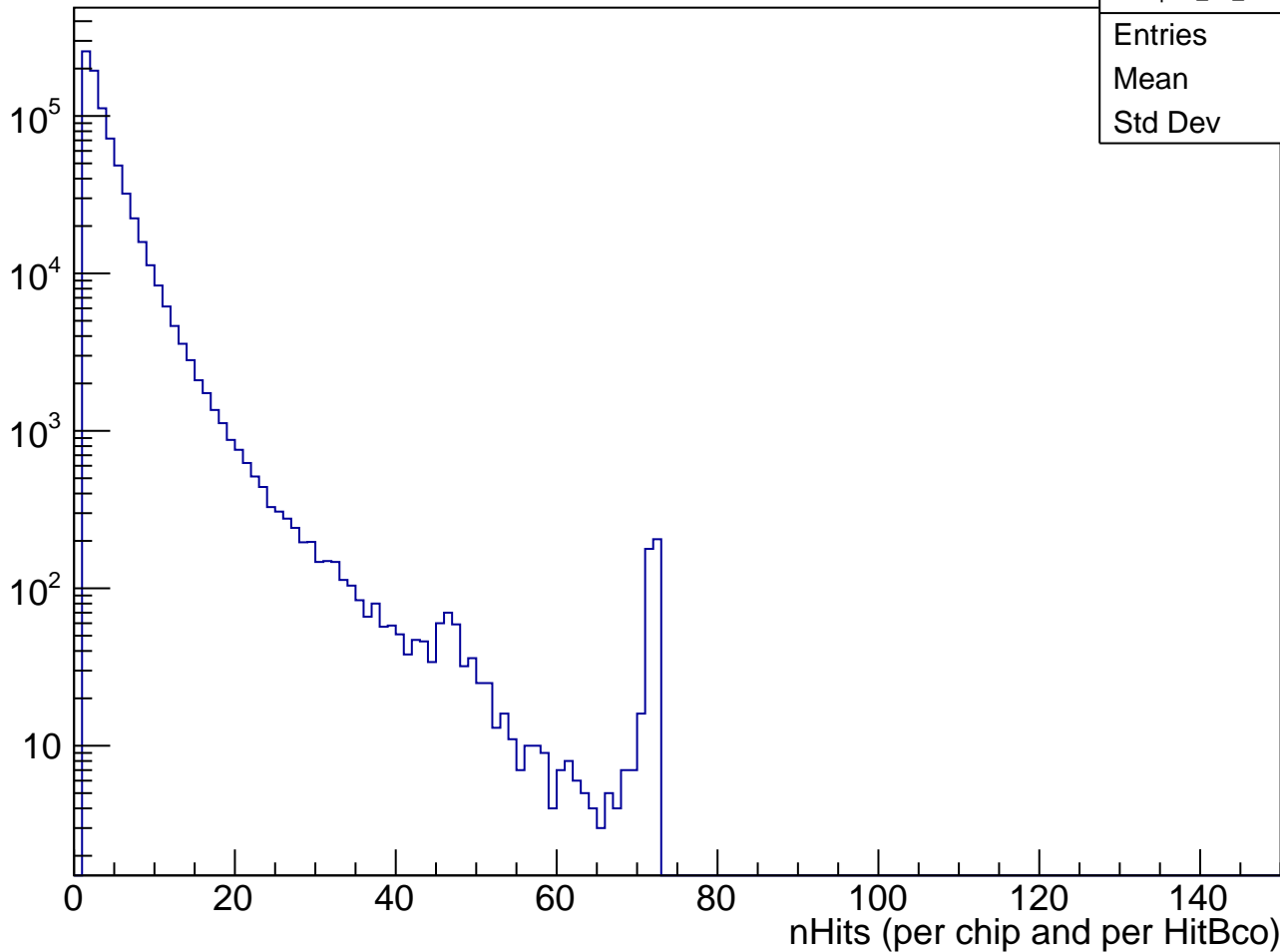
Entries 794498

Mean 3.362

Std Dev 3.857

nChipHit_F4_Fch11_Chip17

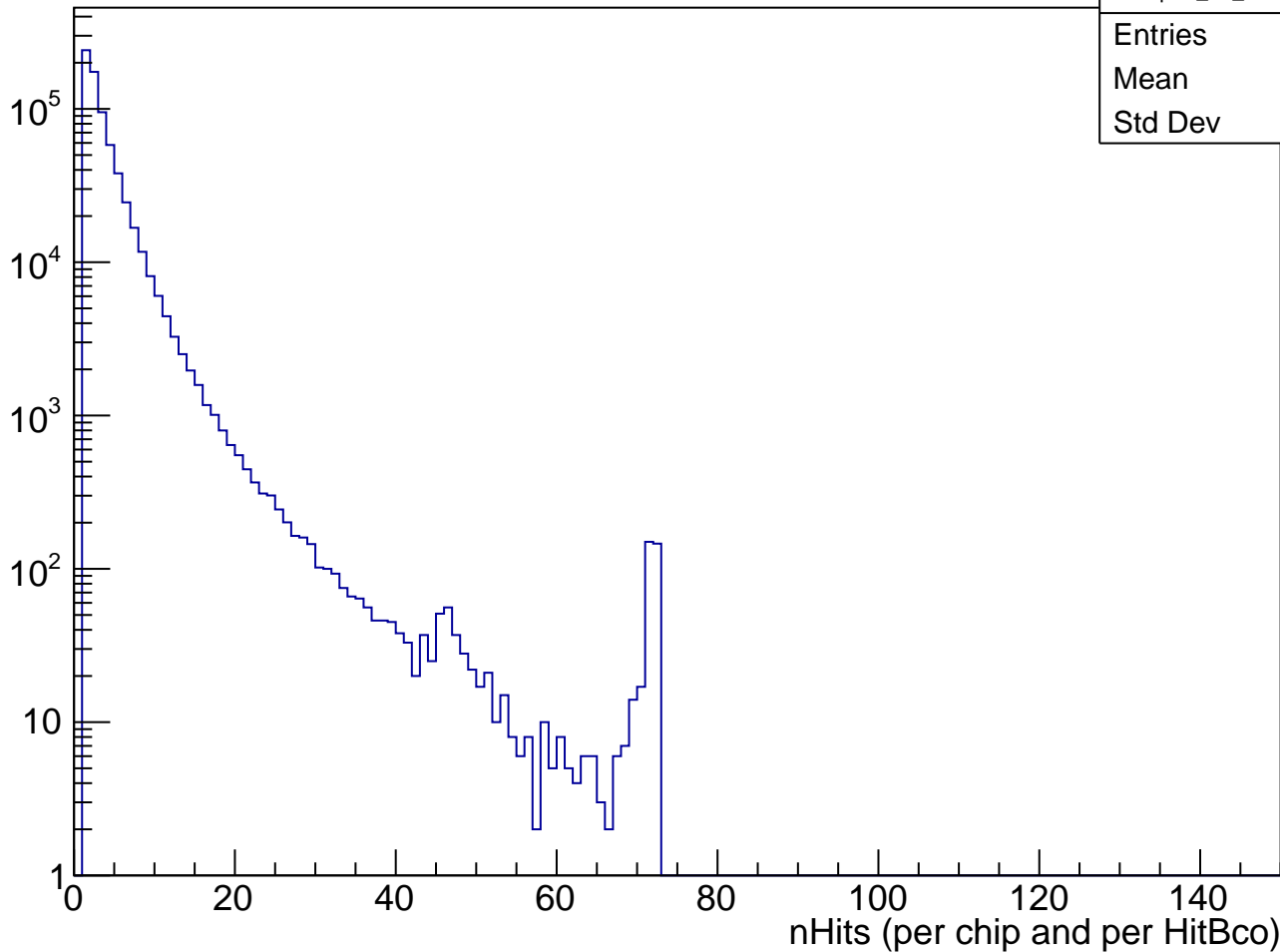
Entries



nChipHit_F4_Fch11_Chip17	
Entries	803887
Mean	3.339
Std Dev	3.84

nChipHit_F4_Fch11_Chip18

Entries



nChipHit_F4_Fch11_Chip18

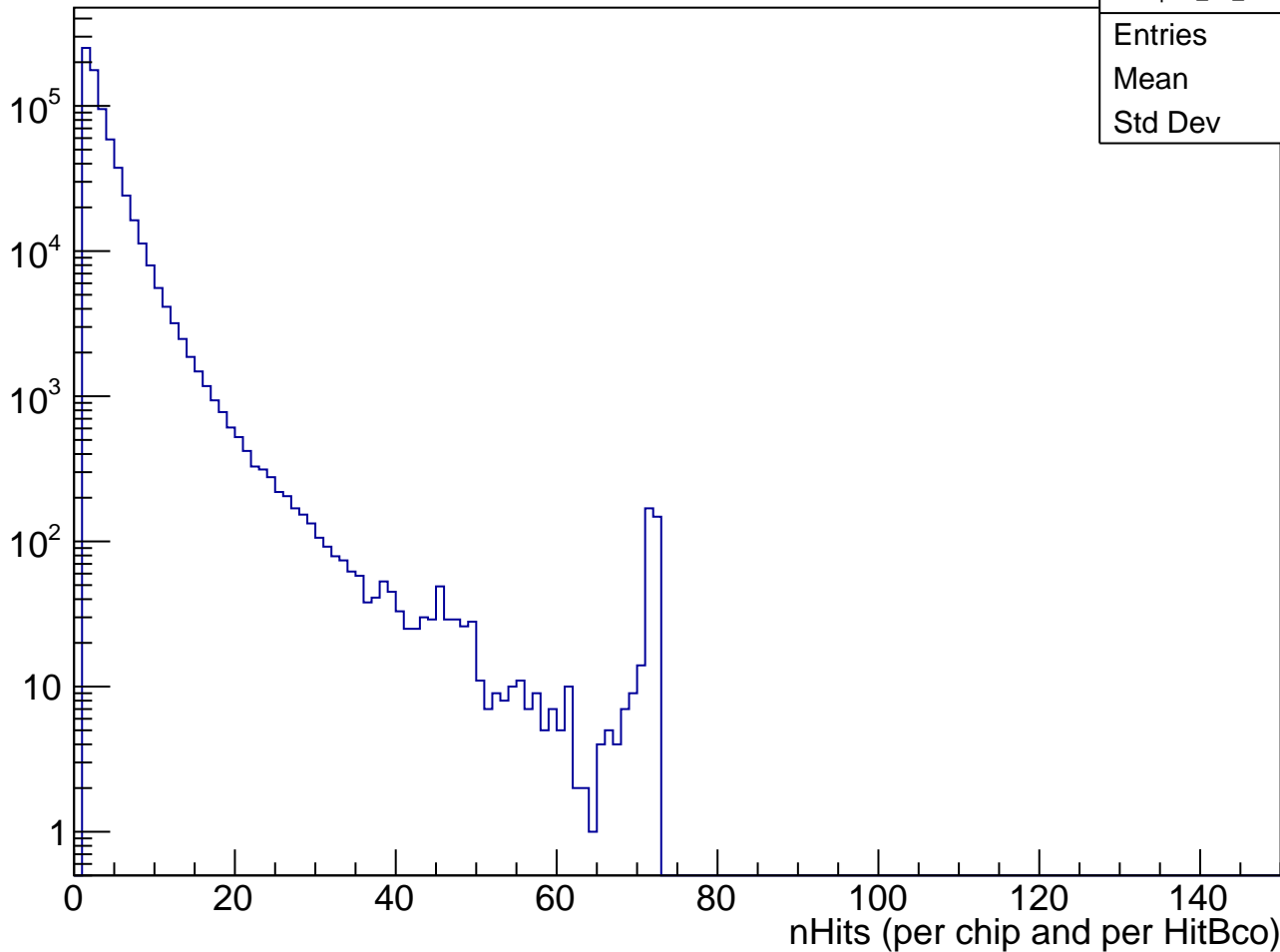
Entries 696098

Mean 3.106

Std Dev 3.624

nChipHit_F4_Fch11_Chip19

Entries



nChipHit_F4_Fch11_Chip19

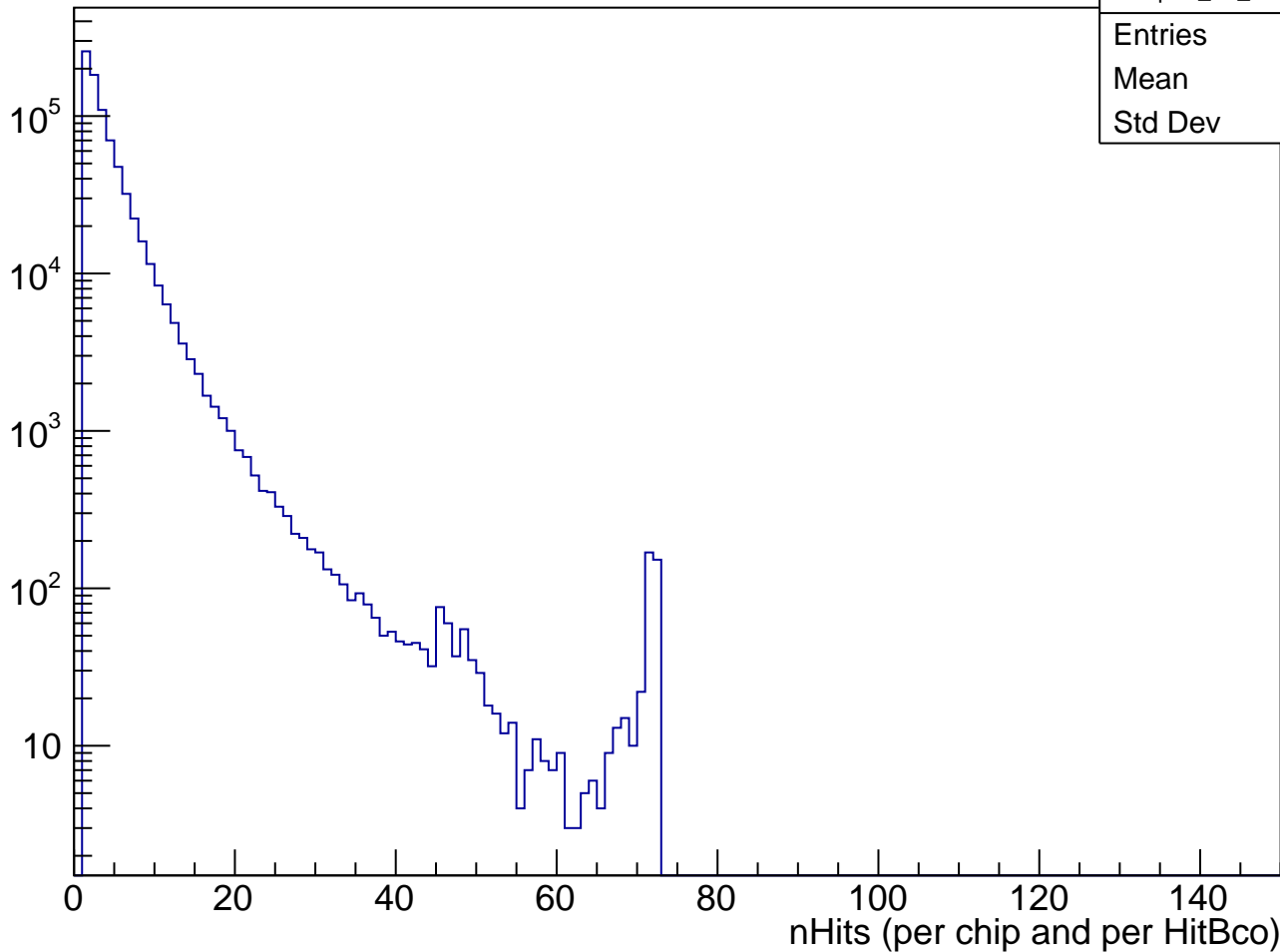
Entries 704530

Mean 3.041

Std Dev 3.549

nChipHit_F4_Fch11_Chip2

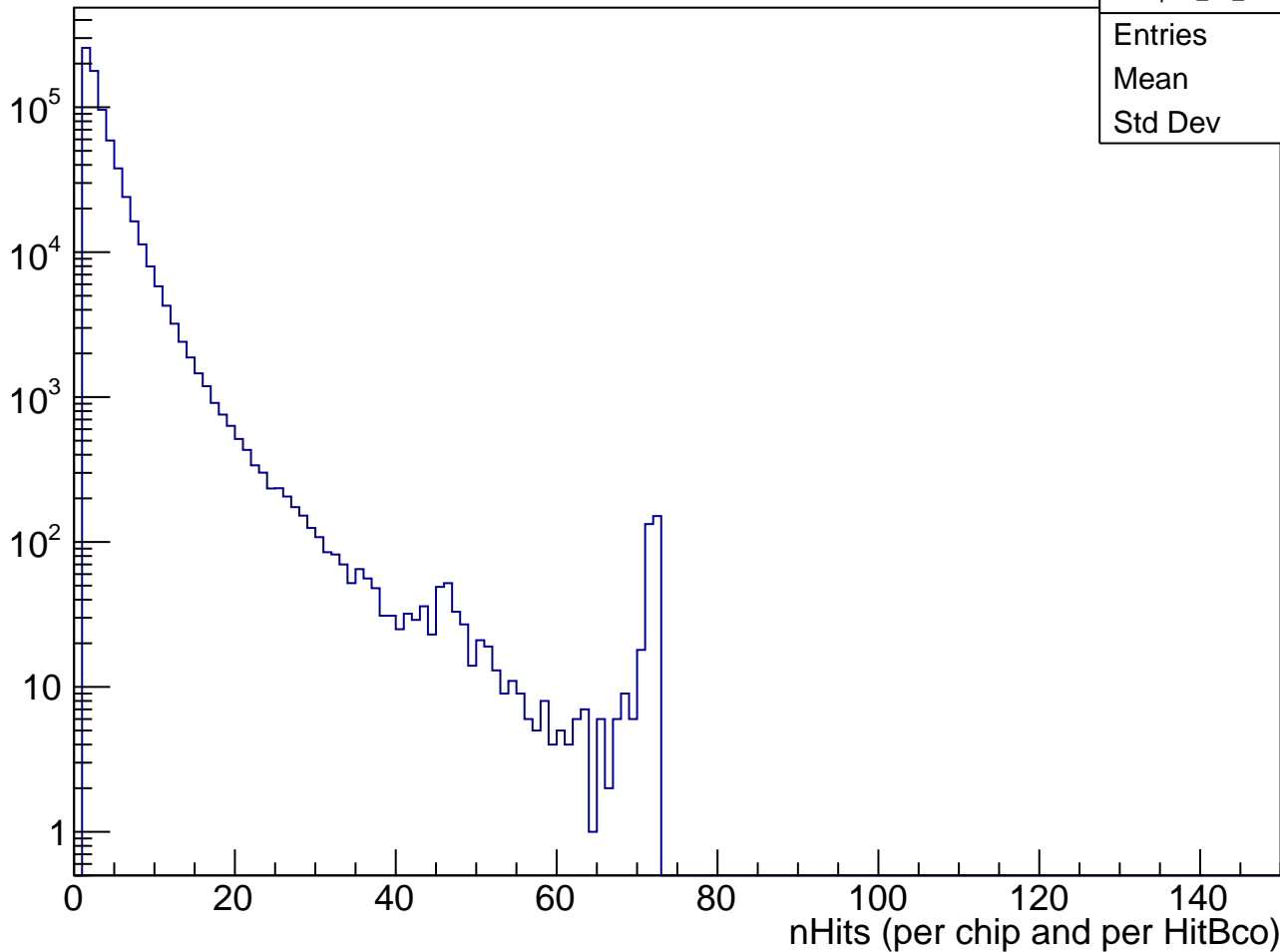
Entries



nChipHit_F4_Fch11_Chip2	
Entries	788807
Mean	3.367
Std Dev	3.861

nChipHit_F4_Fch11_Chip20

Entries



nChipHit_F4_Fch11_Chip20

Entries 713908

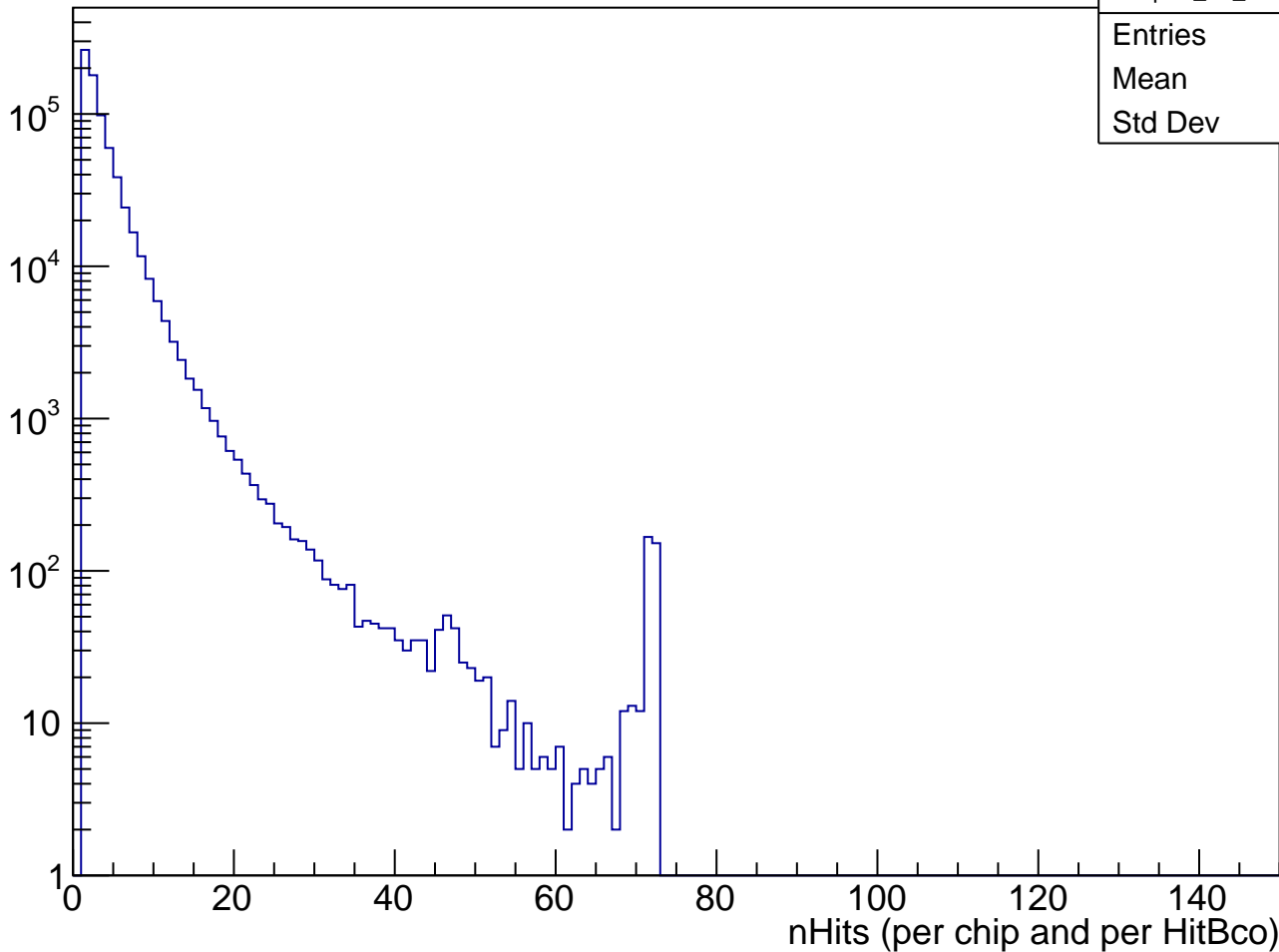
Mean 3.024

Std Dev 3.516

nHits (per chip and per HitBco)

nChipHit_F4_Fch11_Chip21

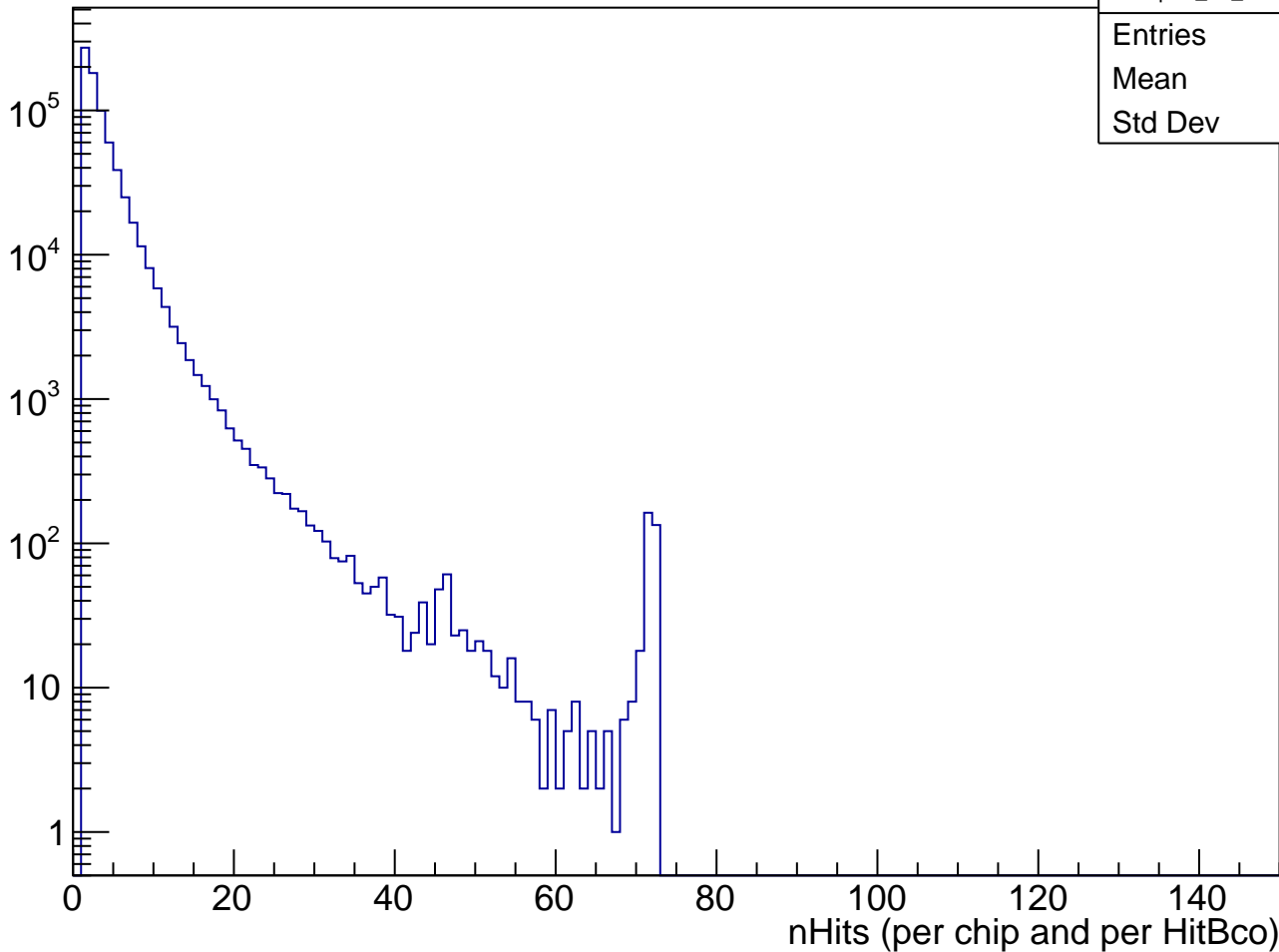
Entries



nChipHit_F4_Fch11_Chip21	
Entries	727435
Mean	3.024
Std Dev	3.547

nChipHit_F4_Fch11_Chip22

Entries



nChipHit_F4_Fch11_Chip22

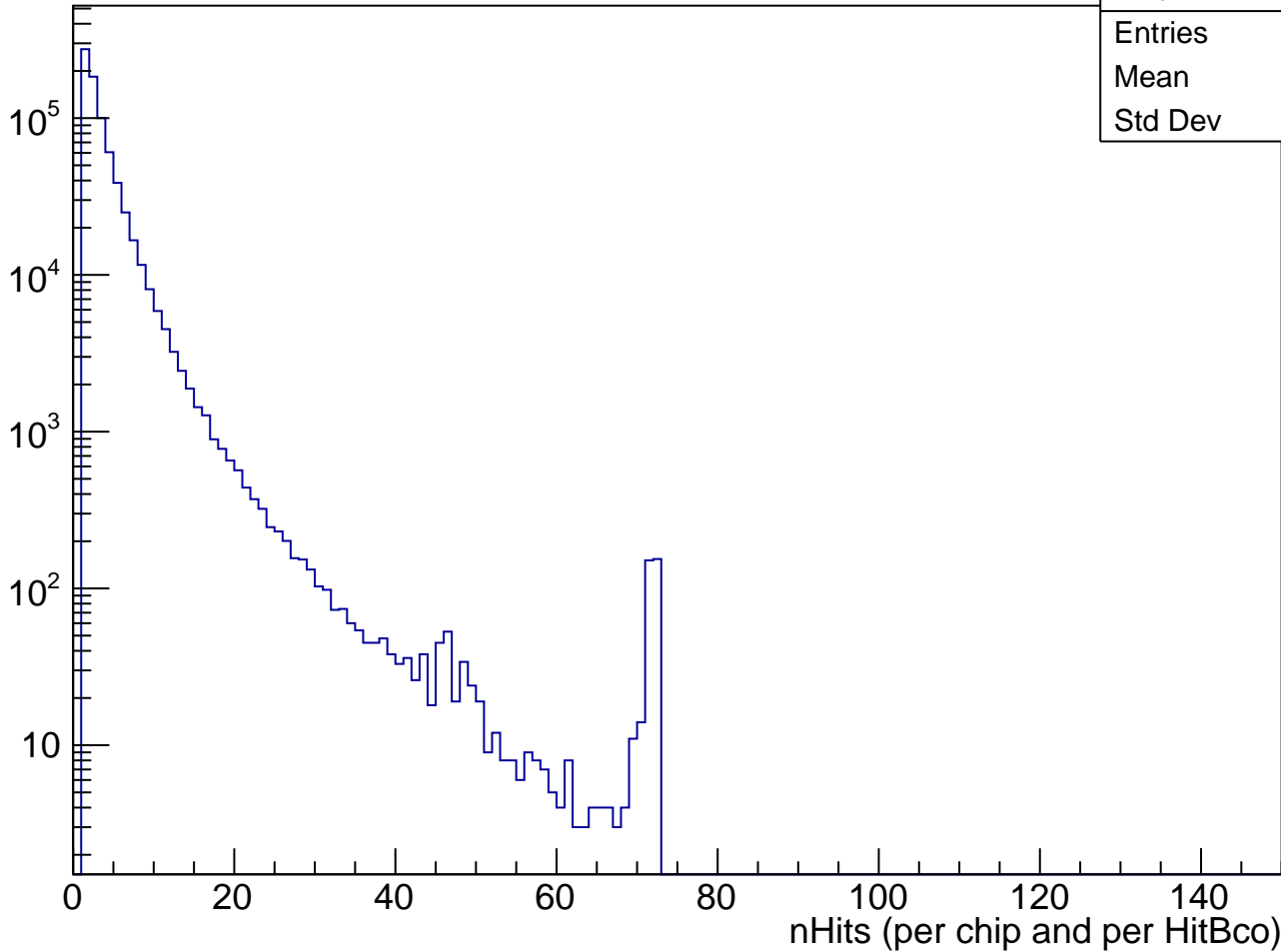
Entries 739669

Mean 3

Std Dev 3.509

nChipHit_F4_Fch11_Chip23

Entries



nChipHit_F4_Fch11_Chip23

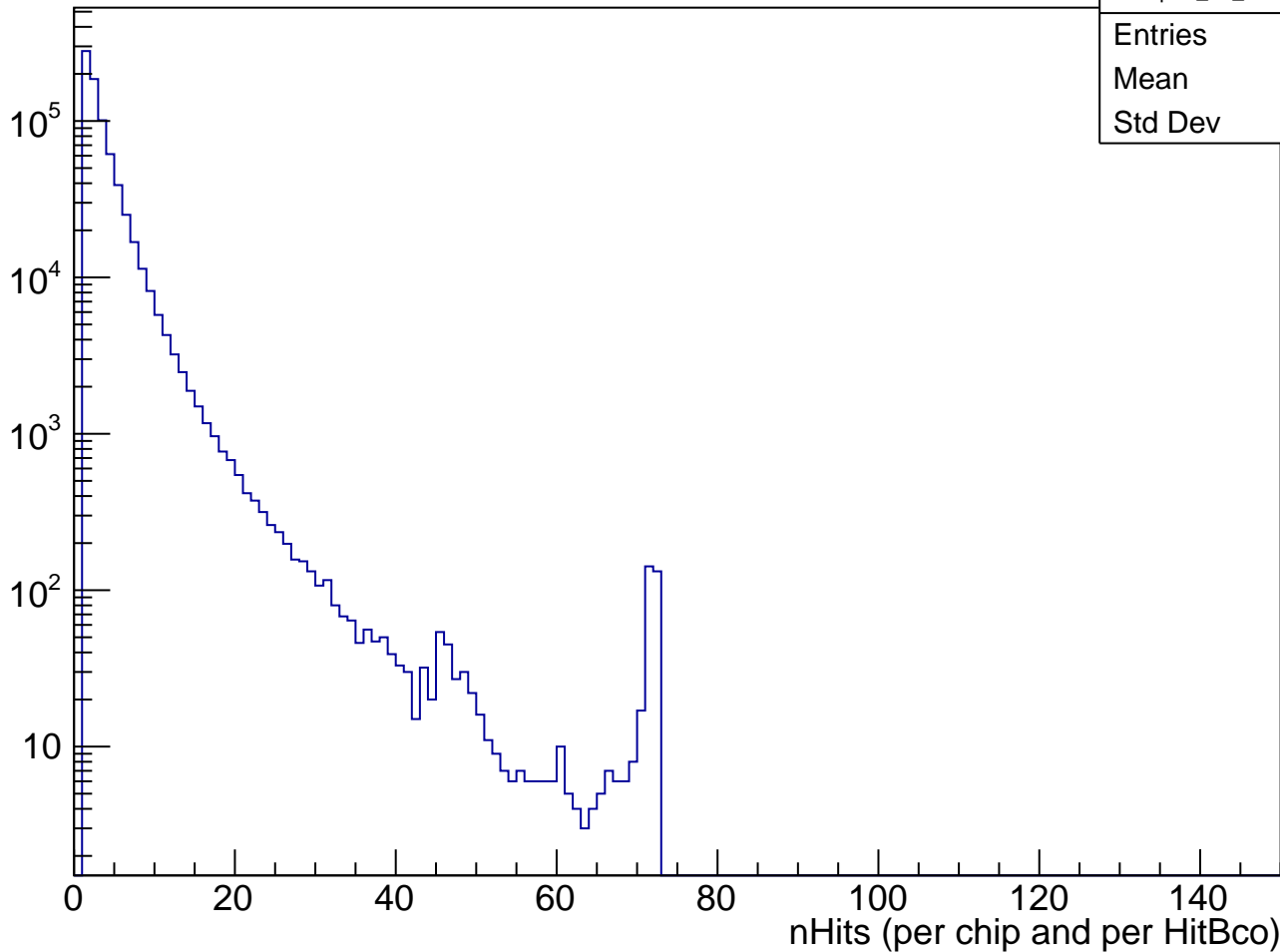
Entries 746916

Mean 2.988

Std Dev 3.484

nChipHit_F4_Fch11_Chip24

Entries



nChipHit_F4_Fch11_Chip24

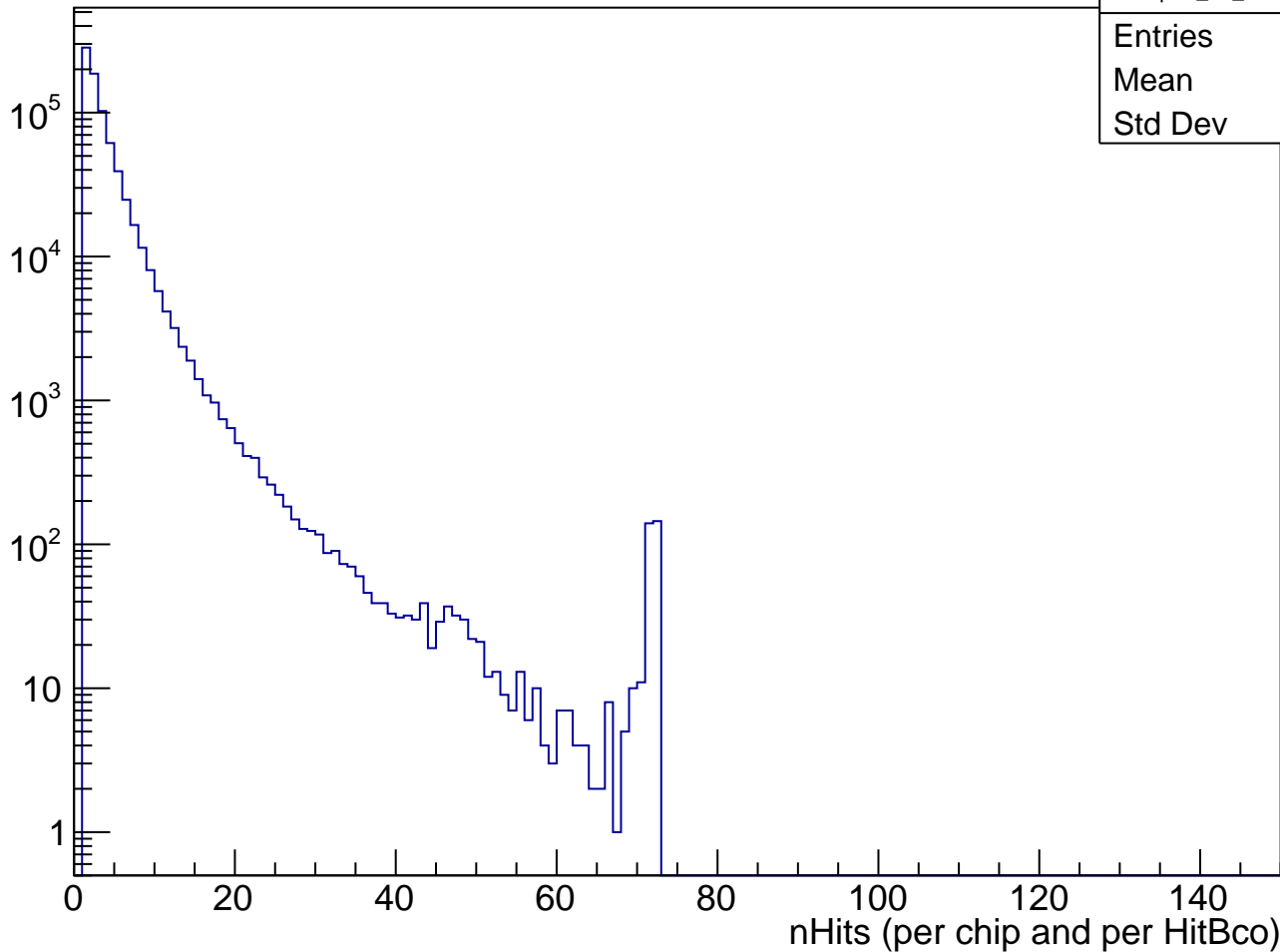
Entries 755293

Mean 2.97

Std Dev 3.443

nChipHit_F4_Fch11_Chip25

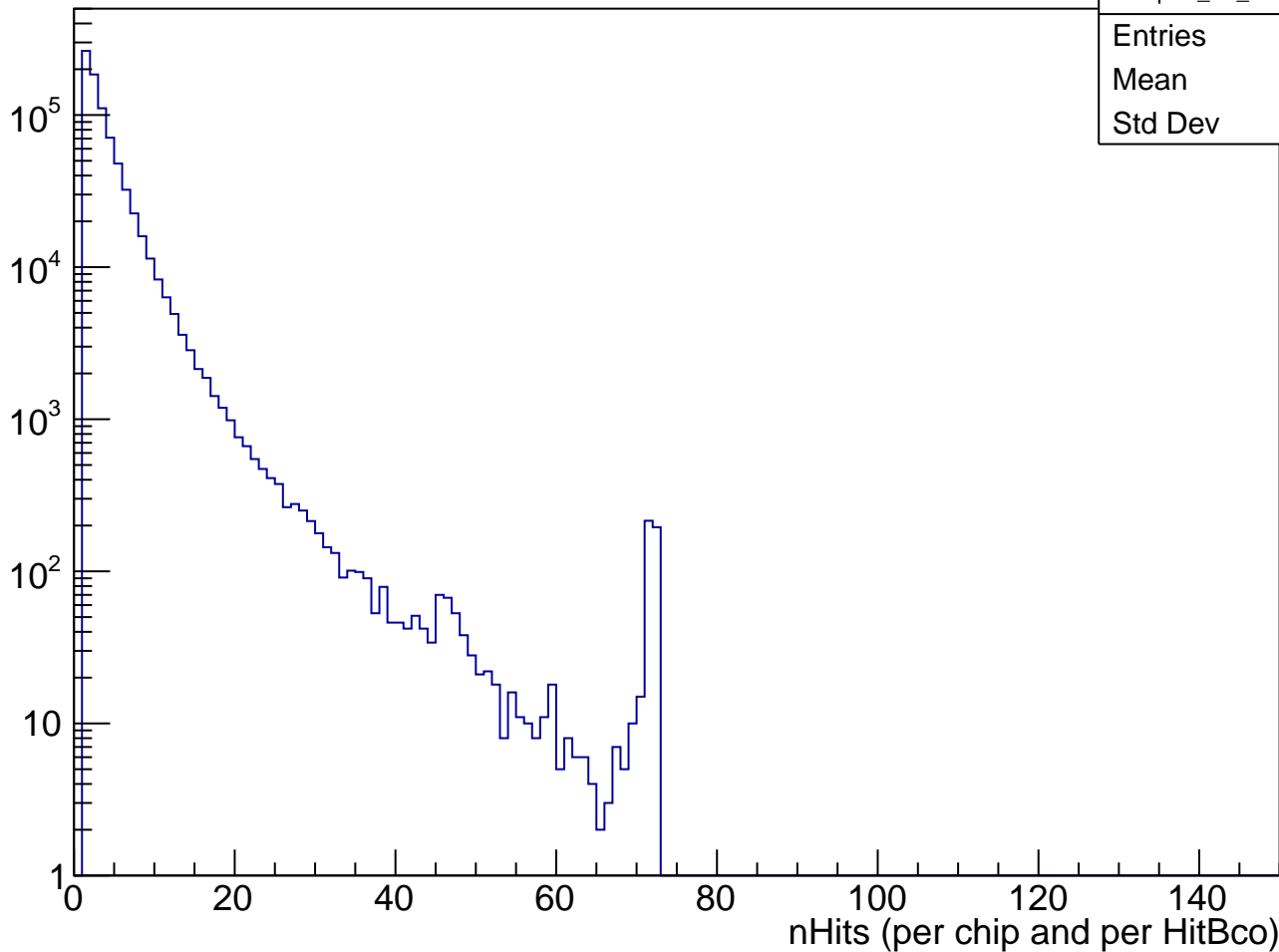
Entries



nChipHit_F4_Fch11_Chip25	
Entries	760091
Mean	2.948
Std Dev	3.41

nChipHit_F4_Fch11_Chip3

Entries



nChipHit_F4_Fch11_Chip3

Entries 800740

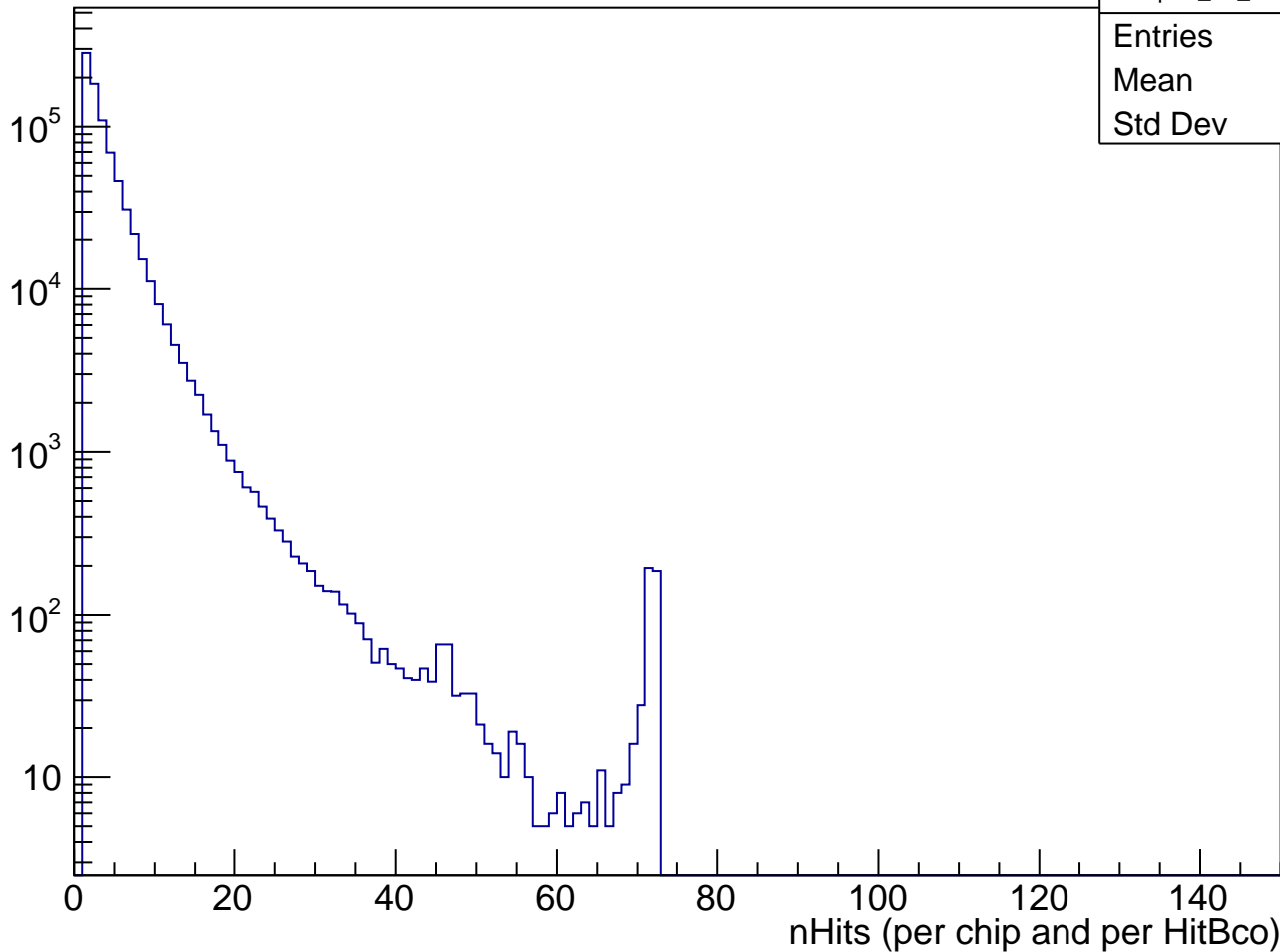
Mean 3.361

Std Dev 3.922

nHits (per chip and per HitBco)

nChipHit_F4_Fch11_Chip4

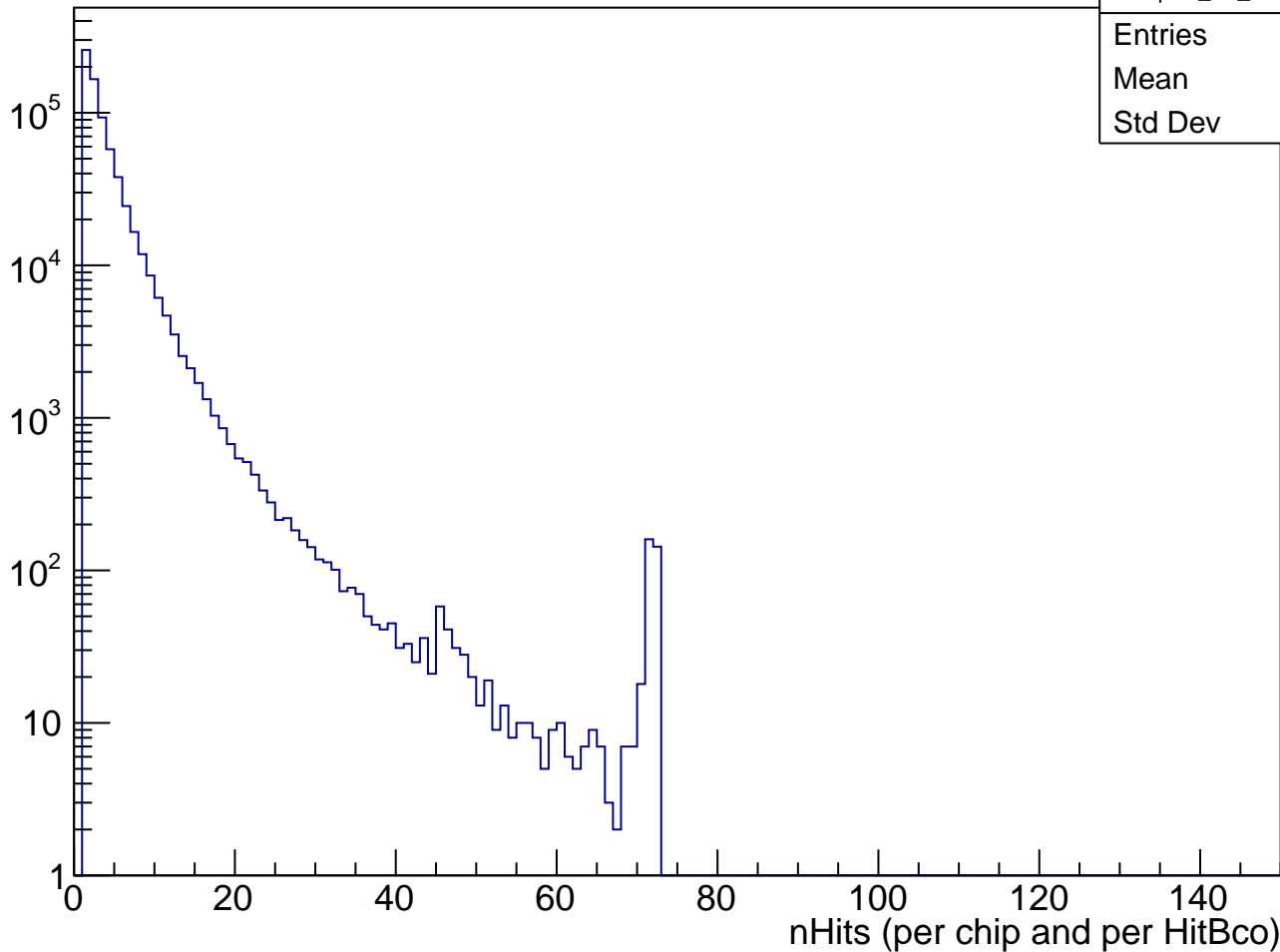
Entries



nChipHit_F4_Fch11_Chip4	
Entries	809261
Mean	3.264
Std Dev	3.847

nChipHit_F4_Fch11_Chip5

Entries

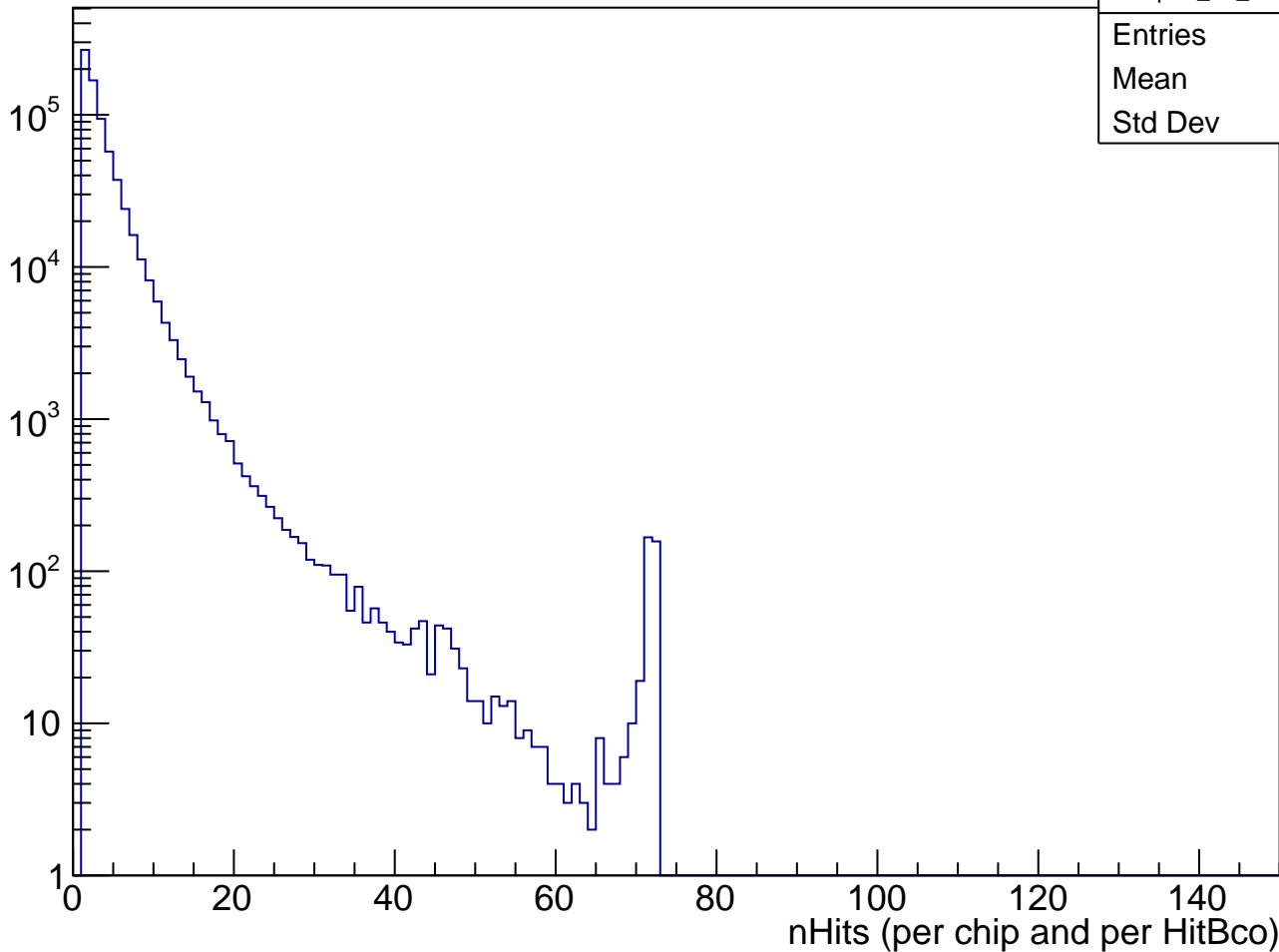


nChipHit_F4_Fch11_Chip5	
Entries	703483
Mean	3.093
Std Dev	3.66

nHits (per chip and per HitBco)

nChipHit_F4_Fch11_Chip6

Entries

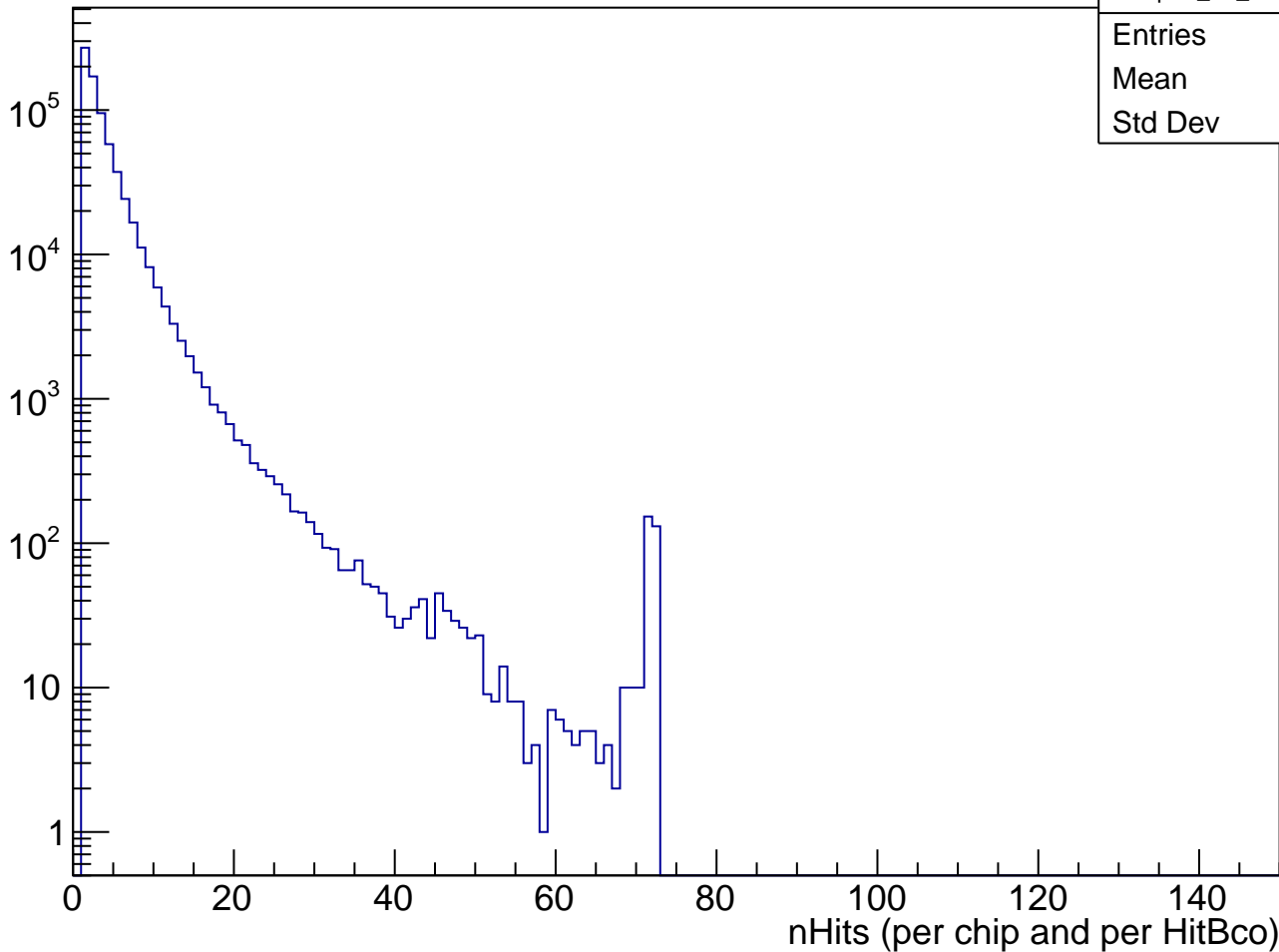


nChipHit_F4_Fch11_Chip6	
Entries	712073
Mean	3.024
Std Dev	3.601

nHits (per chip and per HitBco)

nChipHit_F4_Fch11_Chip7

Entries

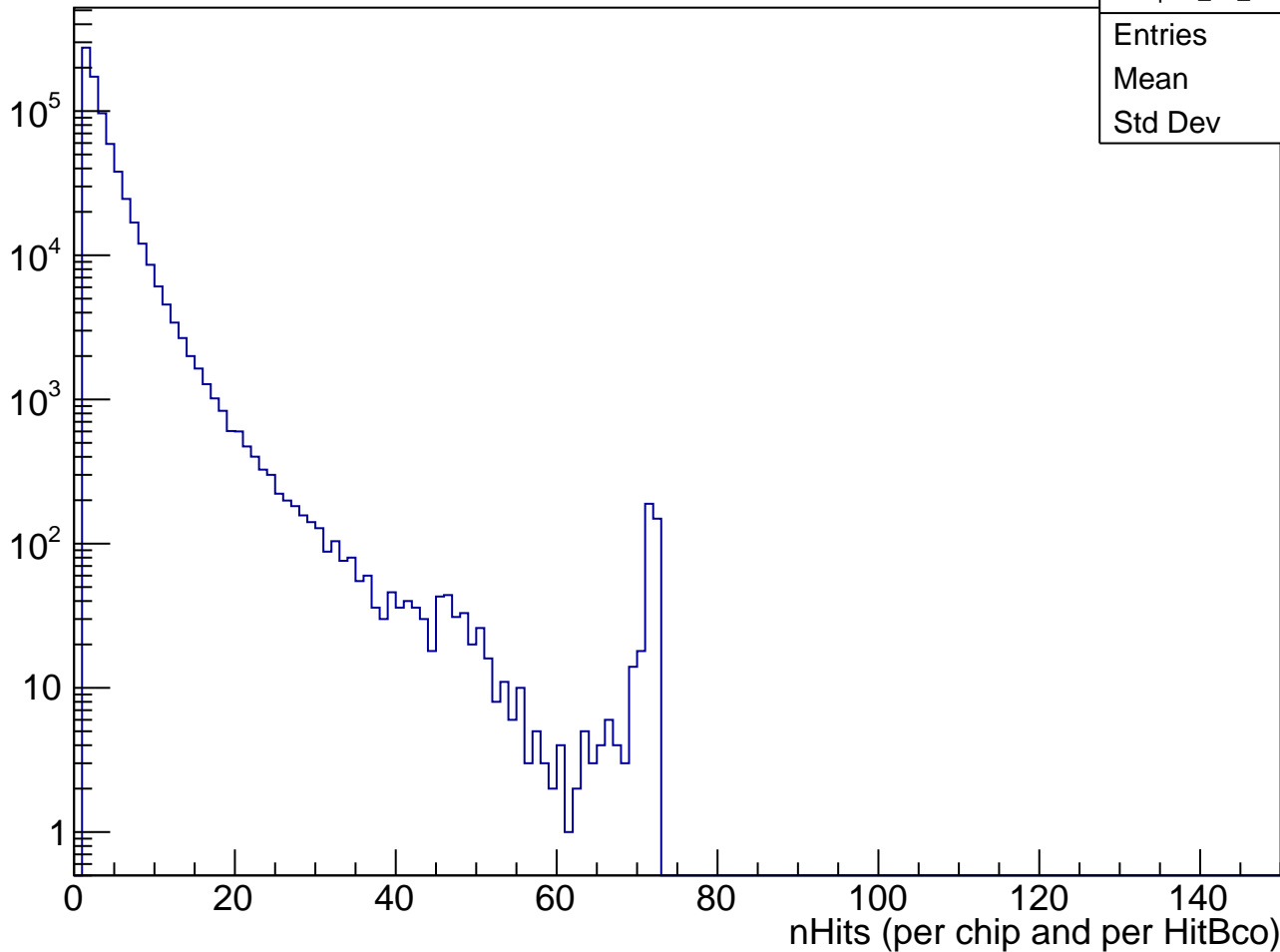


nChipHit_F4_Fch11_Chip7	
Entries	719198
Mean	3.011
Std Dev	3.536

nHits (per chip and per HitBco)

nChipHit_F4_Fch11_Chip8

Entries

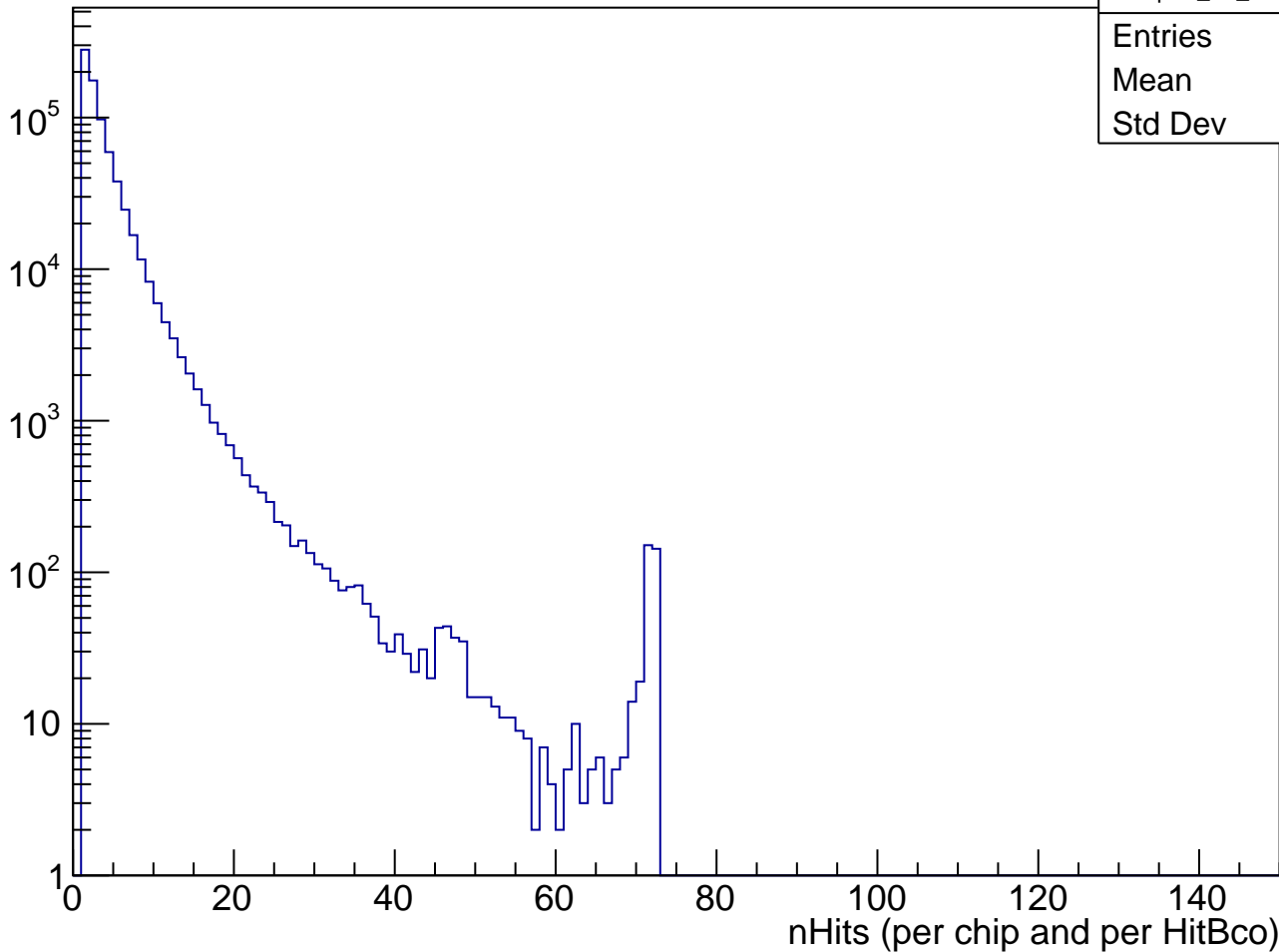


nChipHit_F4_Fch11_Chip8	
Entries	732758
Mean	3.031
Std Dev	3.598

nHits (per chip and per HitBco)

nChipHit_F4_Fch11_Chip9

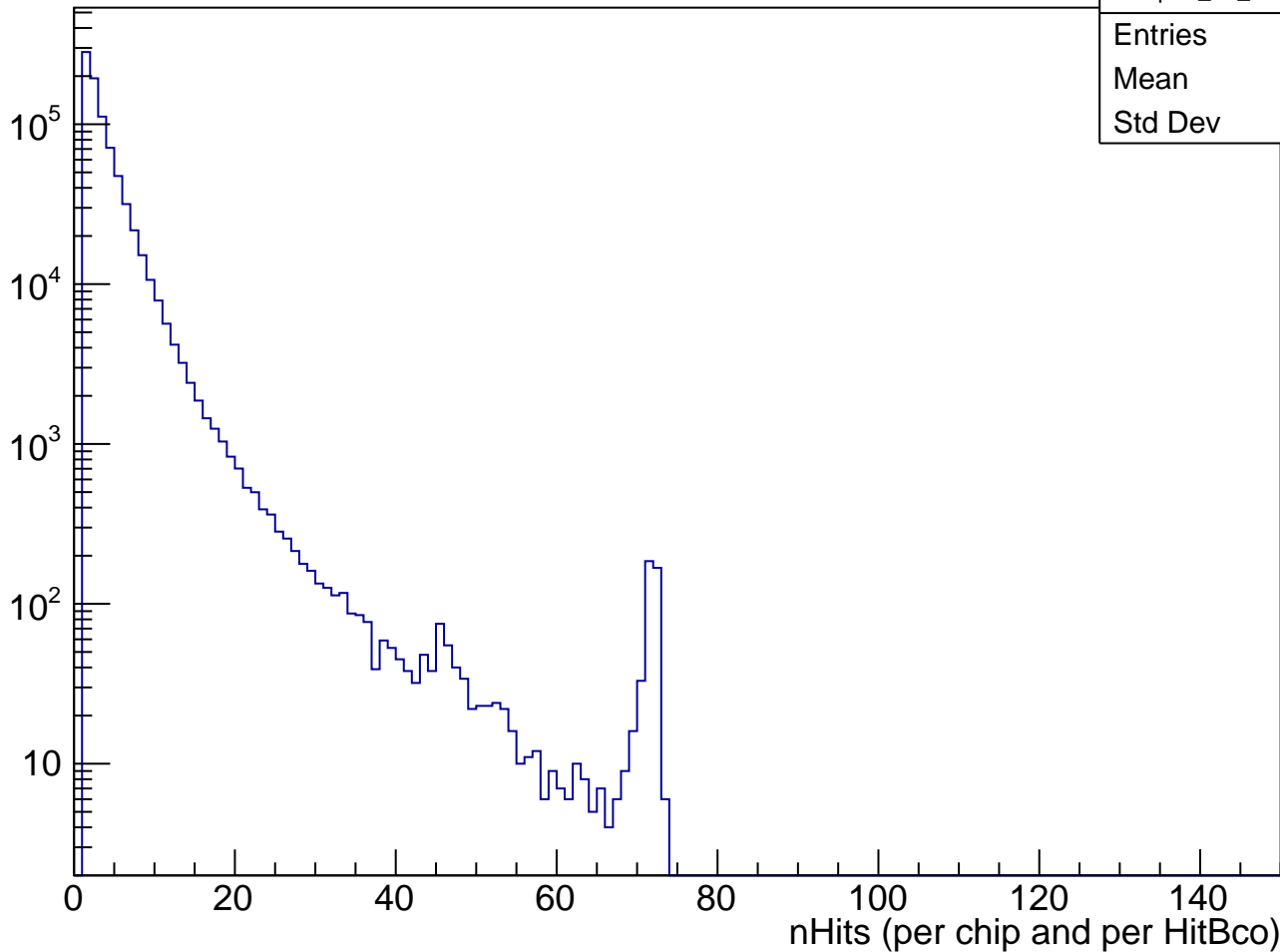
Entries



nChipHit_F4_Fch11_Chip9	
Entries	739961
Mean	3.001
Std Dev	3.545

nChipHit_F4_Fch12_Chip0

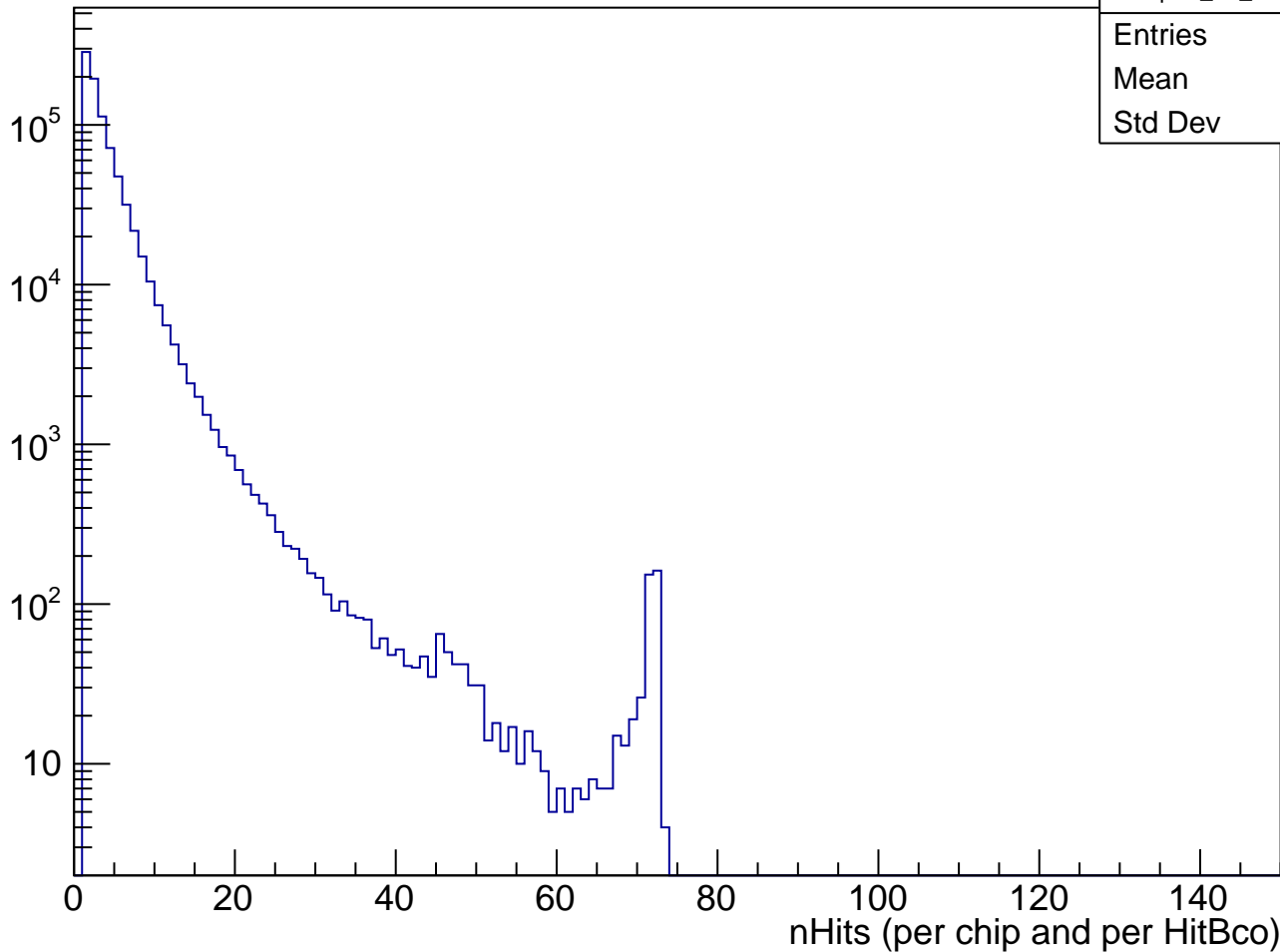
Entries



nChipHit_F4_Fch12_Chip0	
Entries	821482
Mean	3.205
Std Dev	3.731

nChipHit_F4_Fch12_Chip1

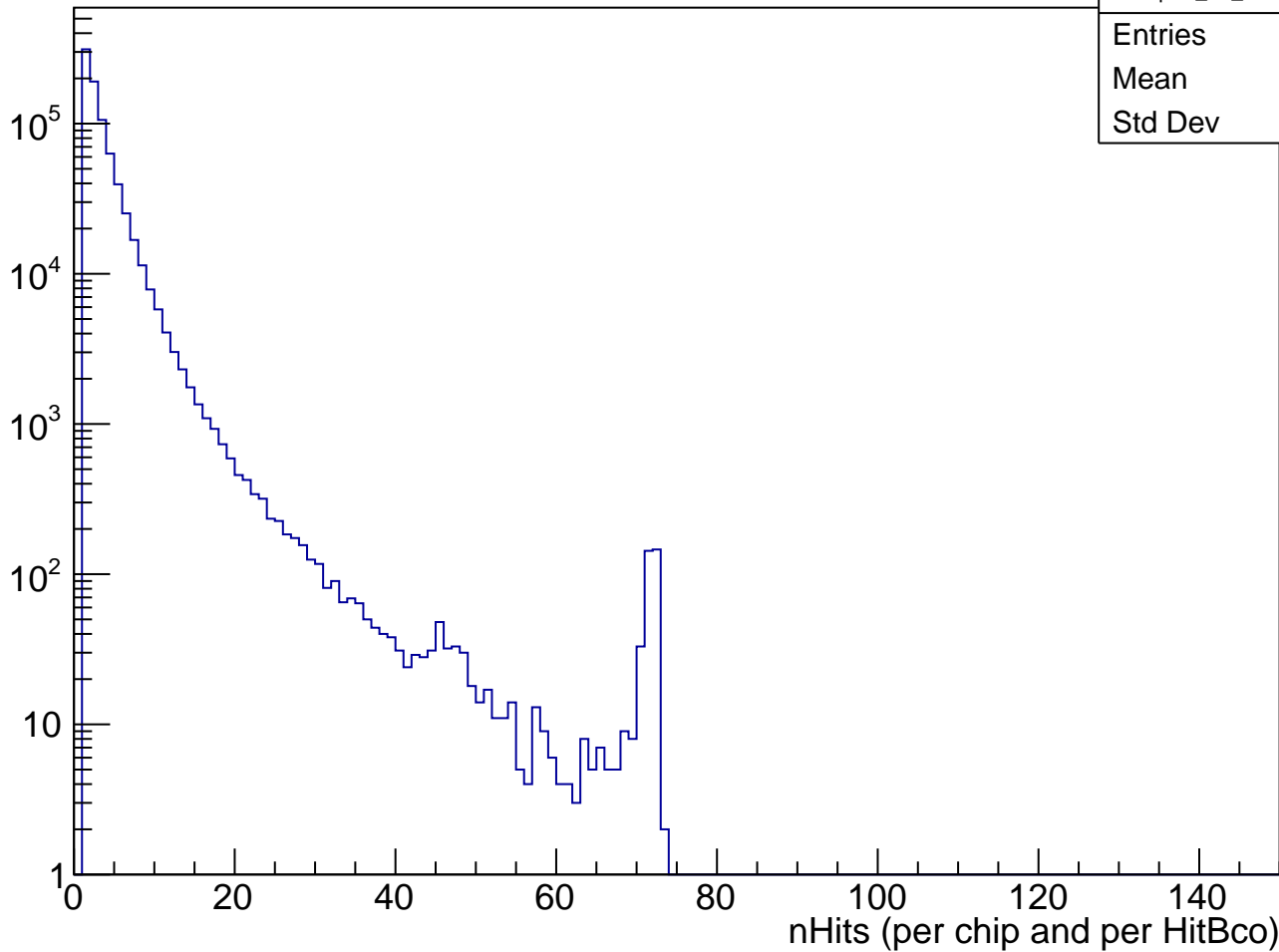
Entries



nChipHit_F4_Fch12_Chip1	
Entries	826604
Mean	3.188
Std Dev	3.694

nChipHit_F4_Fch12_Chip10

Entries



nChipHit_F4_Fch12_Chip10

Entries 798344

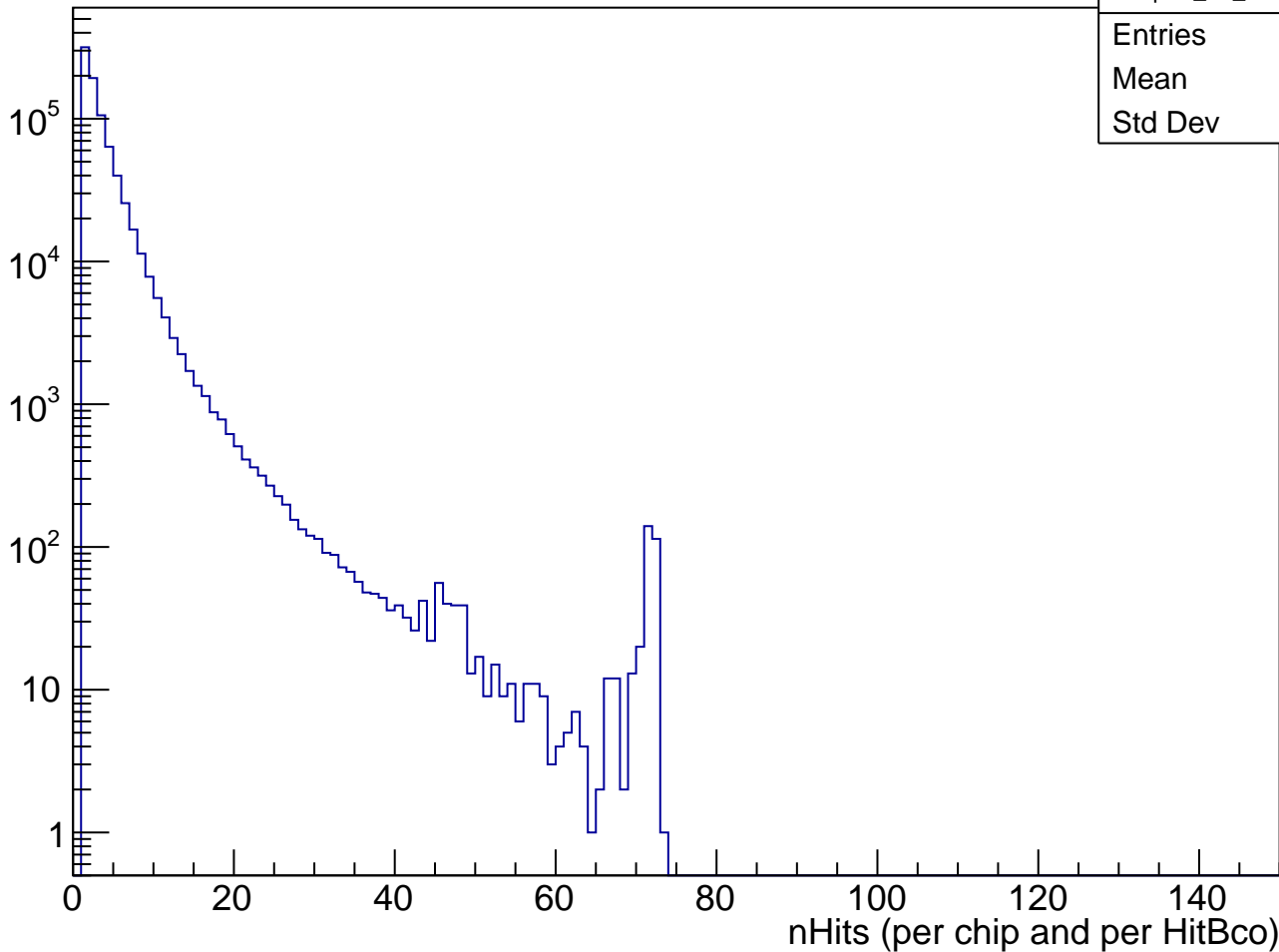
Mean 2.872

Std Dev 3.373

nHits (per chip and per HitBco)

nChipHit_F4_Fch12_Chip11

Entries

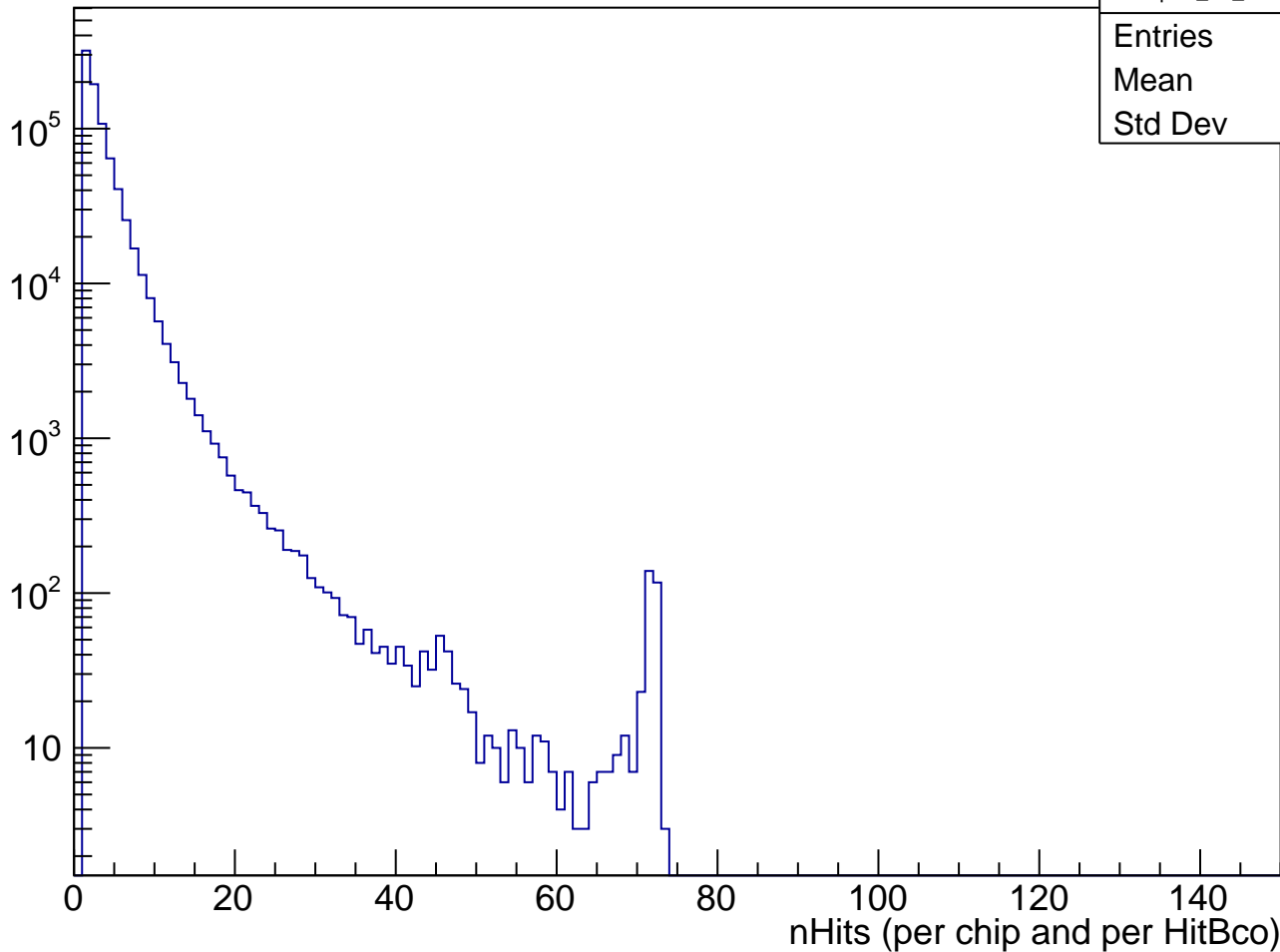


nChipHit_F4_Fch12_Chip11	
Entries	805480
Mean	2.858
Std Dev	3.334

nHits (per chip and per HitBco)

nChipHit_F4_Fch12_Chip12

Entries



nChipHit_F4_Fch12_Chip12

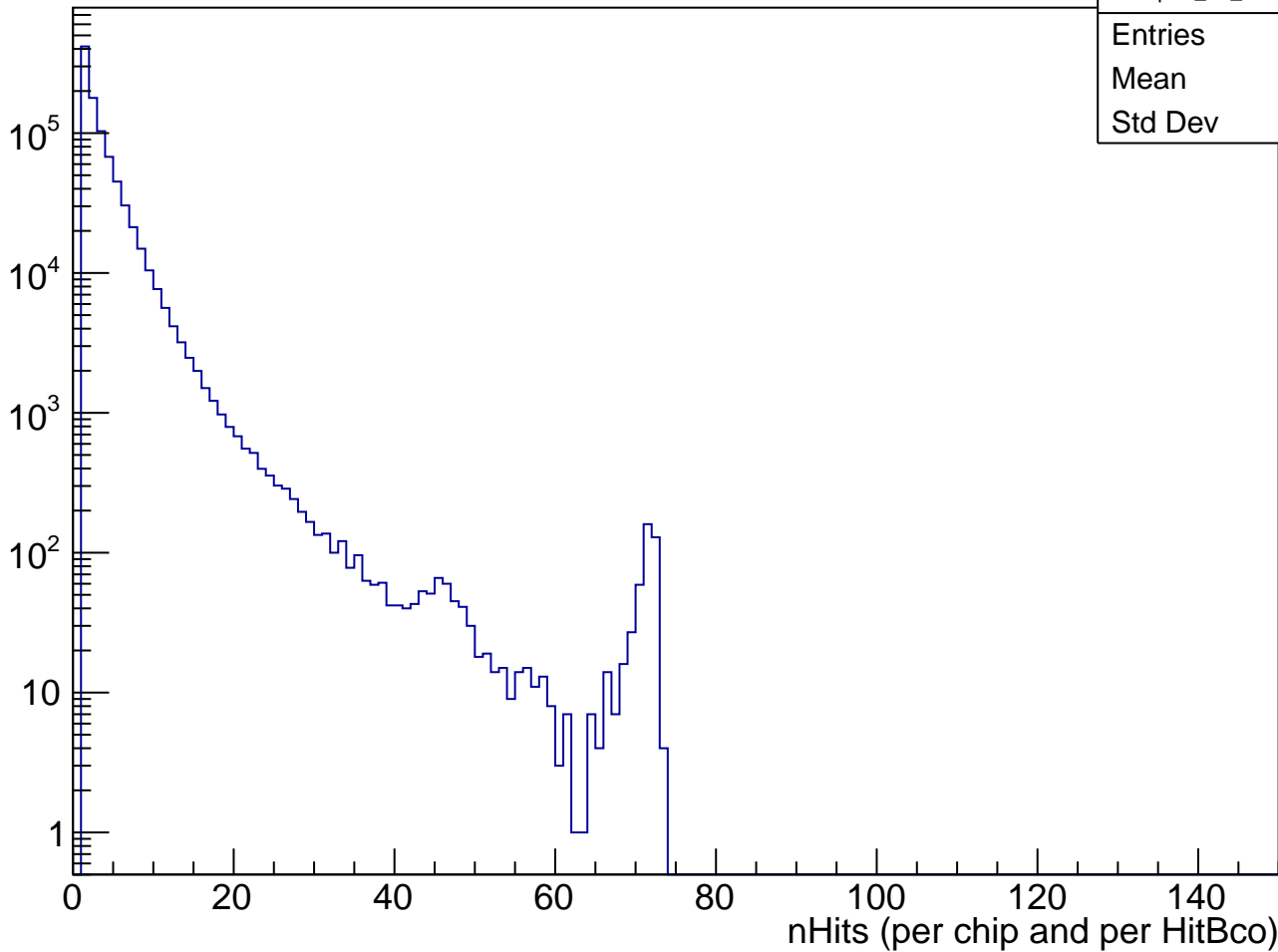
Entries 813051

Mean 2.865

Std Dev 3.343

nChipHit_F4_Fch12_Chip13

Entries



nChipHit_F4_Fch12_Chip13

Entries 923407

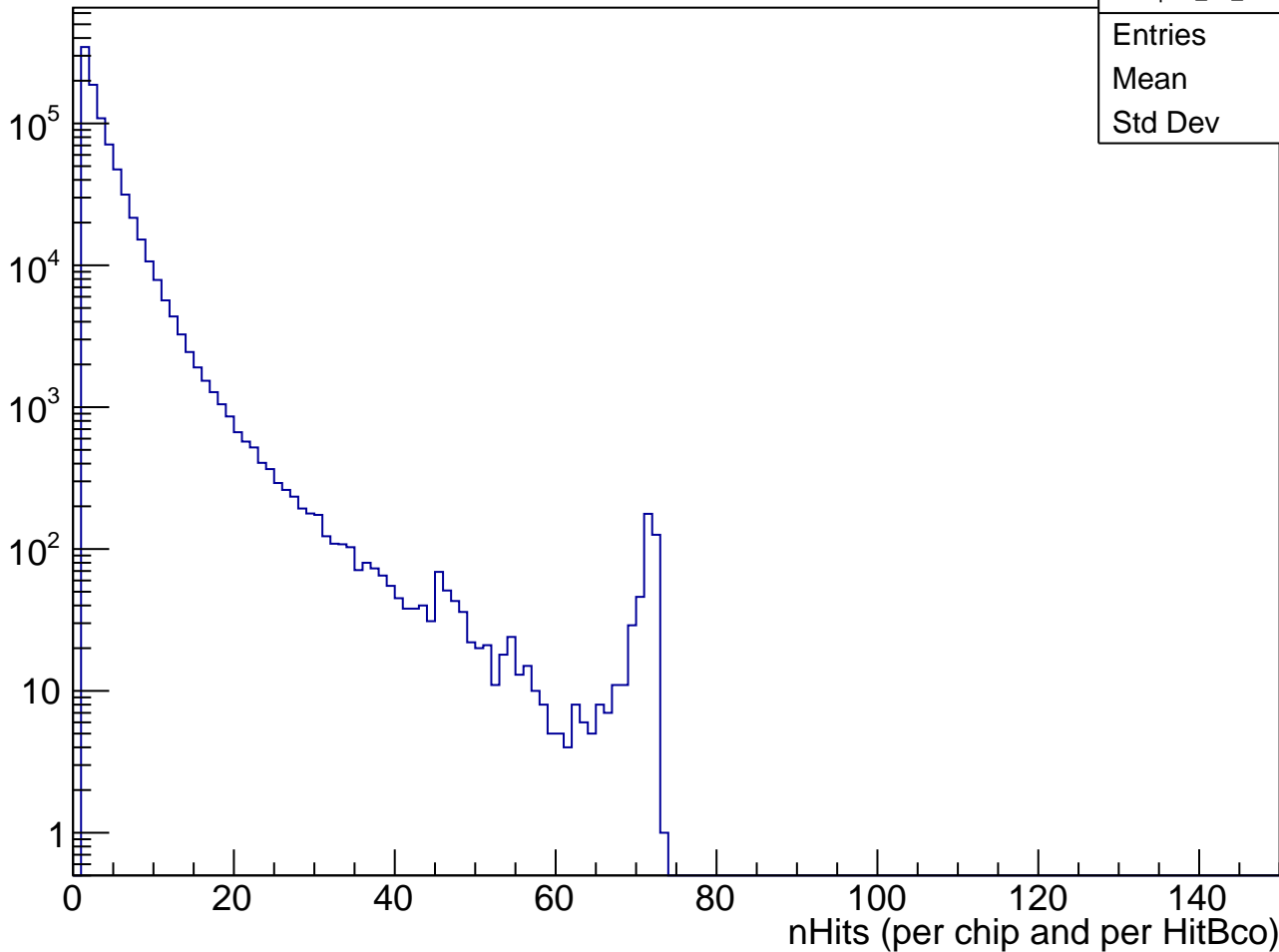
Mean 2.893

Std Dev 3.587

nHits (per chip and per HitBco)

nChipHit_F4_Fch12_Chip14

Entries

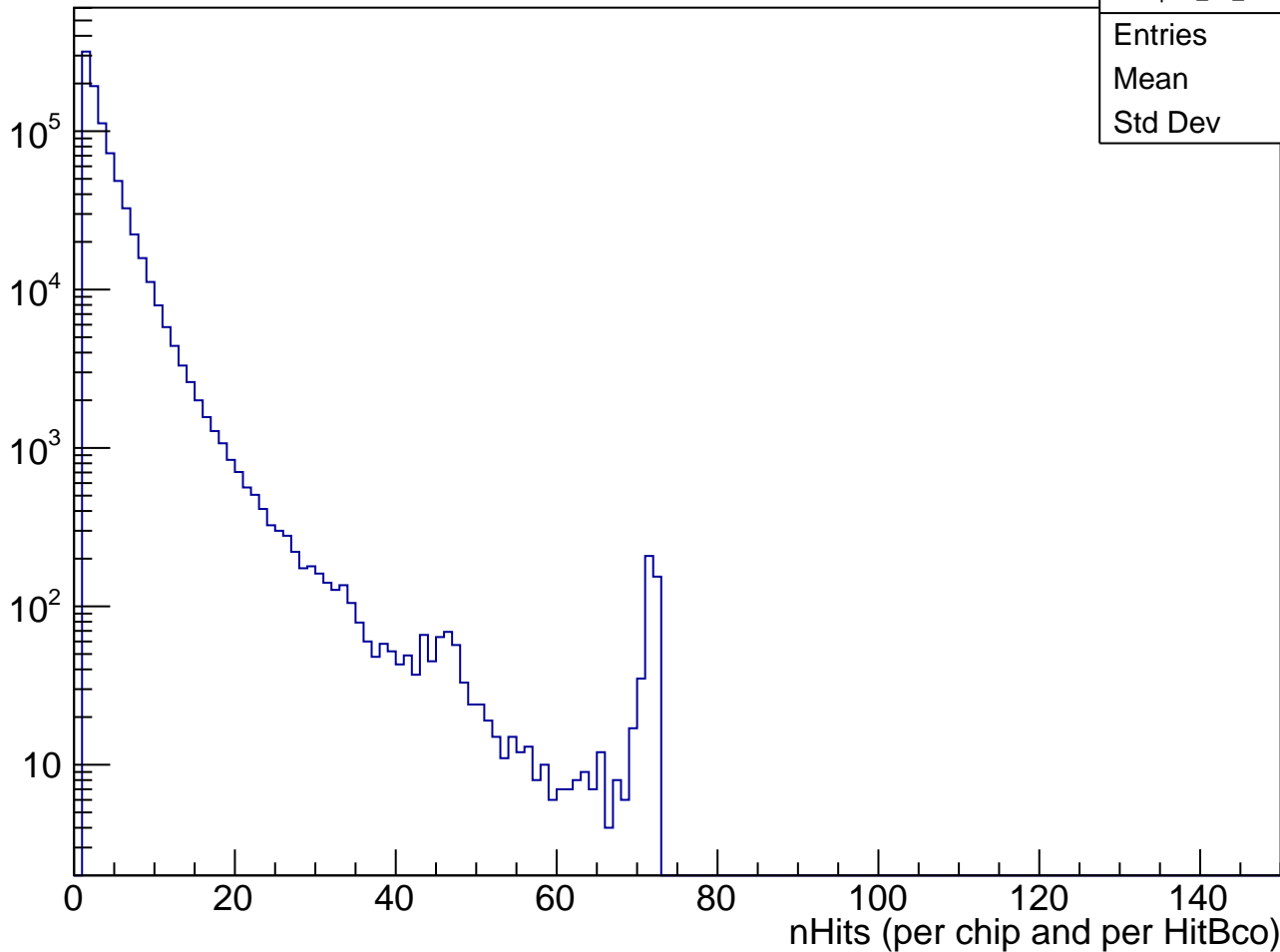


nChipHit_F4_Fch12_Chip14	
Entries	874964
Mean	3.064
Std Dev	3.66

nHits (per chip and per HitBco)

nChipHit_F4_Fch12_Chip15

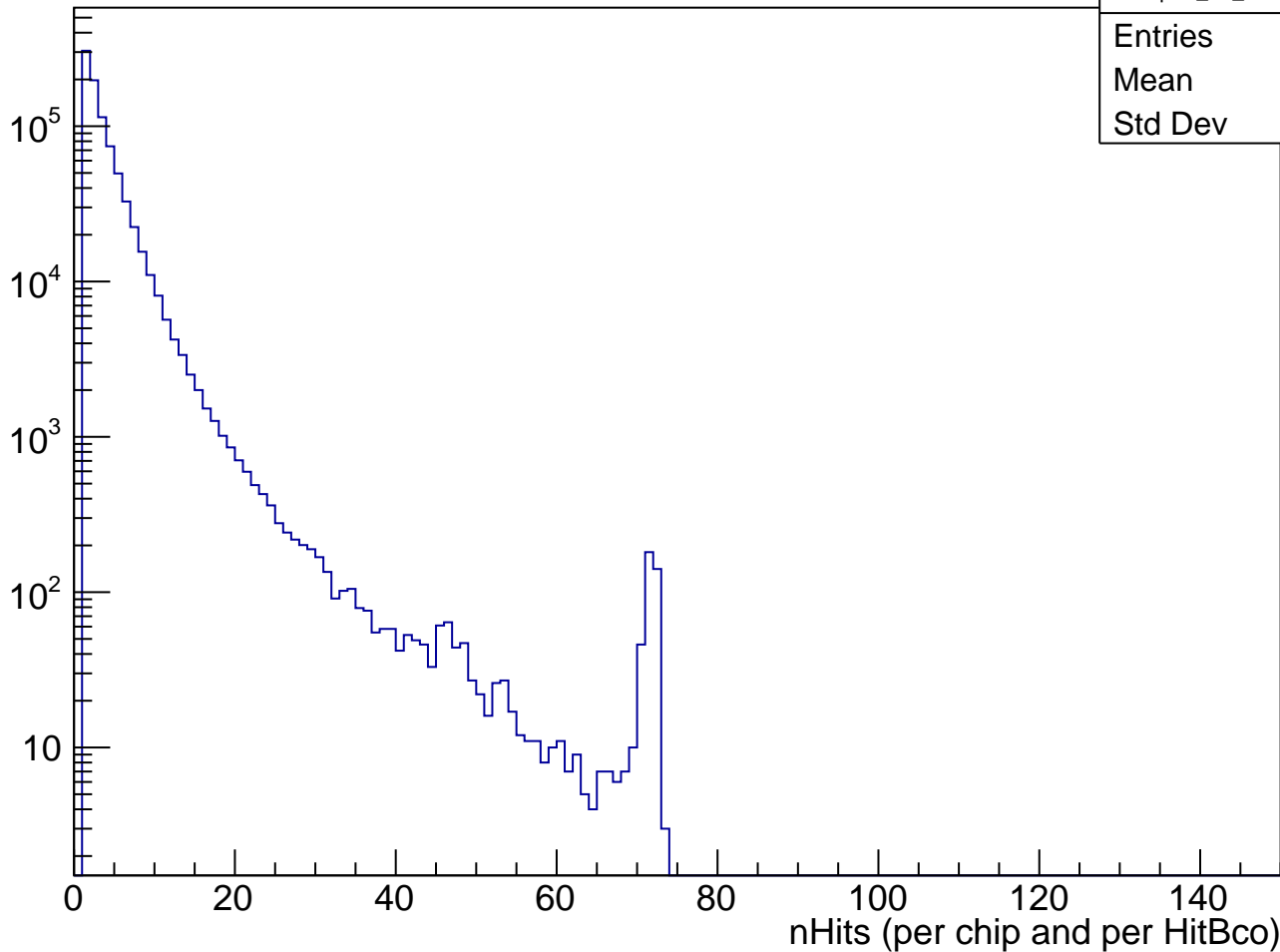
Entries



nChipHit_F4_Fch12_Chip15	
Entries	862065
Mean	3.153
Std Dev	3.725

nChipHit_F4_Fch12_Chip16

Entries



nChipHit_F4_Fch12_Chip16

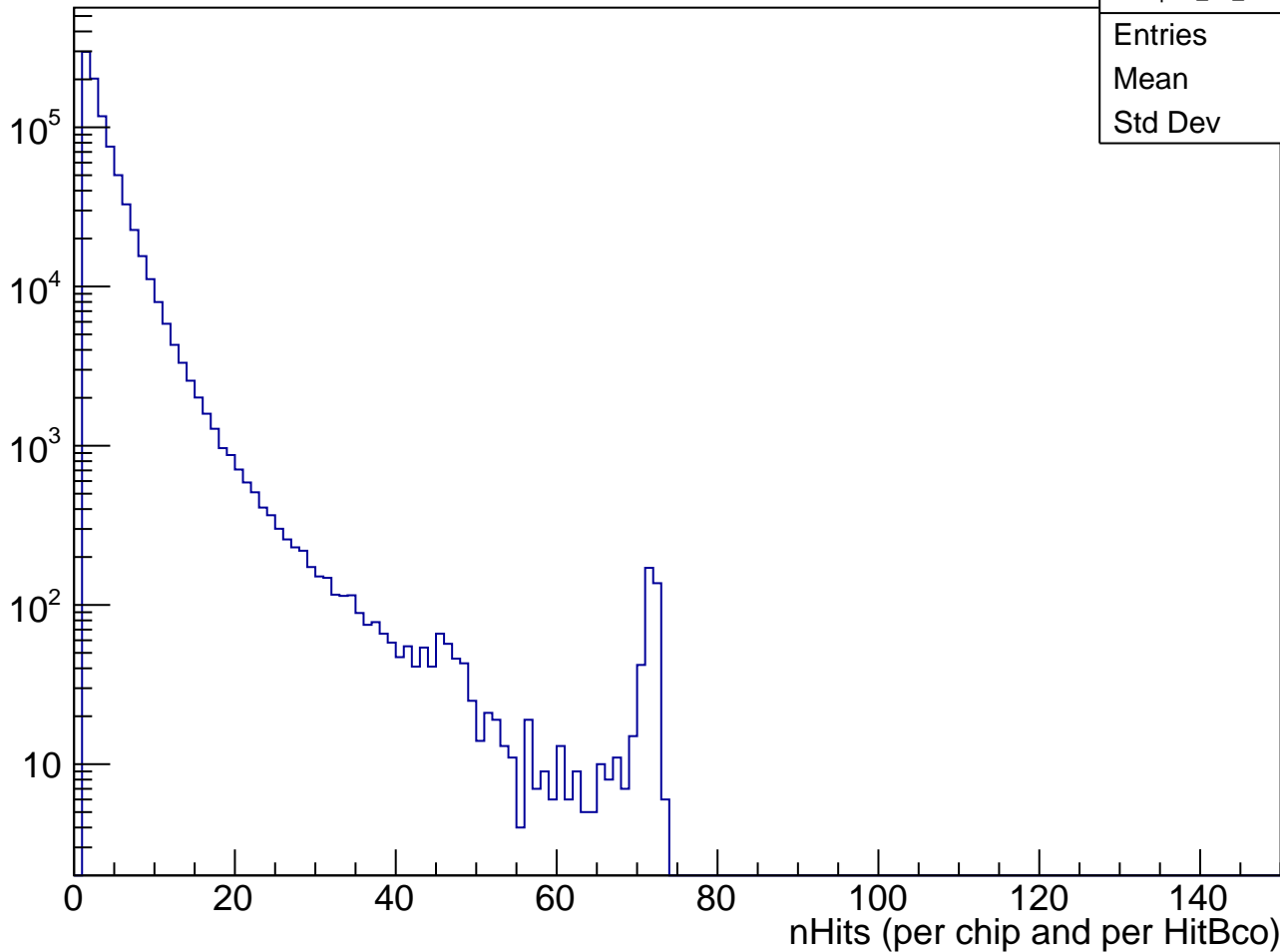
Entries 859701

Mean 3.172

Std Dev 3.693

nChipHit_F4_Fch12_Chip17

Entries



nChipHit_F4_Fch12_Chip17

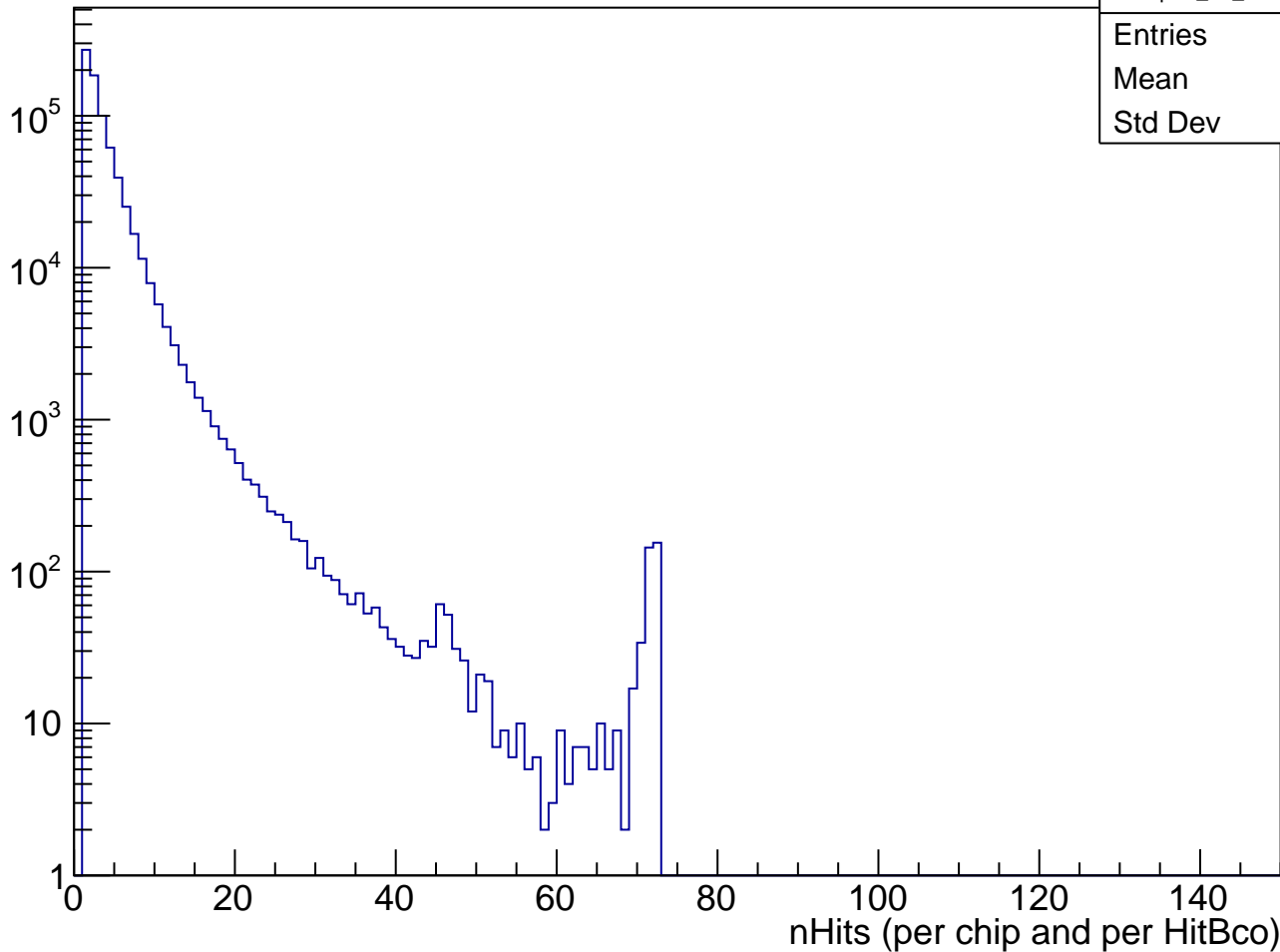
Entries 861728

Mean 3.195

Std Dev 3.692

nChipHit_F4_Fch12_Chip18

Entries



nChipHit_F4_Fch12_Chip18

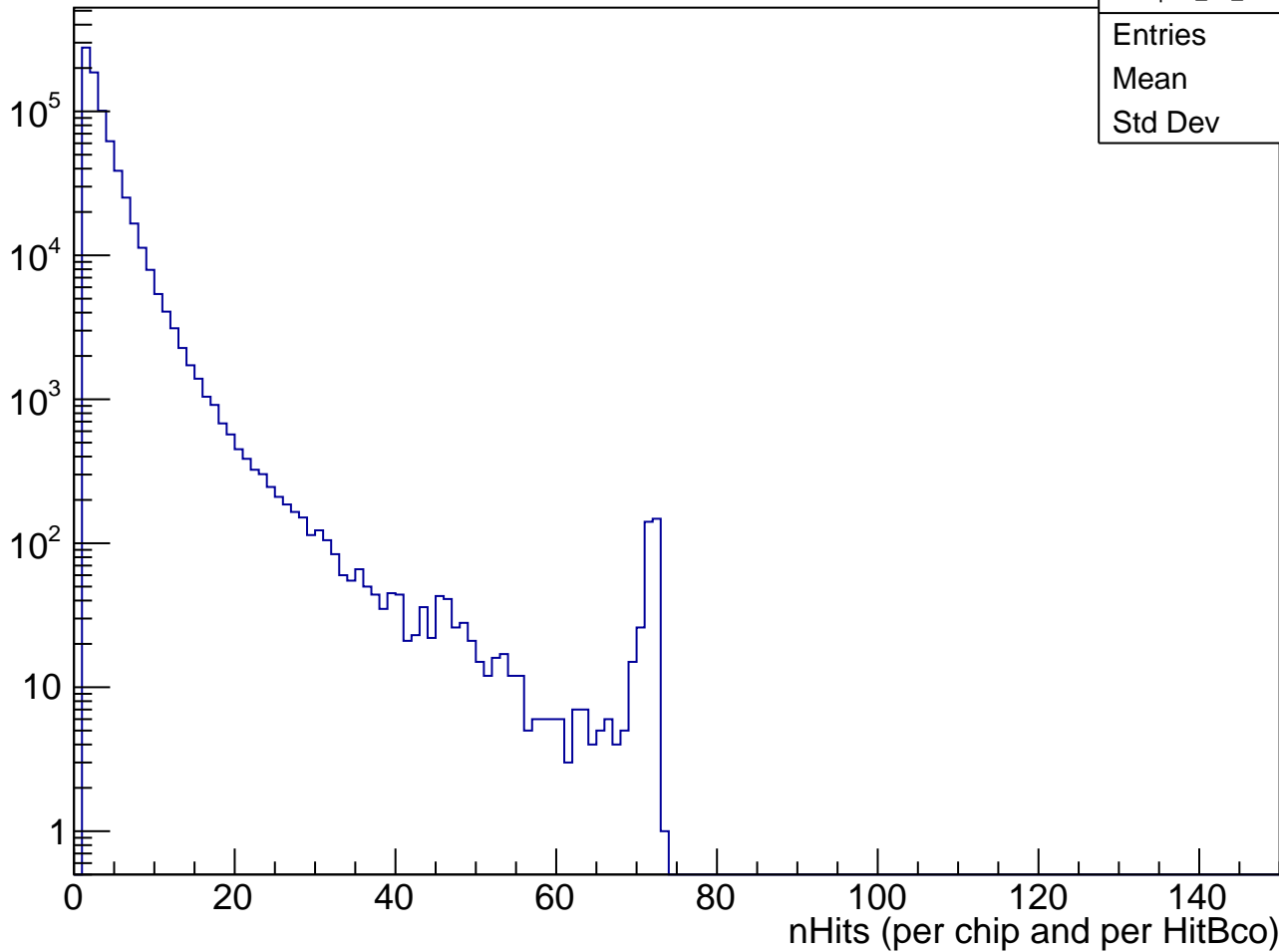
Entries 744887

Mean 2.986

Std Dev 3.501

nChipHit_F4_Fch12_Chip19

Entries



nChipHit_F4_Fch12_Chip19

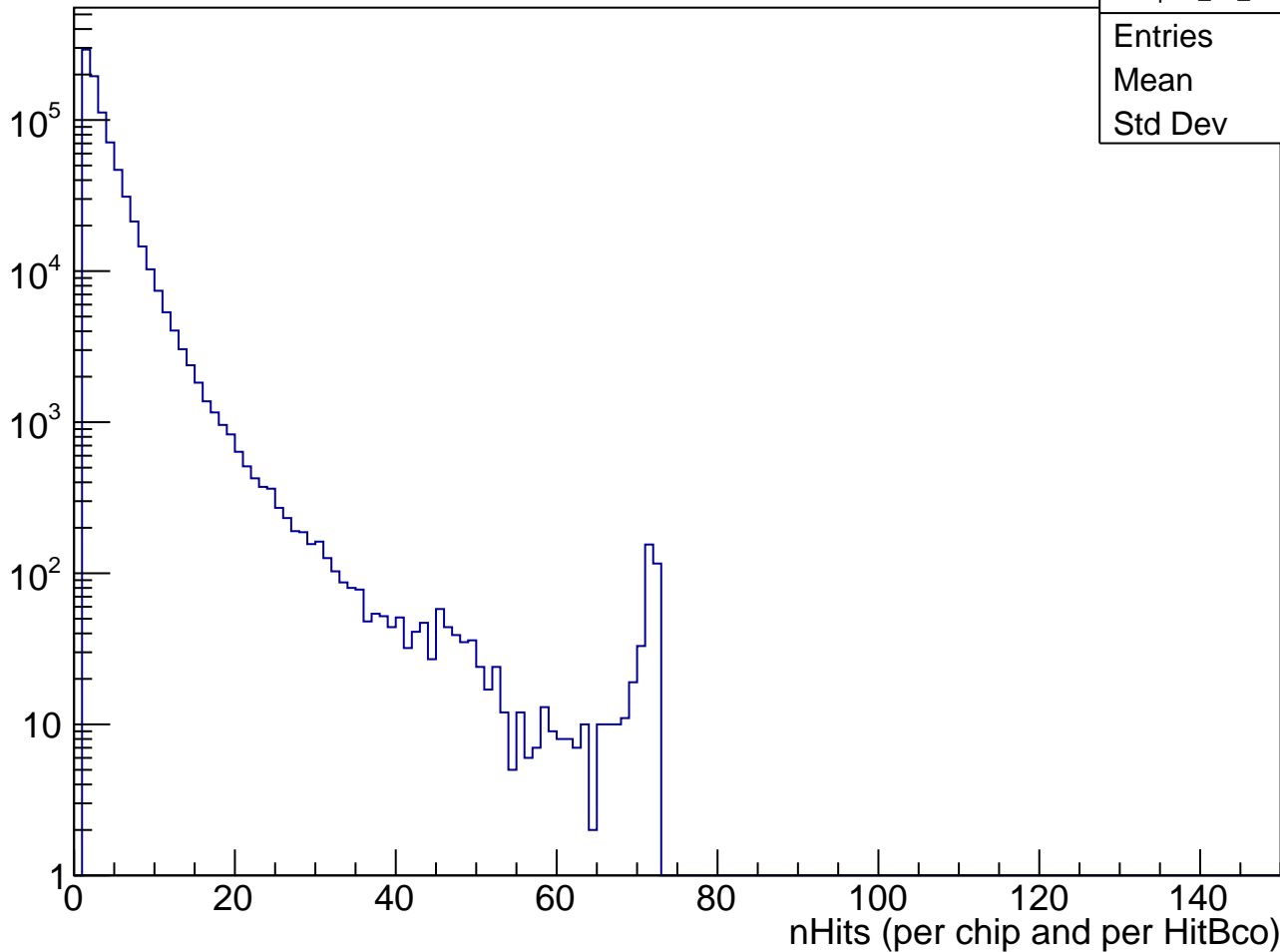
Entries 751502

Mean 2.949

Std Dev 3.43

nChipHit_F4_Fch12_Chip2

Entries

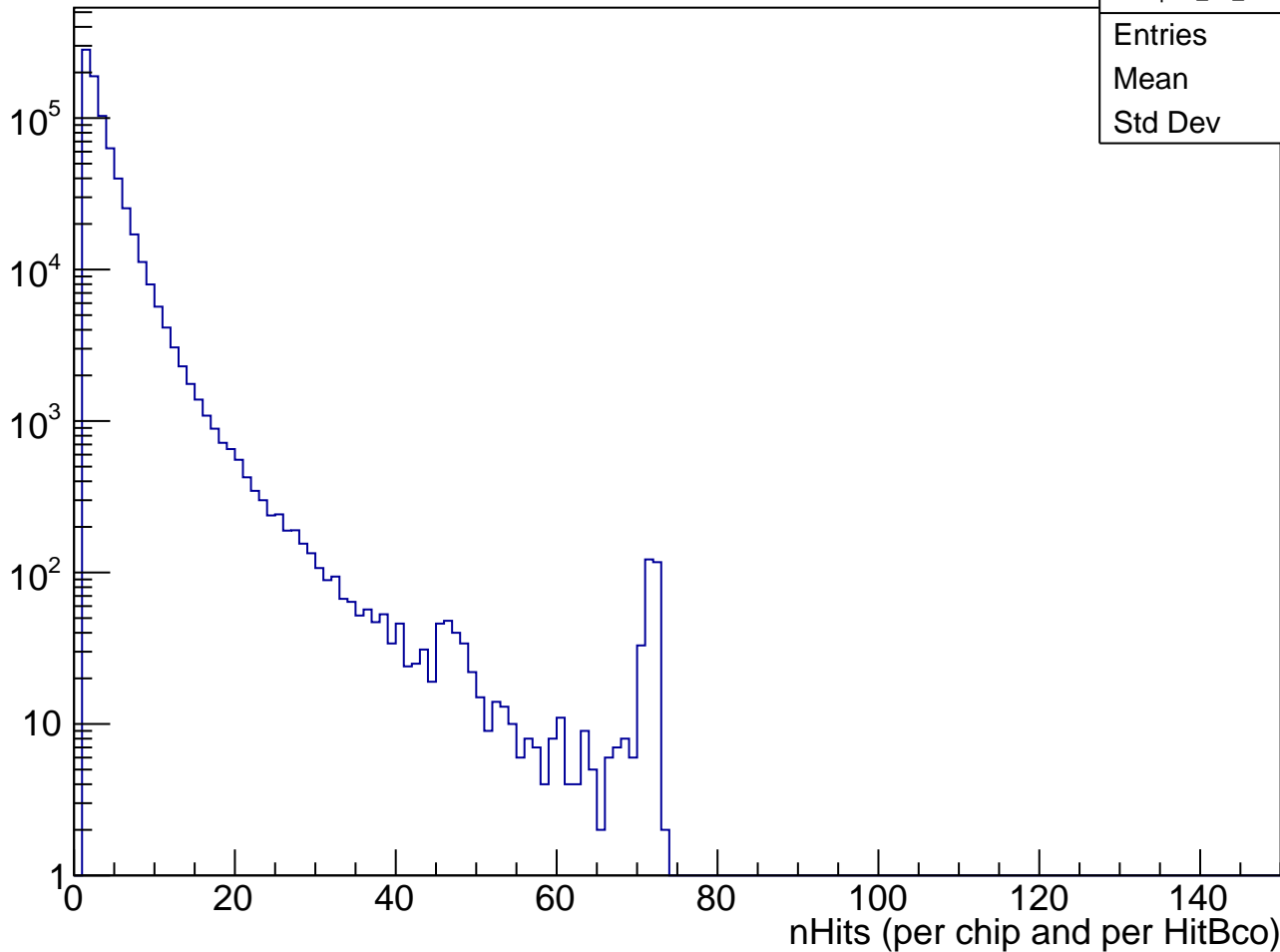


nChipHit_F4_Fch12_Chip2	
Entries	828111
Mean	3.135
Std Dev	3.6

nHits (per chip and per HitBco)

nChipHit_F4_Fch12_Chip20

Entries



nChipHit_F4_Fch12_Chip20

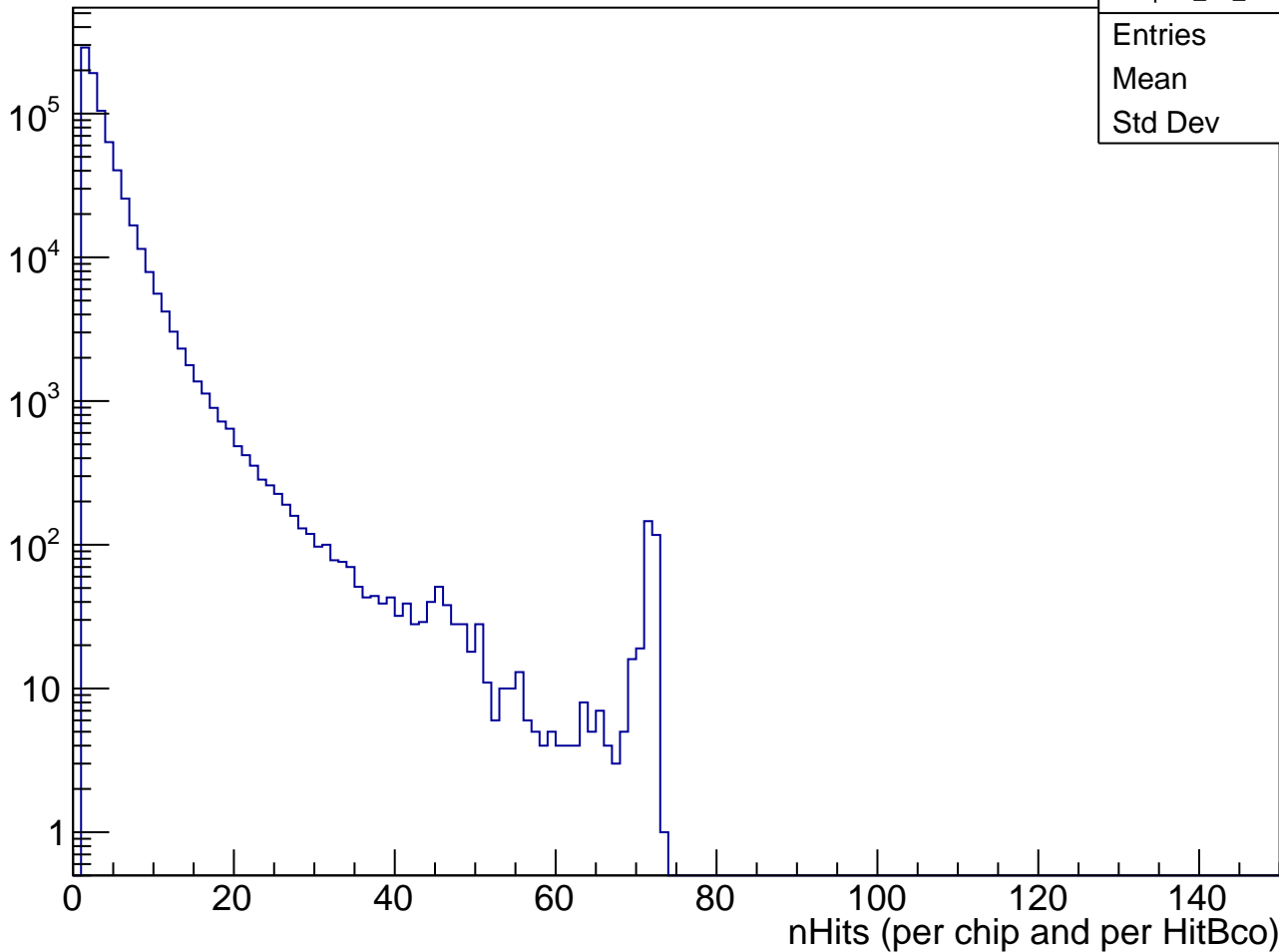
Entries 765403

Mean 2.95

Std Dev 3.401

nChipHit_F4_Fch12_Chip21

Entries

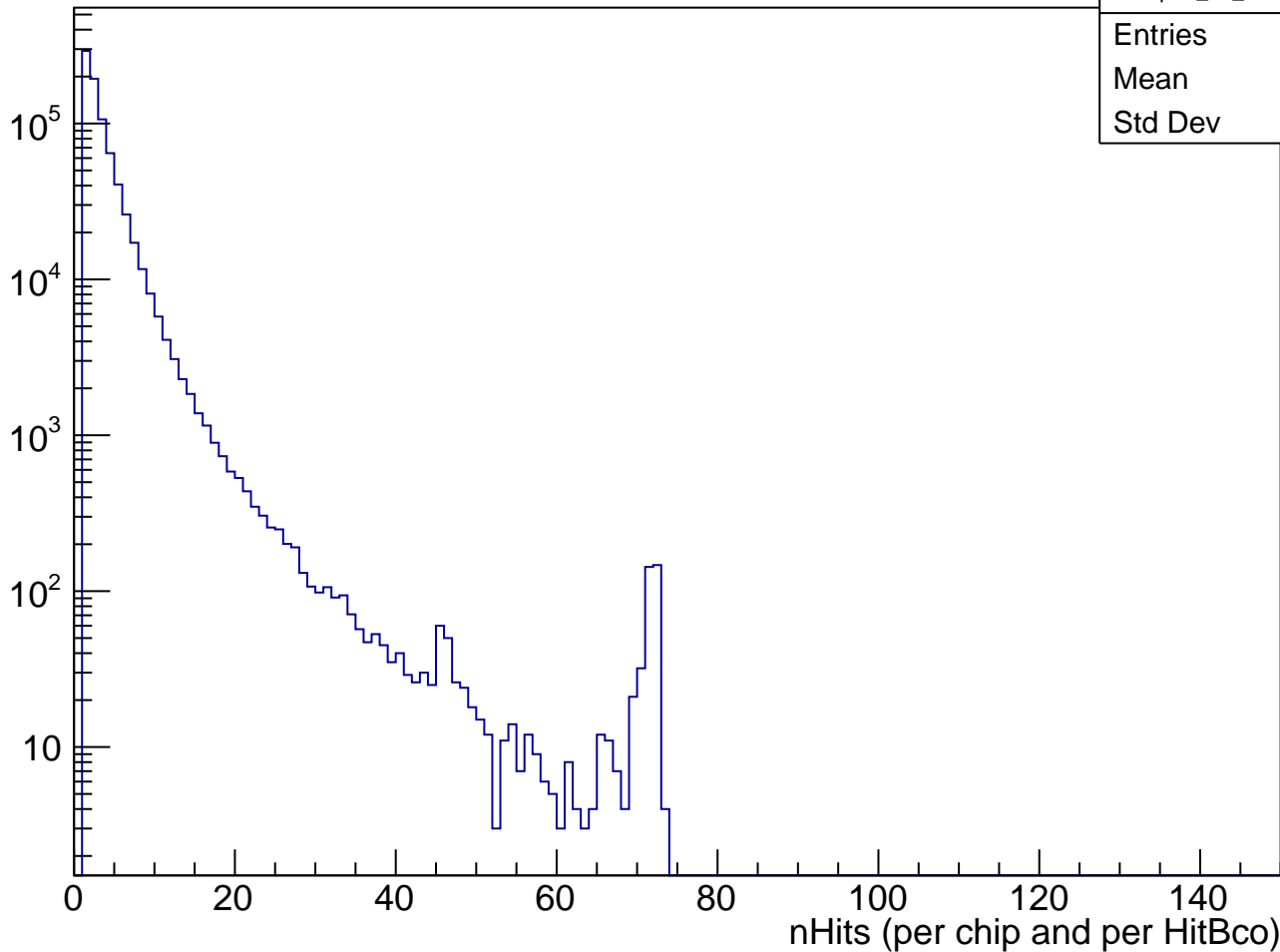


nChipHit_F4_Fch12_Chip21	
Entries	775170
Mean	2.931
Std Dev	3.374

nHits (per chip and per HitBco)

nChipHit_F4_Fch12_Chip22

Entries



nChipHit_F4_Fch12_Chip22

Entries 786525

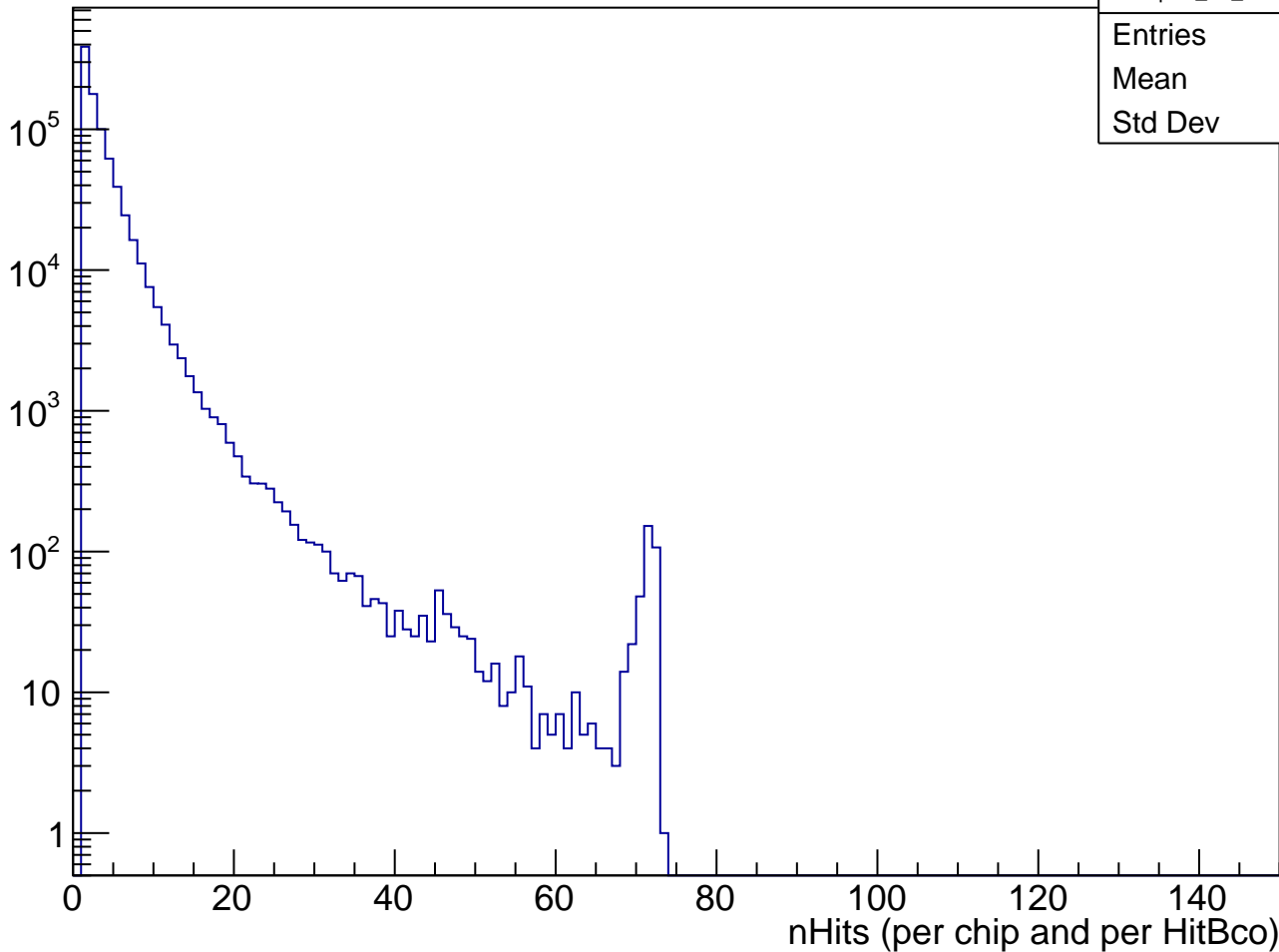
Mean 2.94

Std Dev 3.429

nHits (per chip and per HitBco)

nChipHit_F4_Fch12_Chip23

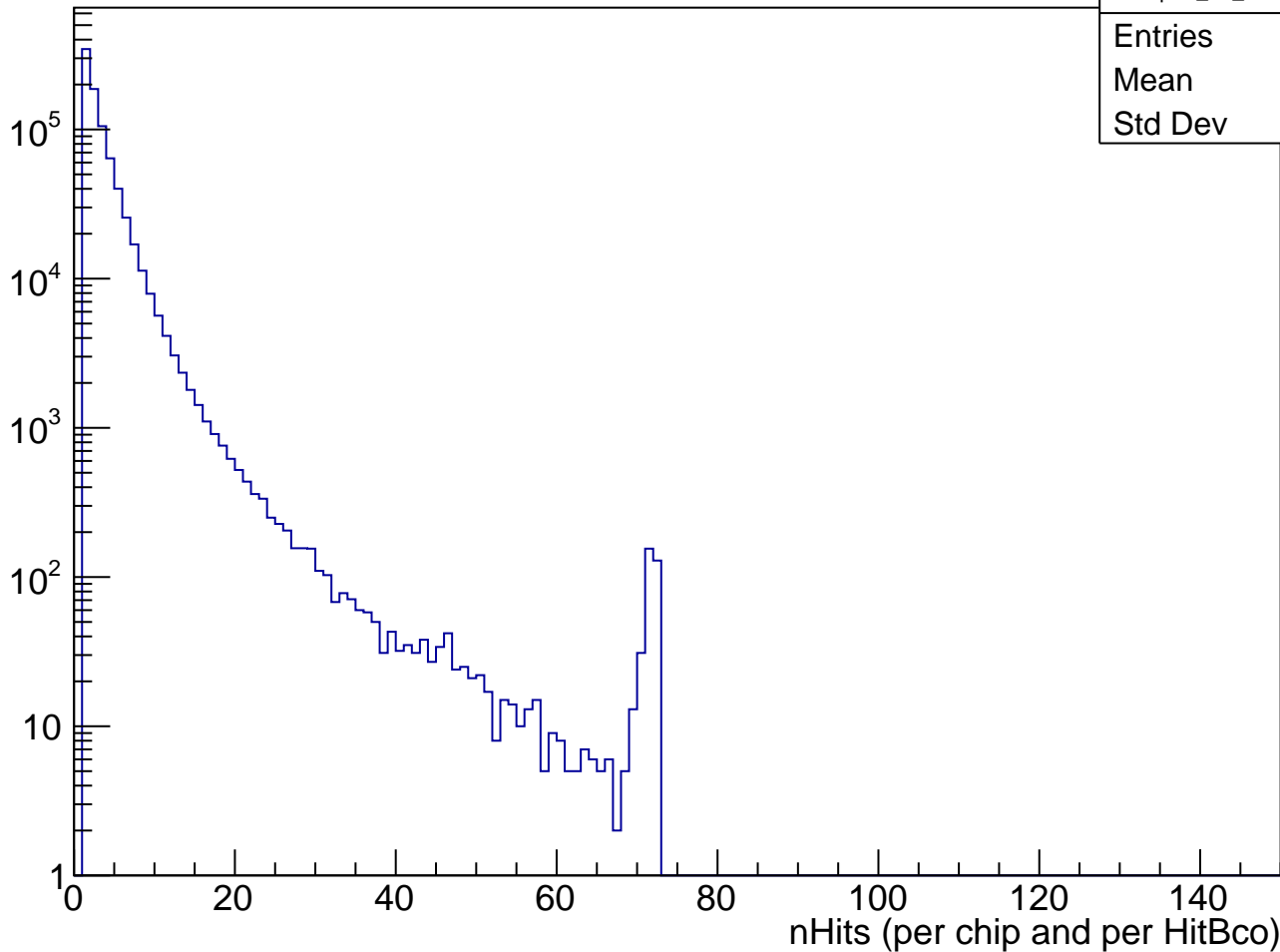
Entries



nChipHit_F4_Fch12_Chip23	
Entries	850095
Mean	2.704
Std Dev	3.29

nChipHit_F4_Fch12_Chip24

Entries



nChipHit_F4_Fch12_Chip24

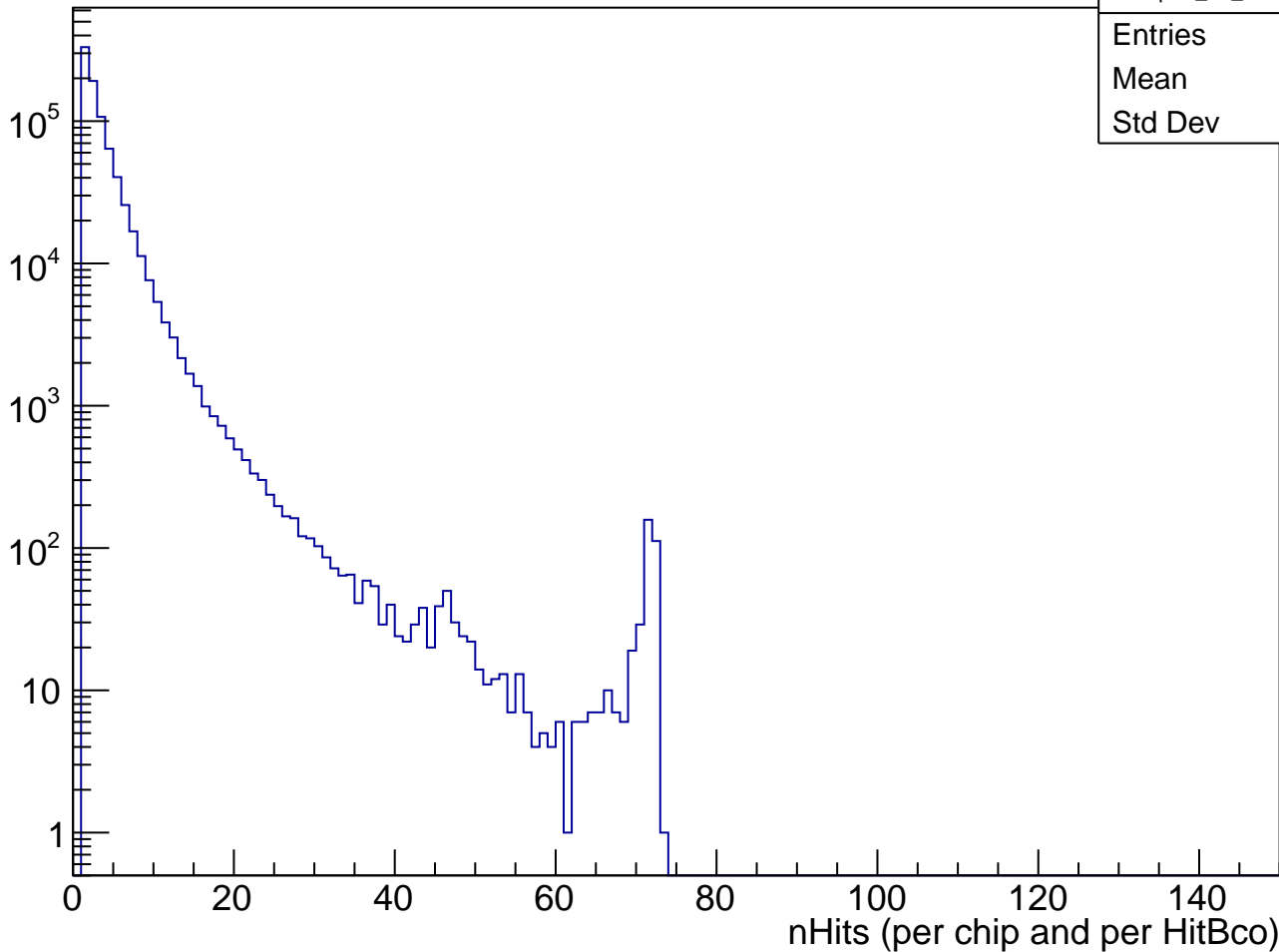
Entries 829631

Mean 2.814

Std Dev 3.357

nChipHit_F4_Fch12_Chip25

Entries



nChipHit_F4_Fch12_Chip25

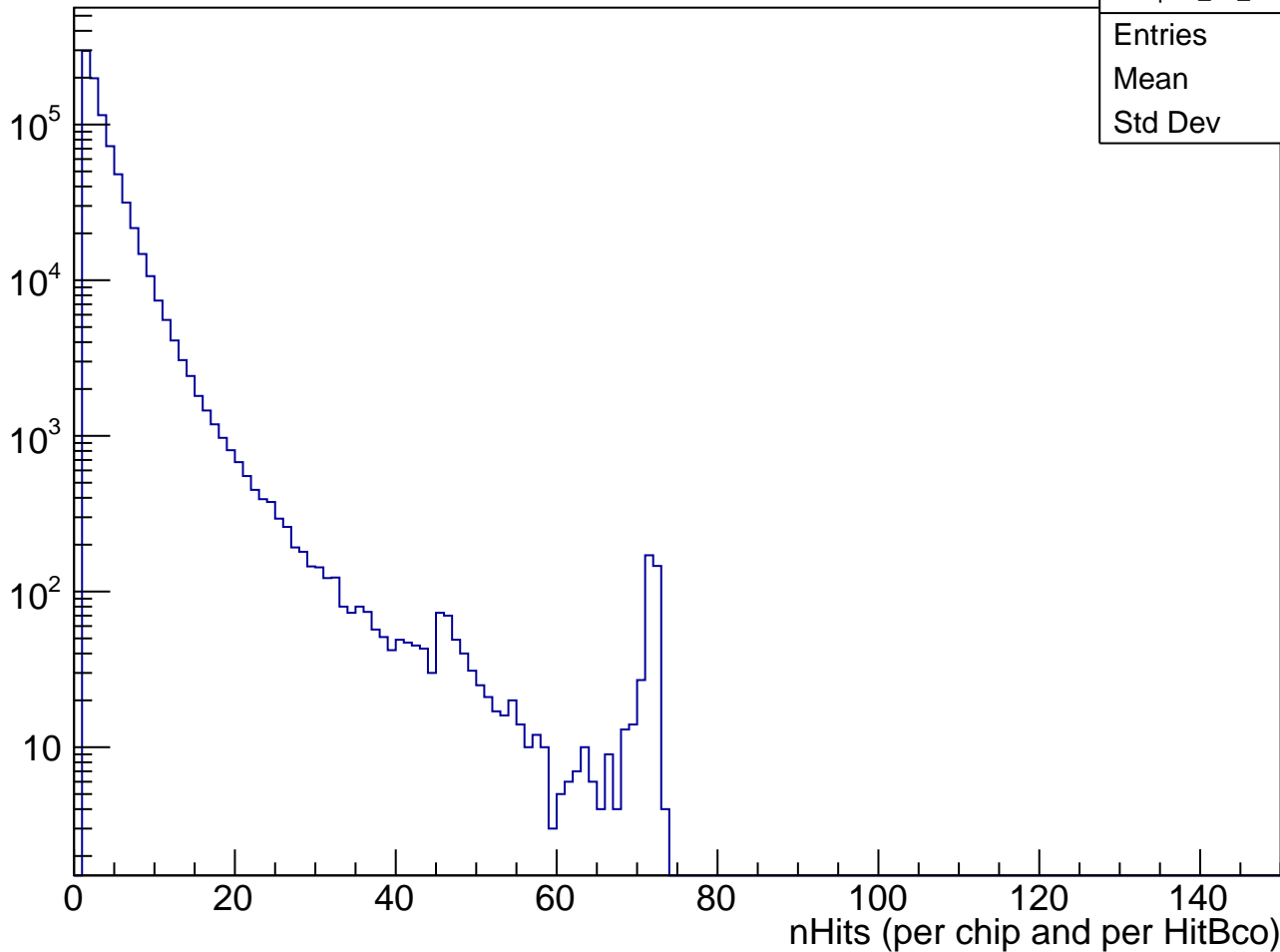
Entries 820413

Mean 2.813

Std Dev 3.287

nChipHit_F4_Fch12_Chip3

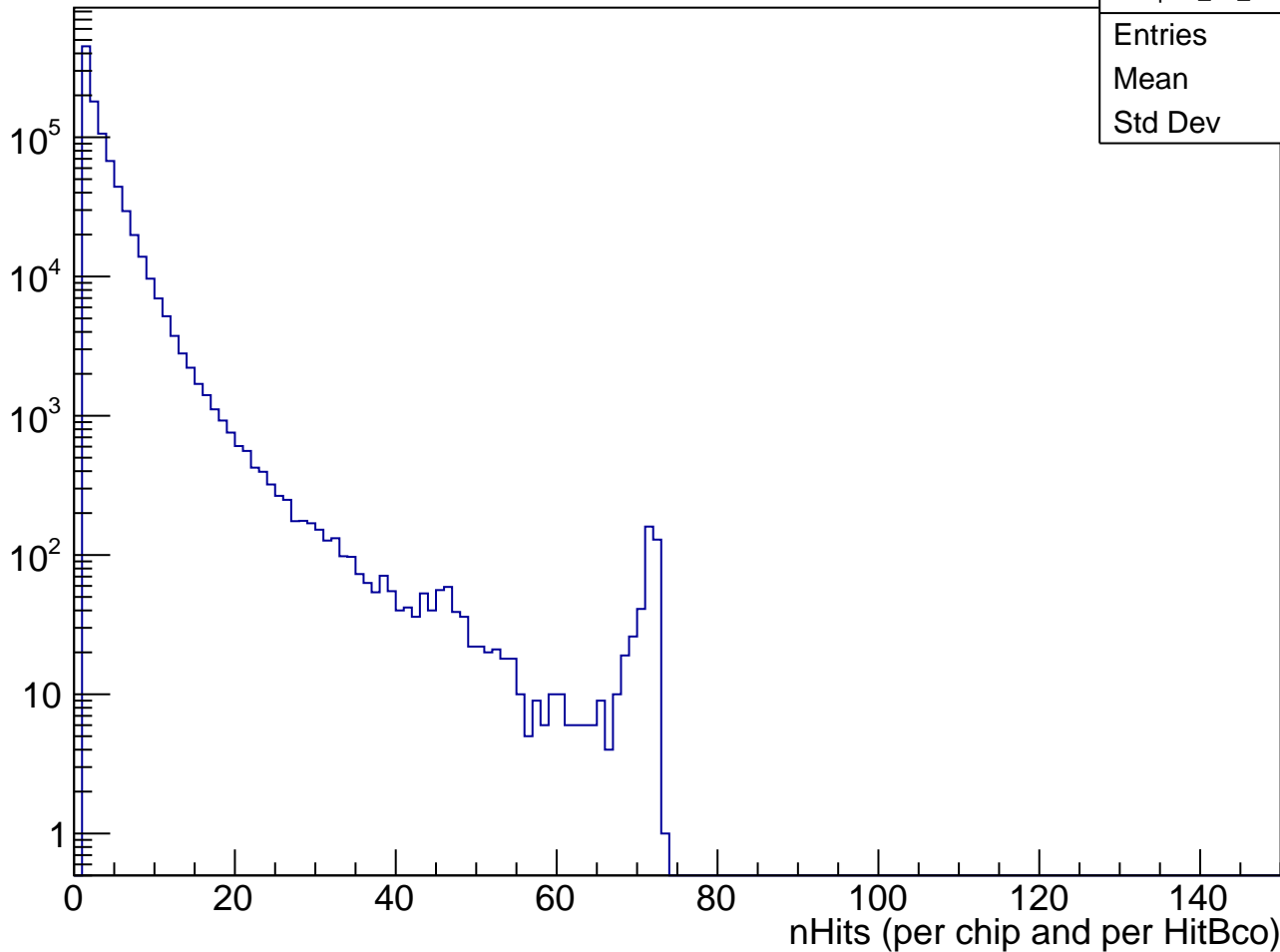
Entries



nChipHit_F4_Fch12_Chip3	
Entries	843877
Mean	3.144
Std Dev	3.645

nChipHit_F4_Fch12_Chip4

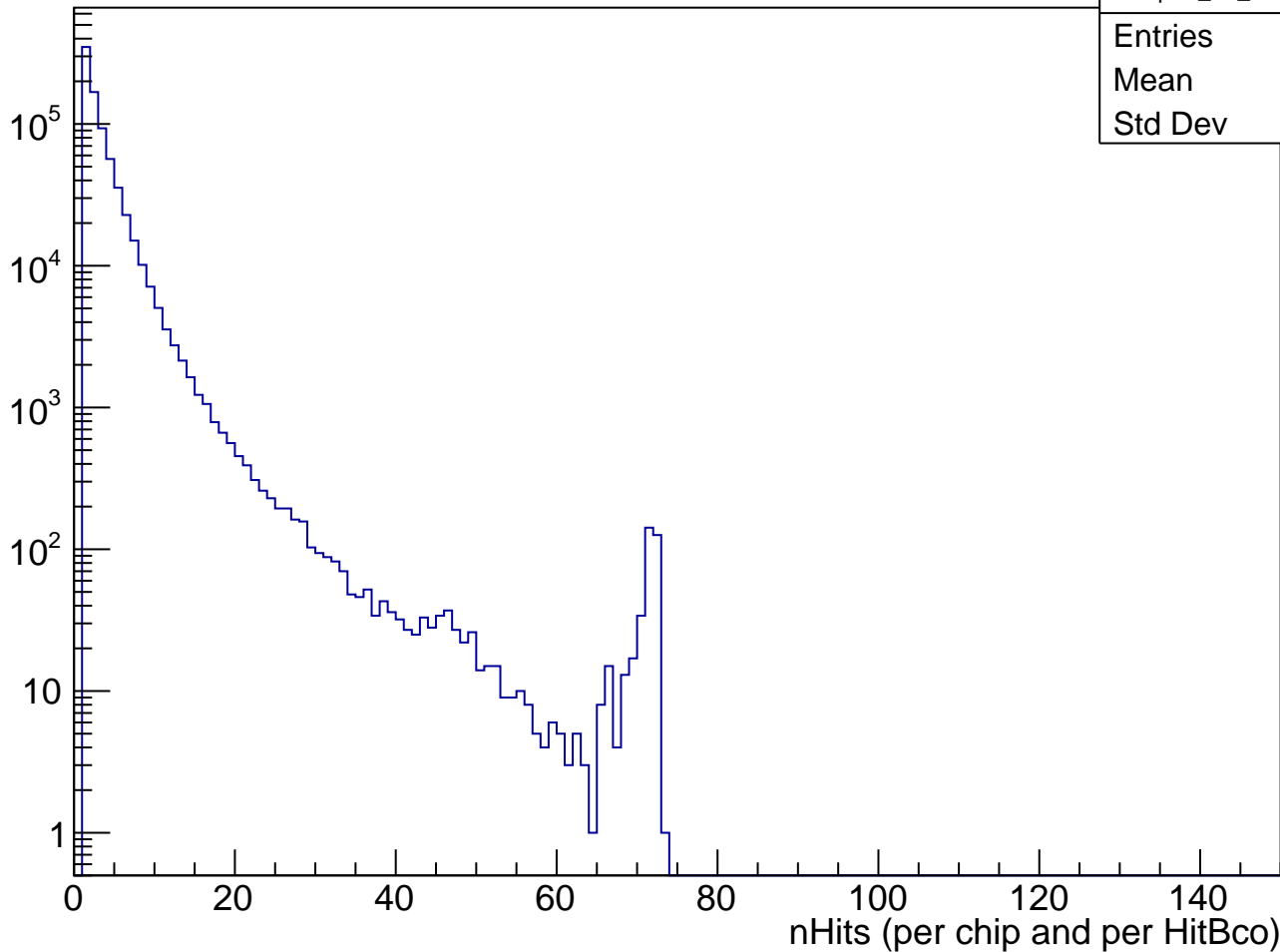
Entries



nChipHit_F4_Fch12_Chip4	
Entries	953285
Mean	2.765
Std Dev	3.452

nChipHit_F4_Fch12_Chip5

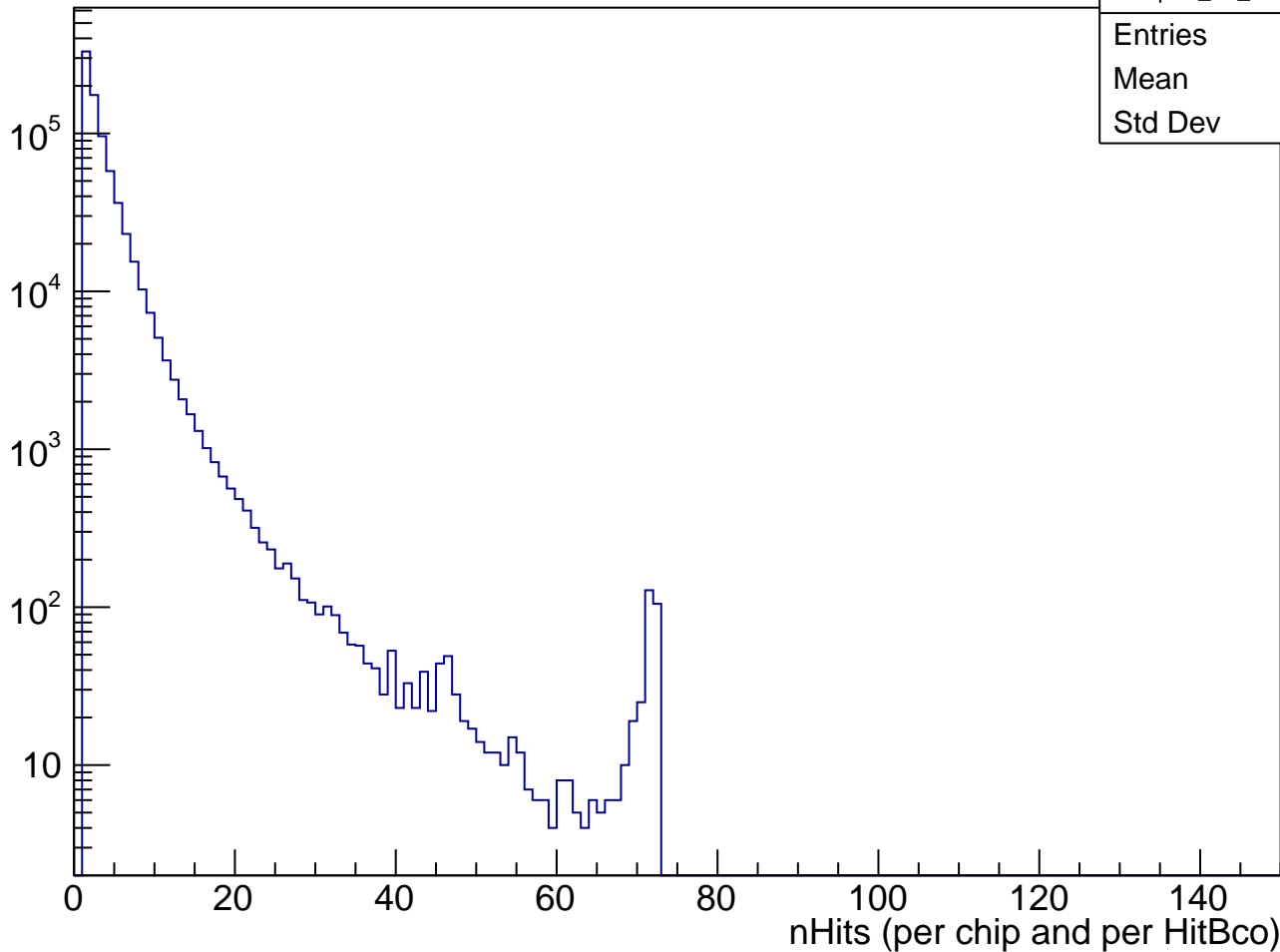
Entries



nChipHit_F4_Fch12_Chip5	
Entries	780941
Mean	2.718
Std Dev	3.32

nChipHit_F4_Fch12_Chip6

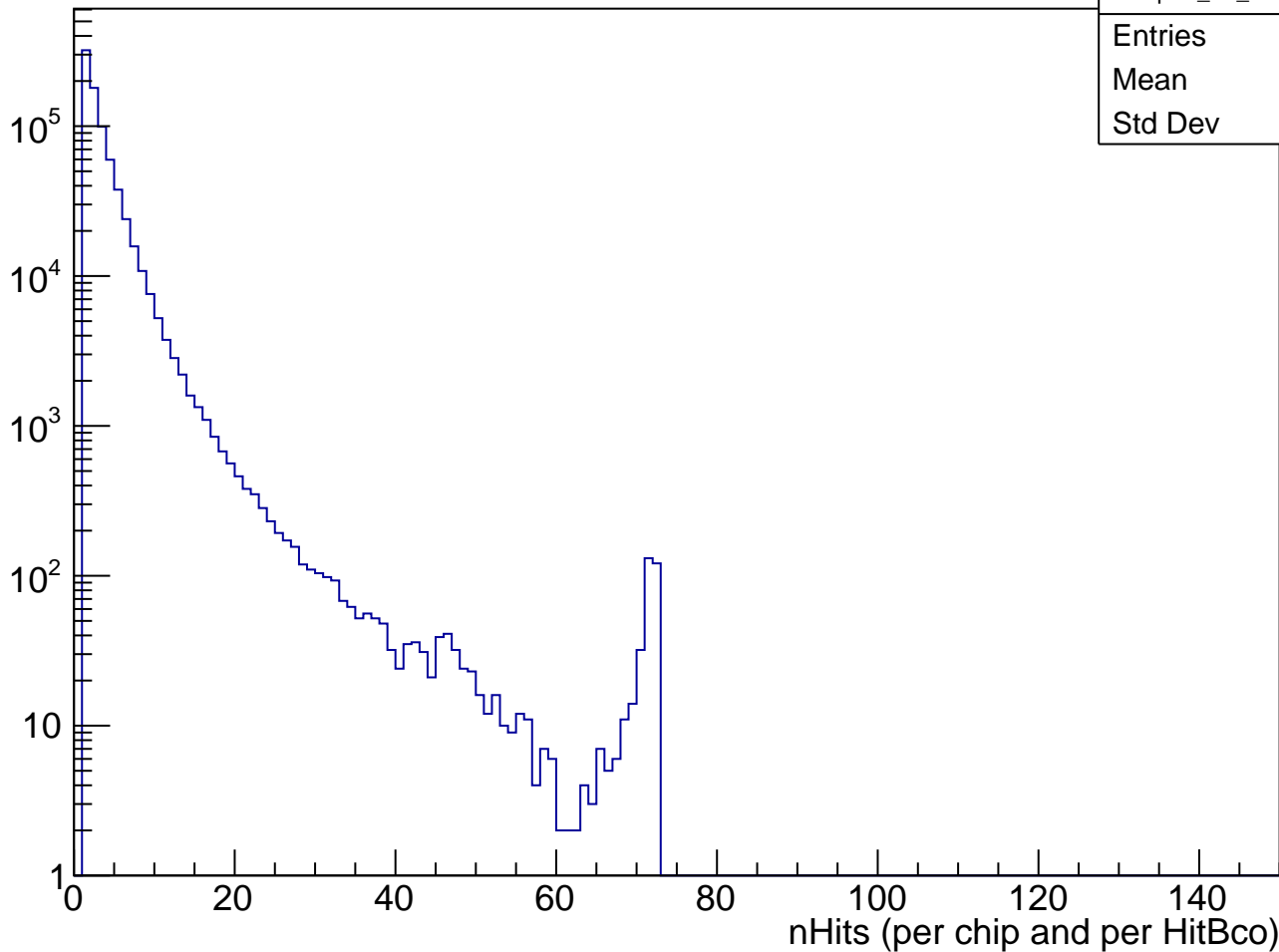
Entries



nChipHit_F4_Fch12_Chip6	
Entries	775036
Mean	2.764
Std Dev	3.298

nChipHit_F4_Fch12_Chip7

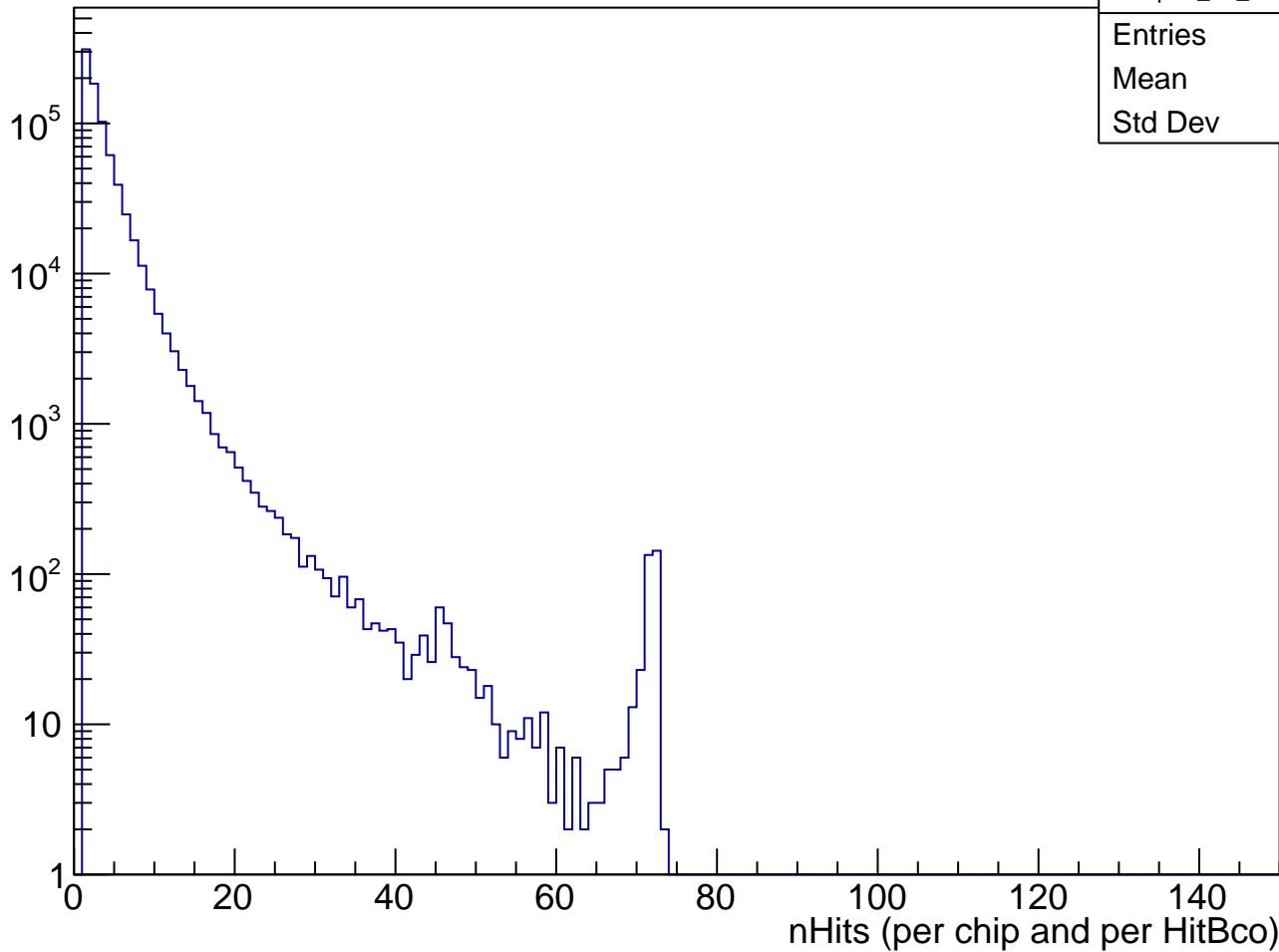
Entries



nChipHit_F4_Fch12_Chip7	
Entries	779604
Mean	2.809
Std Dev	3.326

nChipHit_F4_Fch12_Chip8

Entries

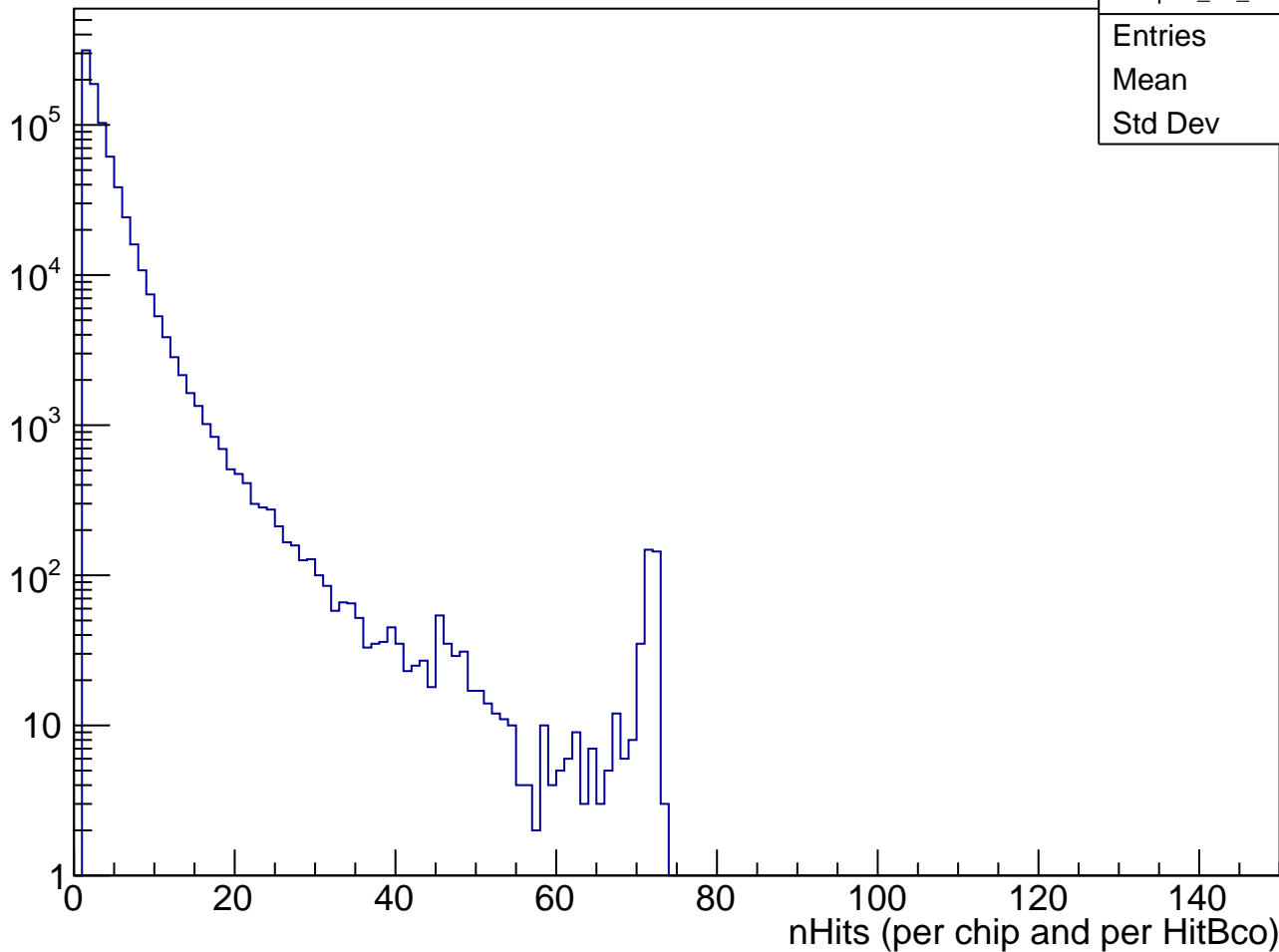


nChipHit_F4_Fch12_Chip8	
Entries	783962
Mean	2.872
Std Dev	3.384

nHits (per chip and per HitBco)

nChipHit_F4_Fch12_Chip9

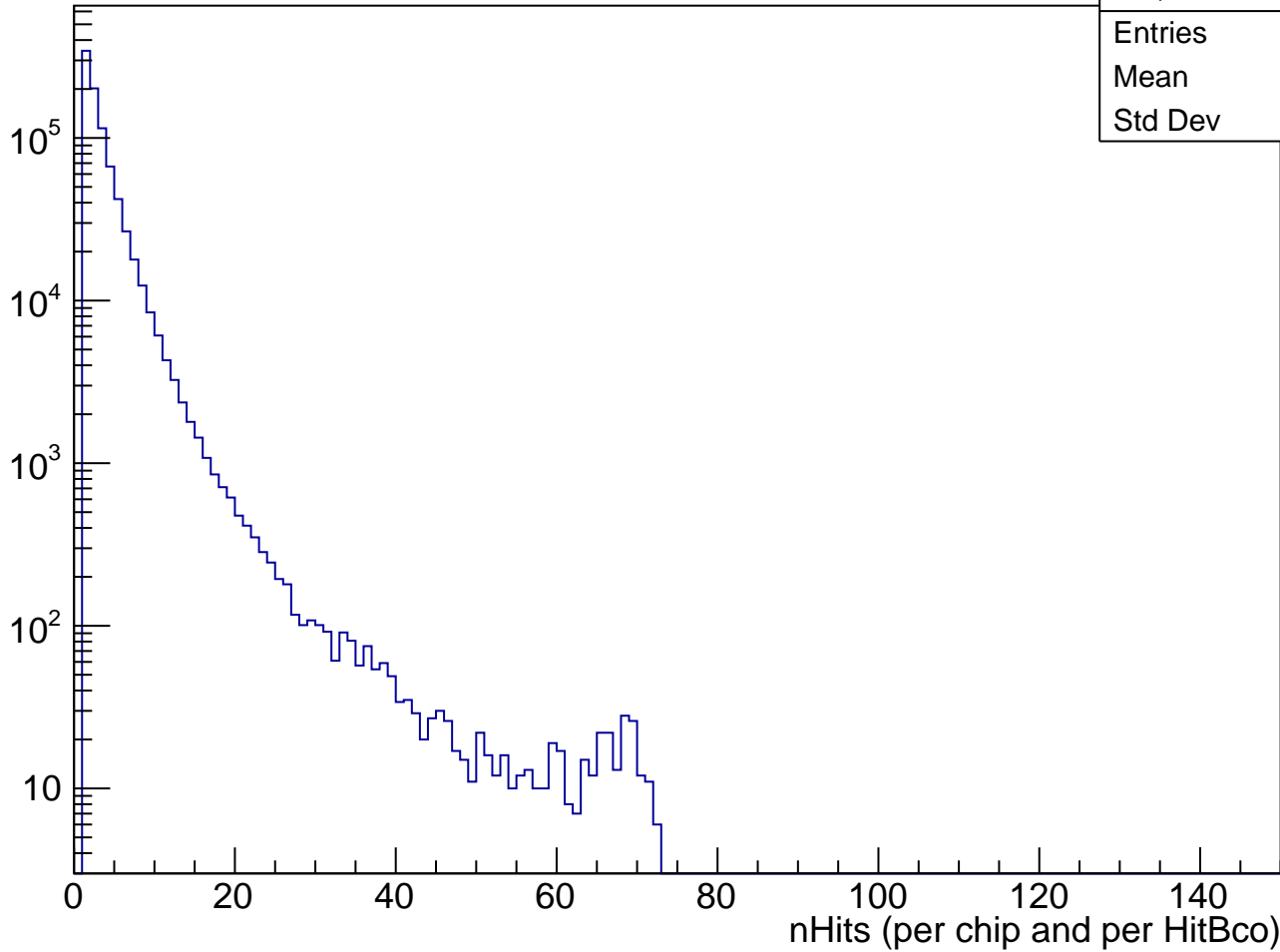
Entries



nChipHit_F4_Fch12_Chip9	
Entries	787014
Mean	2.825
Std Dev	3.33

nChipHit_F4_Fch13_Chip0

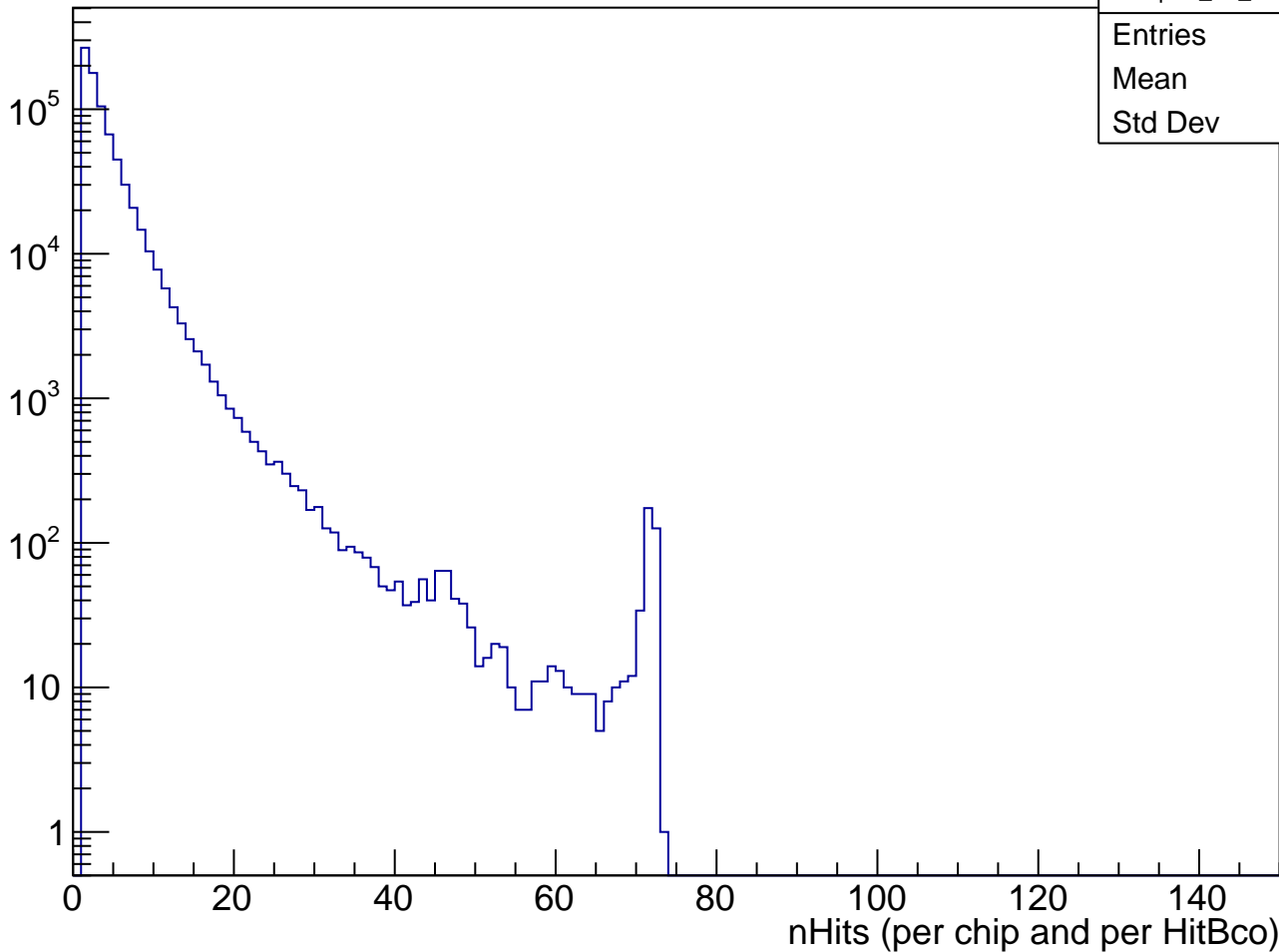
Entries



nChipHit_F4_Fch13_Chip0	
Entries	860693
Mean	2.815
Std Dev	3.117

nChipHit_F4_Fch13_Chip1

Entries

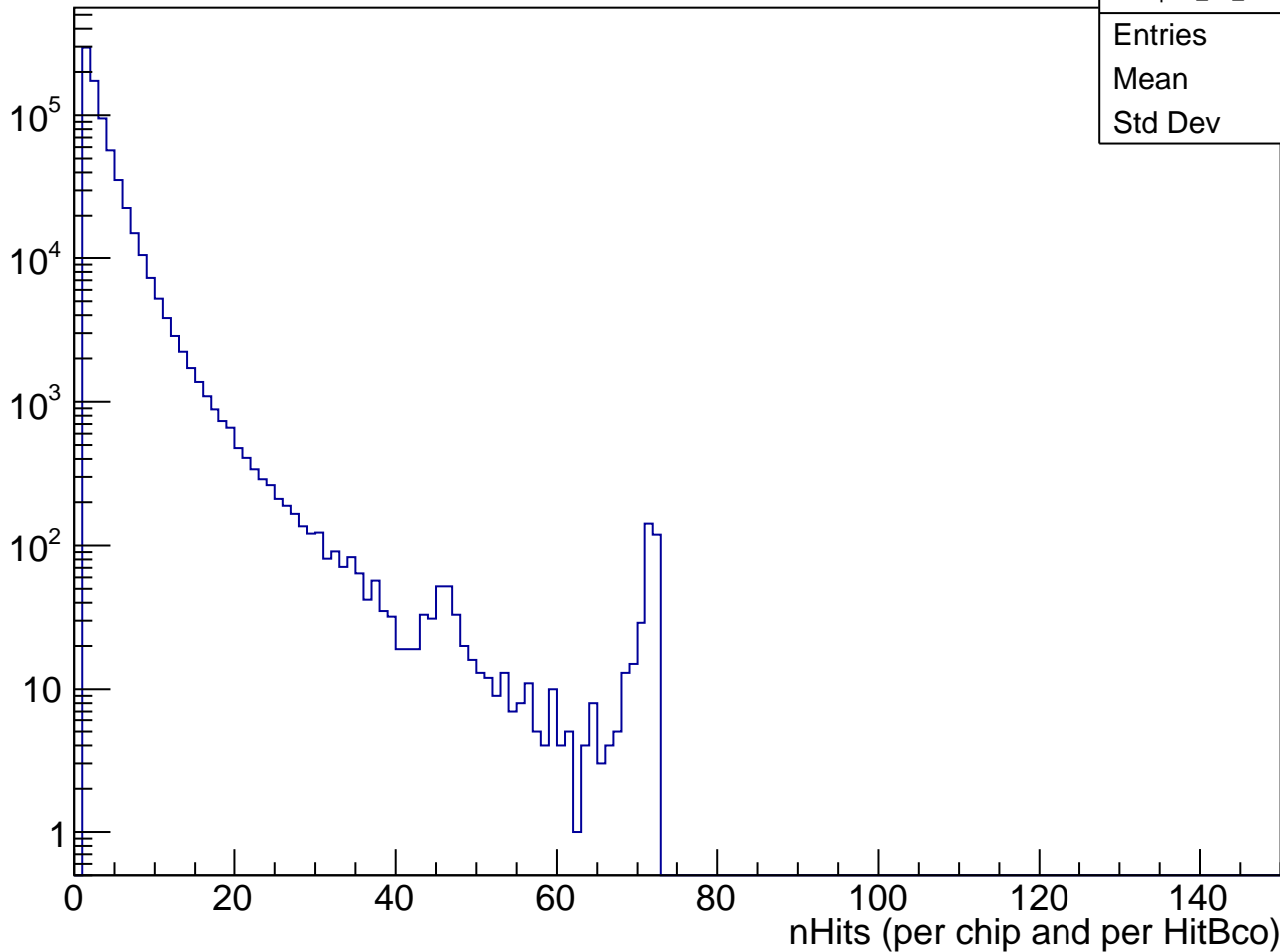


nChipHit_F4_Fch13_Chip1	
Entries	773532
Mean	3.275
Std Dev	3.835

nHits (per chip and per HitBco)

nChipHit_F4_Fch13_Chip10

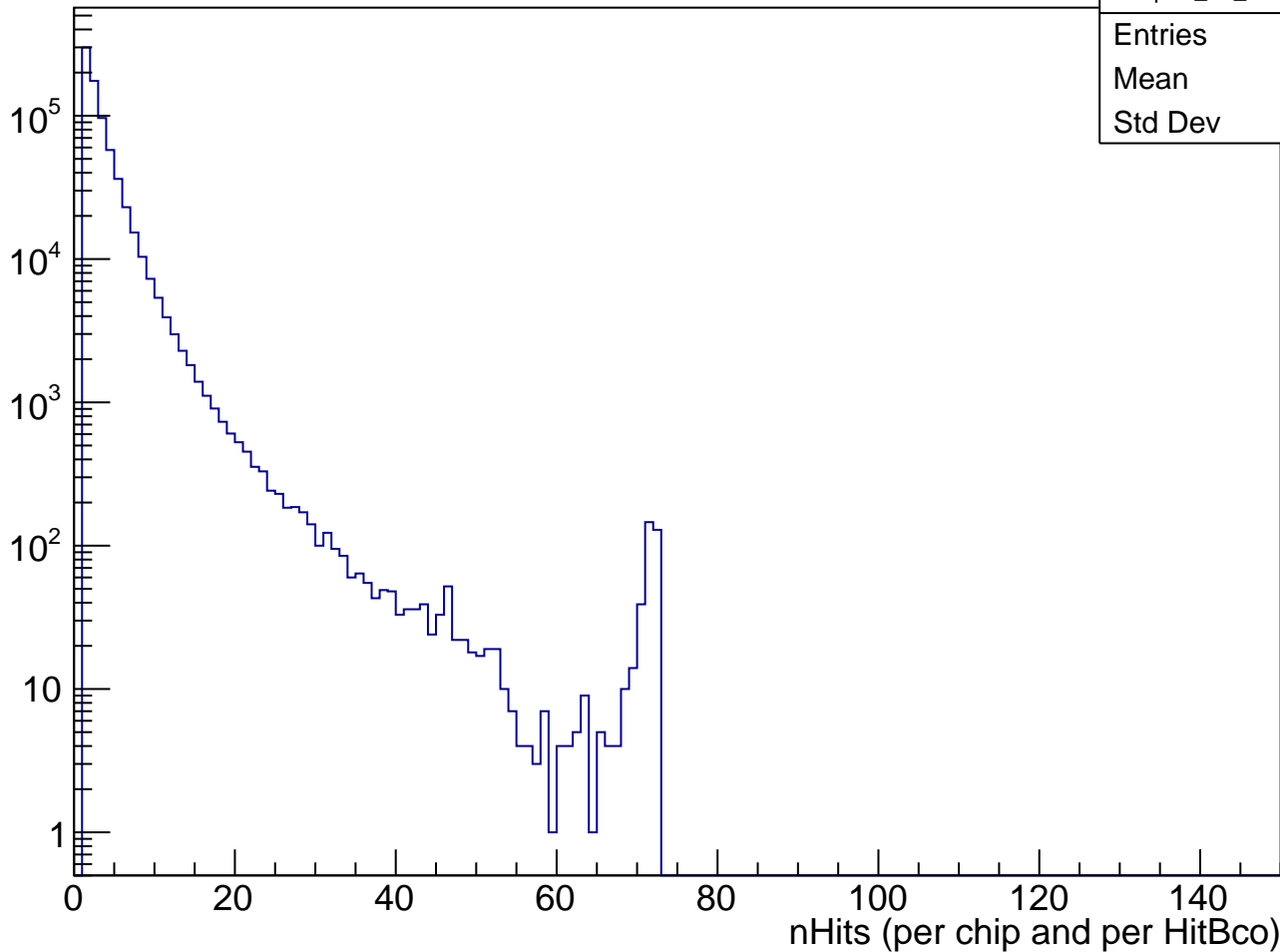
Entries



nChipHit_F4_Fch13_Chip10	
Entries	736674
Mean	2.864
Std Dev	3.424

nChipHit_F4_Fch13_Chip11

Entries

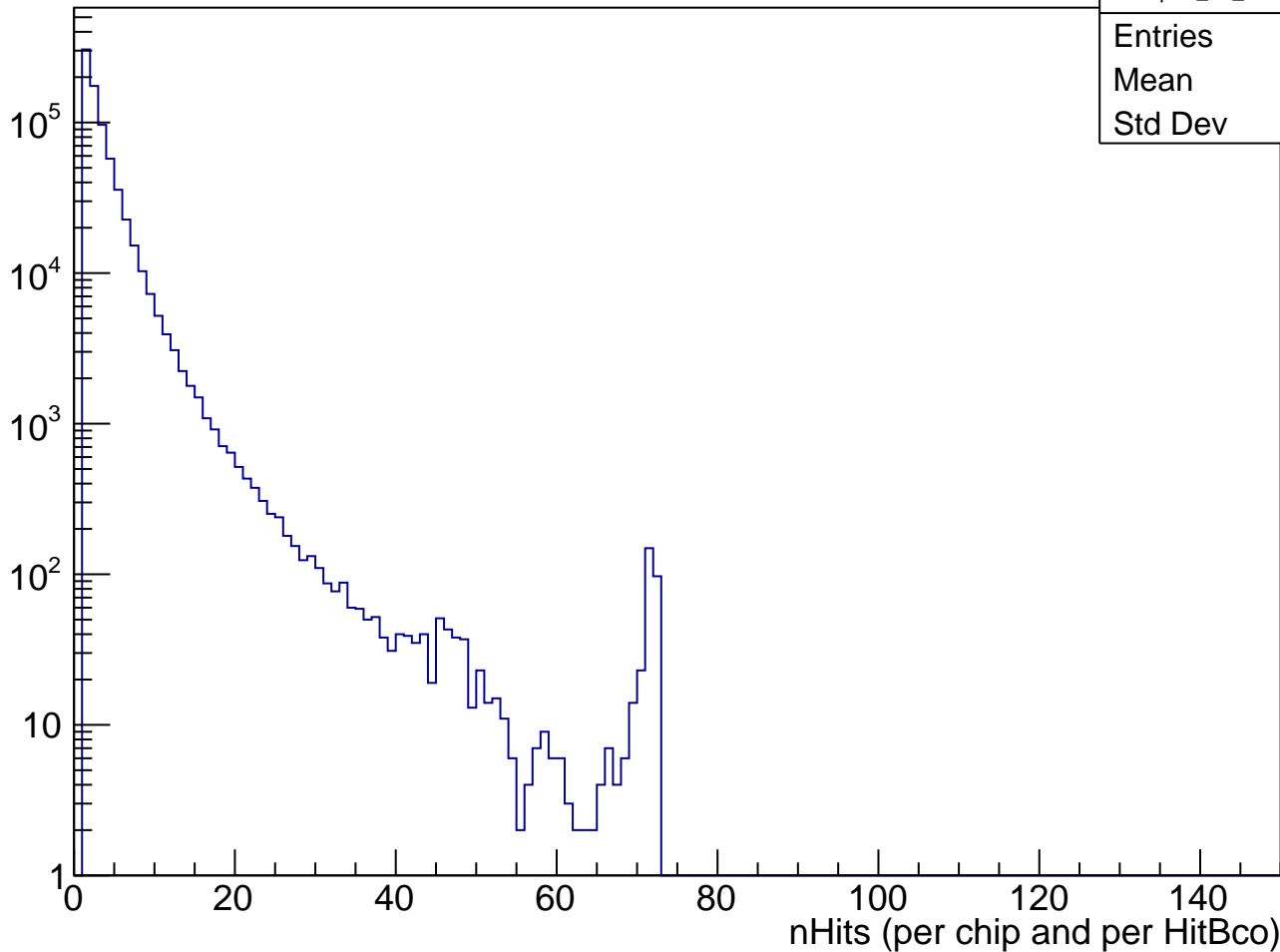


nChipHit_F4_Fch13_Chip11	
Entries	746674
Mean	2.875
Std Dev	3.463

nHits (per chip and per HitBco)

nChipHit_F4_Fch13_Chip12

Entries



nChipHit_F4_Fch13_Chip12

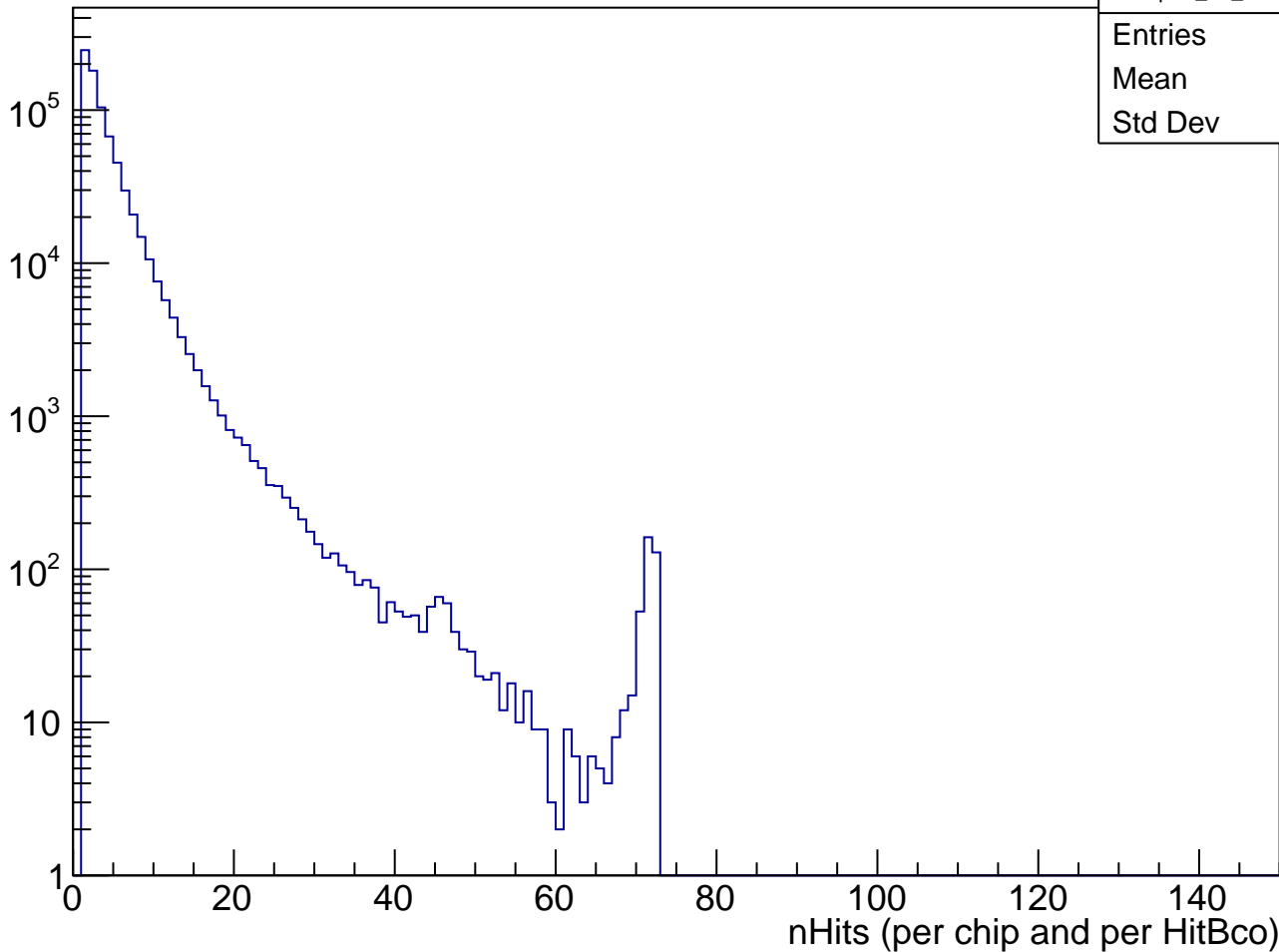
Entries 750811

Mean 2.848

Std Dev 3.4

nChipHit_F4_Fch13_Chip13

Entries



nChipHit_F4_Fch13_Chip13

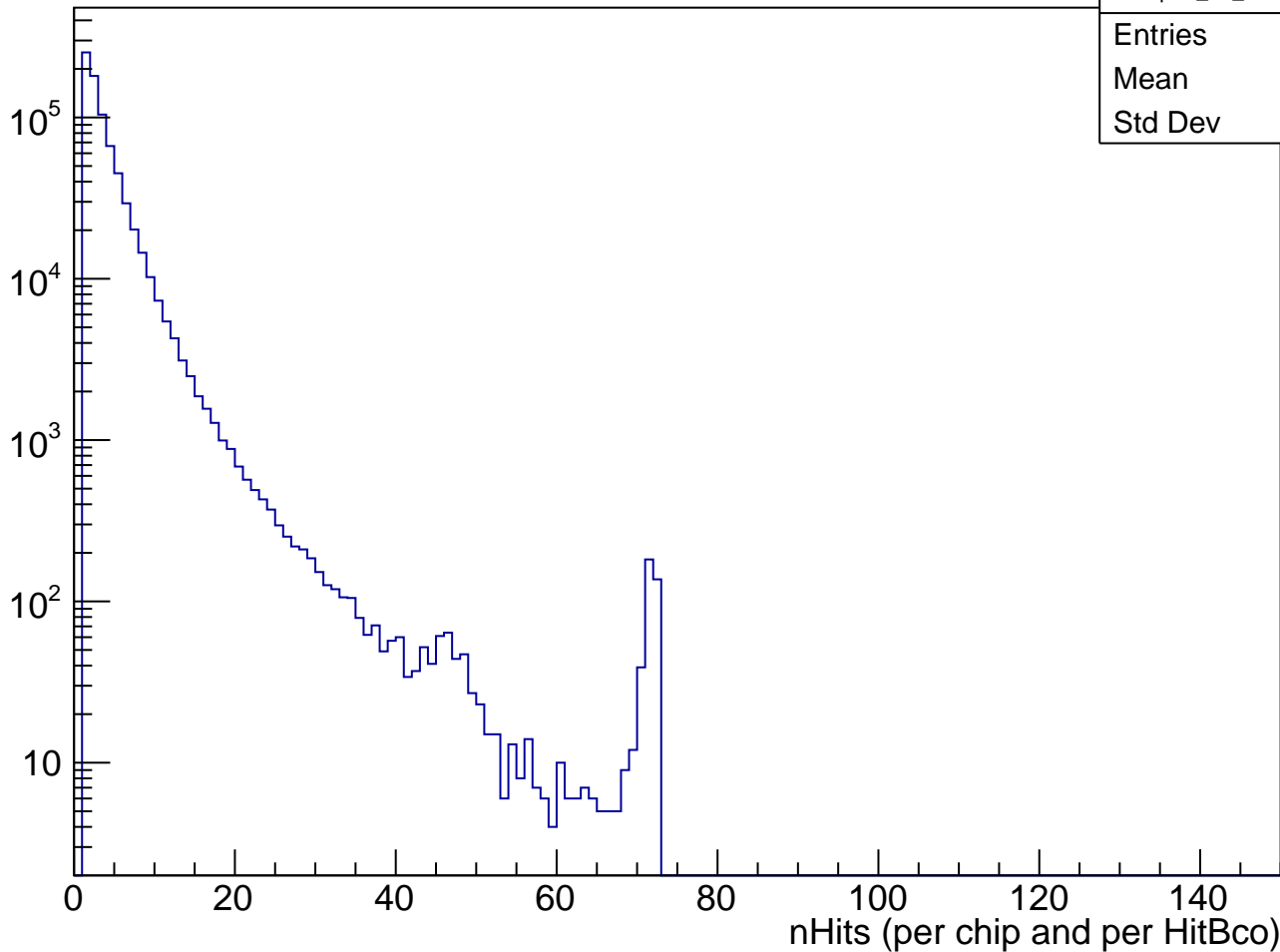
Entries 756282

Mean 3.328

Std Dev 3.855

nChipHit_F4_Fch13_Chip14

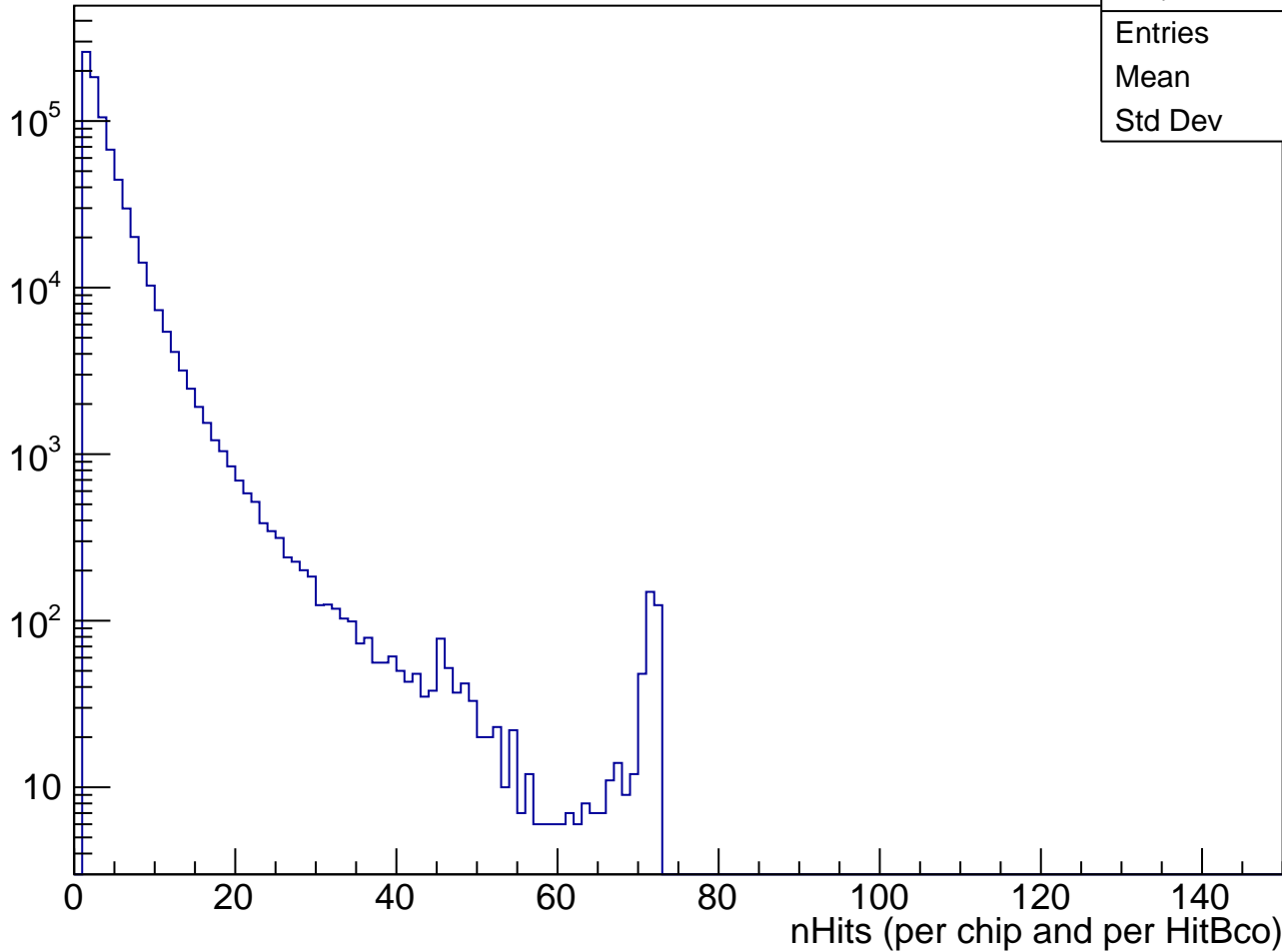
Entries



nChipHit_F4_Fch13_Chip14	
Entries	758896
Mean	3.278
Std Dev	3.815

nChipHit_F4_Fch13_Chip15

Entries

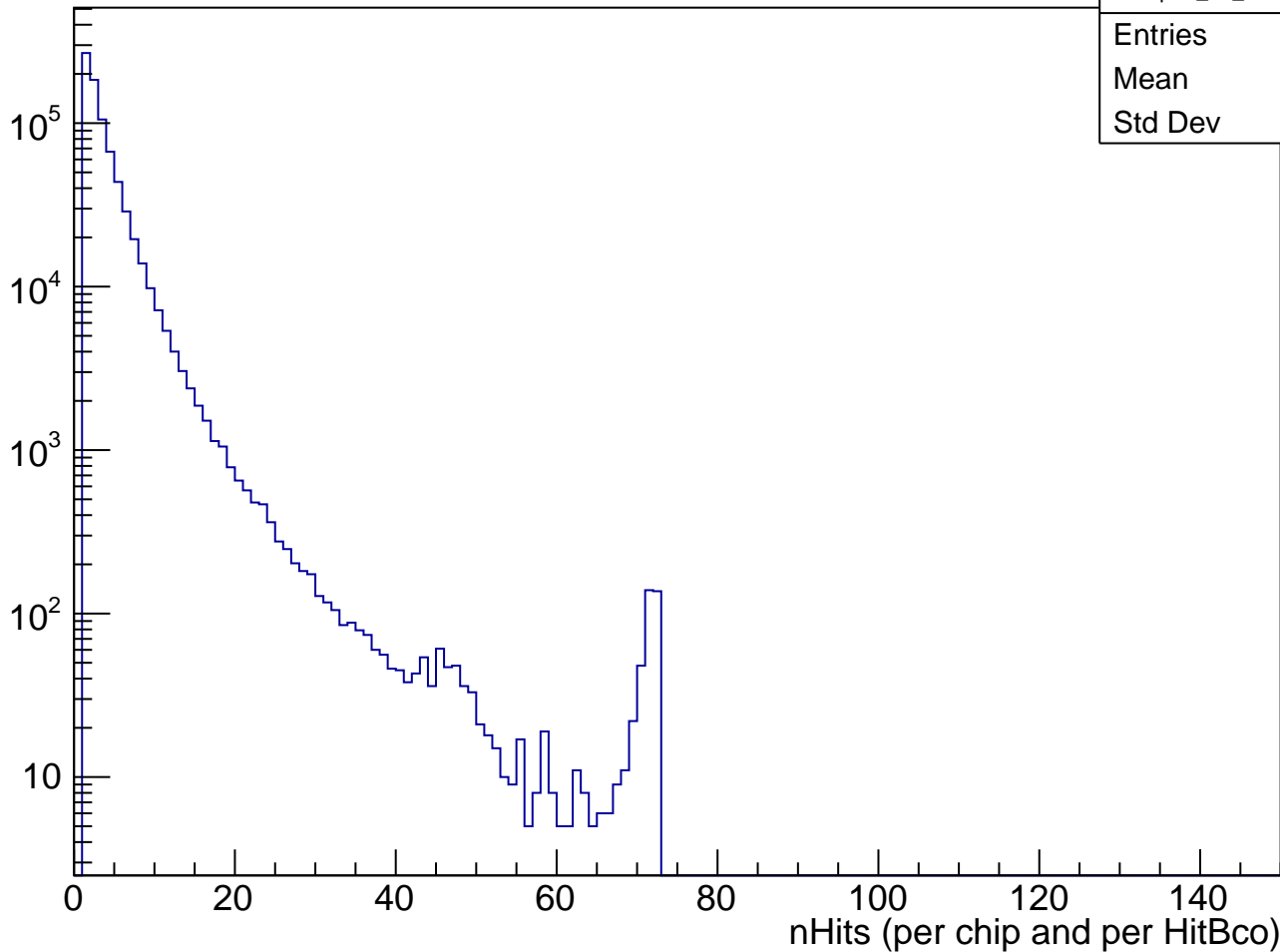


nChipHit_F4_Fch13_Chip15	
Entries	769564
Mean	3.247
Std Dev	3.773

nHits (per chip and per HitBco)

nChipHit_F4_Fch13_Chip16

Entries



nChipHit_F4_Fch13_Chip16

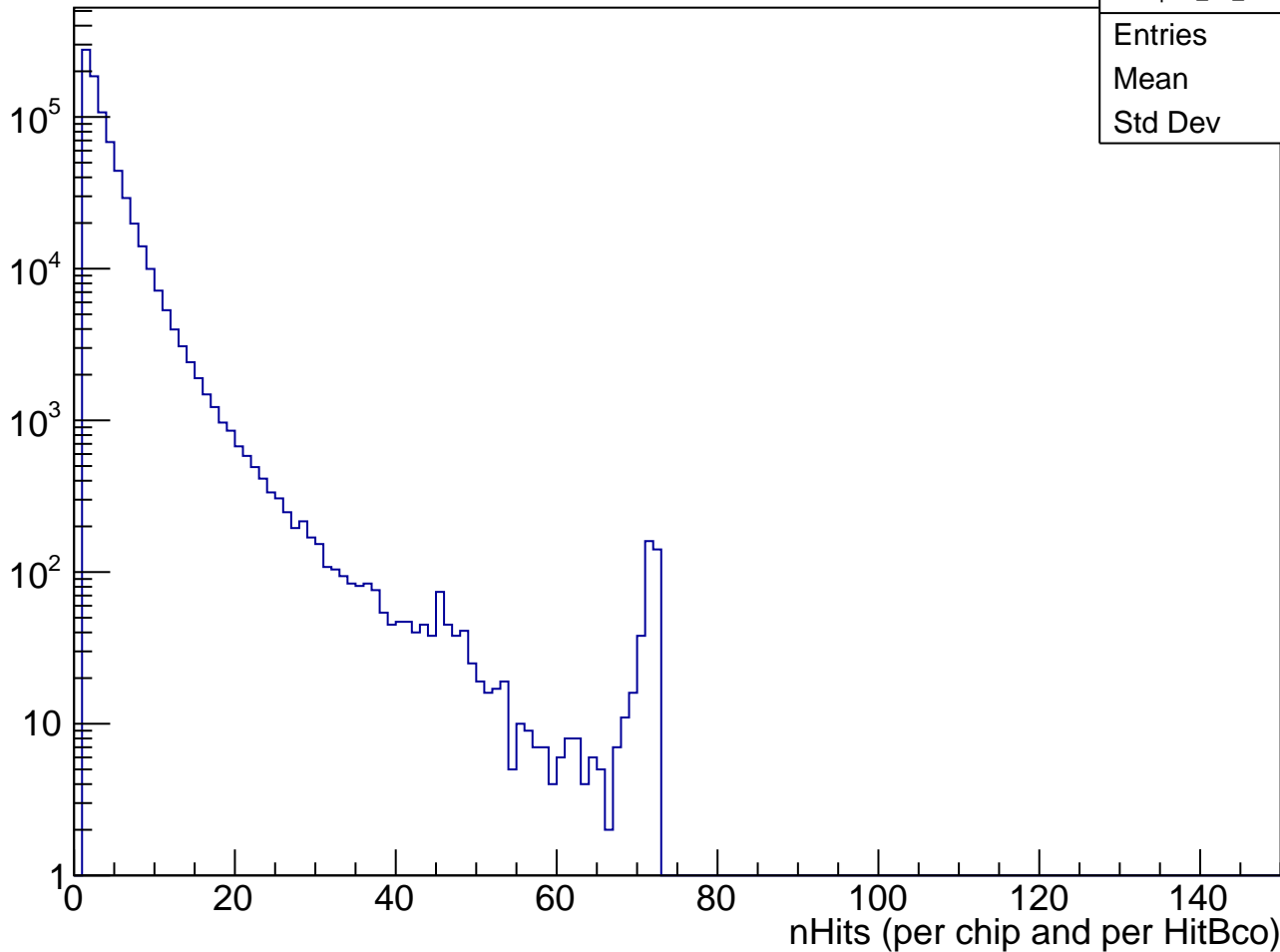
Entries 774129

Mean 3.192

Std Dev 3.733

nChipHit_F4_Fch13_Chip17

Entries



nChipHit_F4_Fch13_Chip17

Entries 789688

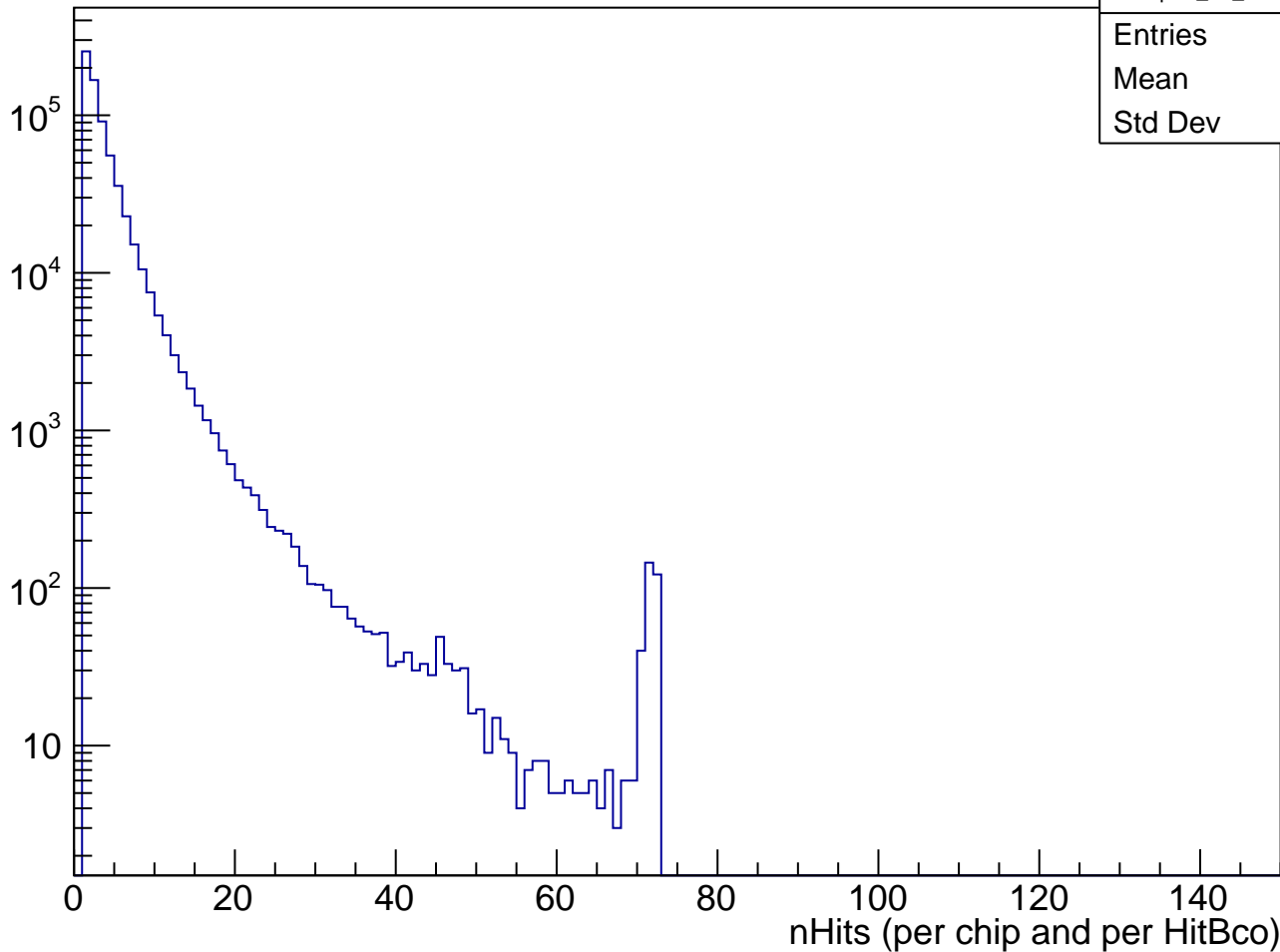
Mean 3.175

Std Dev 3.711

nHits (per chip and per HitBco)

nChipHit_F4_Fch13_Chip18

Entries



nChipHit_F4_Fch13_Chip18

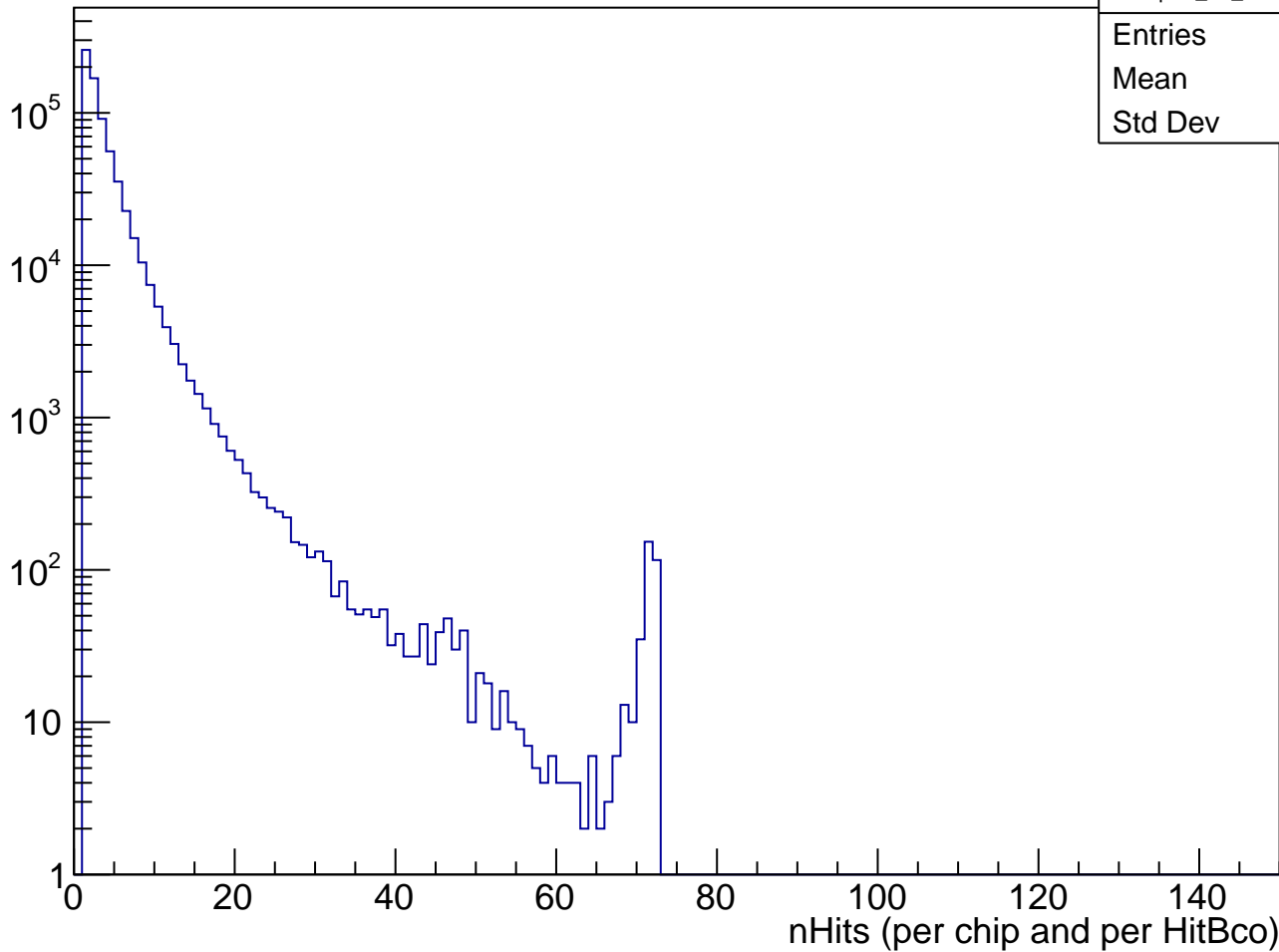
Entries 685917

Mean 3.004

Std Dev 3.56

nChipHit_F4_Fch13_Chip19

Entries



nChipHit_F4_Fch13_Chip19

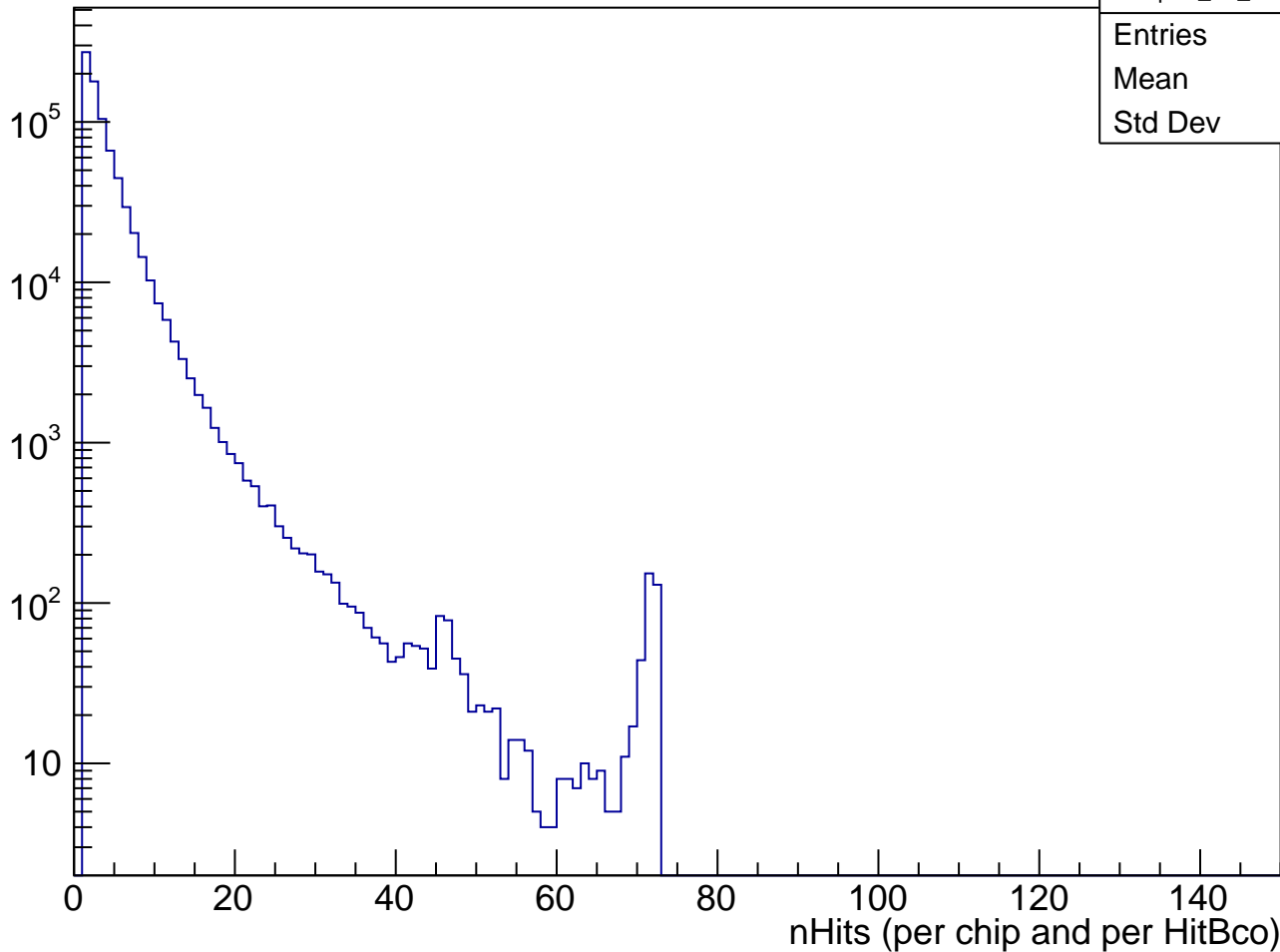
Entries 691536

Mean 2.982

Std Dev 3.549

nChipHit_F4_Fch13_Chip2

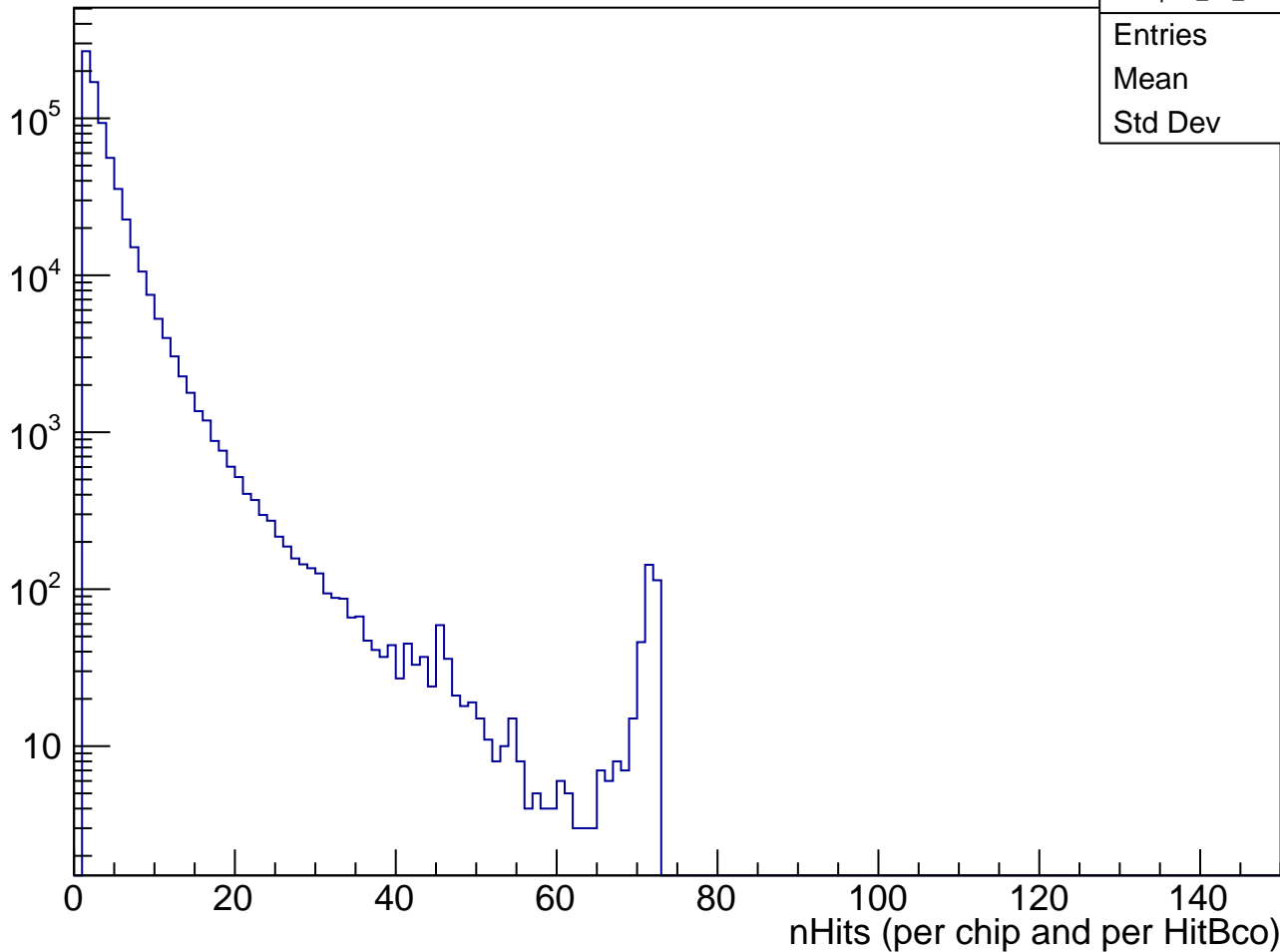
Entries



nChipHit_F4_Fch13_Chip2	
Entries	777077
Mean	3.239
Std Dev	3.814

nChipHit_F4_Fch13_Chip20

Entries



nChipHit_F4_Fch13_Chip20

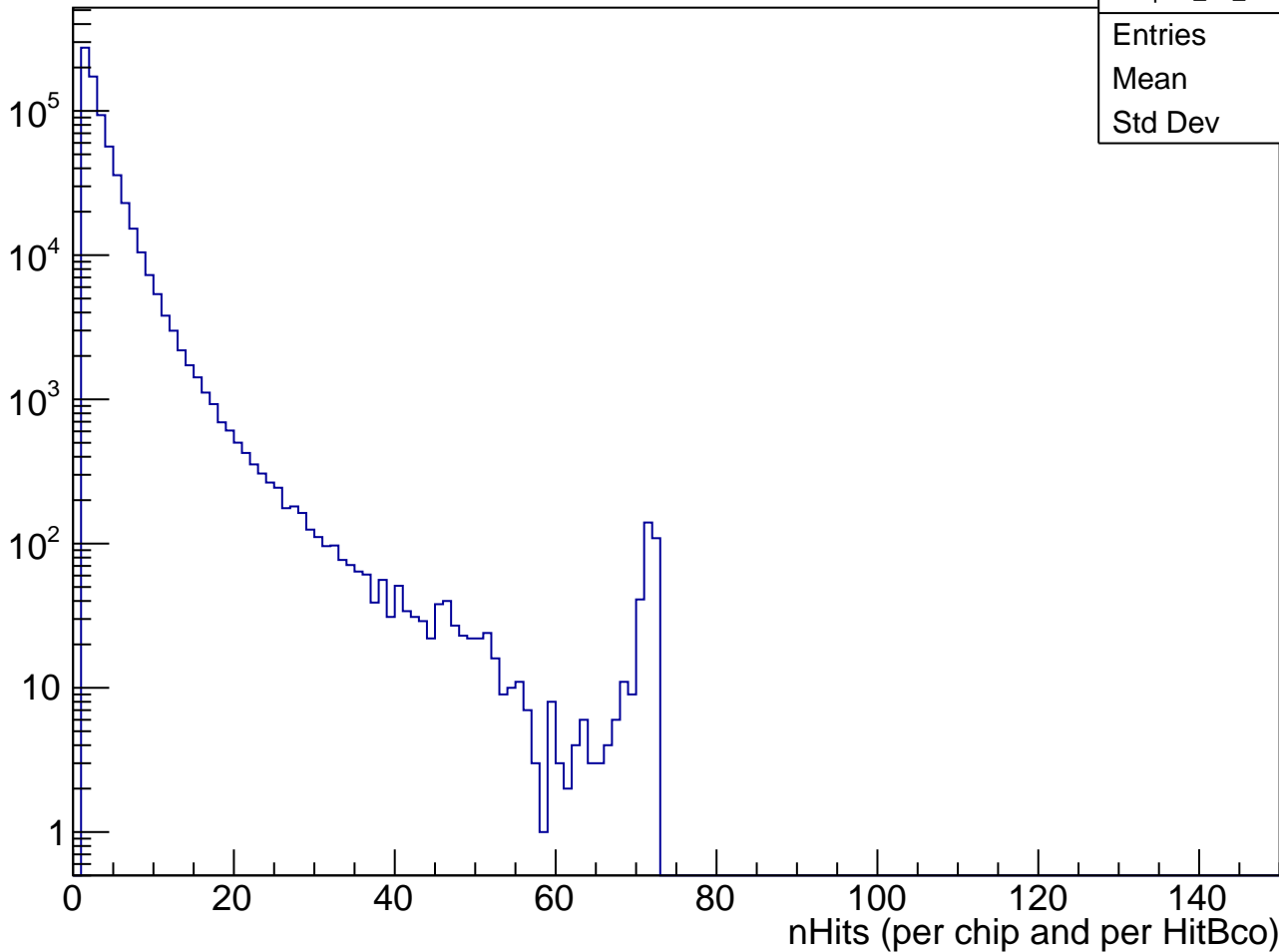
Entries 703788

Mean 2.956

Std Dev 3.517

nChipHit_F4_Fch13_Chip21

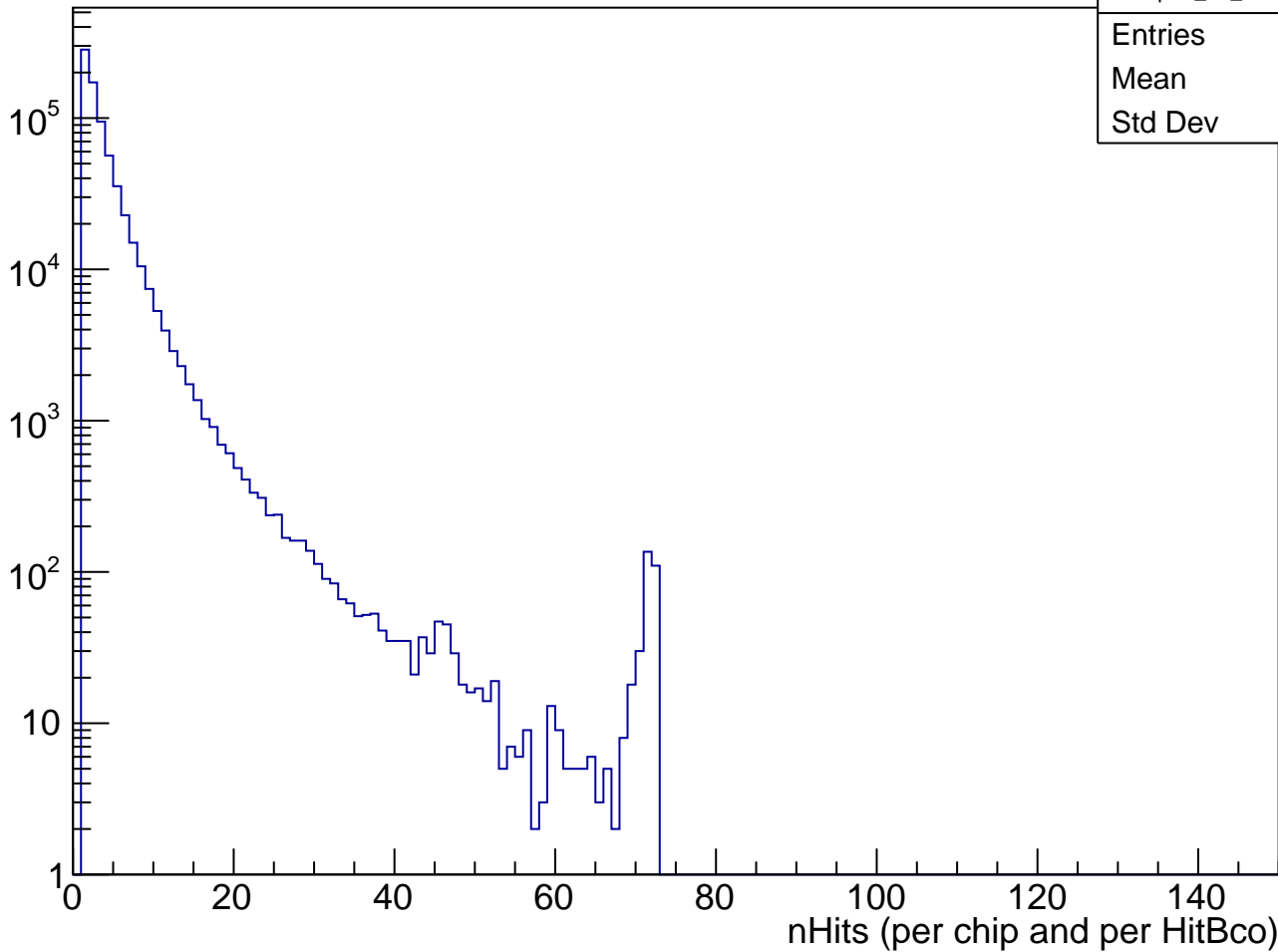
Entries



nChipHit_F4_Fch13_Chip21	
Entries	714567
Mean	2.932
Std Dev	3.485

nChipHit_F4_Fch13_Chip22

Entries



nChipHit_F4_Fch13_Chip22

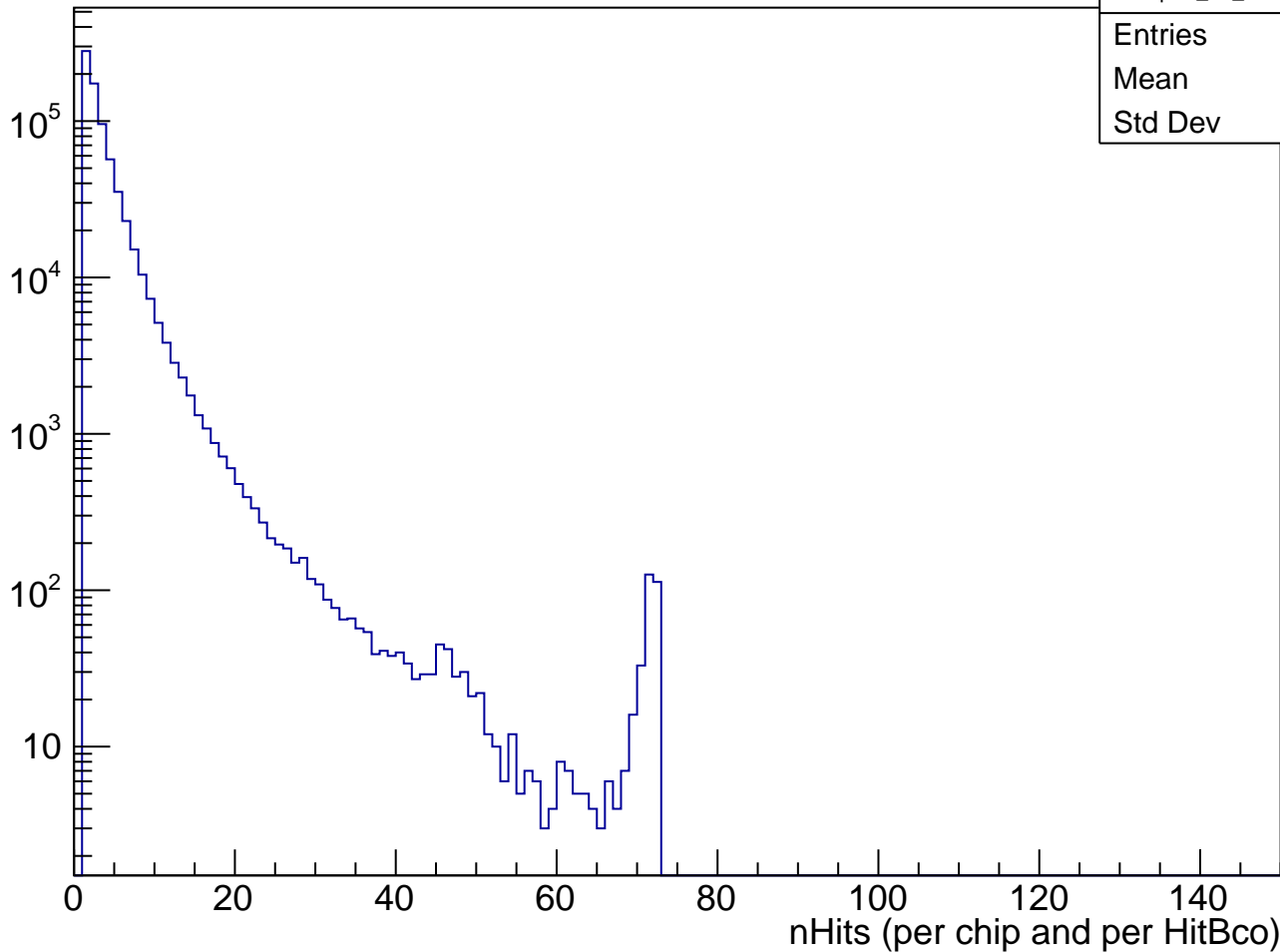
Entries 722749

Mean 2.899

Std Dev 3.44

nChipHit_F4_Fch13_Chip23

Entries



nChipHit_F4_Fch13_Chip23

Entries 722388

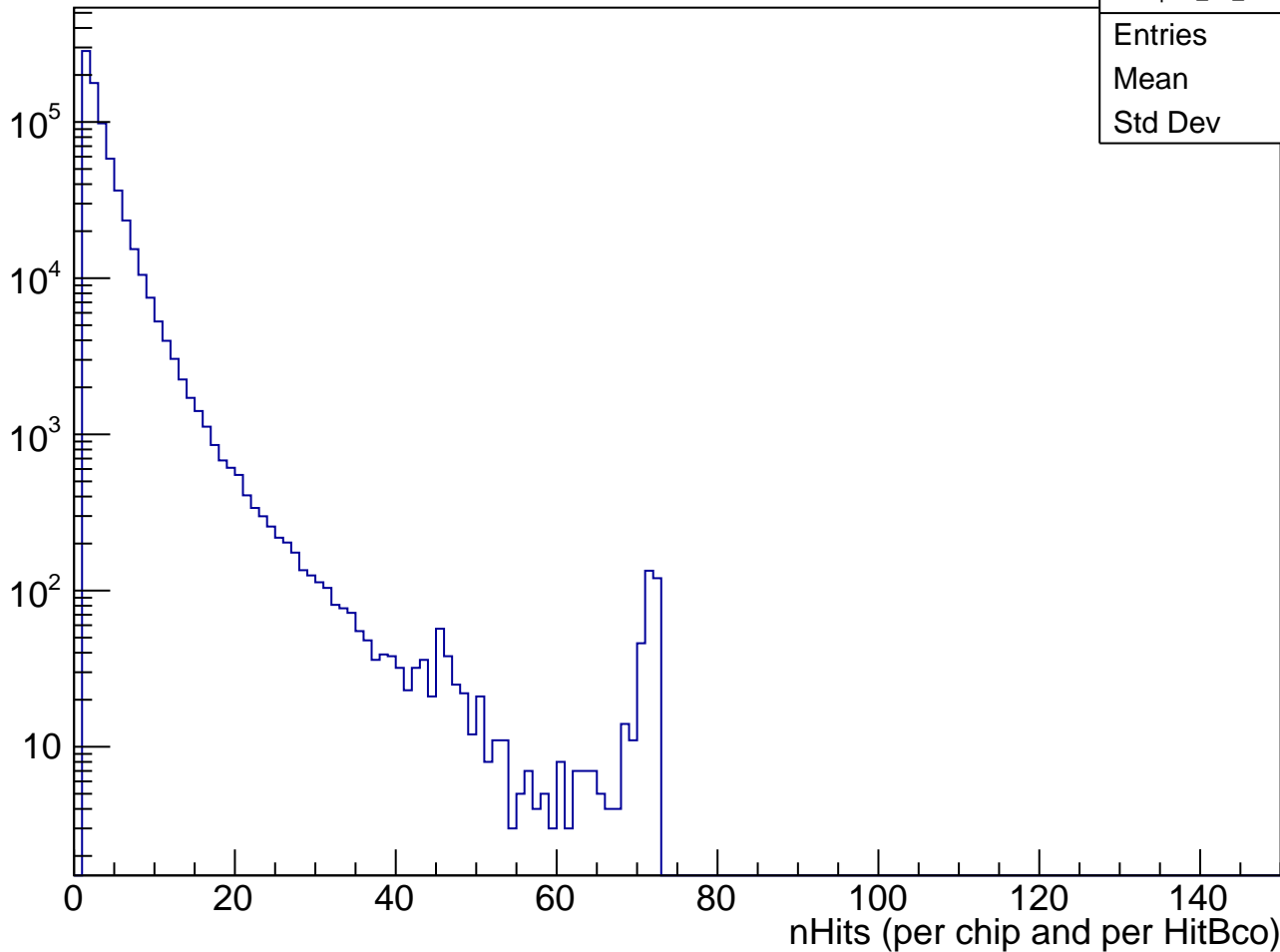
Mean 2.897

Std Dev 3.415

nHits (per chip and per HitBco)

nChipHit_F4_Fch13_Chip24

Entries



nChipHit_F4_Fch13_Chip24

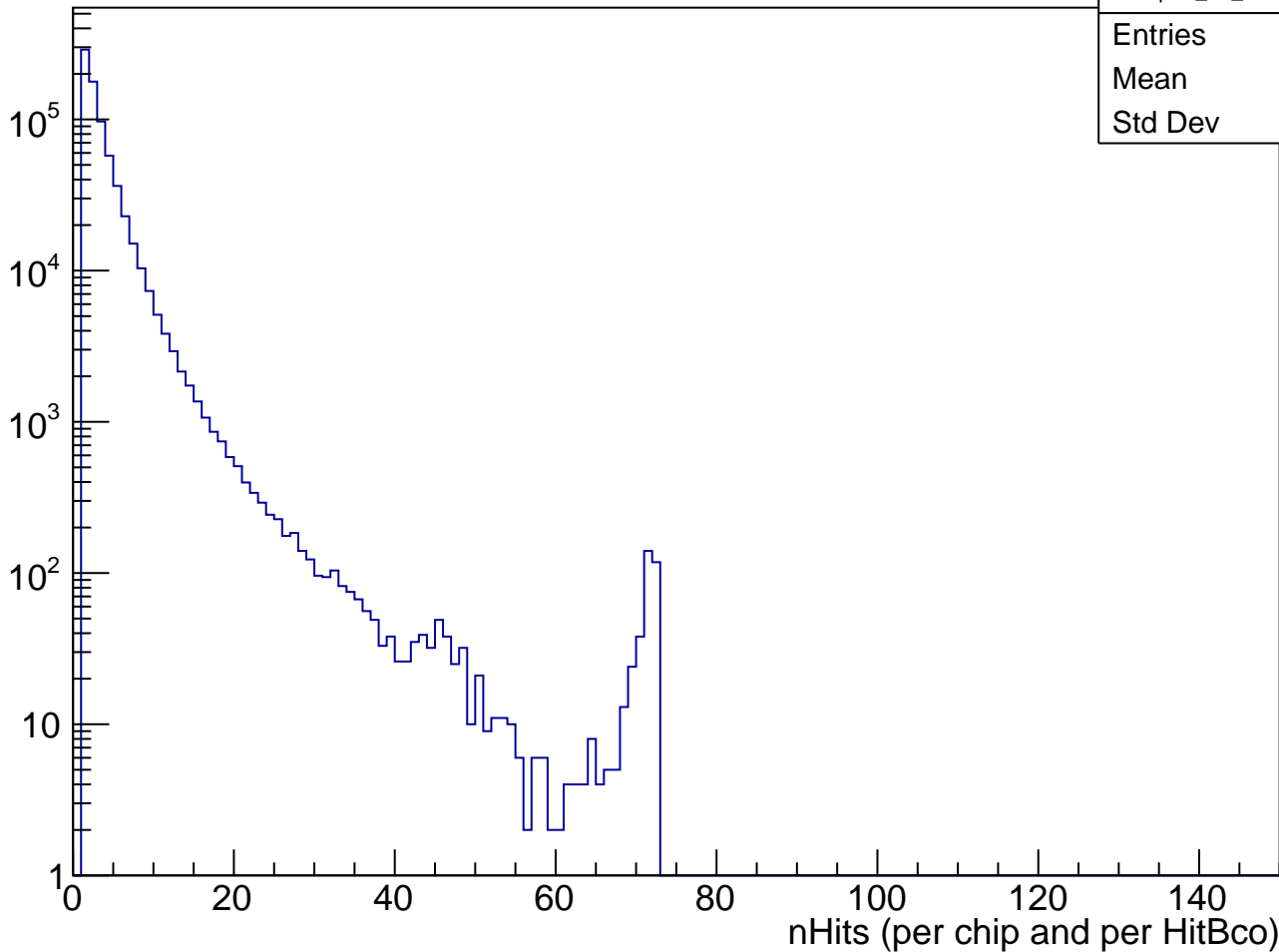
Entries 736924

Mean 2.905

Std Dev 3.432

nChipHit_F4_Fch13_Chip25

Entries



nChipHit_F4_Fch13_Chip25

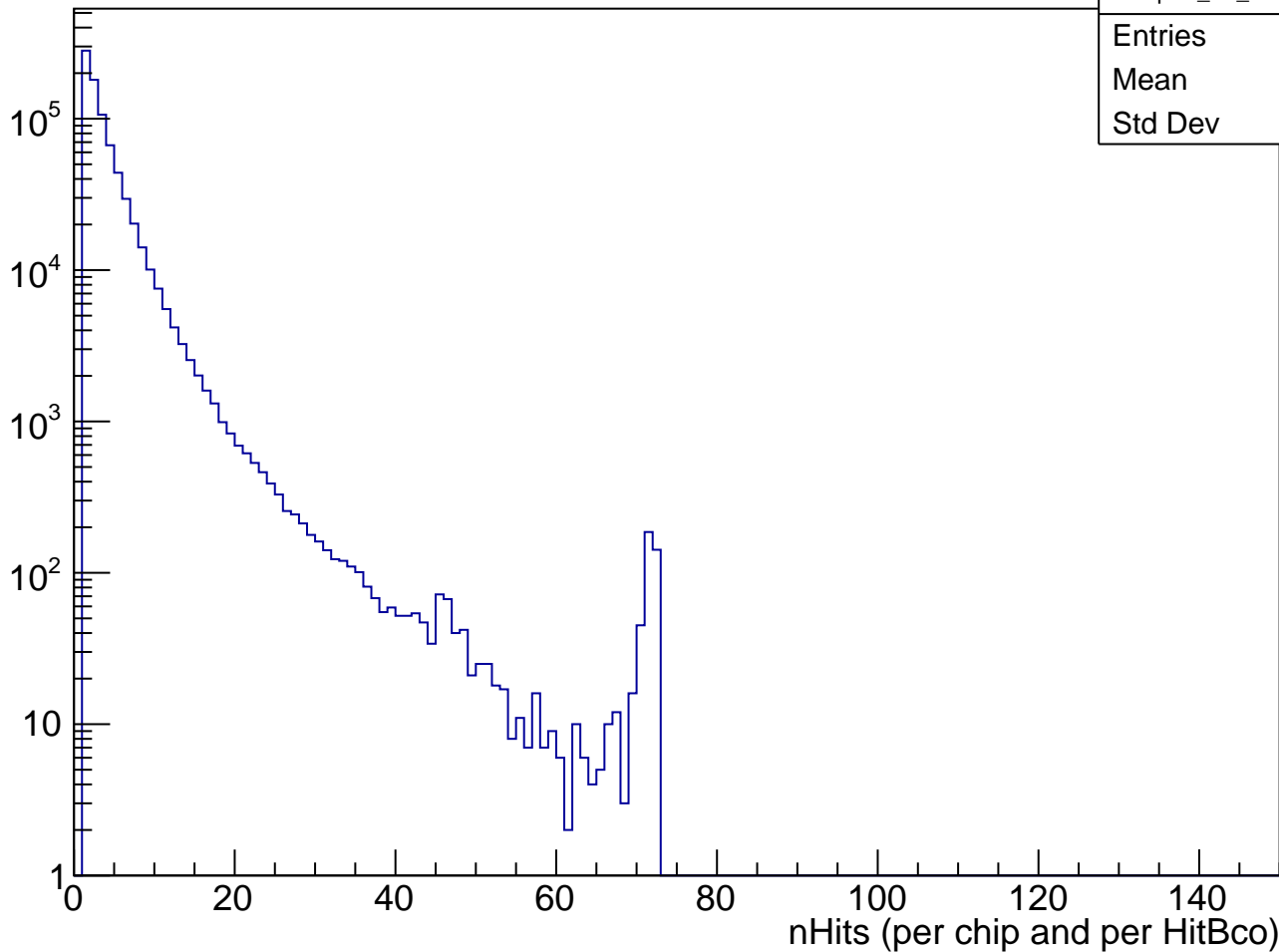
Entries 738084

Mean 2.879

Std Dev 3.431

nChipHit_F4_Fch13_Chip3

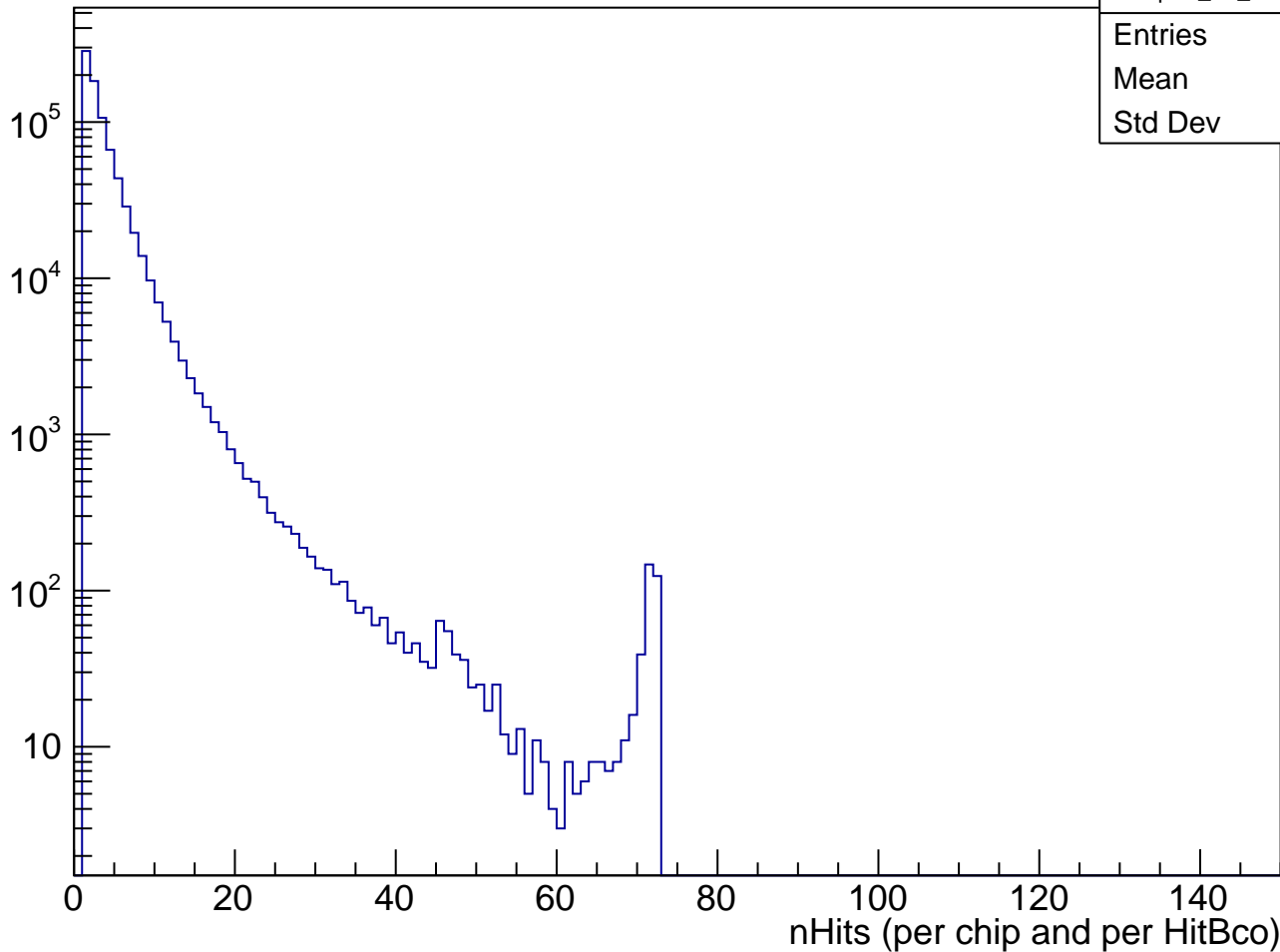
Entries



nChipHit_F4_Fch13_Chip3	
Entries	789369
Mean	3.21
Std Dev	3.829

nChipHit_F4_Fch13_Chip4

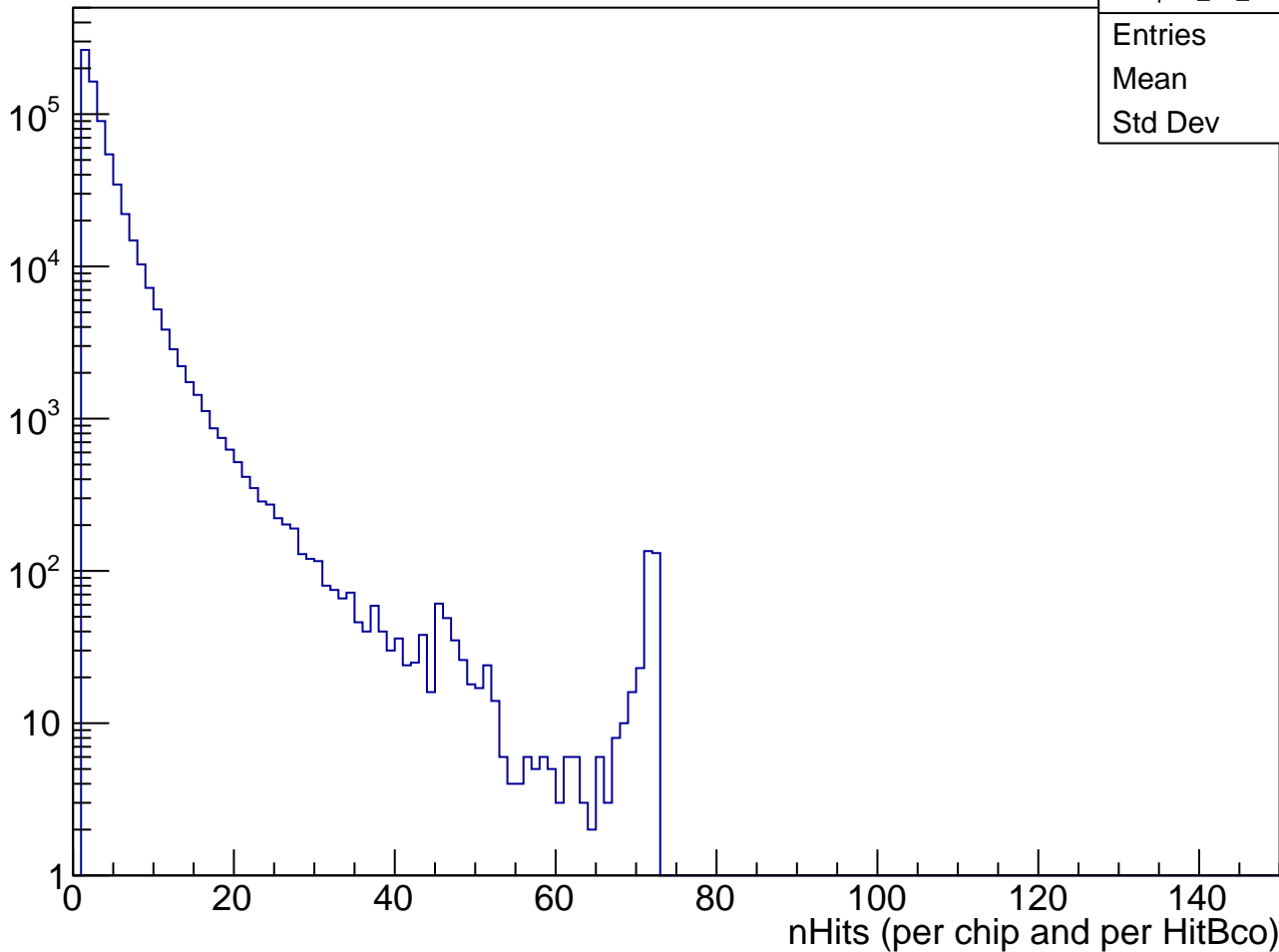
Entries



nChipHit_F4_Fch13_Chip4	
Entries	789878
Mean	3.138
Std Dev	3.682

nChipHit_F4_Fch13_Chip5

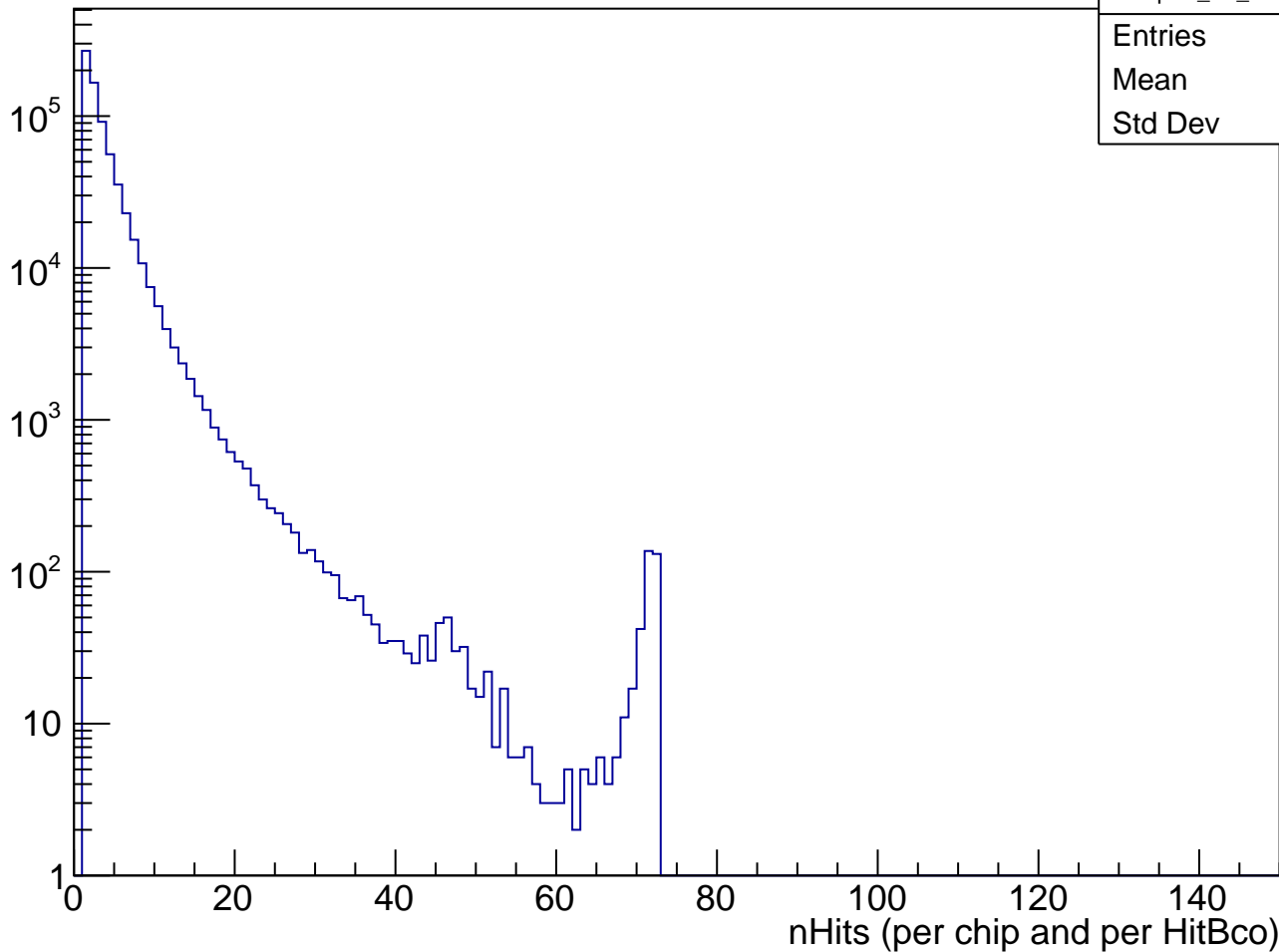
Entries



nChipHit_F4_Fch13_Chip5	
Entries	685989
Mean	2.951
Std Dev	3.527

nChipHit_F4_Fch13_Chip6

Entries



nChipHit_F4_Fch13_Chip6

Entries 700617

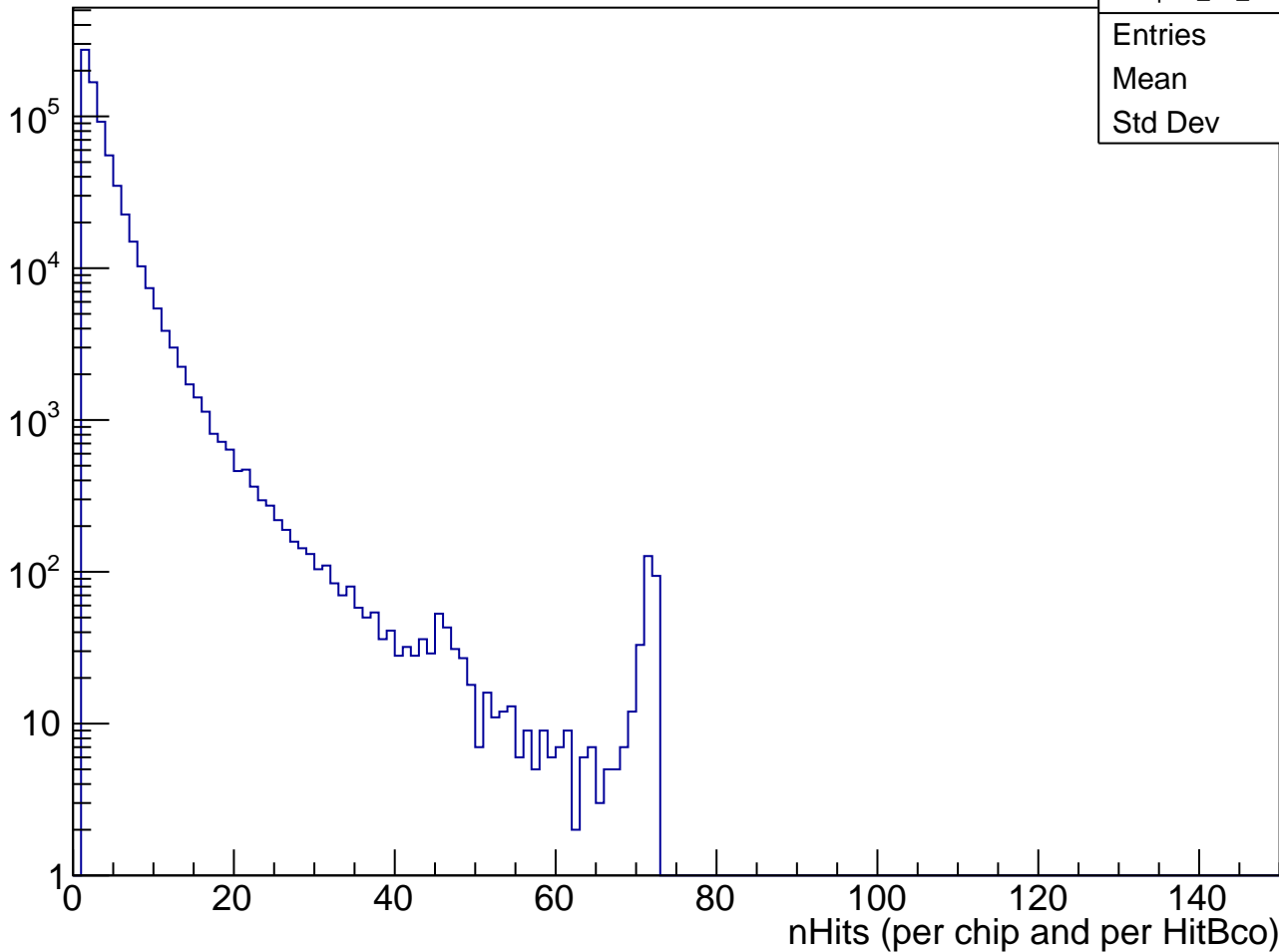
Mean 2.972

Std Dev 3.557

nHits (per chip and per HitBco)

nChipHit_F4_Fch13_Chip7

Entries

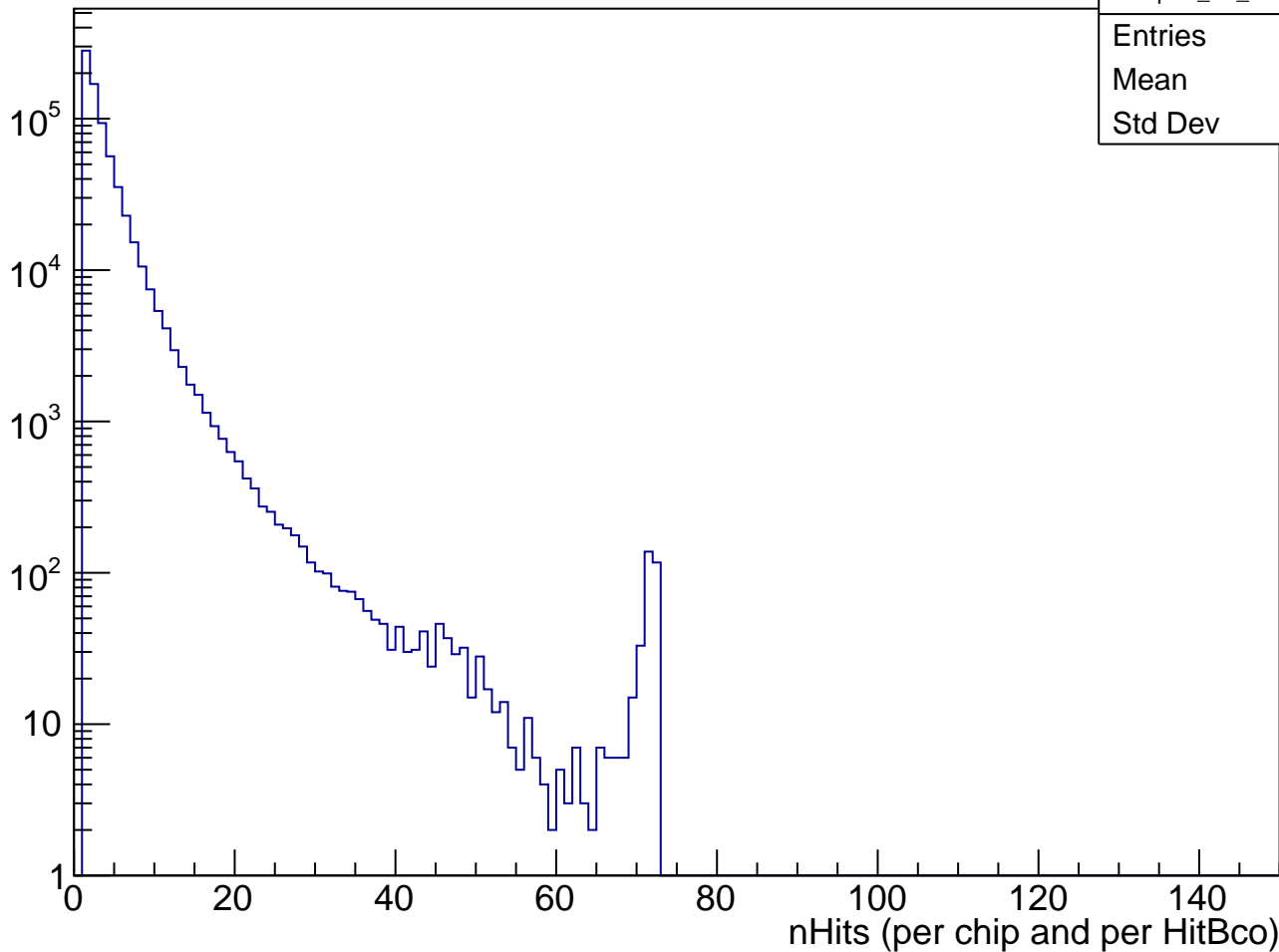


nChipHit_F4_Fch13_Chip7	
Entries	705114
Mean	2.926
Std Dev	3.471

nHits (per chip and per HitBco)

nChipHit_F4_Fch13_Chip8

Entries



nChipHit_F4_Fch13_Chip8

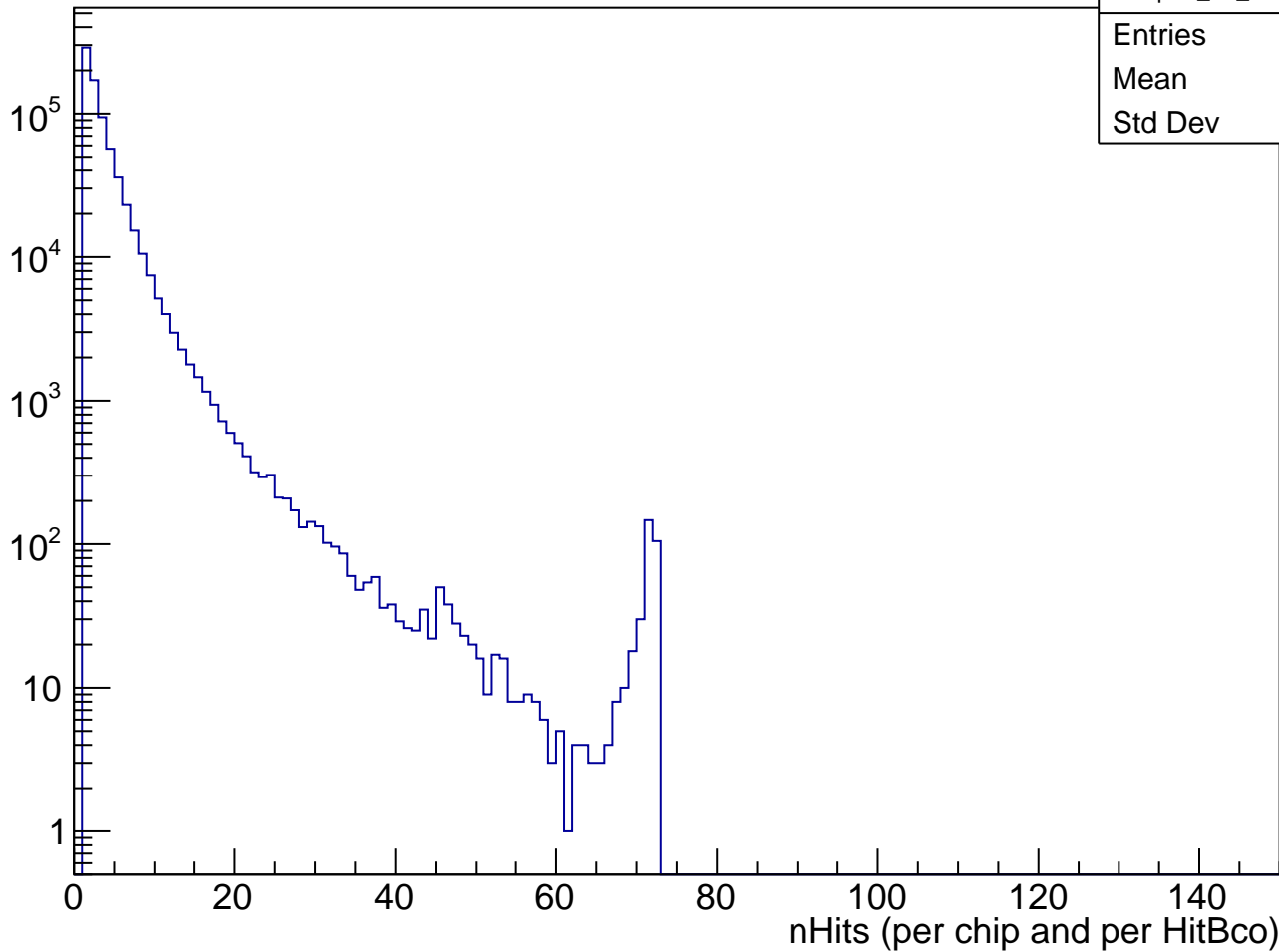
Entries 718464

Mean 2.924

Std Dev 3.494

nChipHit_F4_Fch13_Chip9

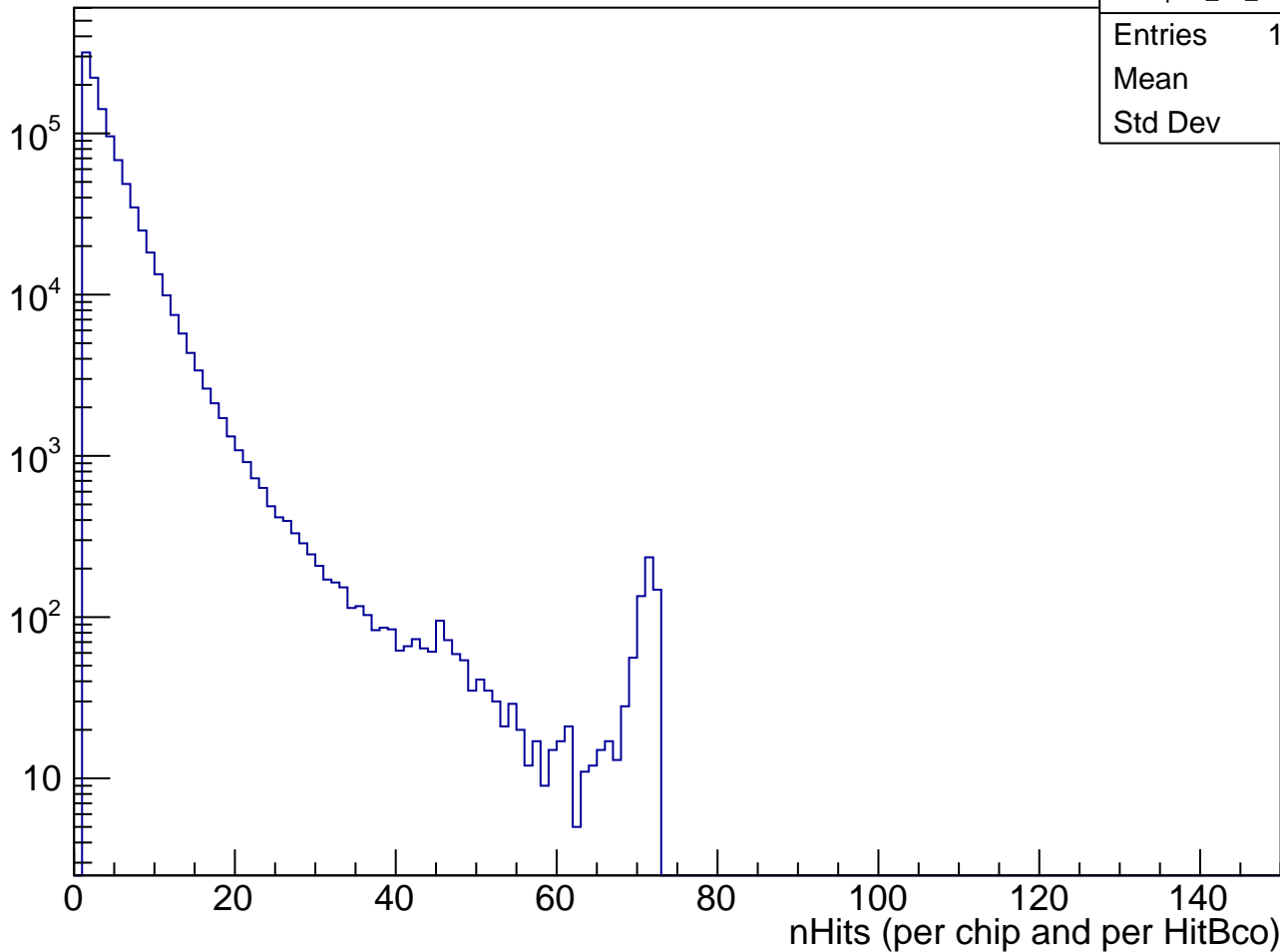
Entries



nChipHit_F4_Fch13_Chip9	
Entries	728032
Mean	2.902
Std Dev	3.46

nChipHit_F4_Fch1_Chip0

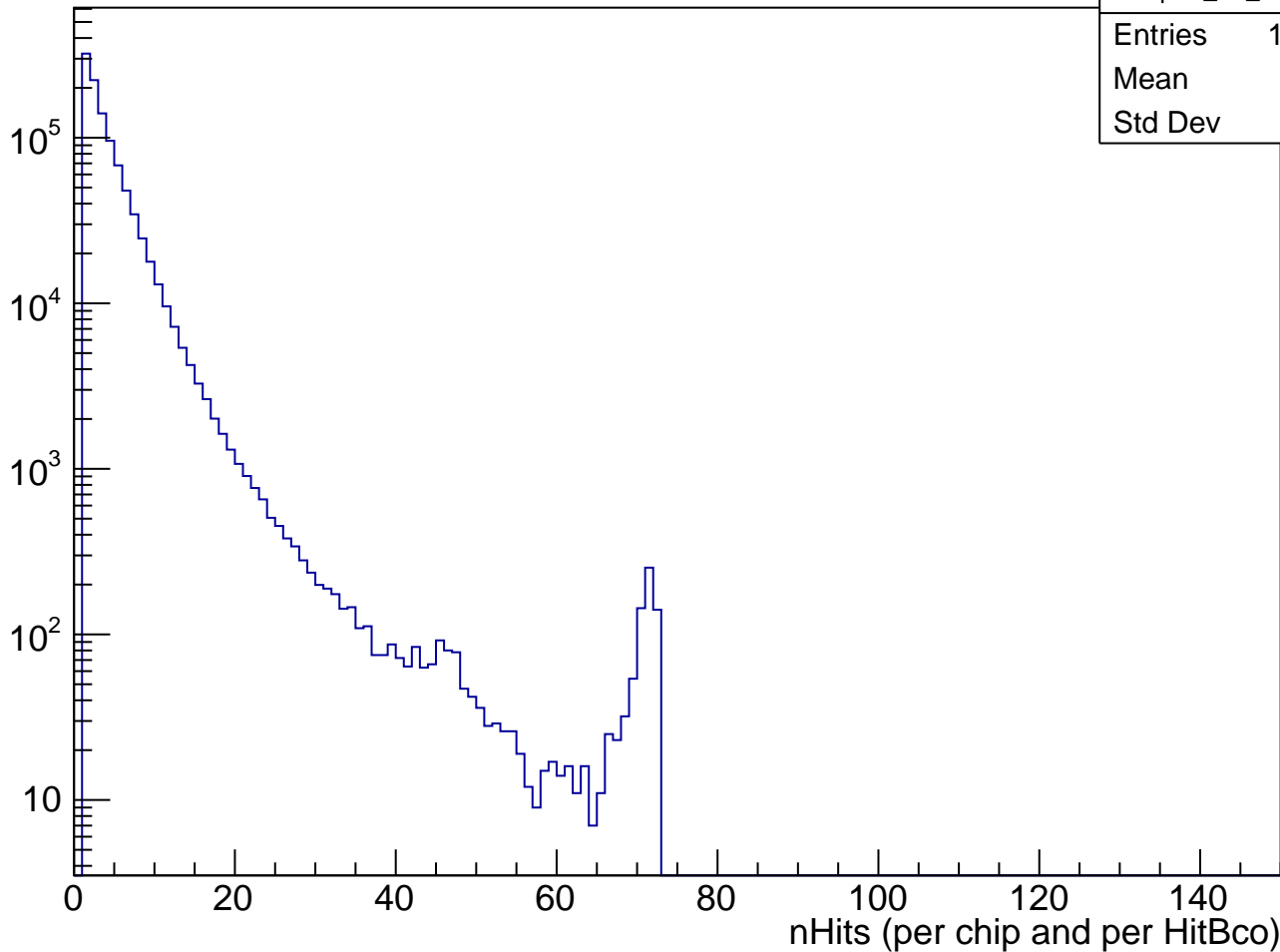
Entries



nChipHit_F4_Fch1_Chip0	
Entries	1032018
Mean	3.607
Std Dev	4.078

nChipHit_F4_Fch1_Chip1

Entries

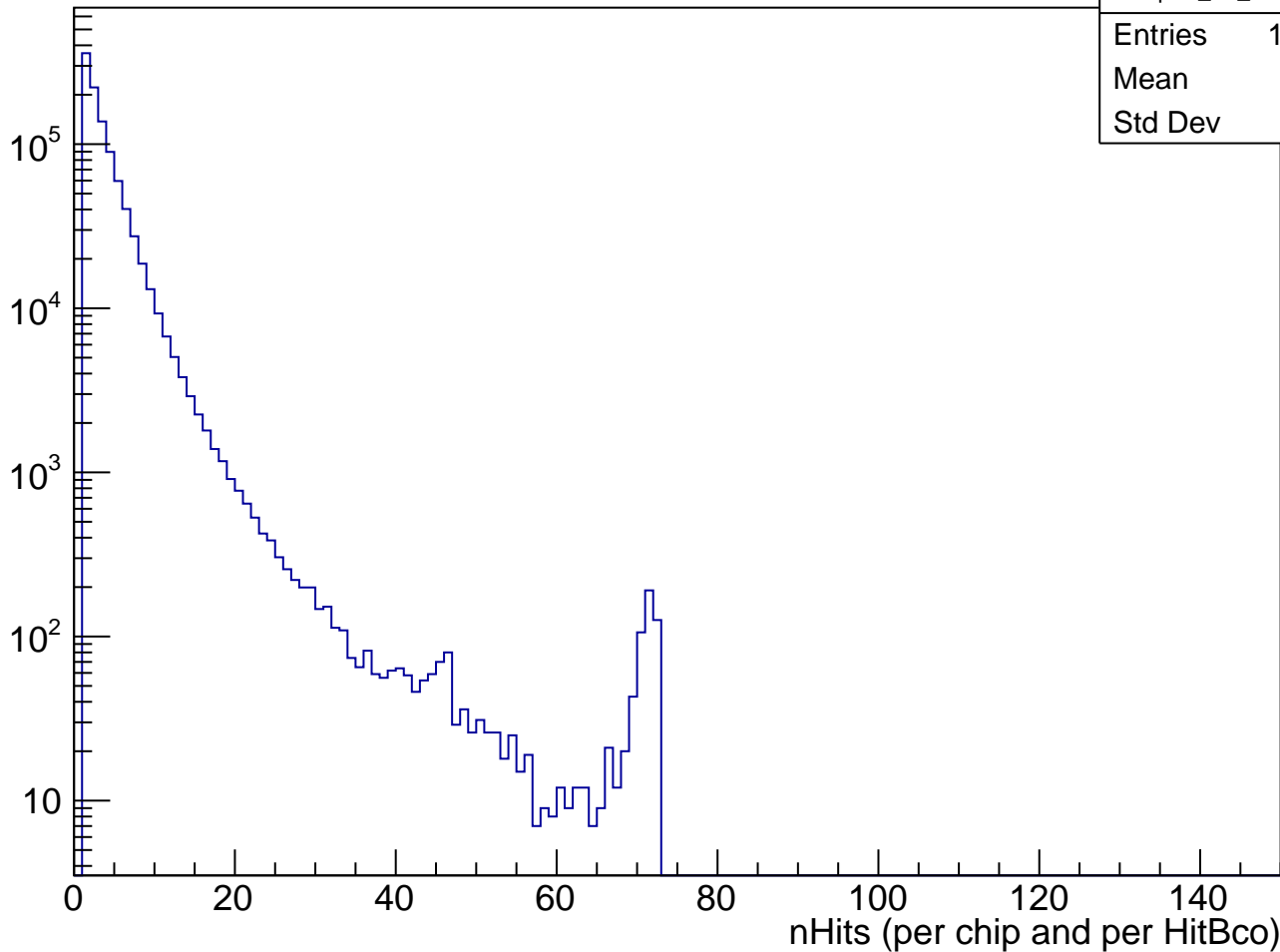


nChipHit_F4_Fch1_Chip1	
Entries	1032668
Mean	3.58
Std Dev	4.089

nHits (per chip and per HitBco)

nChipHit_F4_Fch1_Chip10

Entries

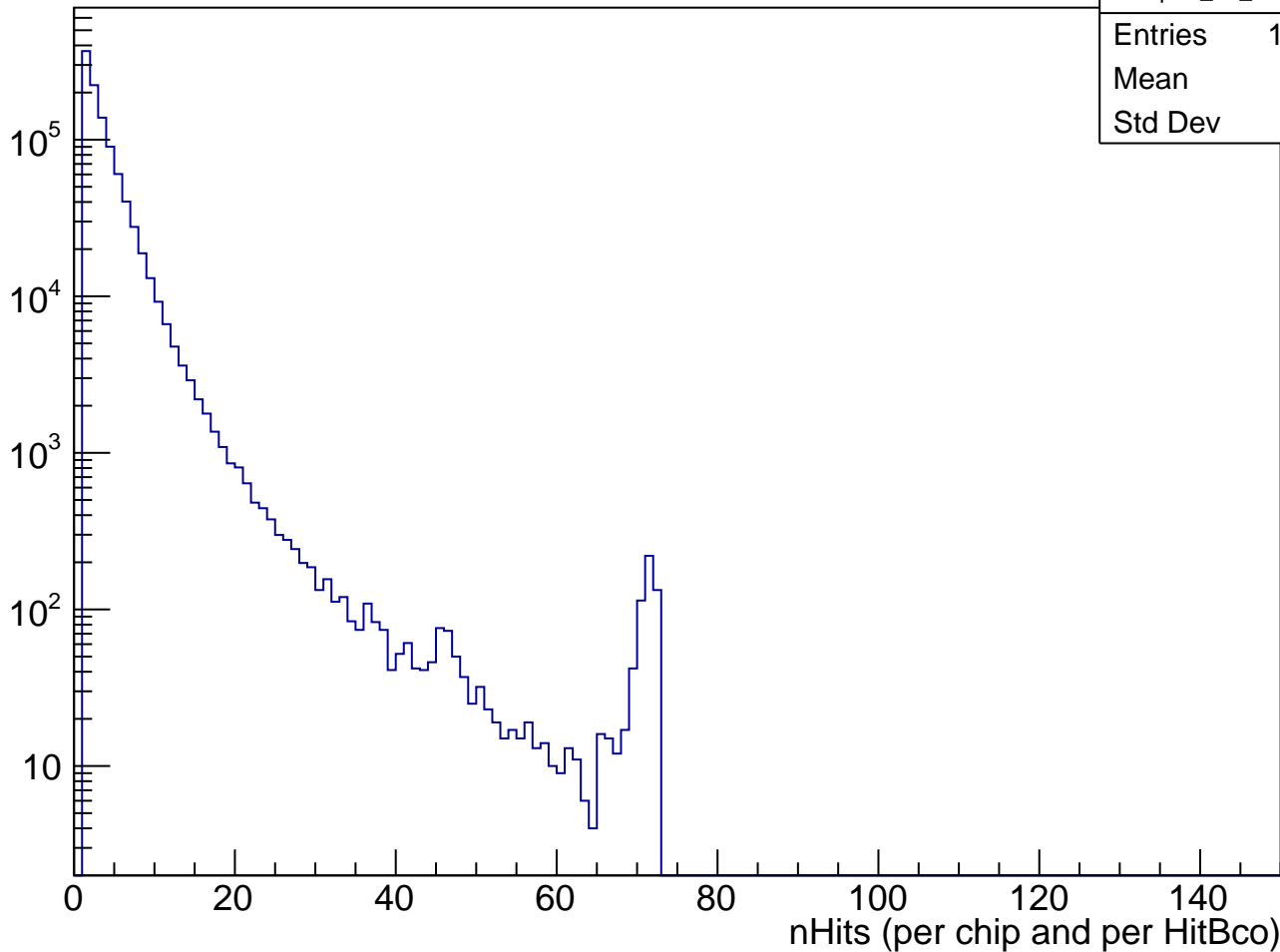


nChipHit_F4_Fch1_Chip10	
Entries	1007575
Mean	3.185
Std Dev	3.665

nHits (per chip and per HitBco)

nChipHit_F4_Fch1_Chip11

Entries

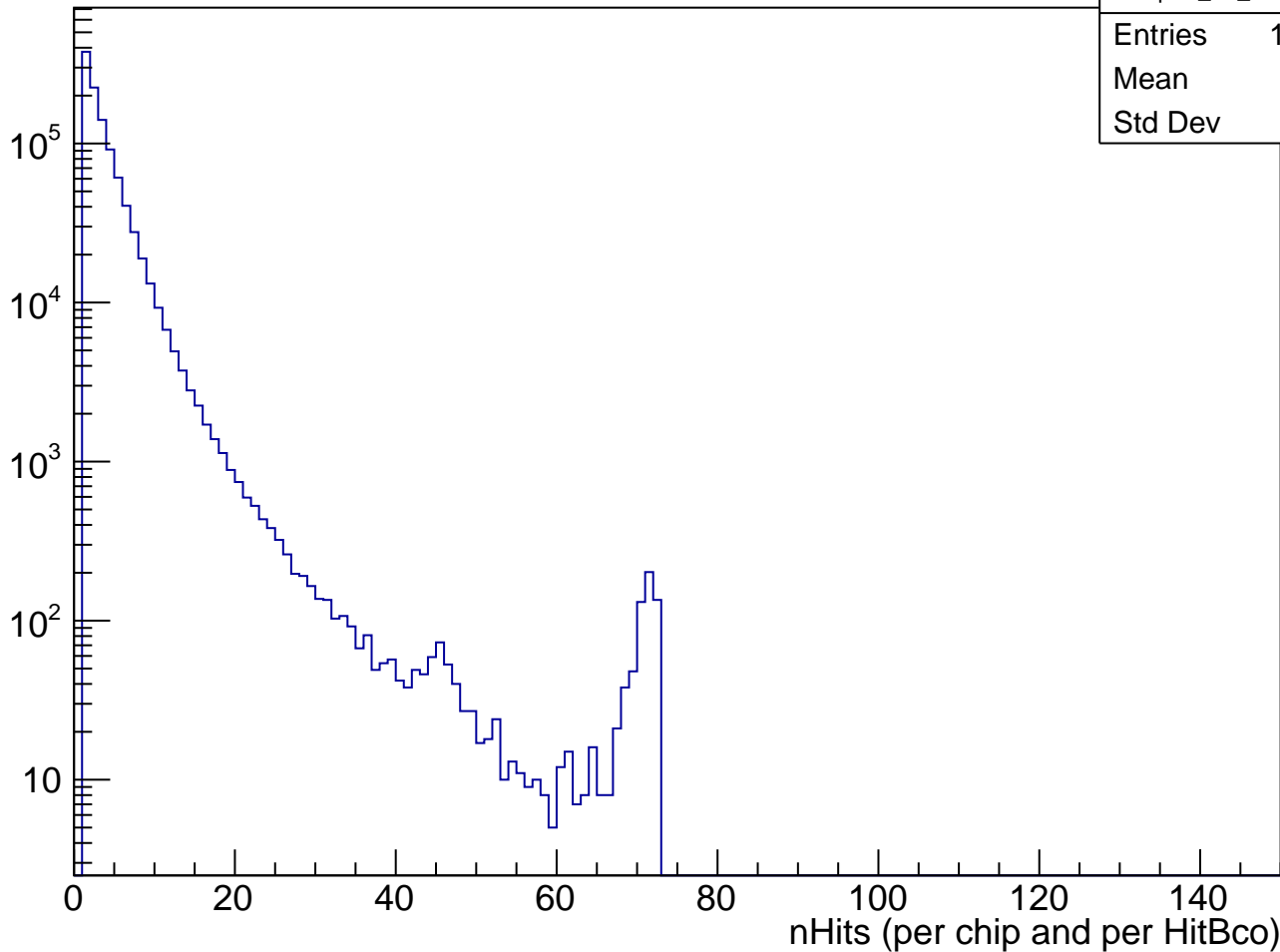


nChipHit_F4_Fch1_Chip11	
Entries	1020264
Mean	3.161
Std Dev	3.664

nHits (per chip and per HitBco)

nChipHit_F4_Fch1_Chip12

Entries

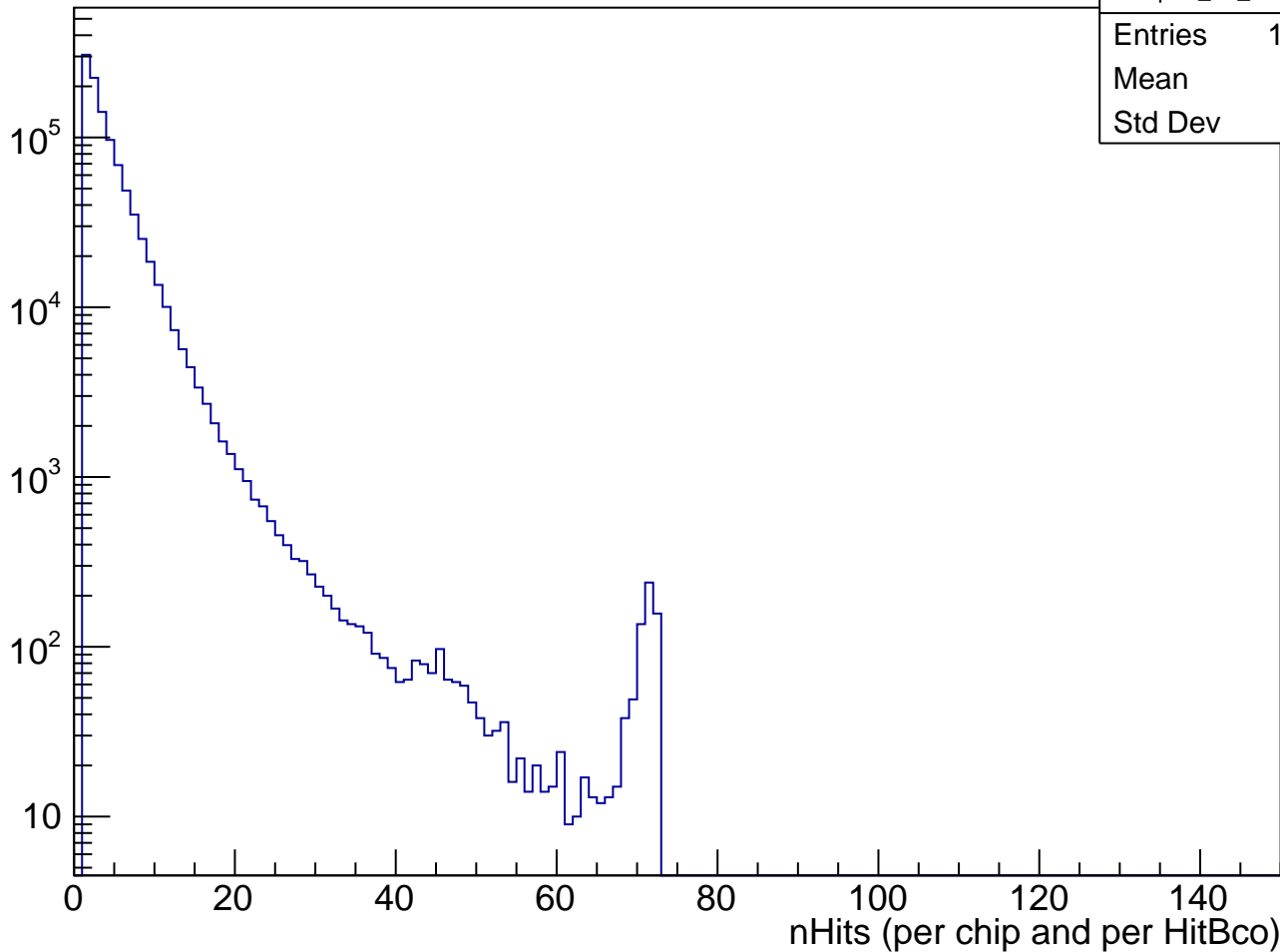


nChipHit_F4_Fch1_Chip12	
Entries	1036540
Mean	3.139
Std Dev	3.617

nHits (per chip and per HitBco)

nChipHit_F4_Fch1_Chip13

Entries

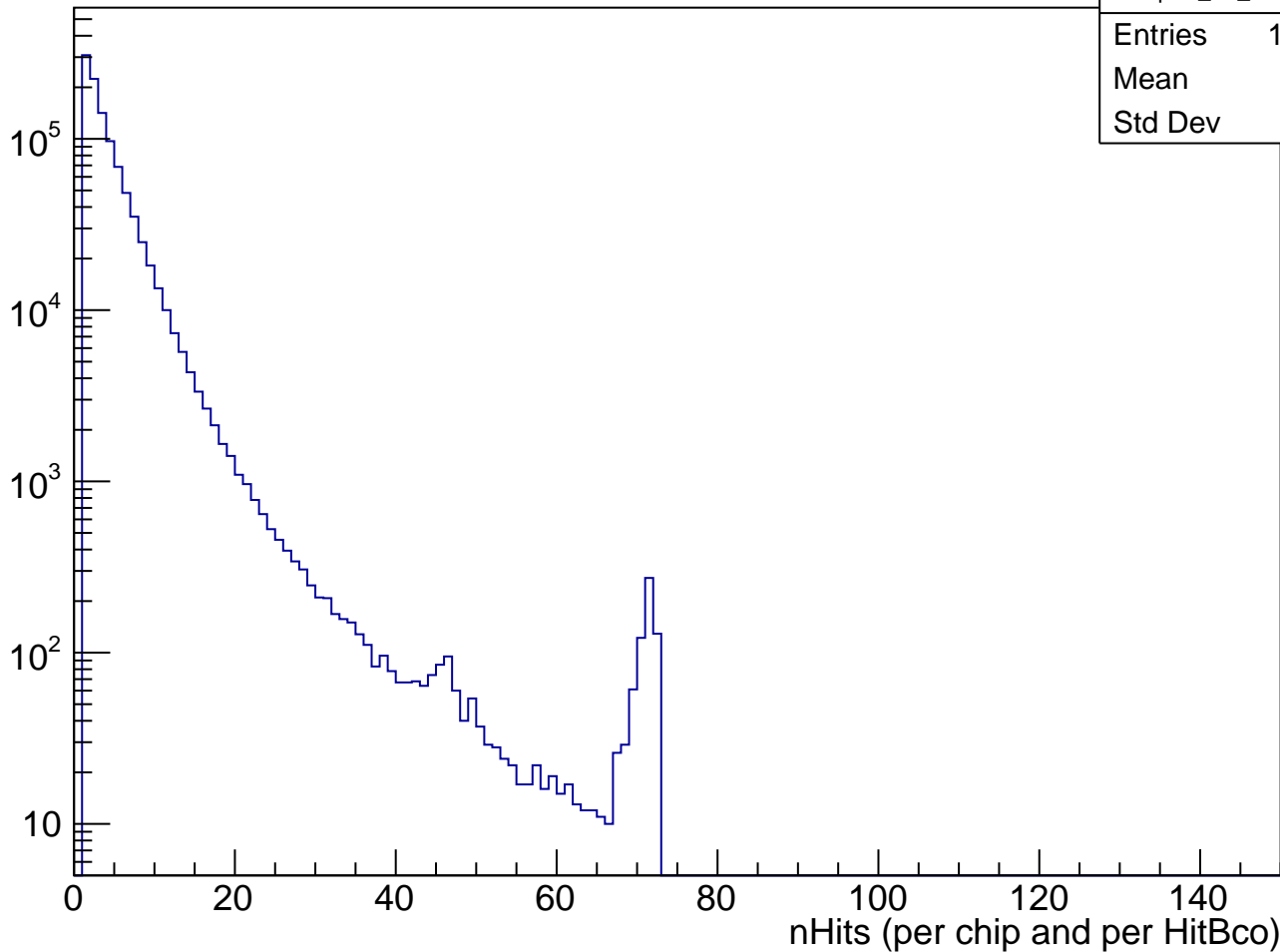


nChipHit_F4_Fch1_Chip13	
Entries	1026816
Mean	3.647
Std Dev	4.127

nHits (per chip and per HitBco)

nChipHit_F4_Fch1_Chip14

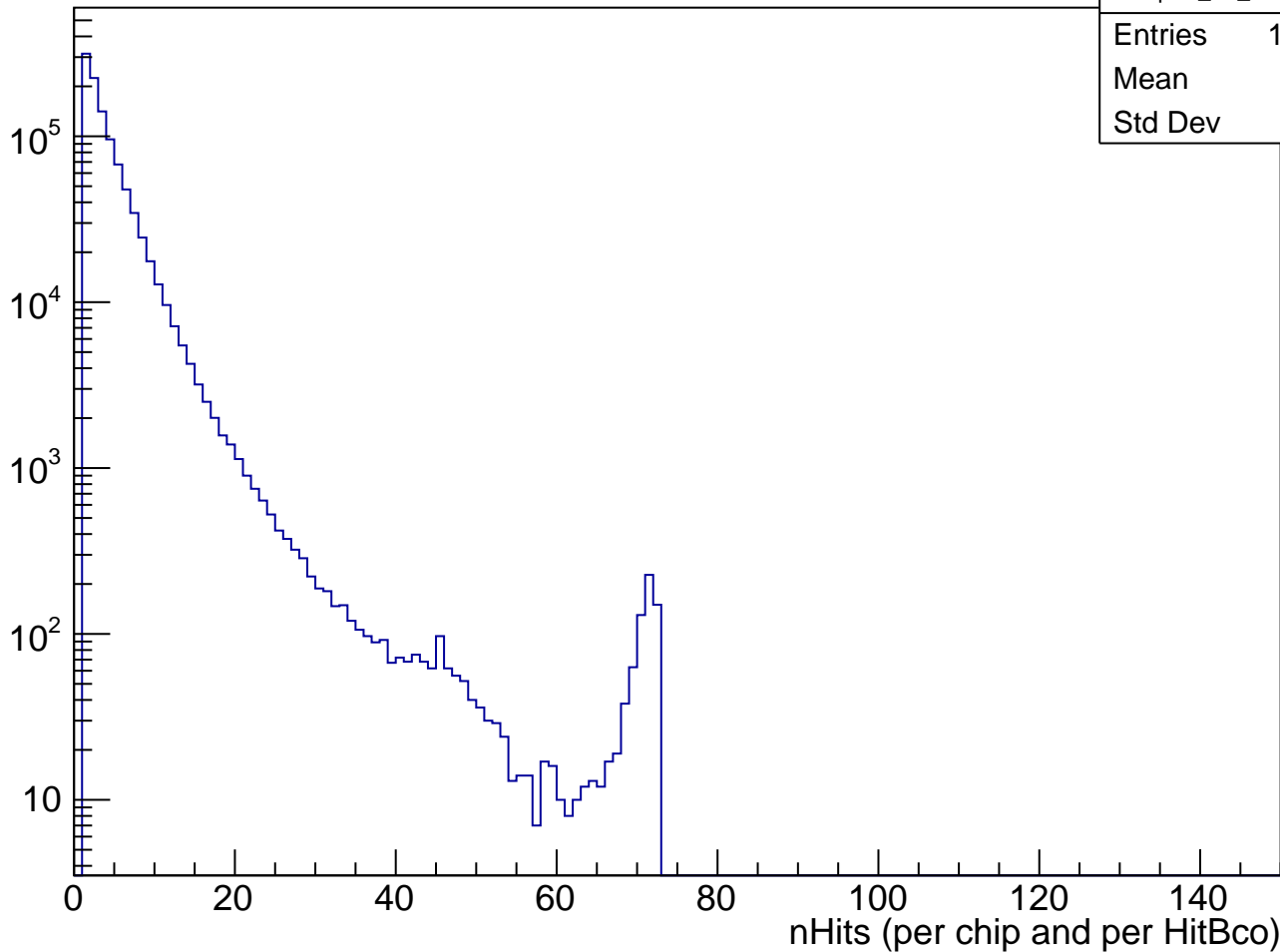
Entries



nChipHit_F4_Fch1_Chip14	
Entries	1026705
Mean	3.639
Std Dev	4.121

nChipHit_F4_Fch1_Chip15

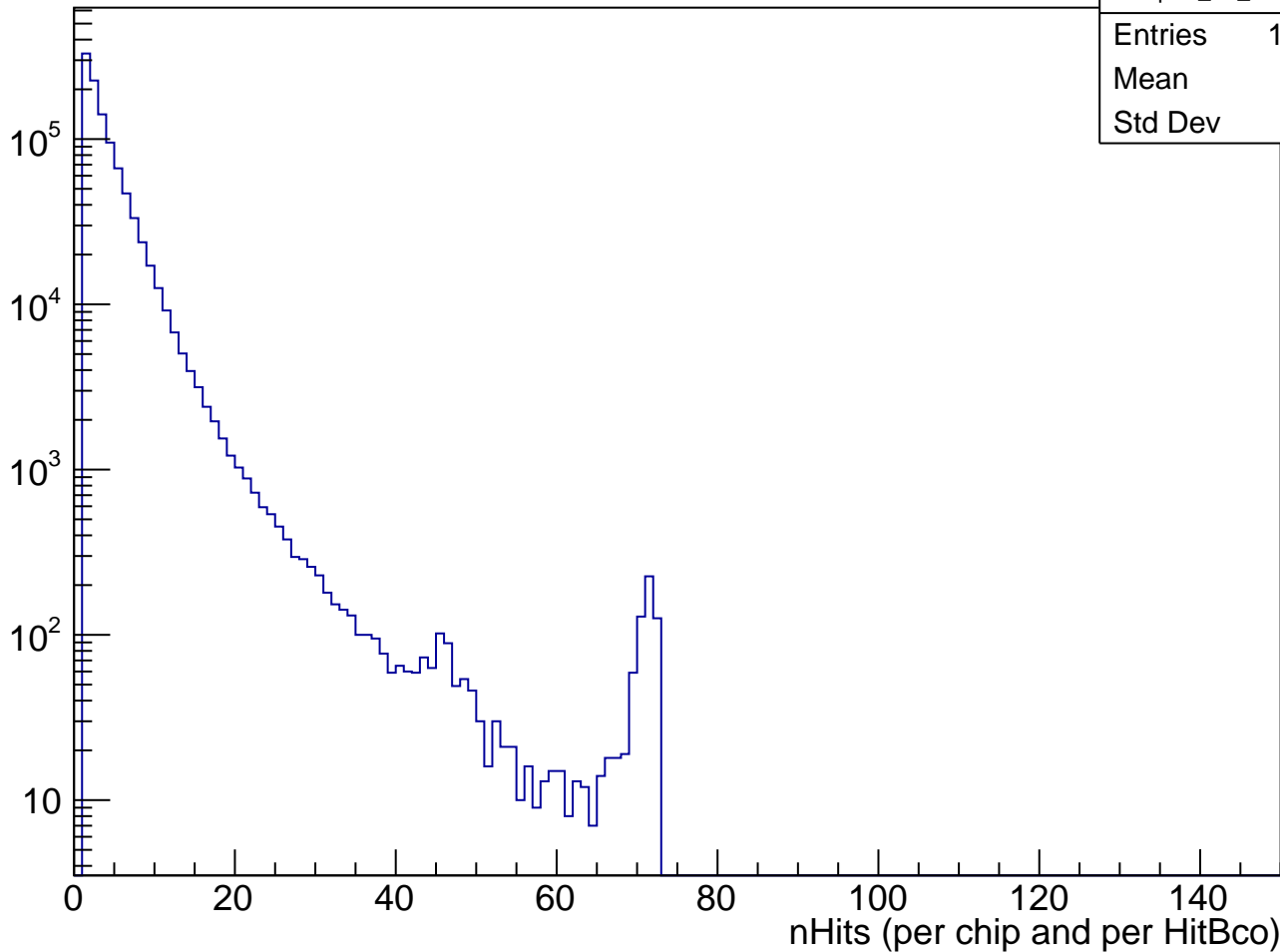
Entries



nChipHit_F4_Fch1_Chip15	
Entries	1026788
Mean	3.583
Std Dev	4.05

nChipHit_F4_Fch1_Chip16

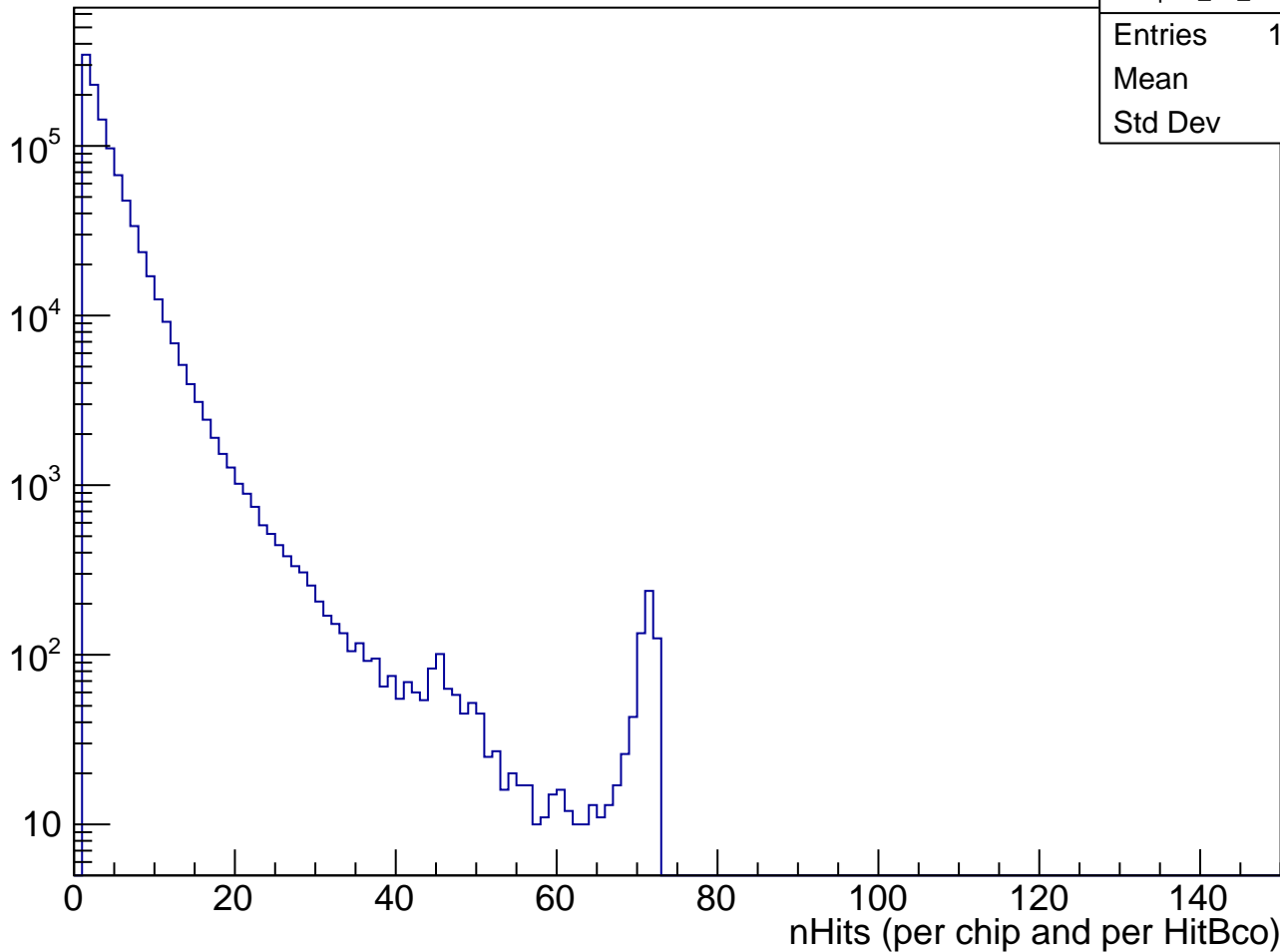
Entries



nChipHit_F4_Fch1_Chip16	
Entries	1035427
Mean	3.505
Std Dev	3.984

nChipHit_F4_Fch1_Chip17

Entries

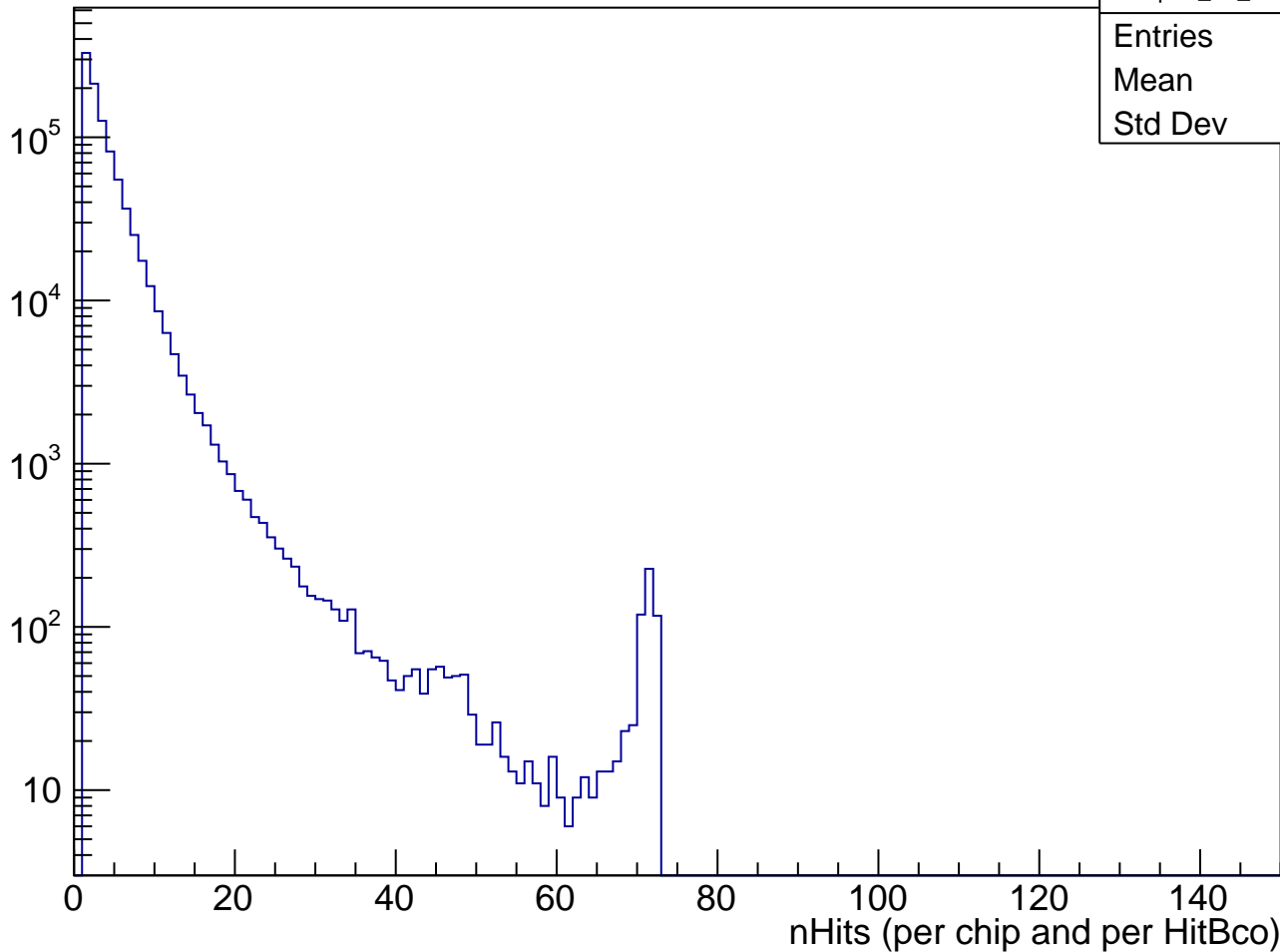


nChipHit_F4_Fch1_Chip17	
Entries	1059272
Mean	3.467
Std Dev	3.955

nHits (per chip and per HitBco)

nChipHit_F4_Fch1_Chip18

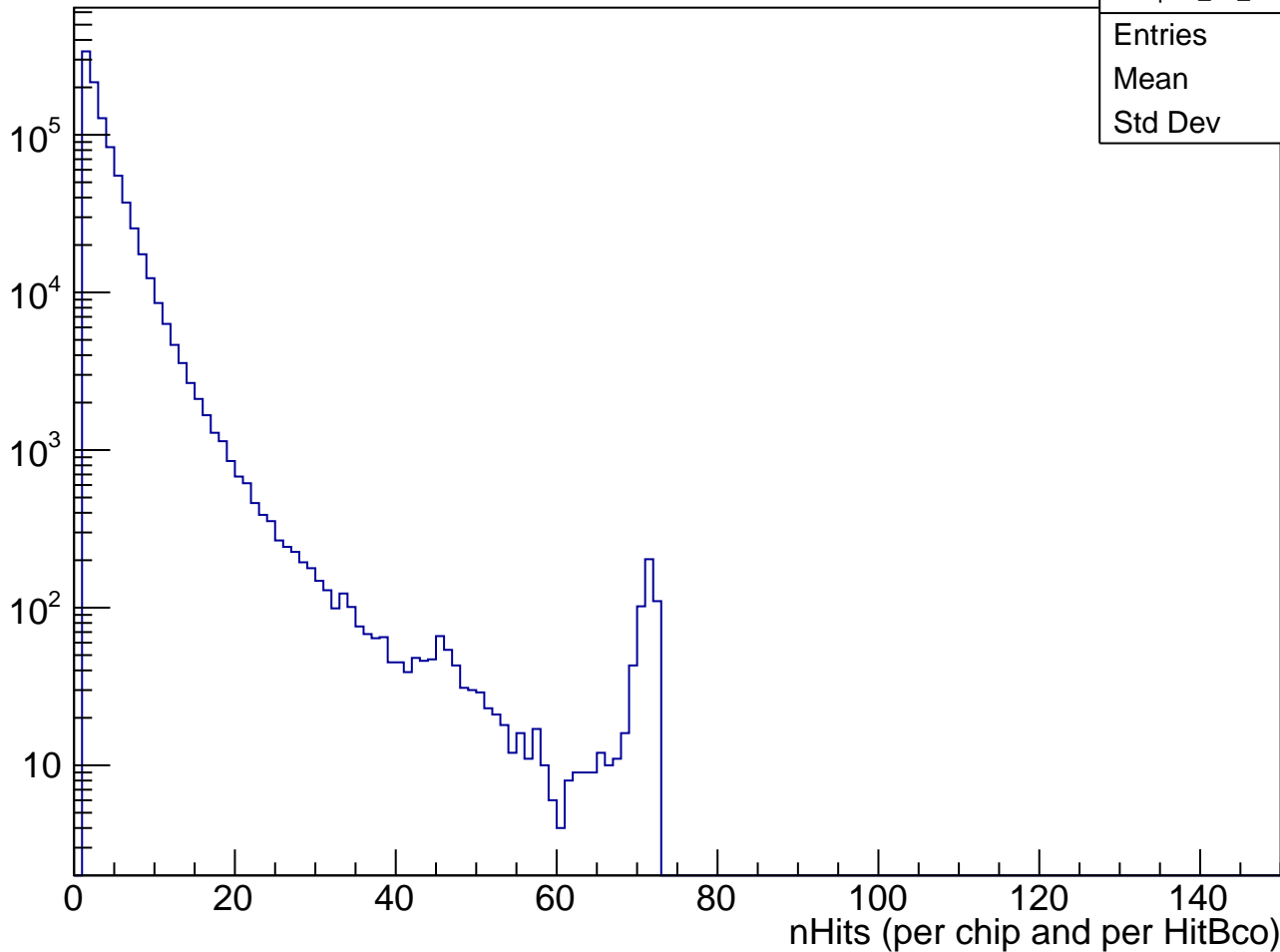
Entries



nChipHit_F4_Fch1_Chip18	
Entries	934661
Mean	3.188
Std Dev	3.715

nChipHit_F4_Fch1_Chip19

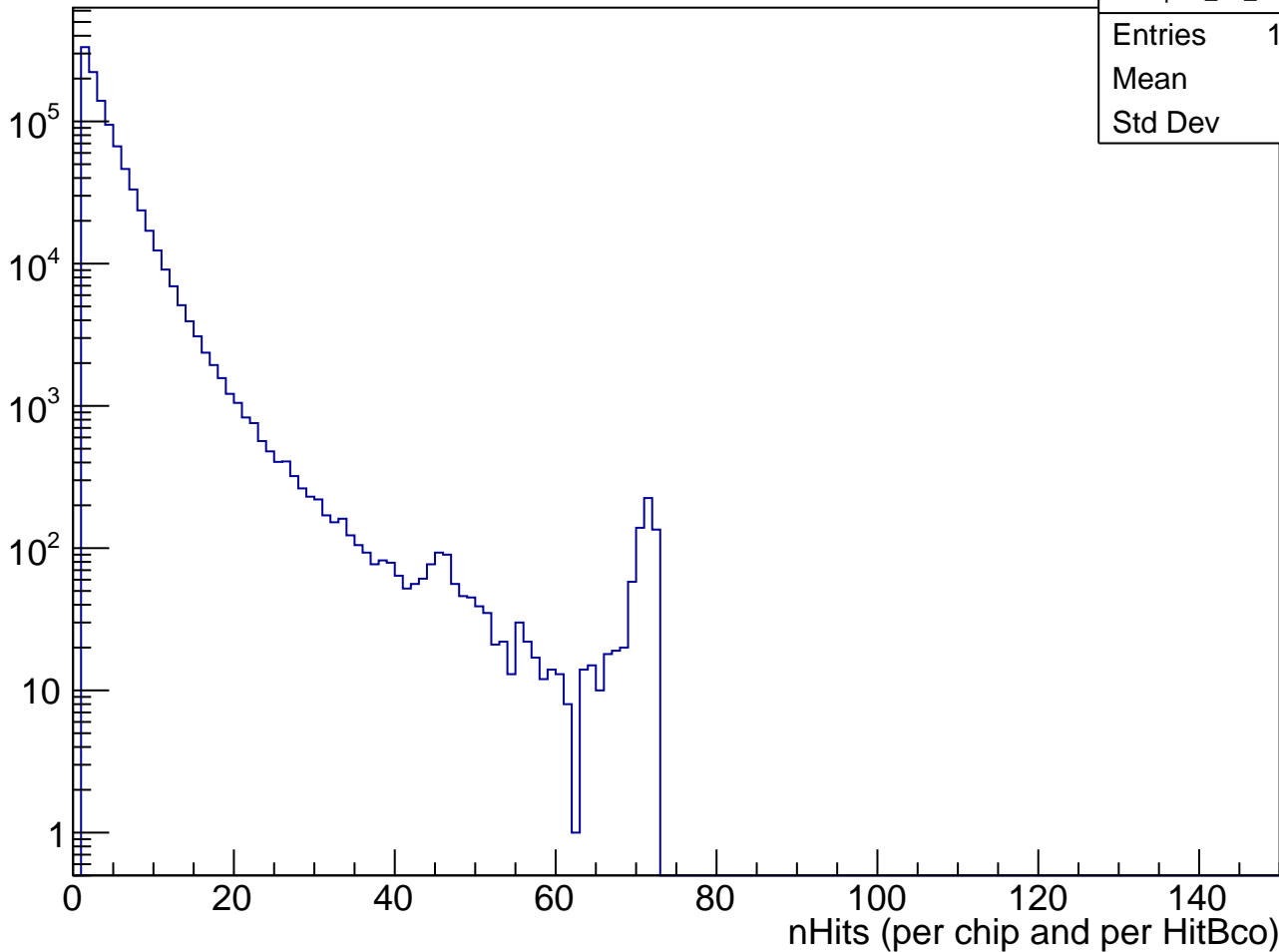
Entries



nChipHit_F4_Fch1_Chip19	
Entries	950933
Mean	3.161
Std Dev	3.648

nChipHit_F4_Fch1_Chip2

Entries



nChipHit_F4_Fch1_Chip2

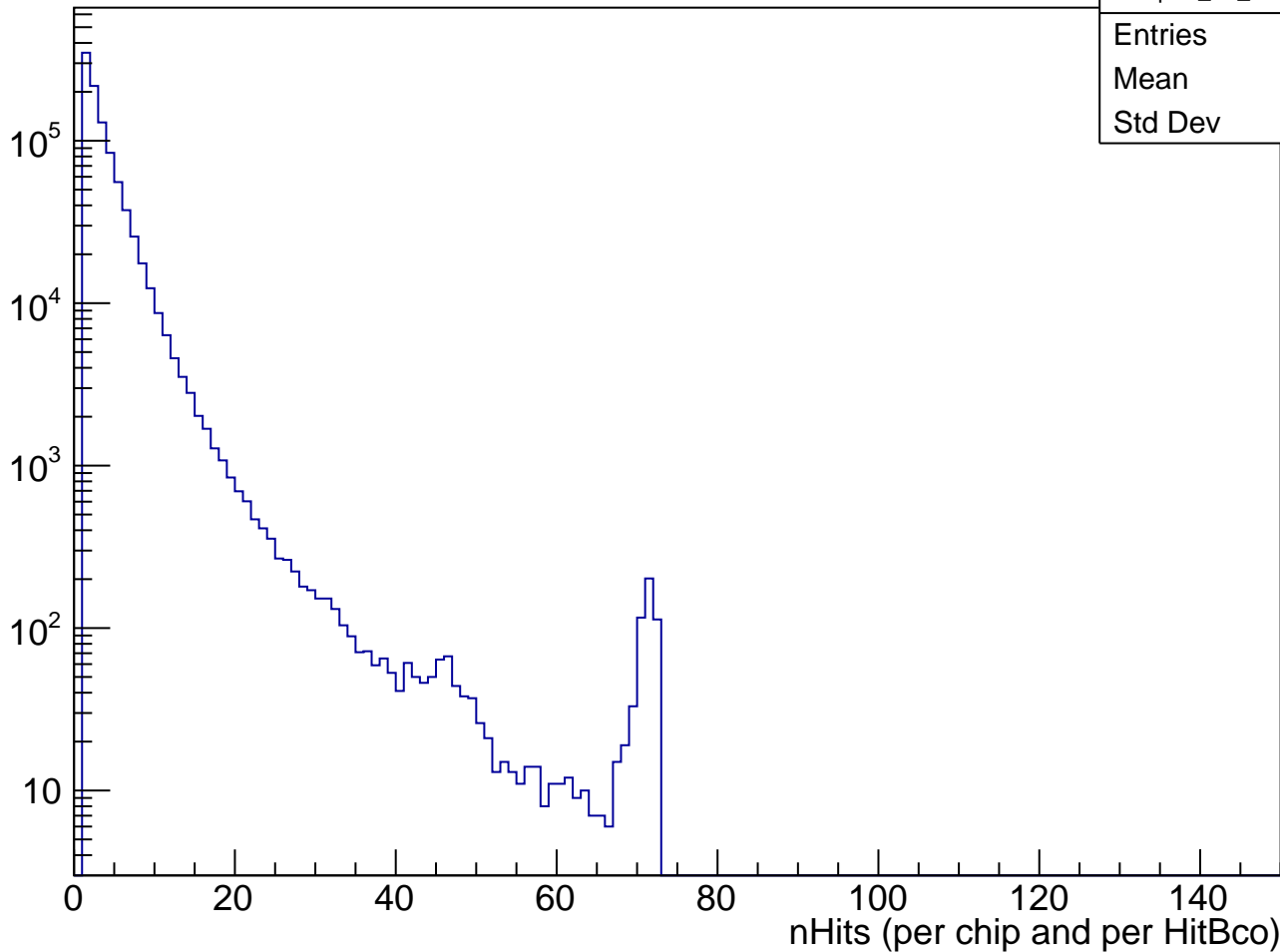
Entries 1031878

Mean 3.499

Std Dev 4

nChipHit_F4_Fch1_Chip20

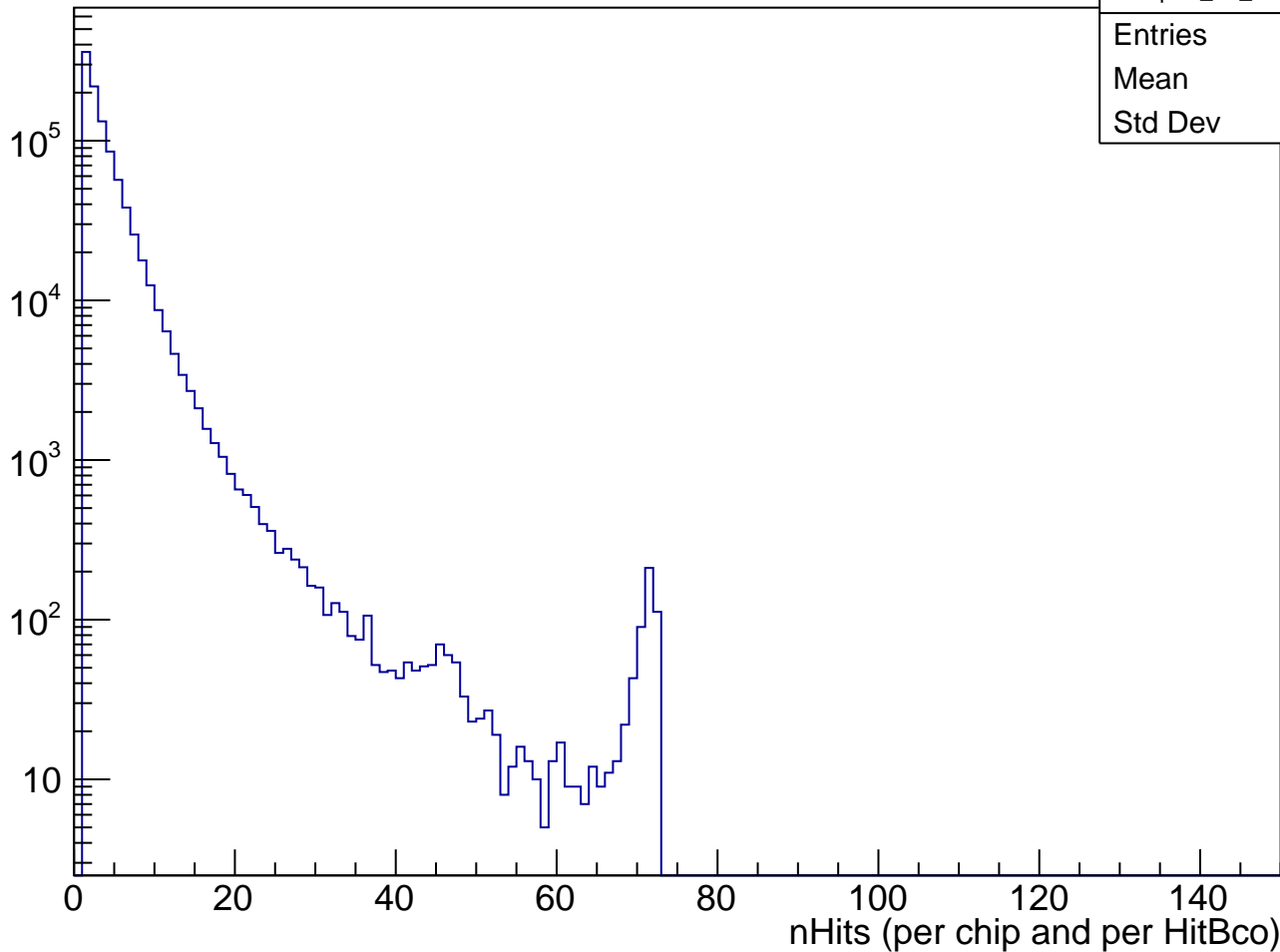
Entries



nChipHit_F4_Fch1_Chip20	
Entries	966668
Mean	3.144
Std Dev	3.644

nChipHit_F4_Fch1_Chip21

Entries

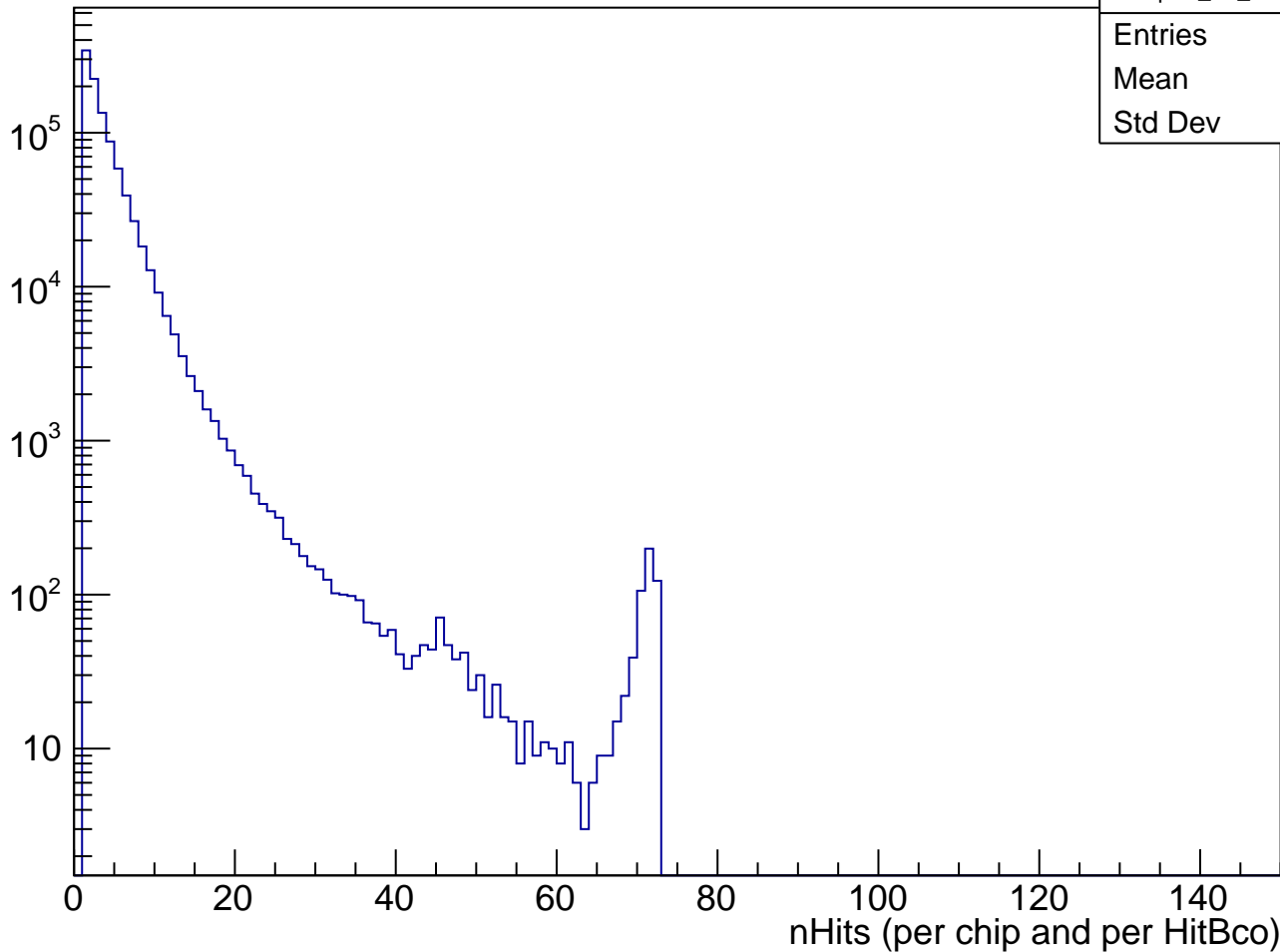


nChipHit_F4_Fch1_Chip21	
Entries	985733
Mean	3.122
Std Dev	3.613

nHits (per chip and per HitBco)

nChipHit_F4_Fch1_Chip22

Entries

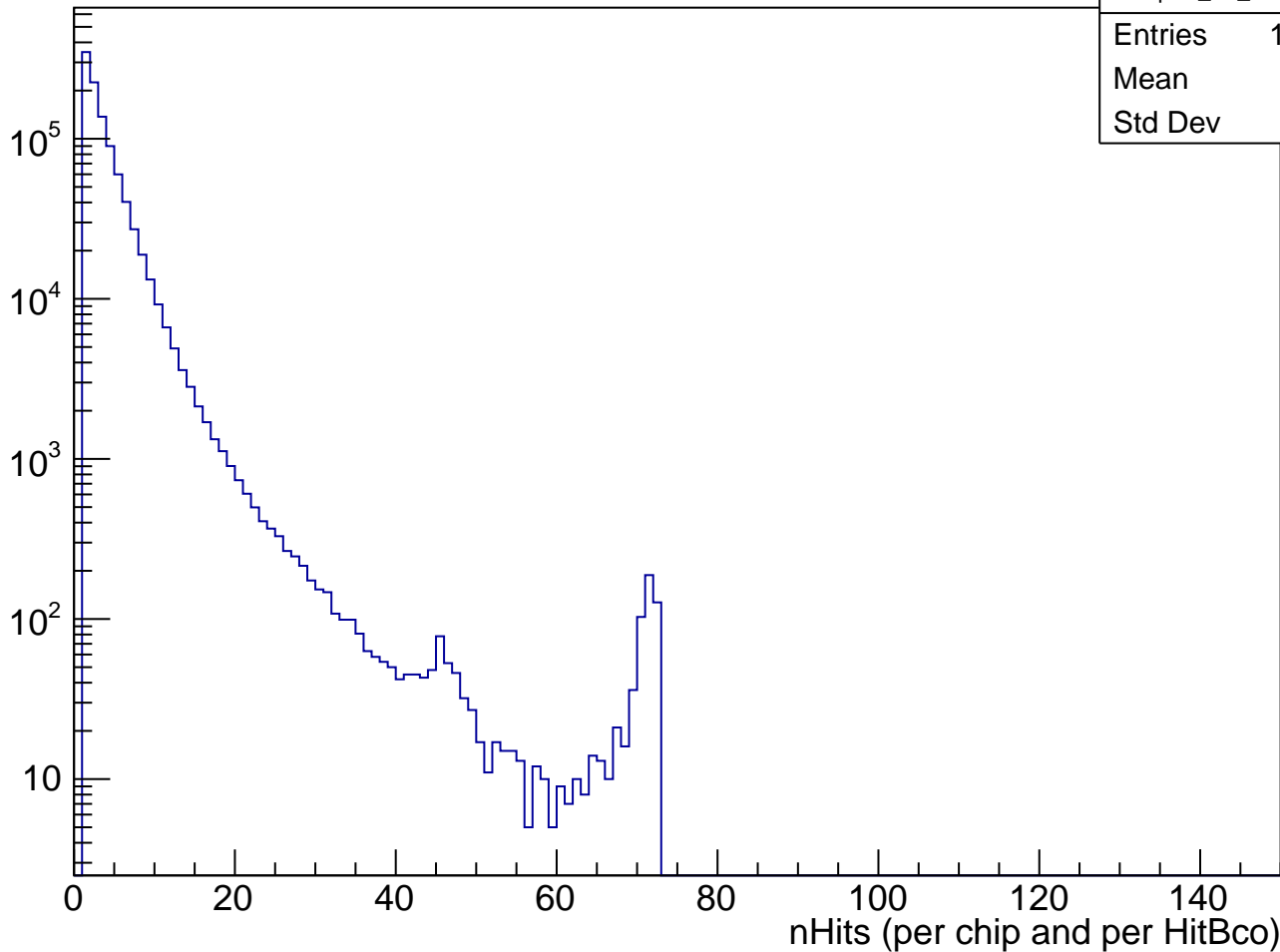


nChipHit_F4_Fch1_Chip22	
Entries	983248
Mean	3.171
Std Dev	3.603

nHits (per chip and per HitBco)

nChipHit_F4_Fch1_Chip23

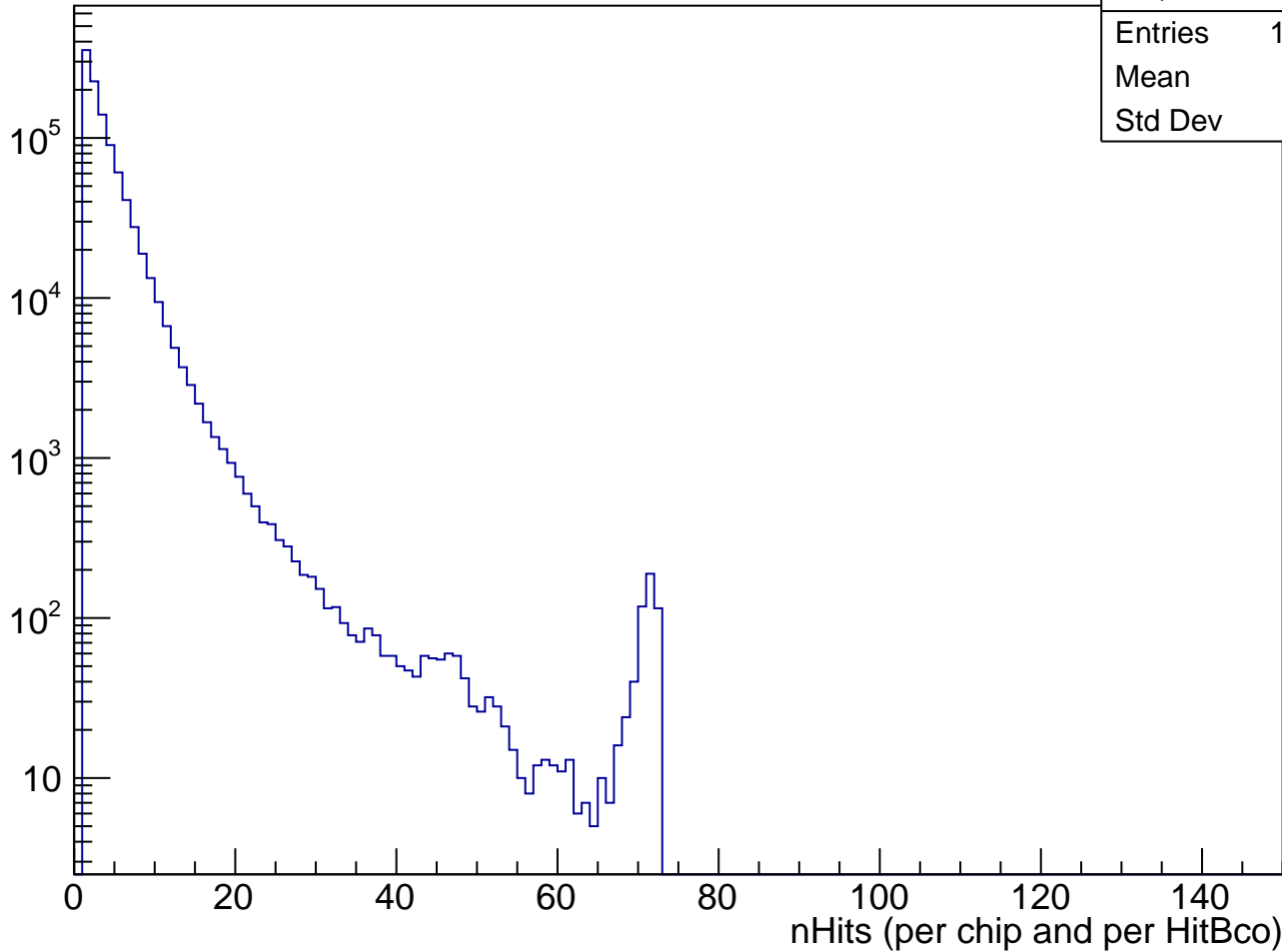
Entries



nChipHit_F4_Fch1_Chip23	
Entries	1000063
Mean	3.186
Std Dev	3.609

nChipHit_F4_Fch1_Chip24

Entries

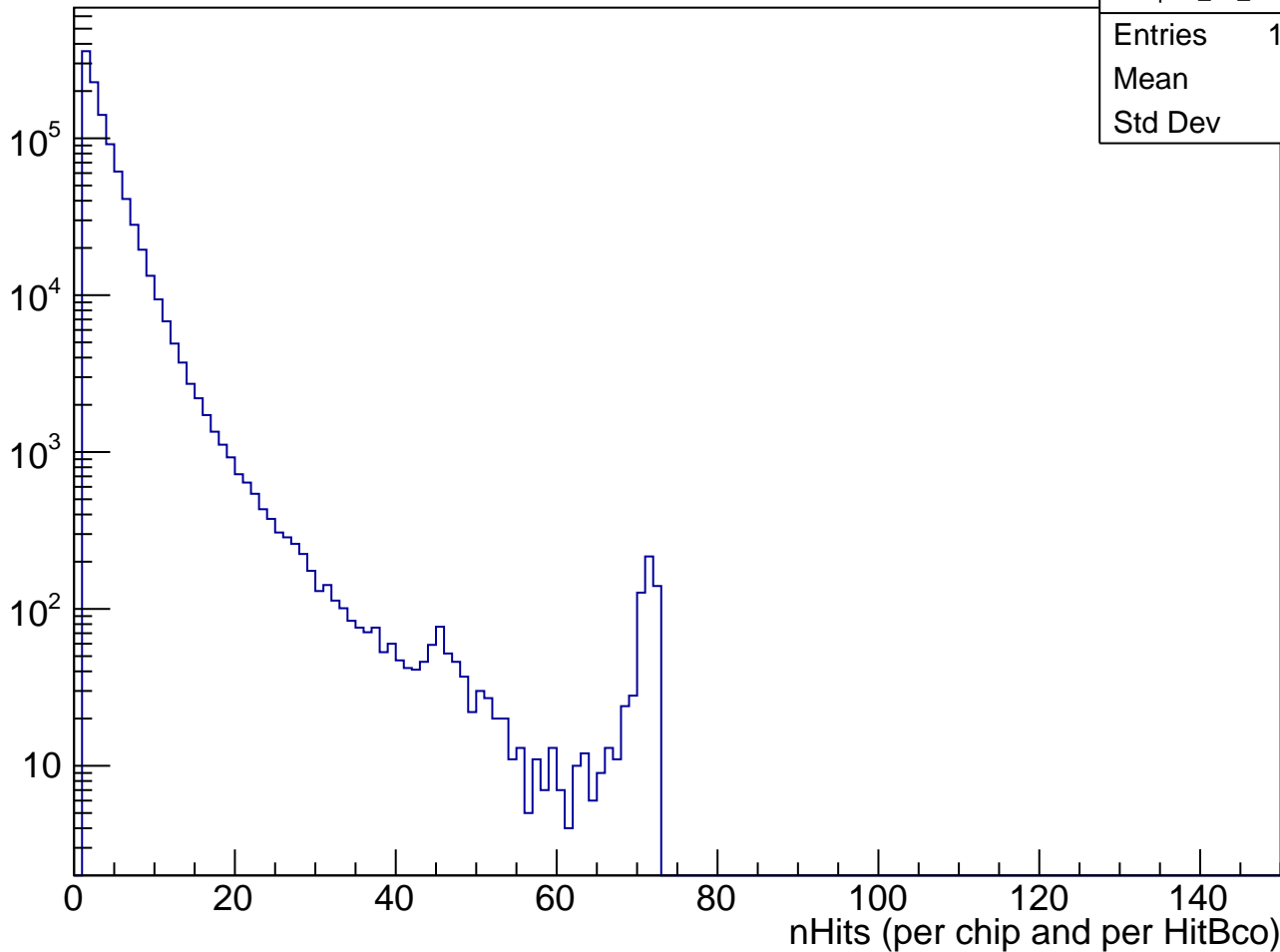


nChipHit_F4_Fch1_Chip24	
Entries	1012949
Mean	3.186
Std Dev	3.626

nHits (per chip and per HitBco)

nChipHit_F4_Fch1_Chip25

Entries

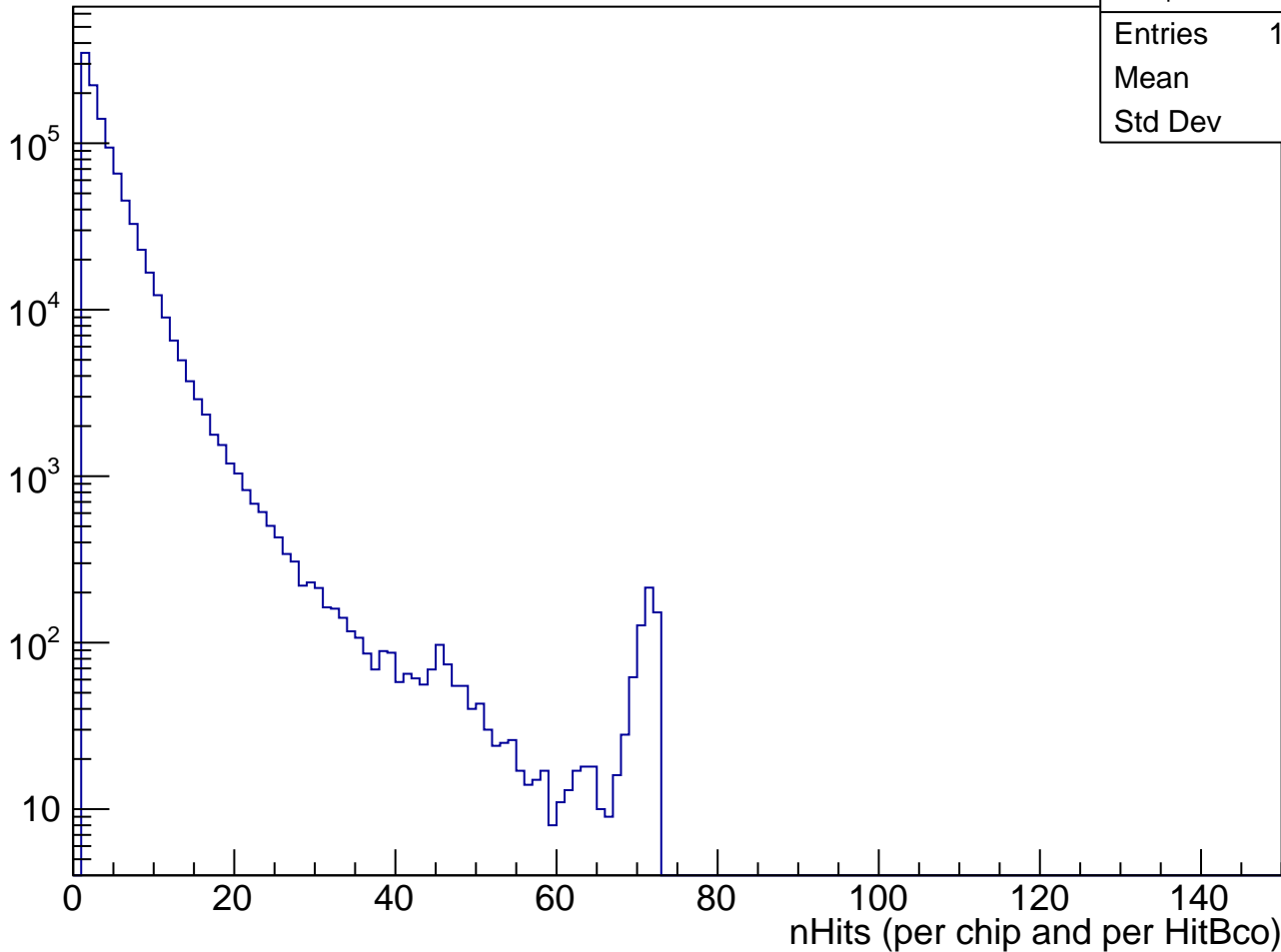


nChipHit_F4_Fch1_Chip25	
Entries	1023819
Mean	3.184
Std Dev	3.638

nHits (per chip and per HitBco)

nChipHit_F4_Fch1_Chip3

Entries



nChipHit_F4_Fch1_Chip3

Entries 1044202

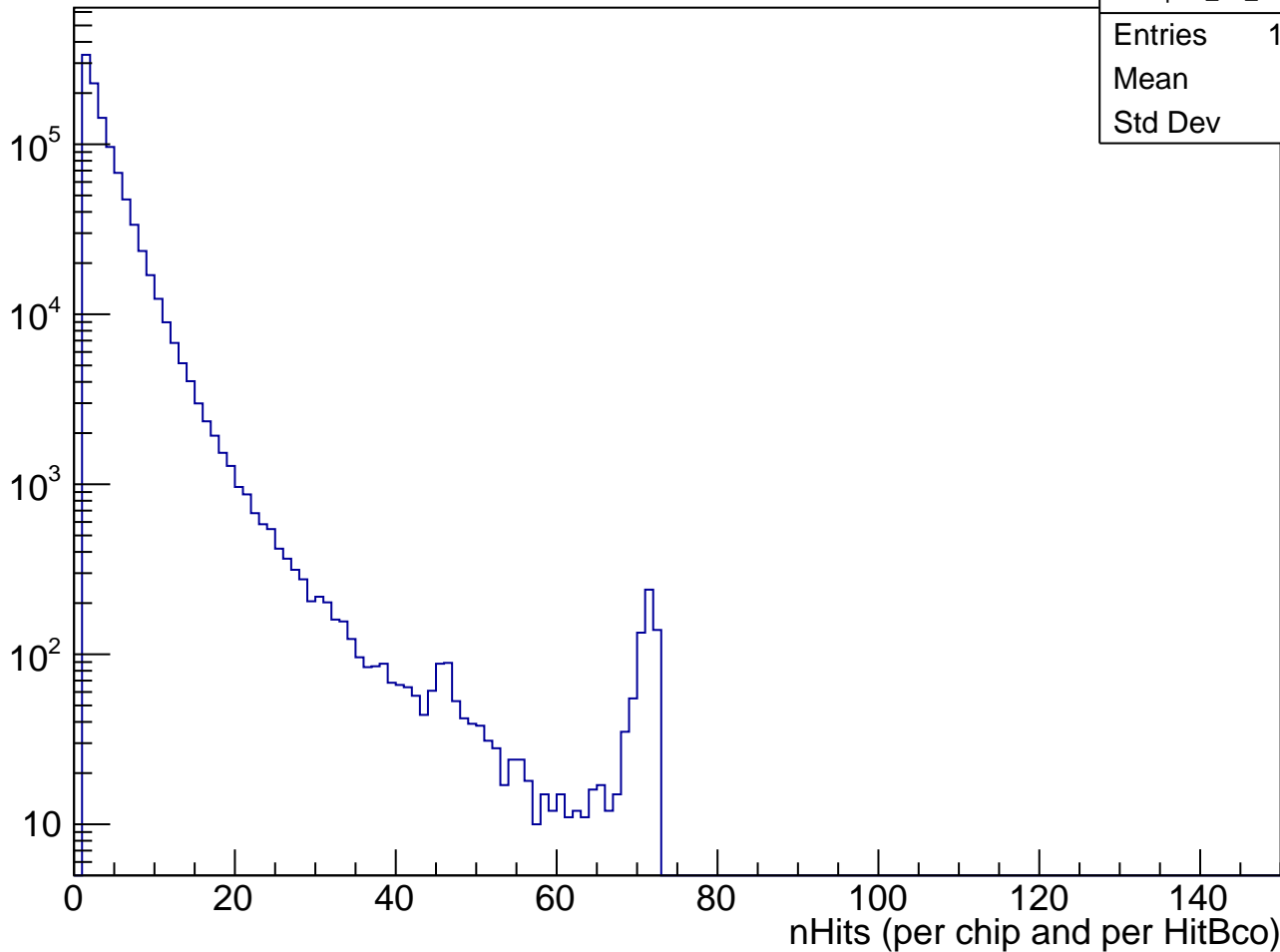
Mean 3.431

Std Dev 3.96

nHits (per chip and per HitBco)

nChipHit_F4_Fch1_Chip4

Entries



nChipHit_F4_Fch1_Chip4

Entries 1048327

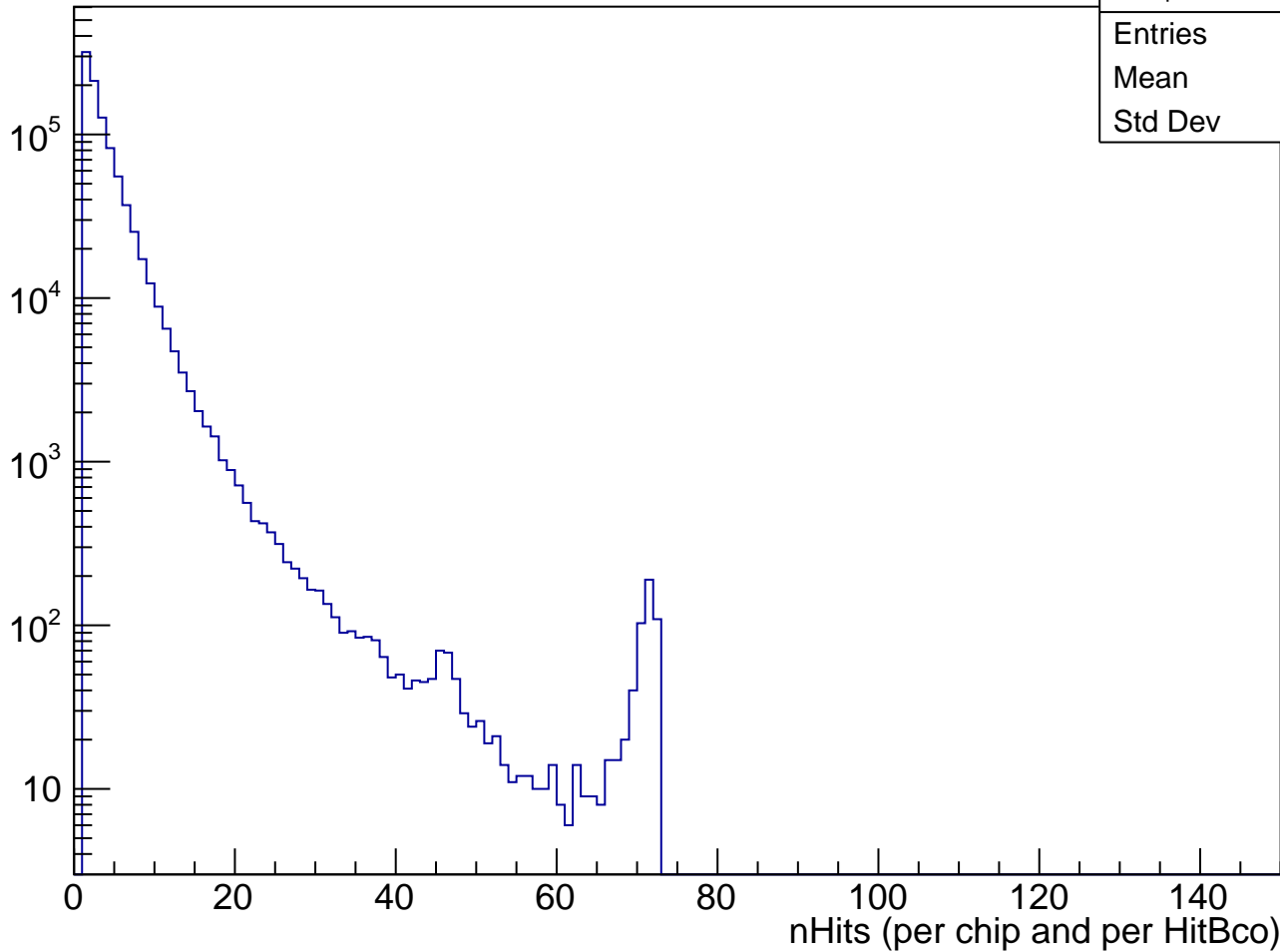
Mean 3.485

Std Dev 3.977

nHits (per chip and per HitBco)

nChipHit_F4_Fch1_Chip5

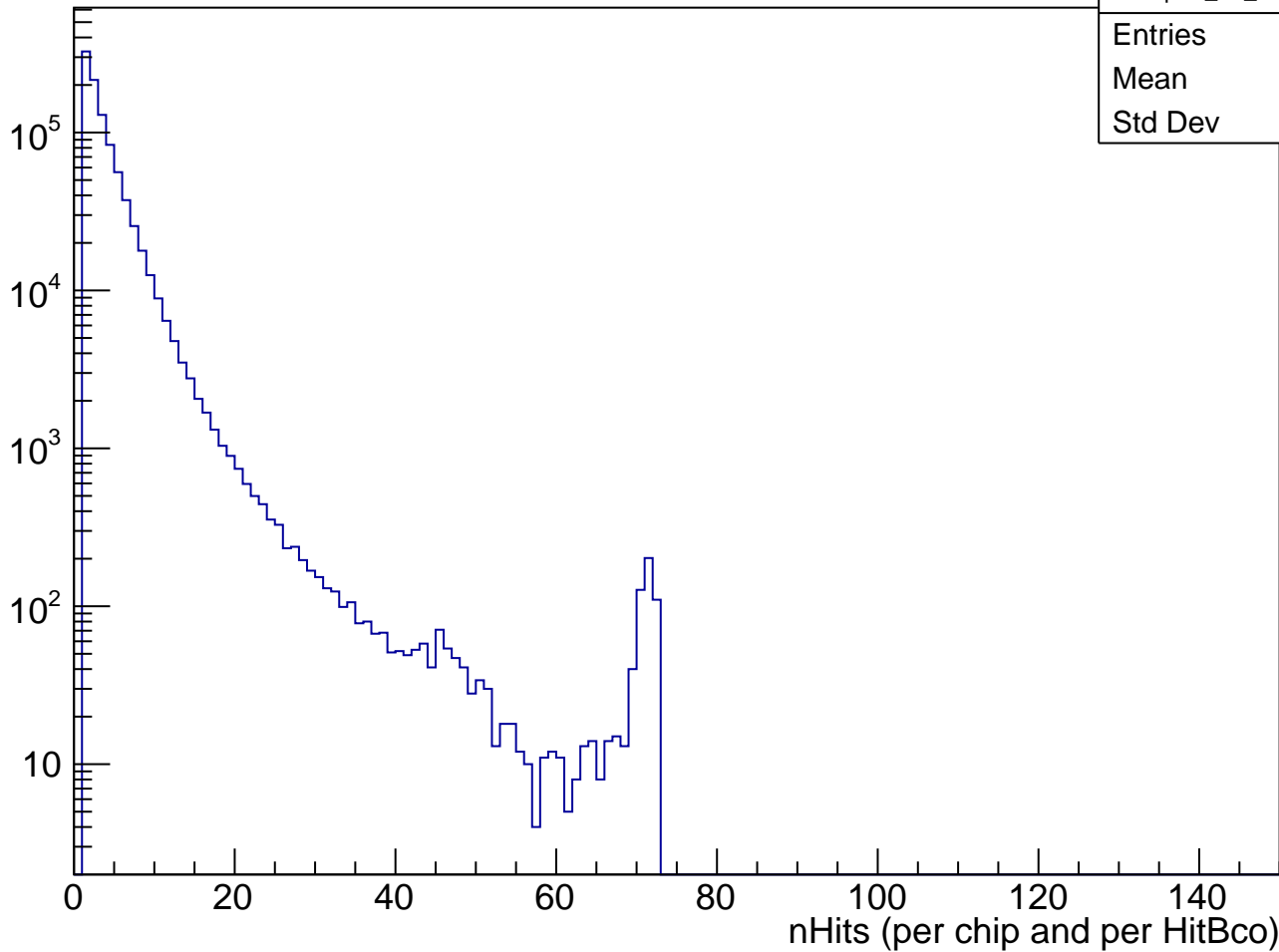
Entries



nChipHit_F4_Fch1_Chip5	
Entries	927017
Mean	3.214
Std Dev	3.689

nChipHit_F4_Fch1_Chip6

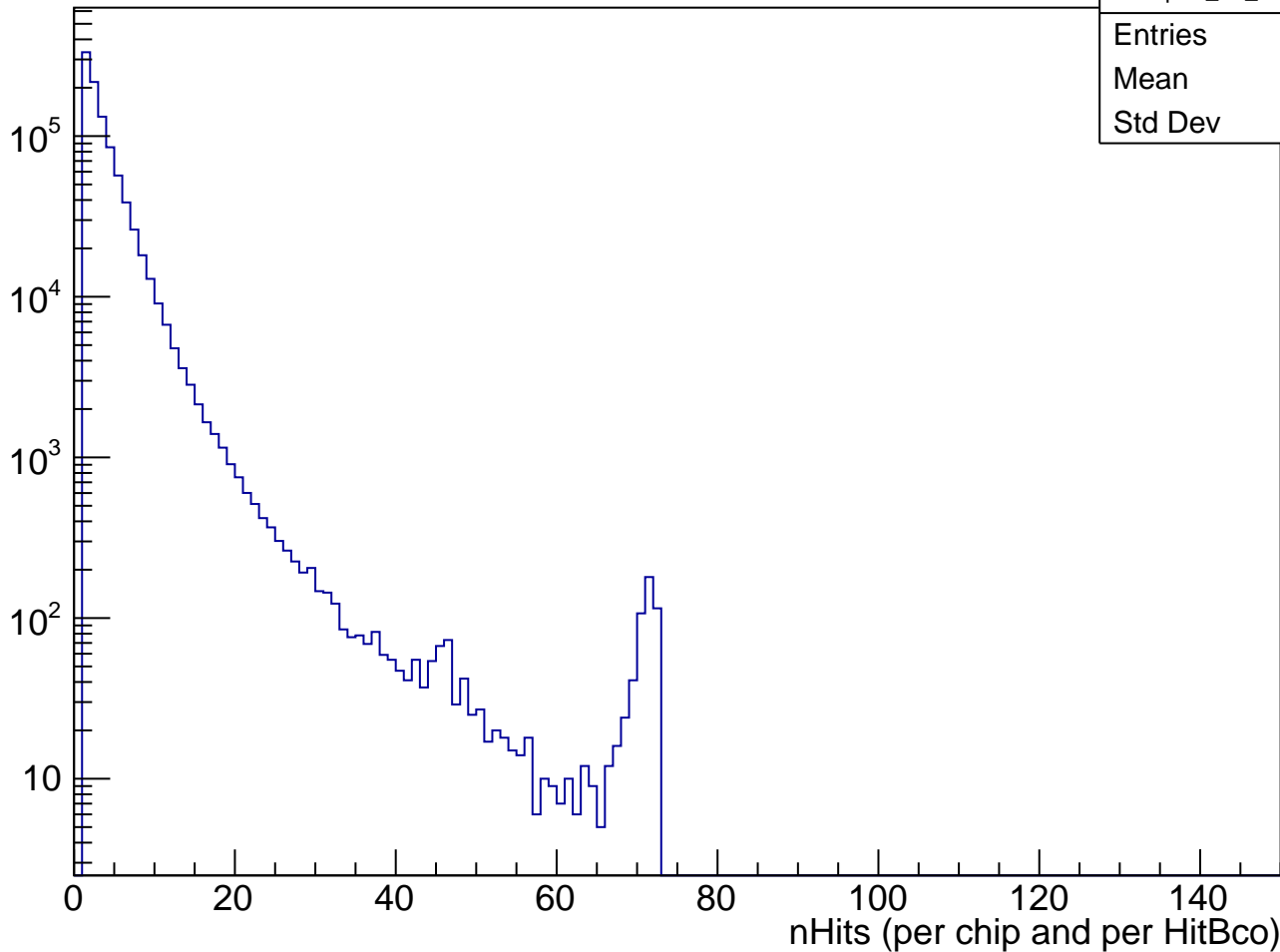
Entries



nChipHit_F4_Fch1_Chip6	
Entries	942993
Mean	3.209
Std Dev	3.708

nChipHit_F4_Fch1_Chip7

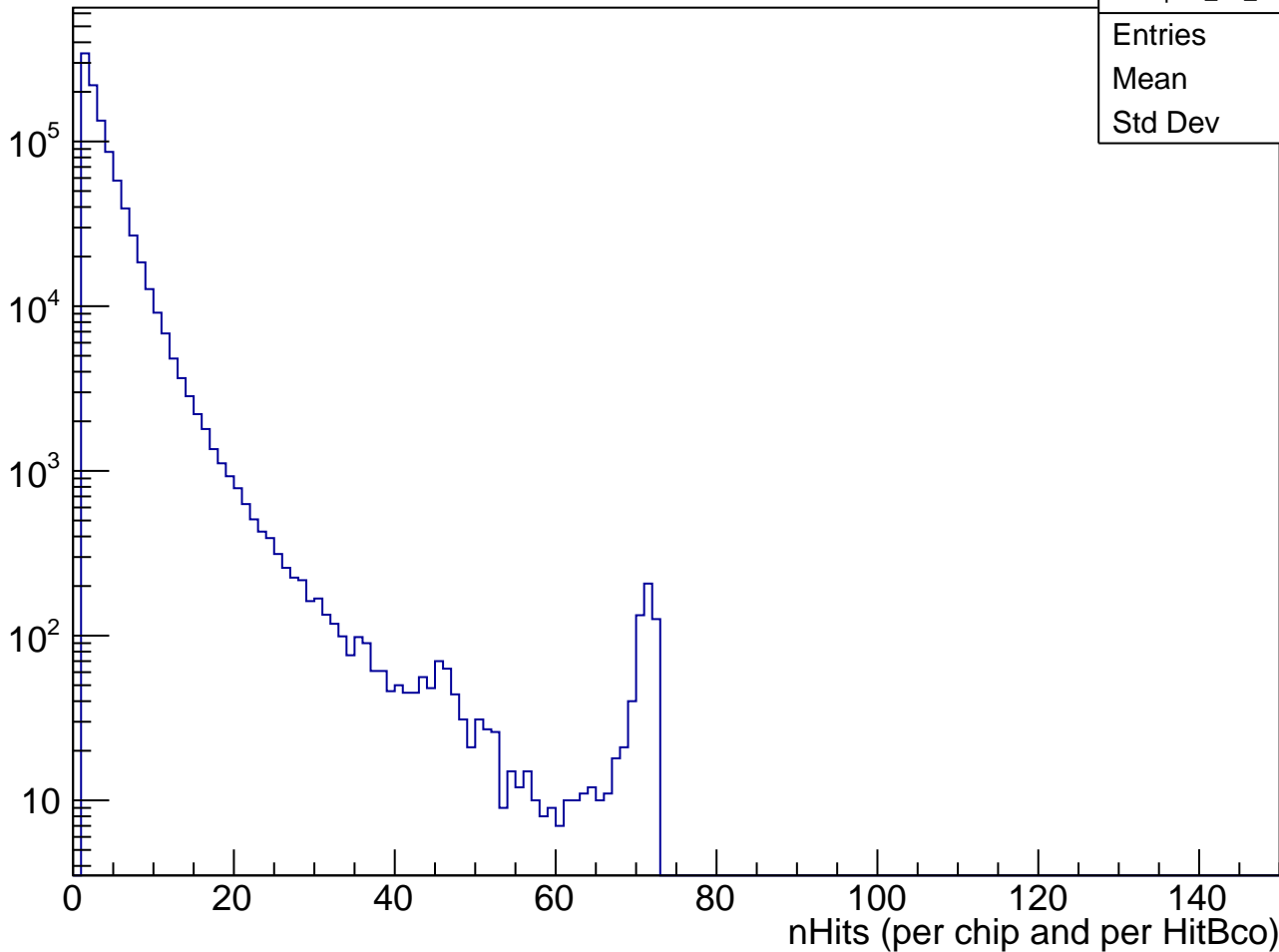
Entries



nChipHit_F4_Fch1_Chip7	
Entries	959890
Mean	3.209
Std Dev	3.668

nChipHit_F4_Fch1_Chip8

Entries



nChipHit_F4_Fch1_Chip8

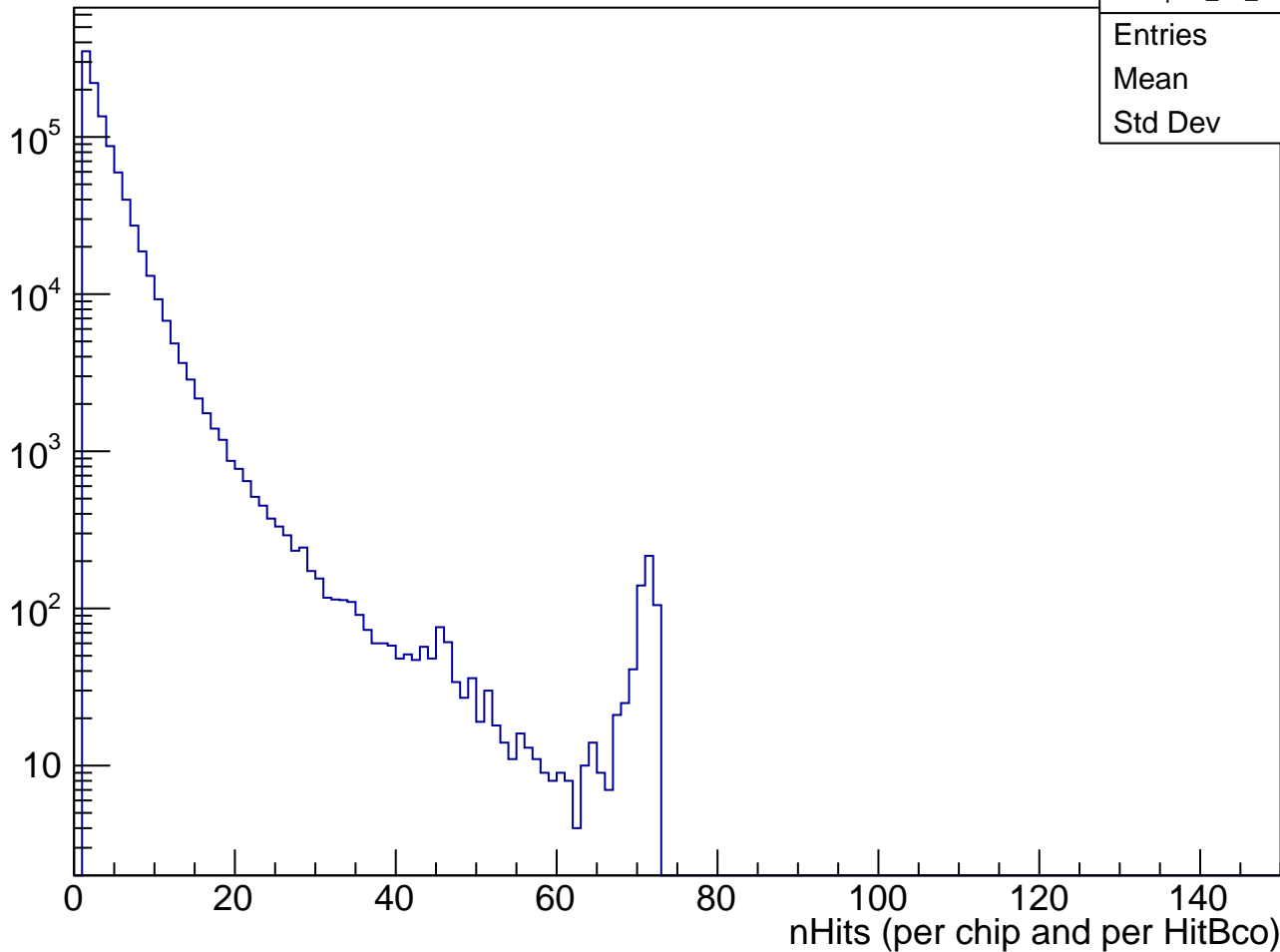
Entries 978228

Mean 3.202

Std Dev 3.702

nChipHit_F4_Fch1_Chip9

Entries

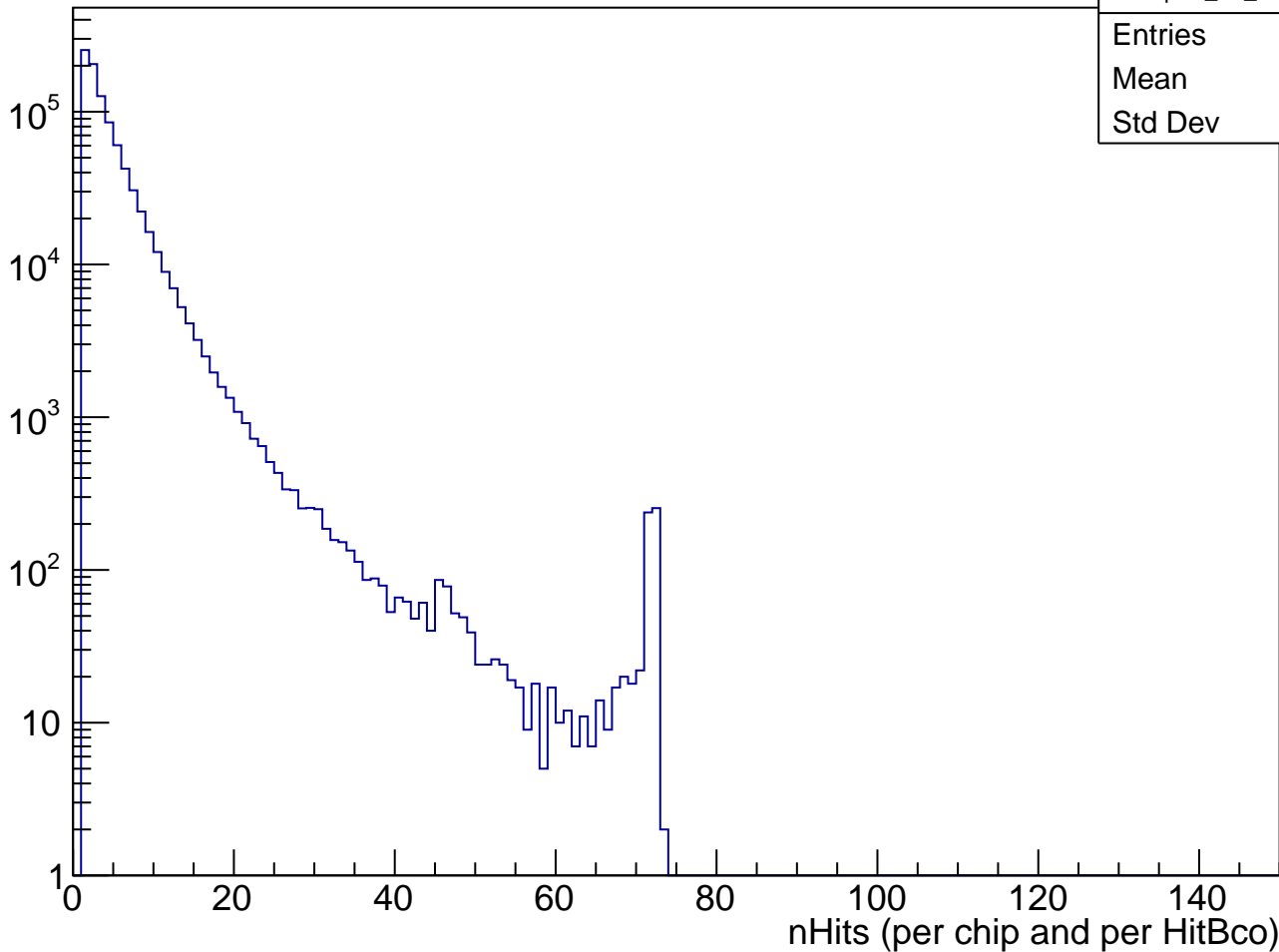


nChipHit_F4_Fch1_Chip9

Entries	993574
Mean	3.195
Std Dev	3.688

nChipHit_F4_Fch2_Chip0

Entries



nChipHit_F4_Fch2_Chip0

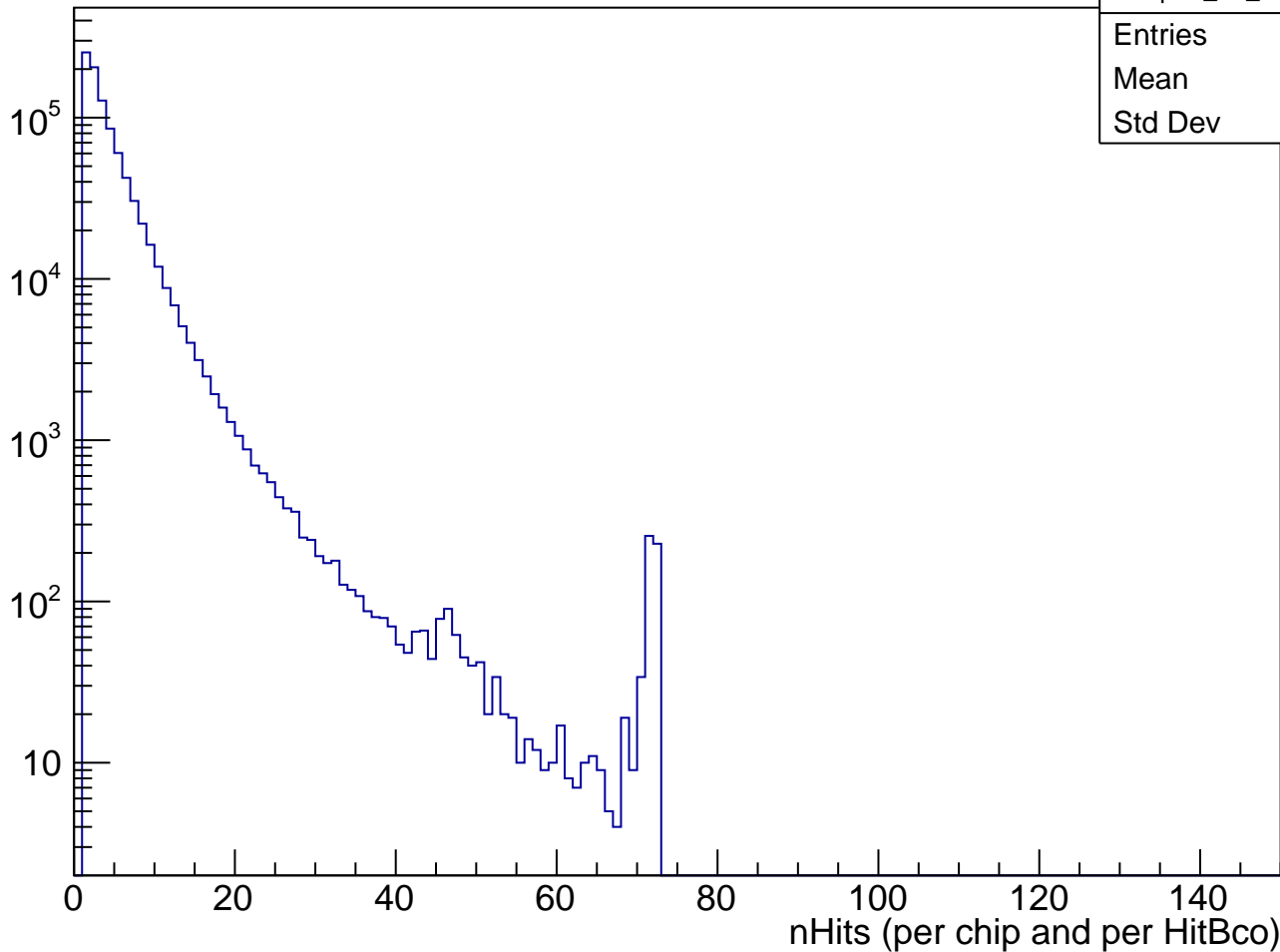
Entries 898100

Mean 3.712

Std Dev 4.176

nChipHit_F4_Fch2_Chip1

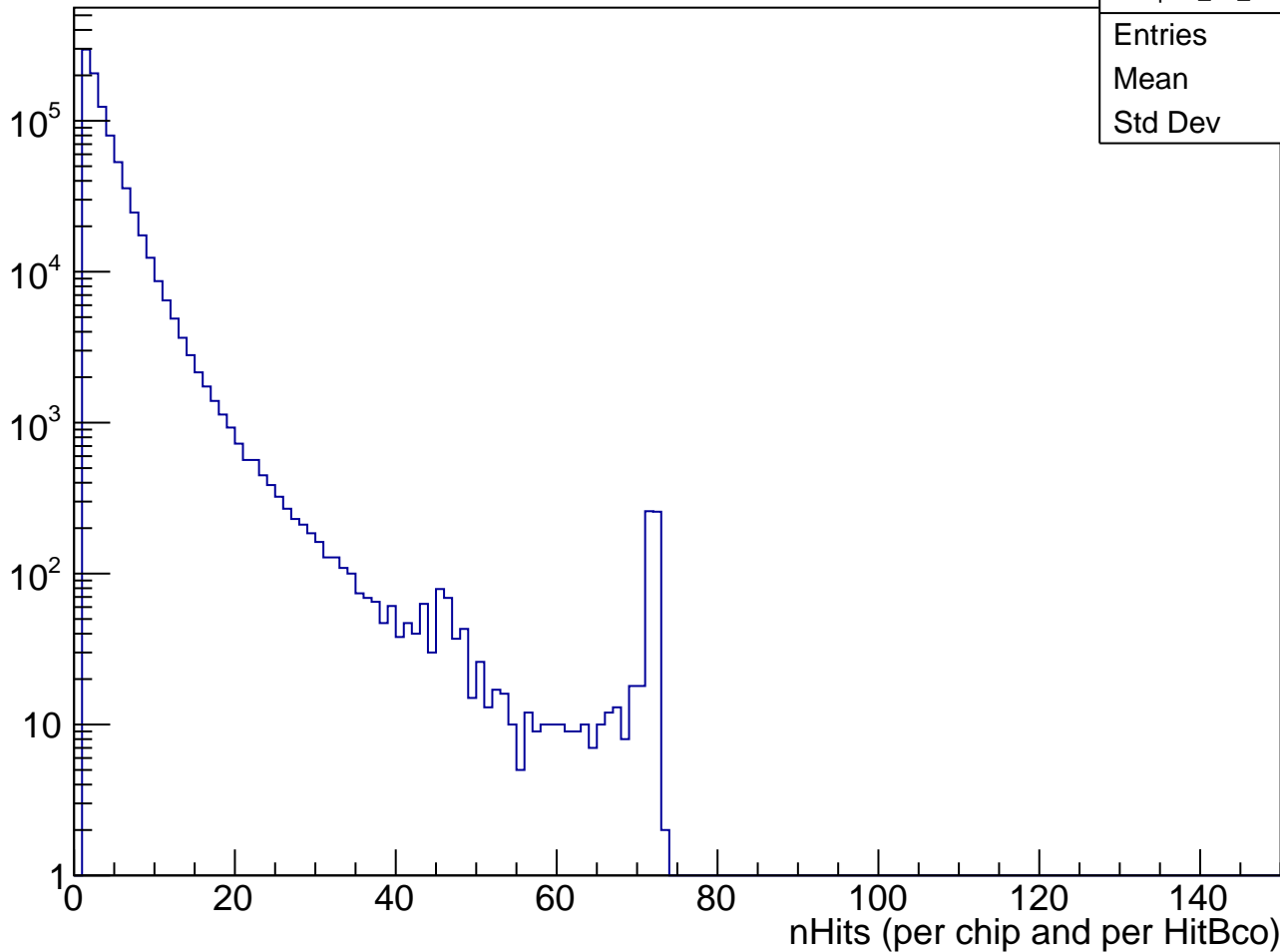
Entries



nChipHit_F4_Fch2_Chip1	
Entries	898946
Mean	3.695
Std Dev	4.147

nChipHit_F4_Fch2_Chip10

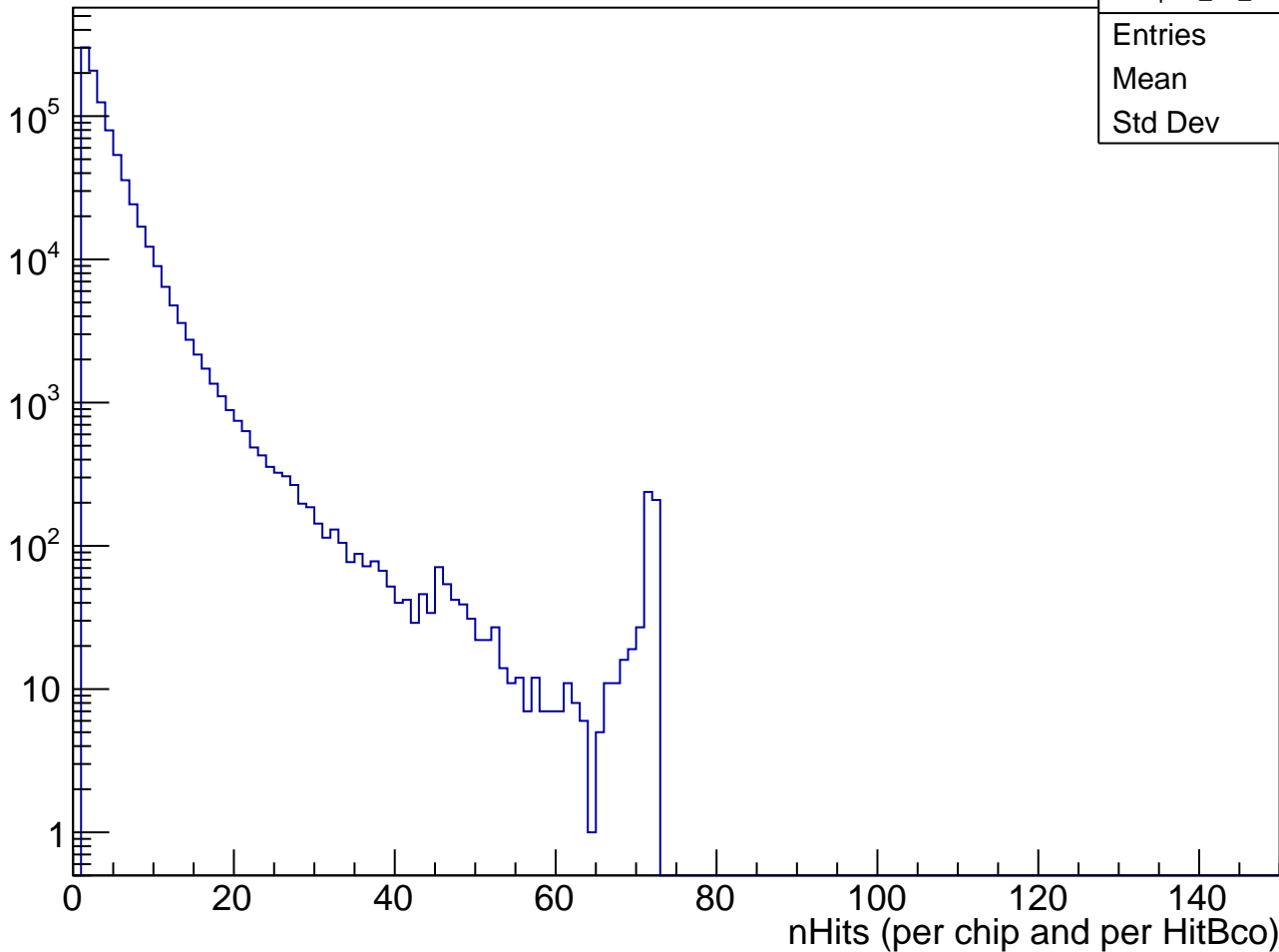
Entries



nChipHit_F4_Fch2_Chip10	
Entries	889810
Mean	3.286
Std Dev	3.833

nChipHit_F4_Fch2_Chip11

Entries



nChipHit_F4_Fch2_Chip11

Entries 896308

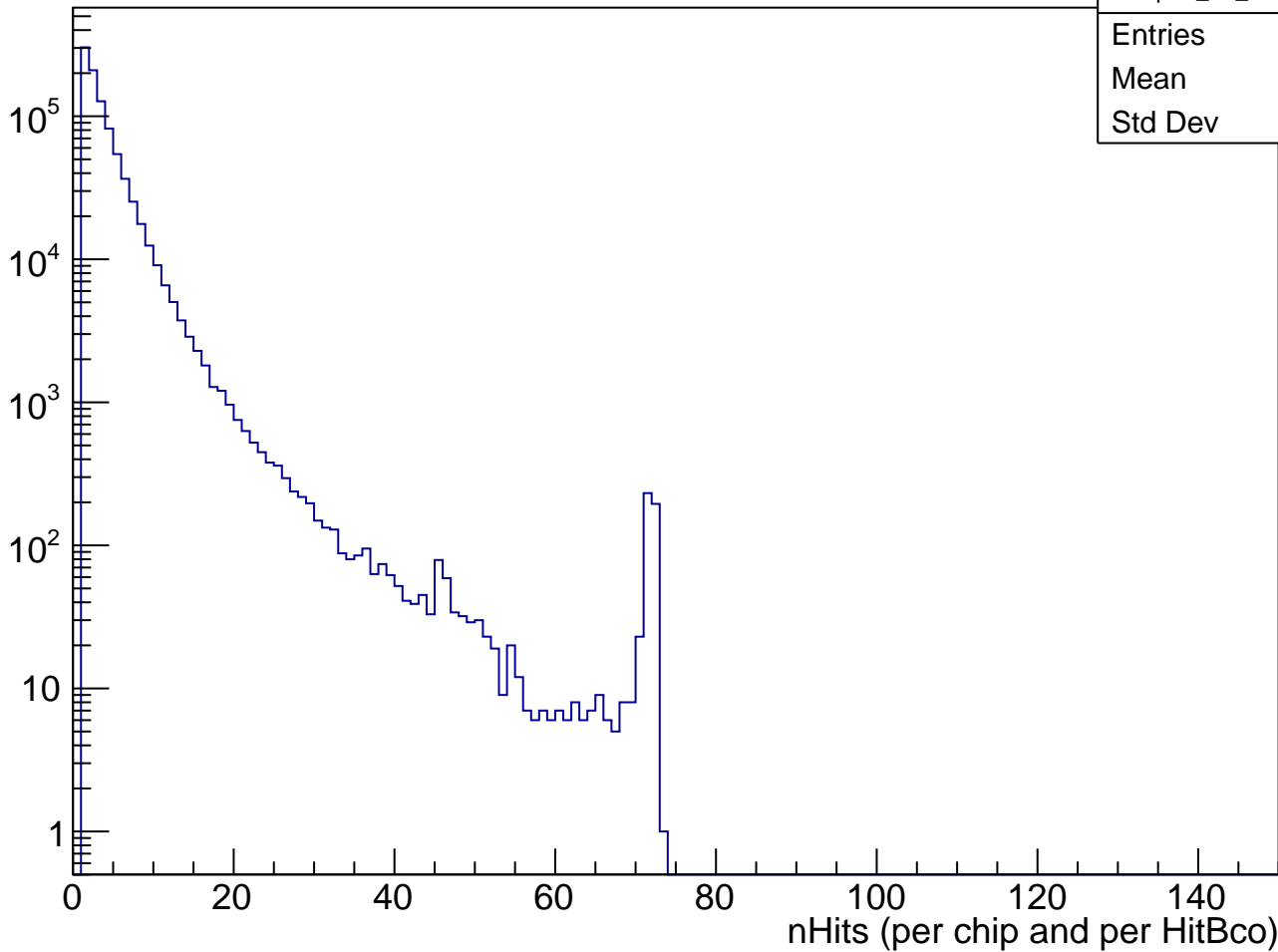
Mean 3.257

Std Dev 3.765

nHits (per chip and per HitBco)

nChipHit_F4_Fch2_Chip12

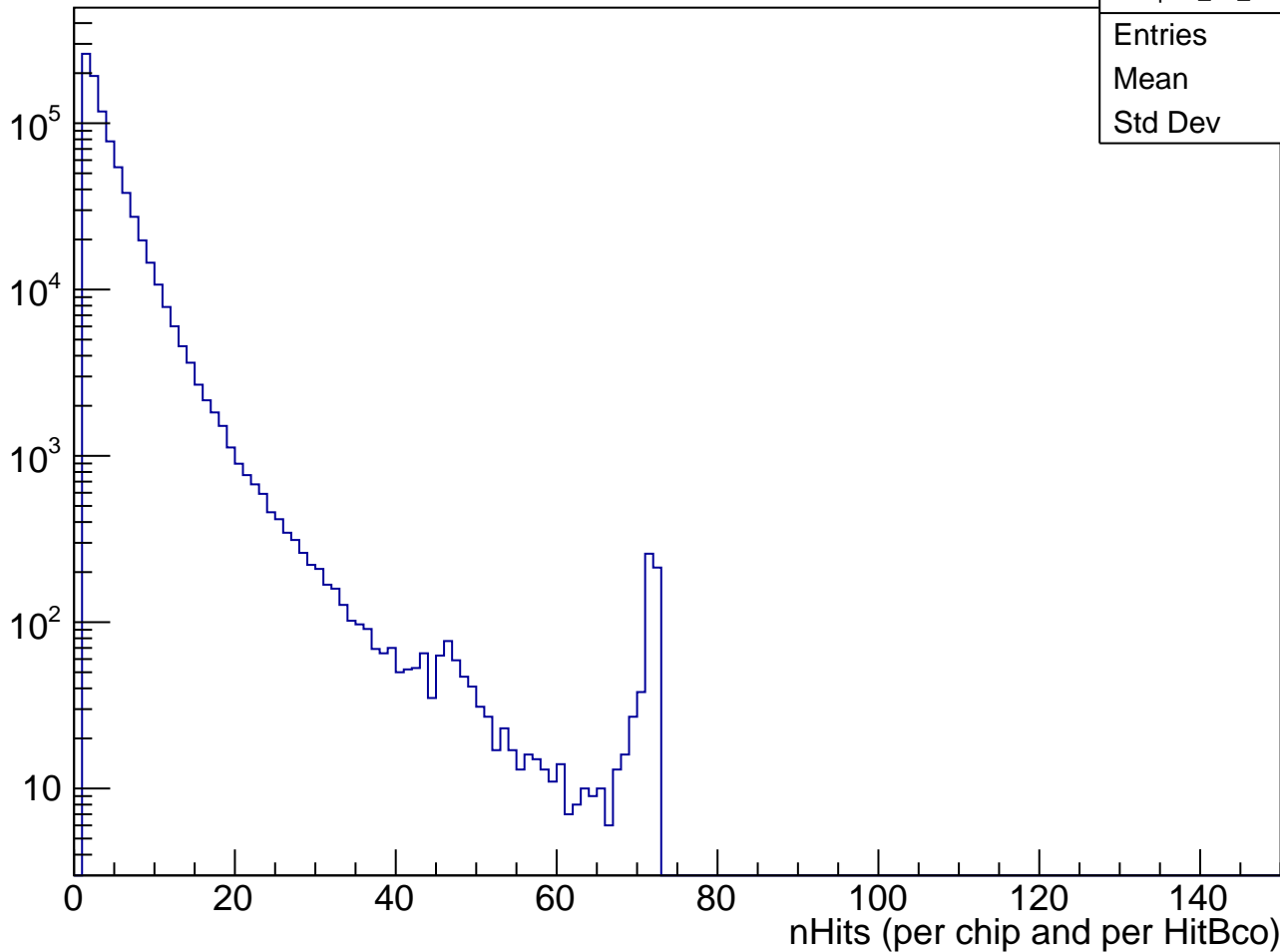
Entries



nChipHit_F4_Fch2_Chip12	
Entries	909258
Mean	3.278
Std Dev	3.746

nChipHit_F4_Fch2_Chip13

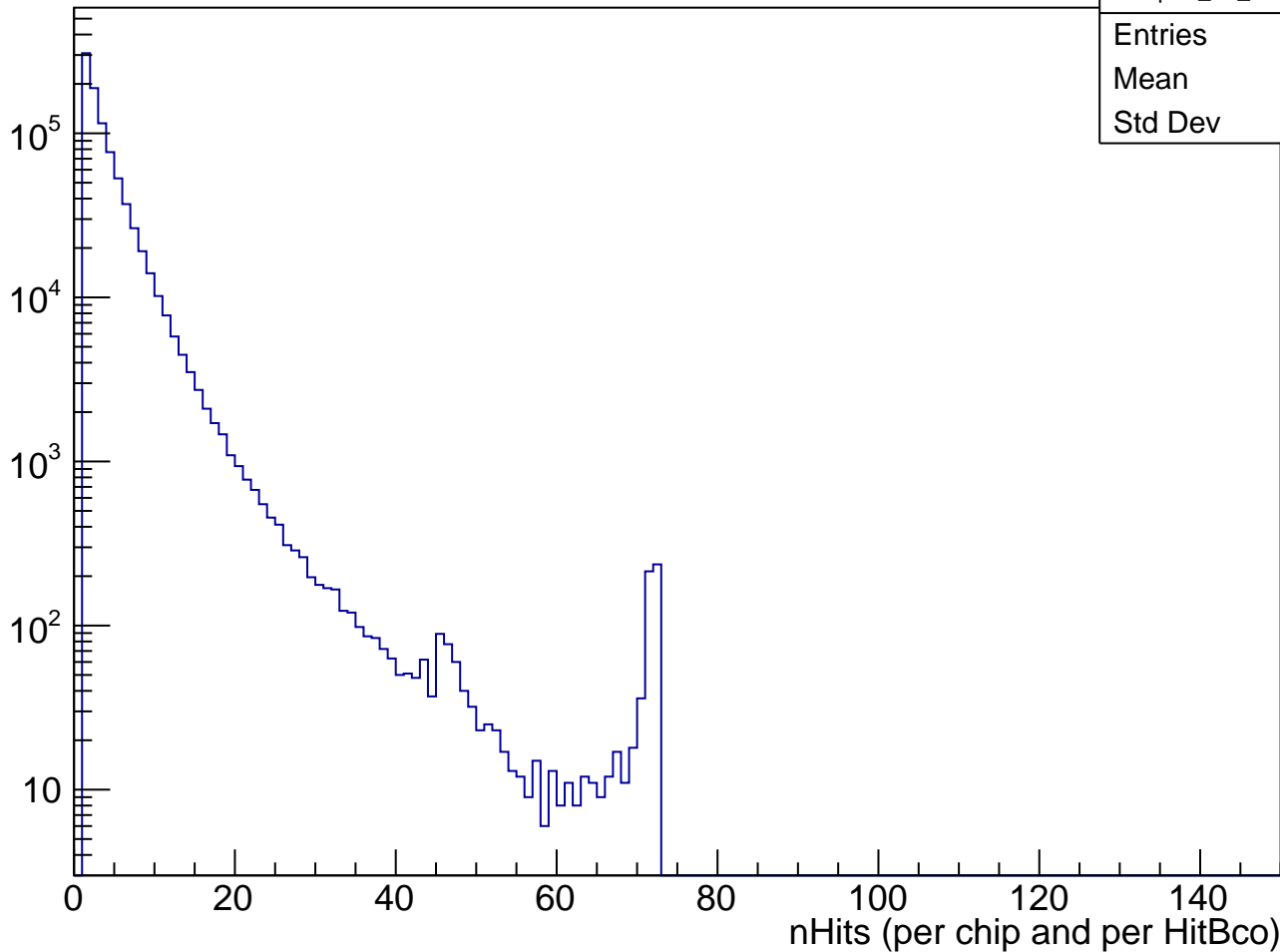
Entries



nChipHit_F4_Fch2_Chip13	
Entries	852843
Mean	3.579
Std Dev	4.136

nChipHit_F4_Fch2_Chip14

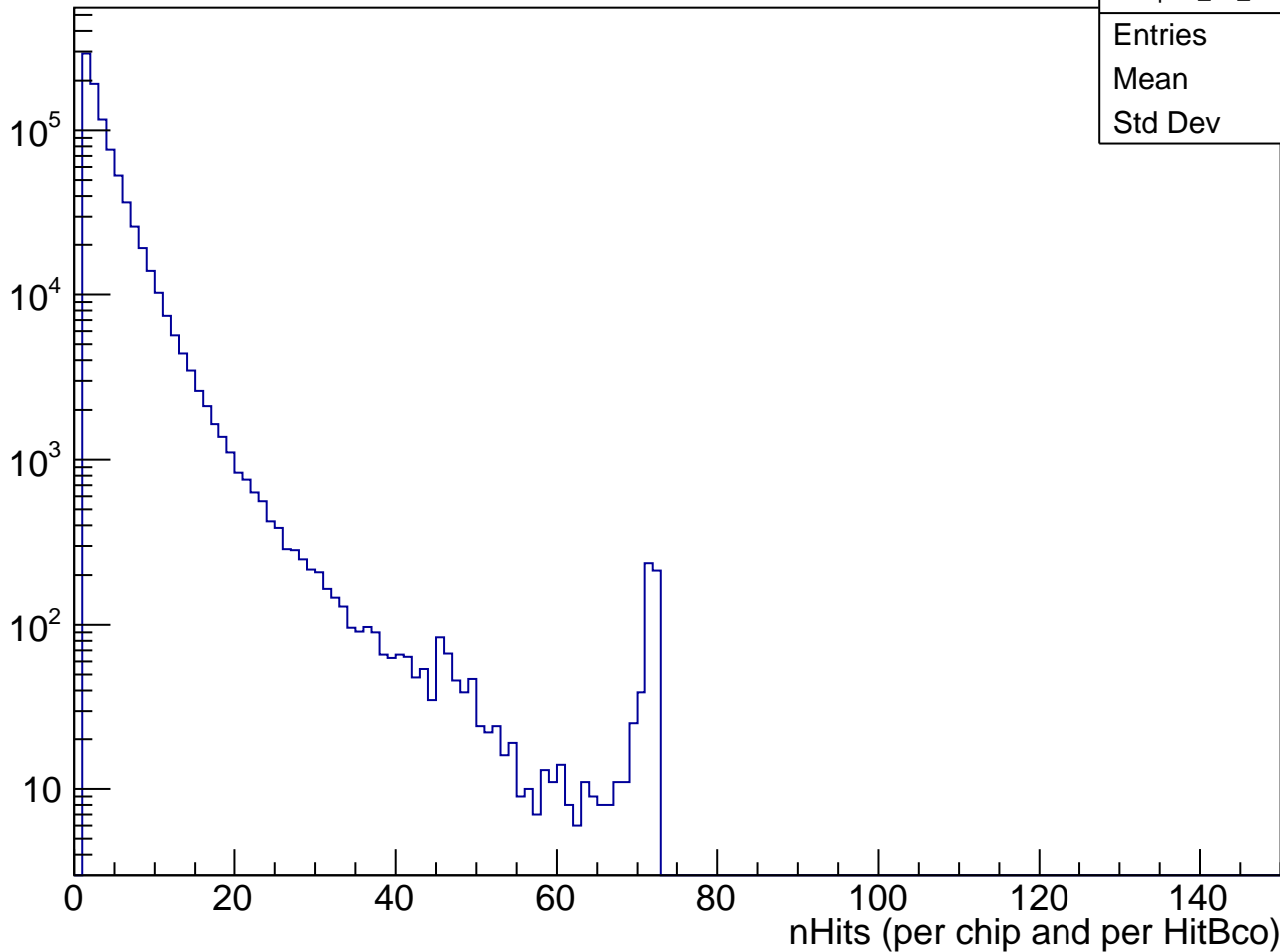
Entries



nChipHit_F4_Fch2_Chip14	
Entries	886103
Mean	3.419
Std Dev	4.043

nChipHit_F4_Fch2_Chip15

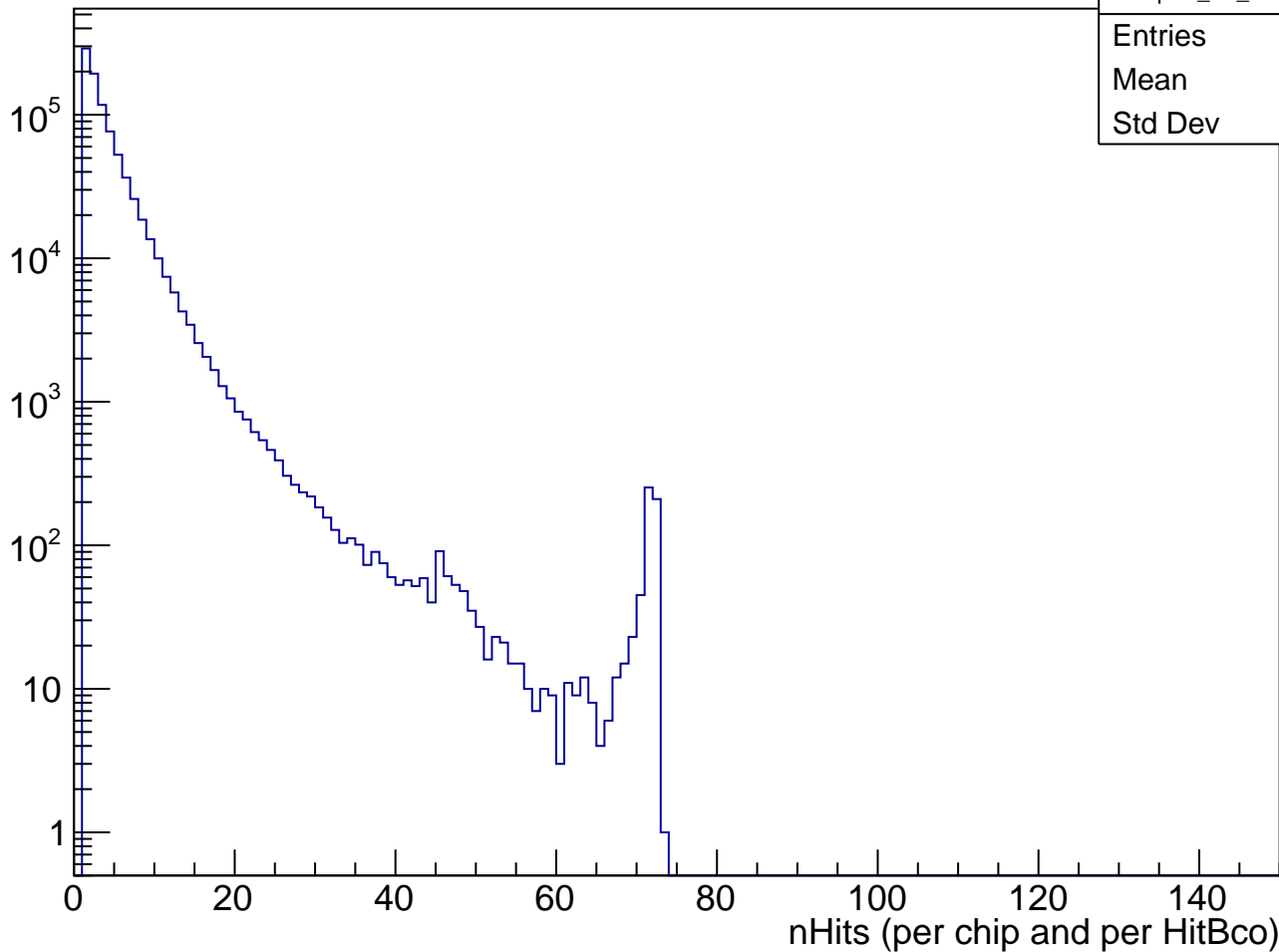
Entries



nChipHit_F4_Fch2_Chip15	
Entries	872114
Mean	3.441
Std Dev	4.035

nChipHit_F4_Fch2_Chip16

Entries

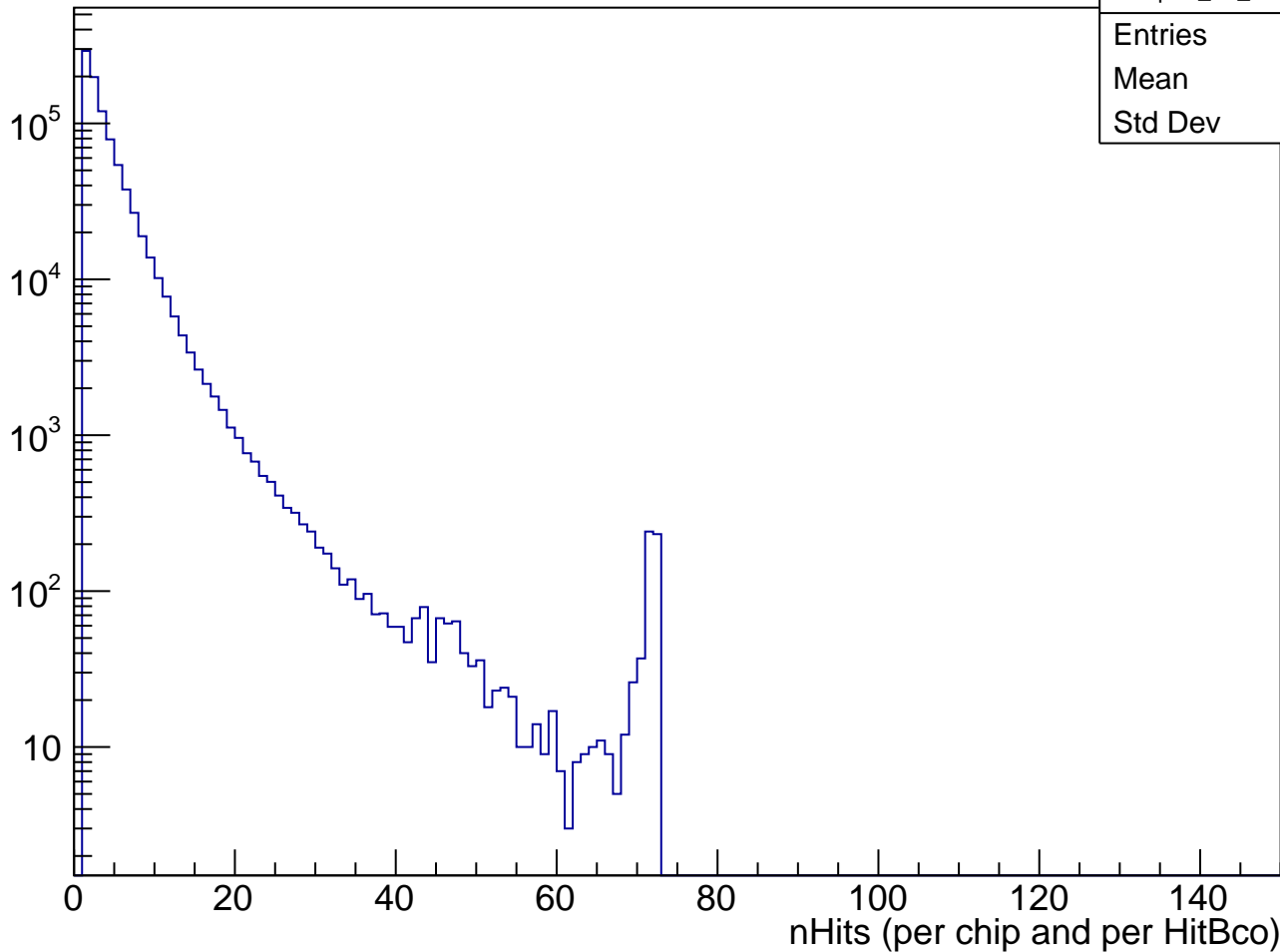


nChipHit_F4_Fch2_Chip16	
Entries	870874
Mean	3.429
Std Dev	4.027

nHits (per chip and per HitBco)

nChipHit_F4_Fch2_Chip17

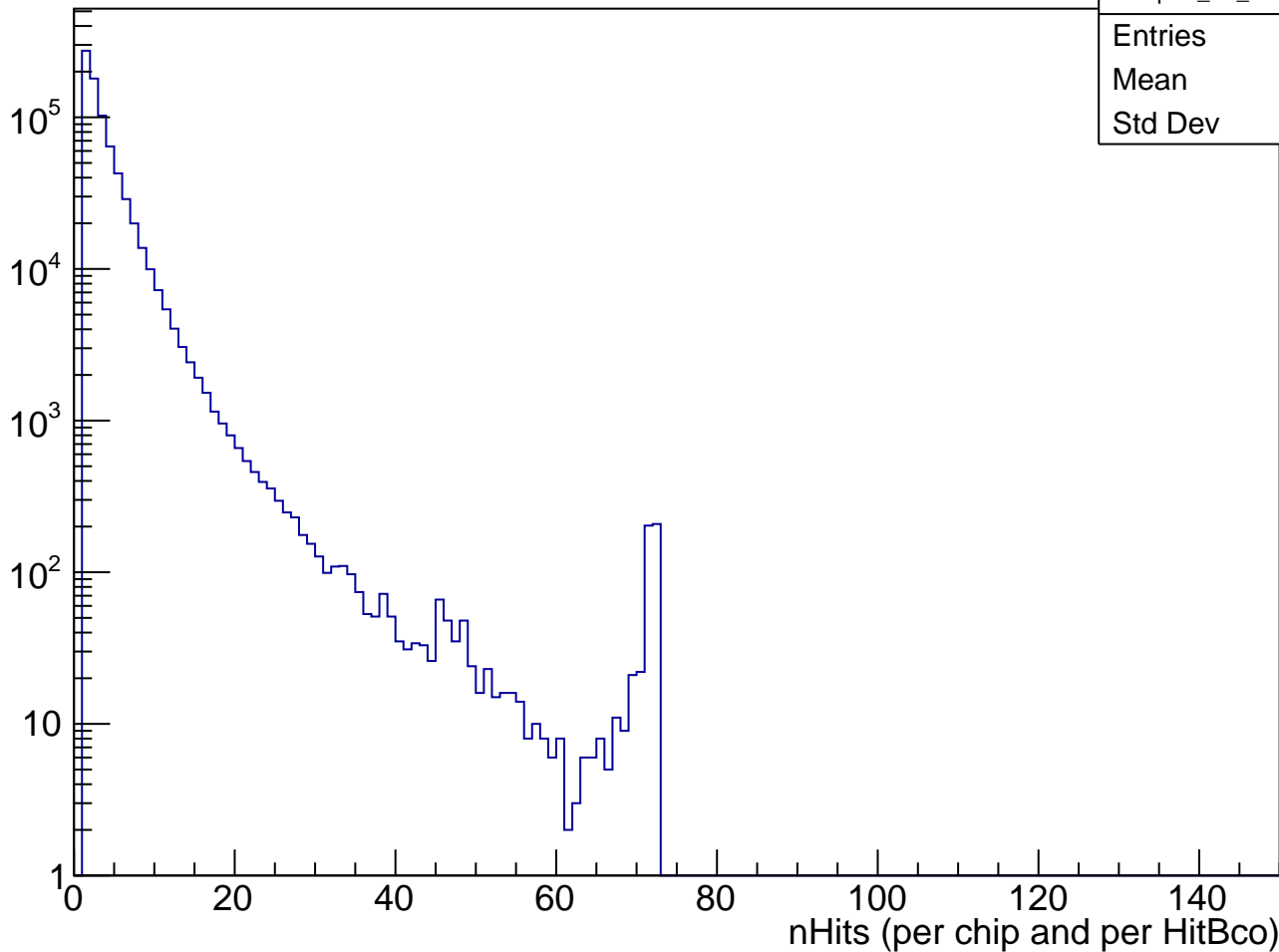
Entries



nChipHit_F4_Fch2_Chip17	
Entries	887834
Mean	3.453
Std Dev	4.05

nChipHit_F4_Fch2_Chip18

Entries



nChipHit_F4_Fch2_Chip18

Entries 770907

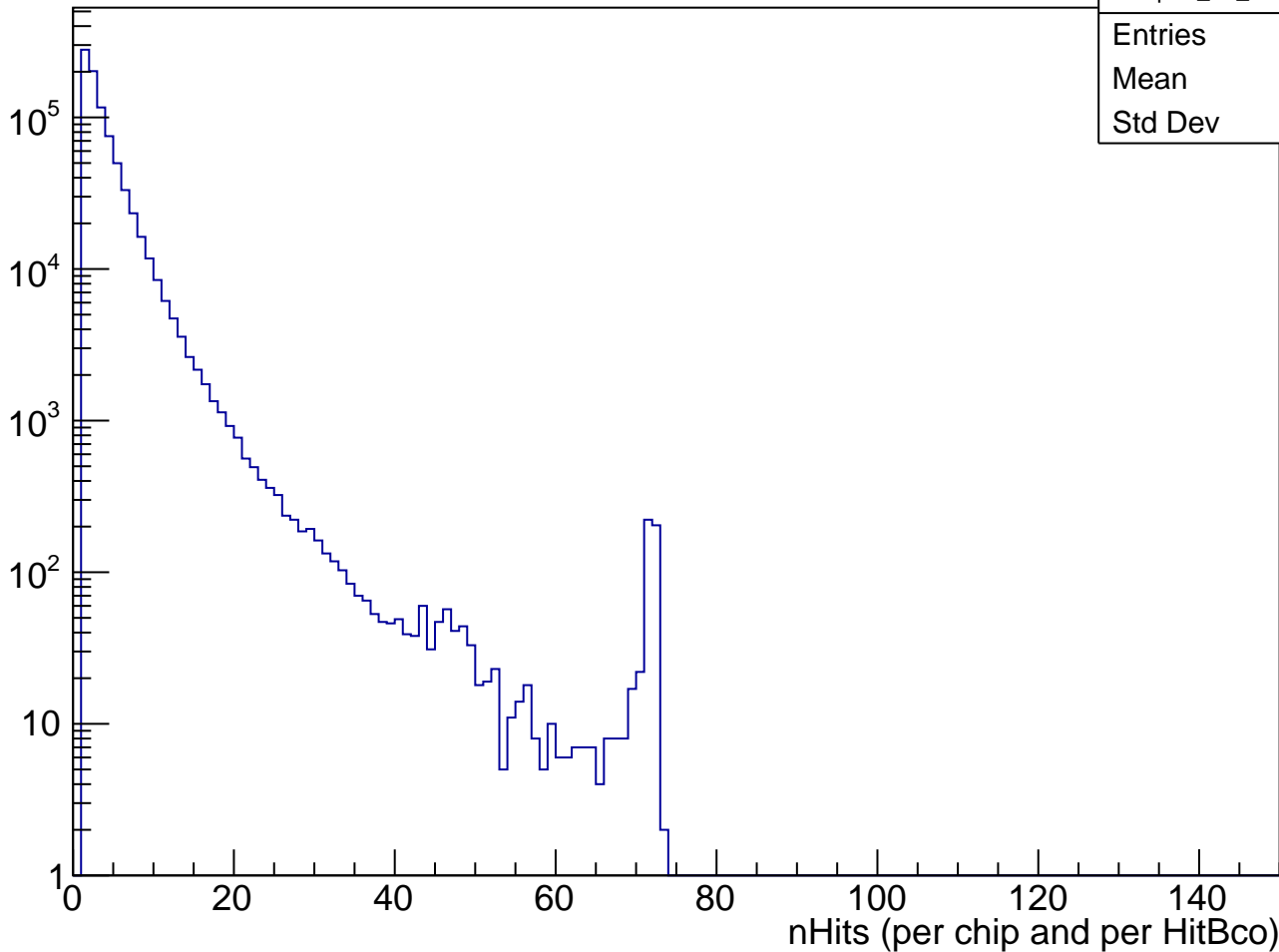
Mean 3.185

Std Dev 3.809

nHits (per chip and per HitBco)

nChipHit_F4_Fch2_Chip19

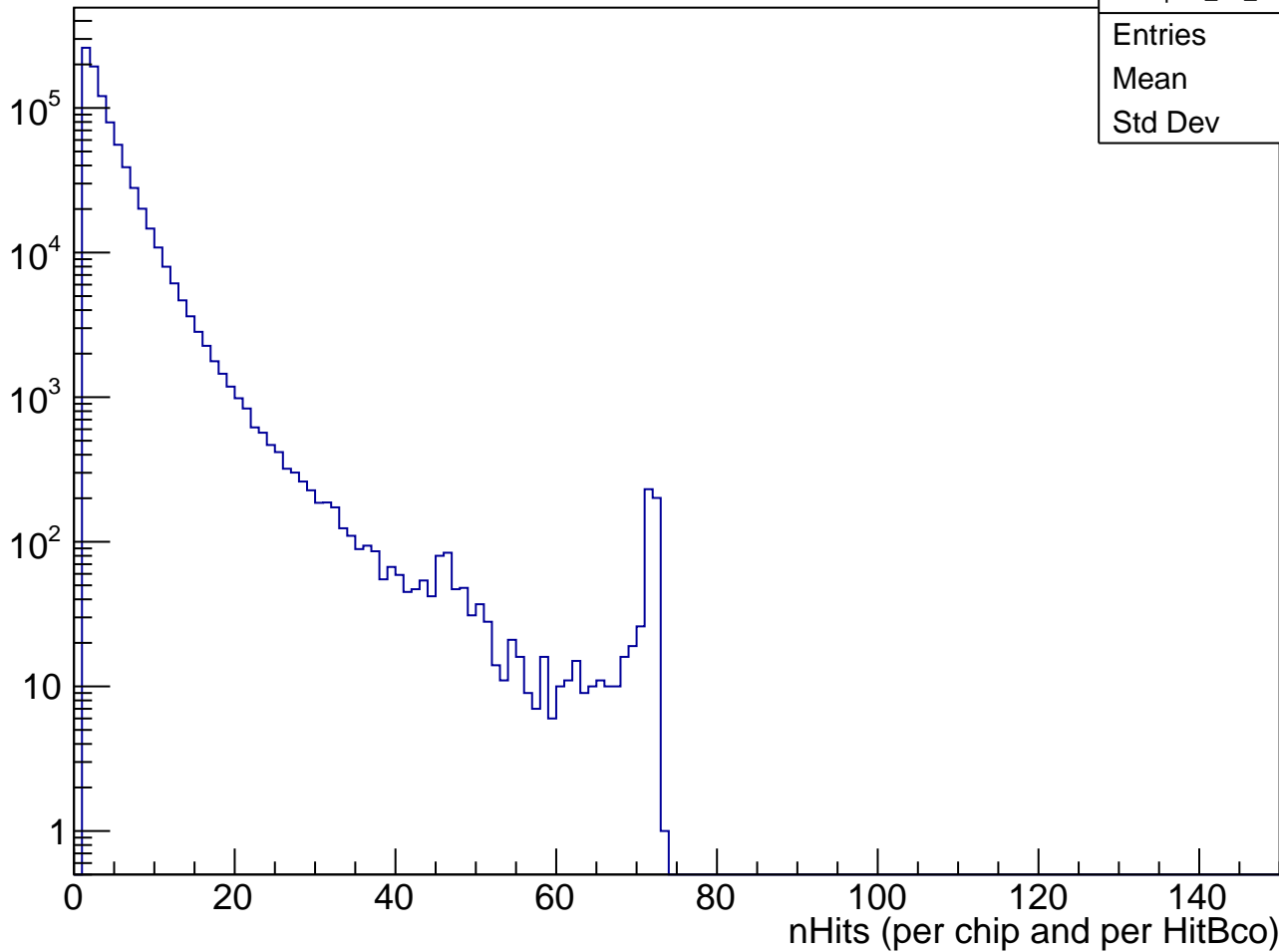
Entries



nChipHit_F4_Fch2_Chip19	
Entries	845883
Mean	3.28
Std Dev	3.788

nChipHit_F4_Fch2_Chip2

Entries



nChipHit_F4_Fch2_Chip2

Entries 862052

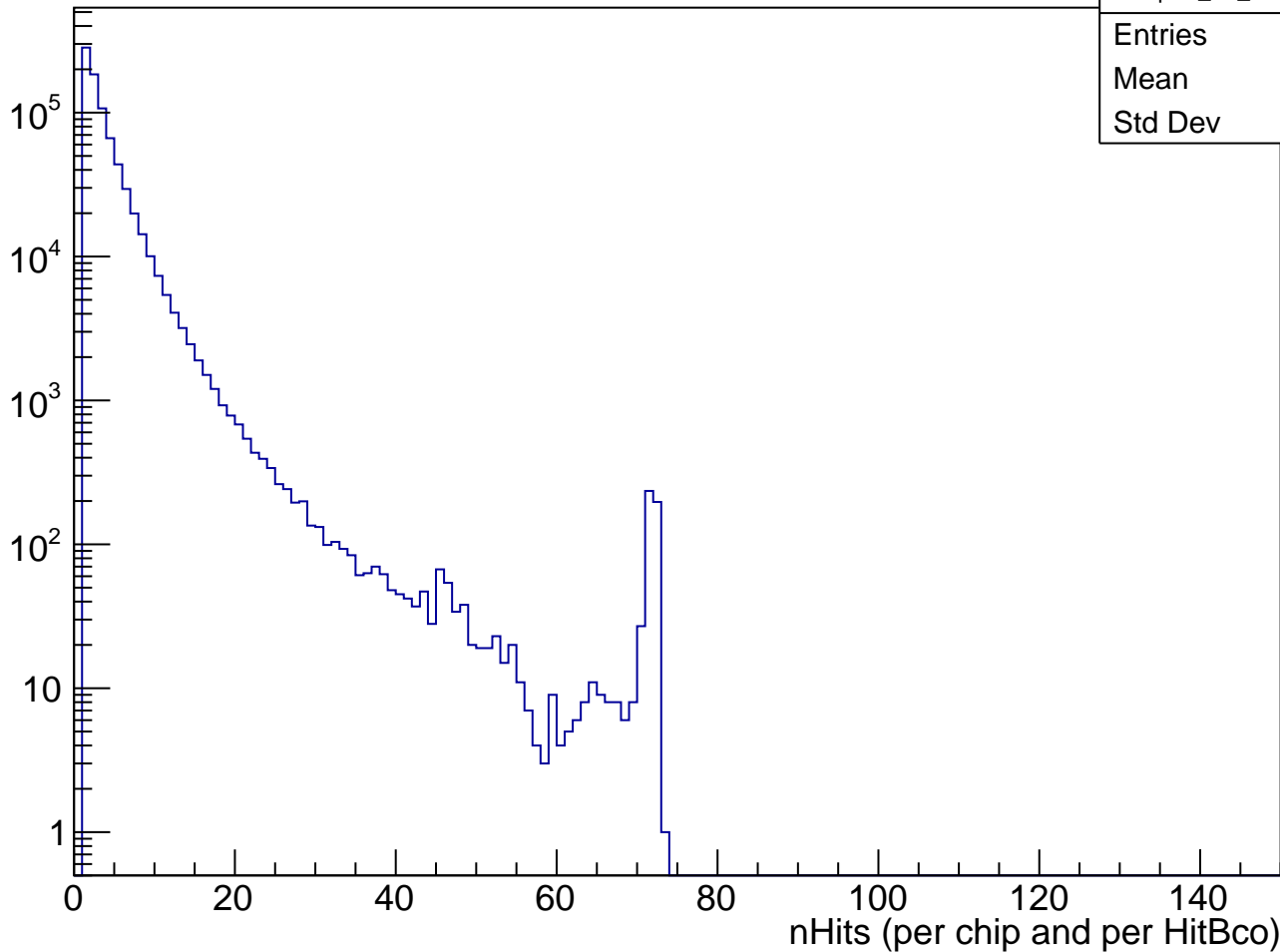
Mean 3.591

Std Dev 4.086

nHits (per chip and per HitBco)

nChipHit_F4_Fch2_Chip20

Entries

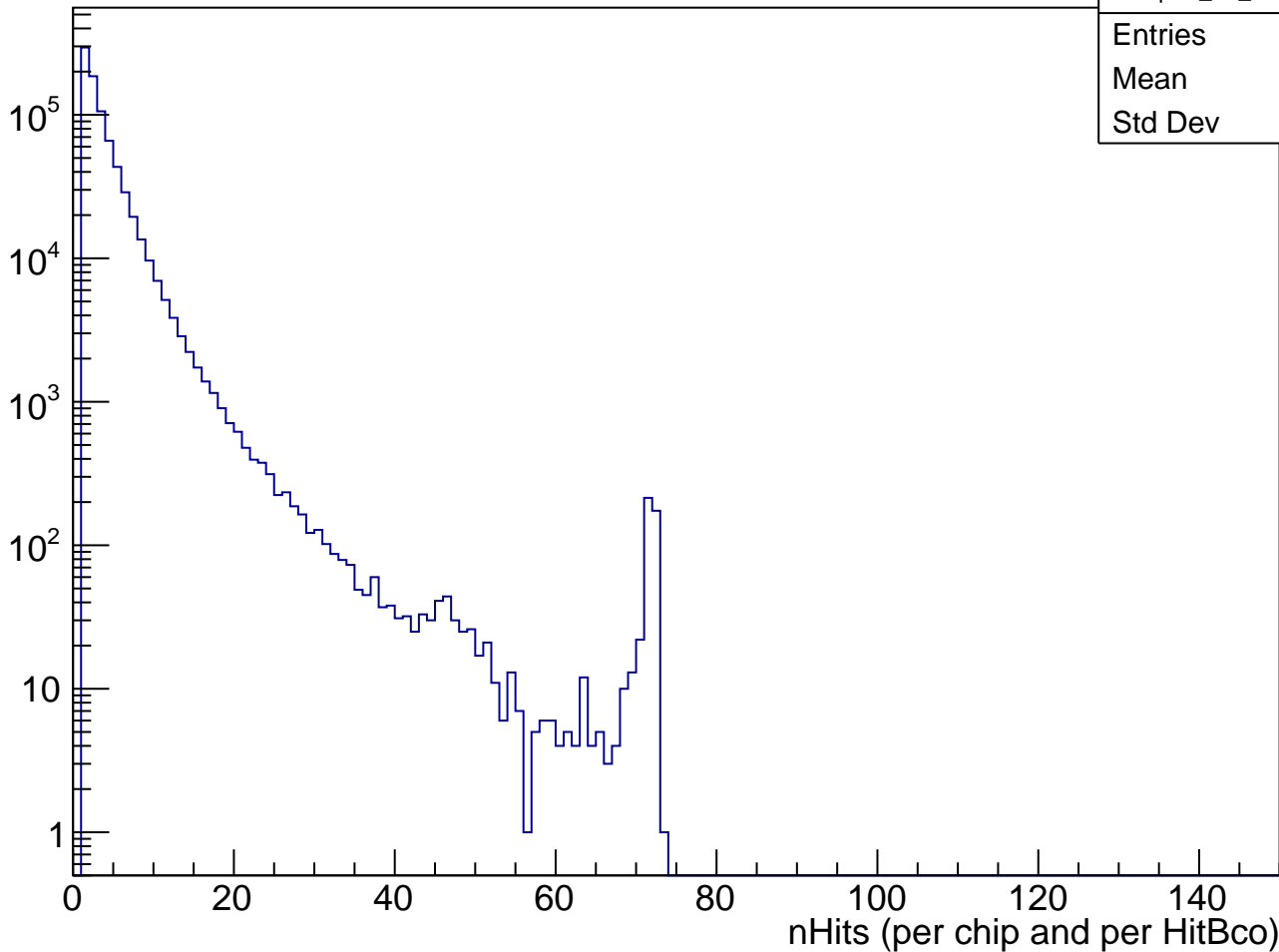


nChipHit_F4_Fch2_Chip20	
Entries	792614
Mean	3.167
Std Dev	3.779

nHits (per chip and per HitBco)

nChipHit_F4_Fch2_Chip21

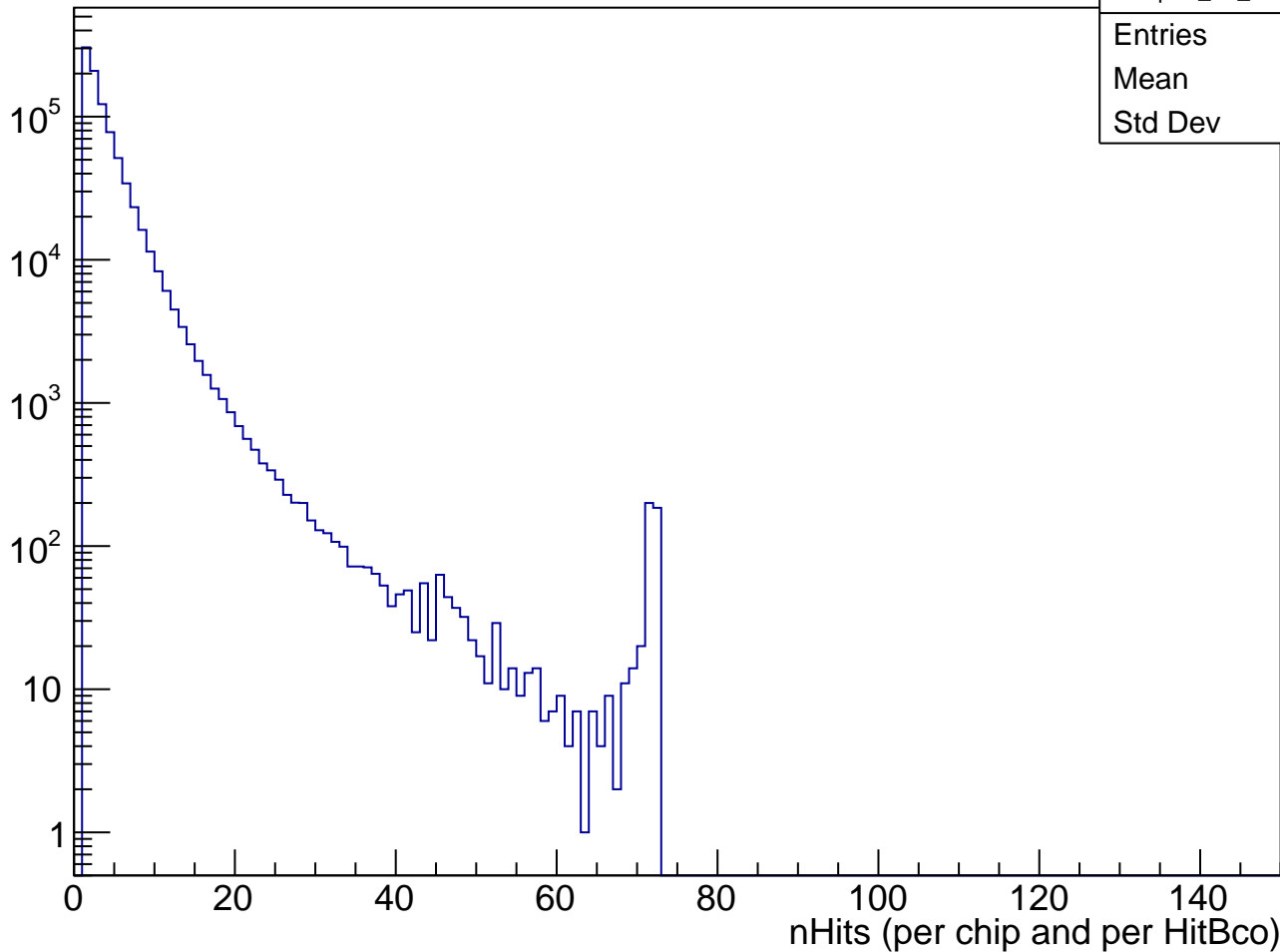
Entries



nChipHit_F4_Fch2_Chip21	
Entries	798585
Mean	3.073
Std Dev	3.605

nChipHit_F4_Fch2_Chip22

Entries

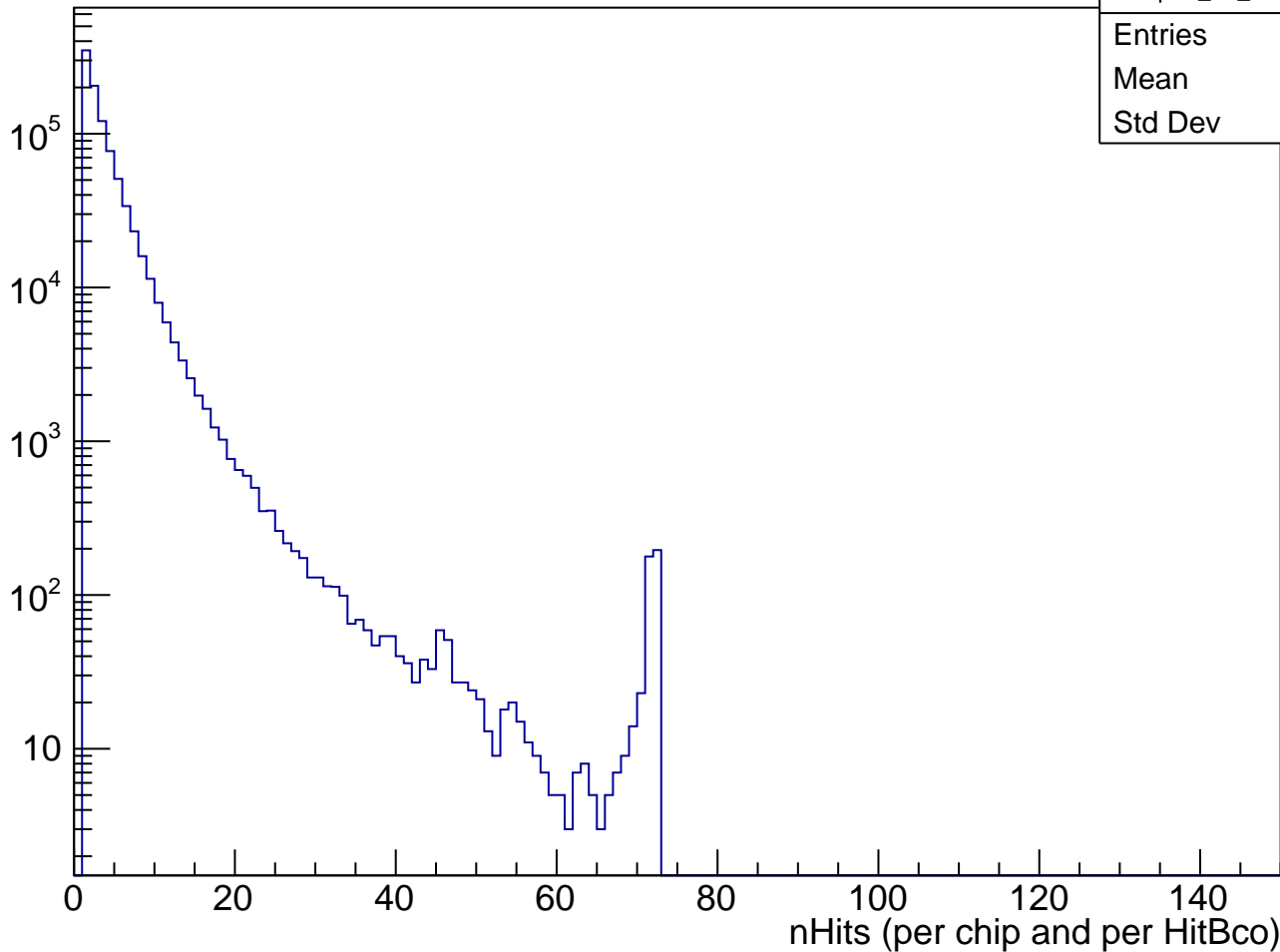


nChipHit_F4_Fch2_Chip22	
Entries	887083
Mean	3.179
Std Dev	3.623

nHits (per chip and per HitBco)

nChipHit_F4_Fch2_Chip23

Entries



nChipHit_F4_Fch2_Chip23

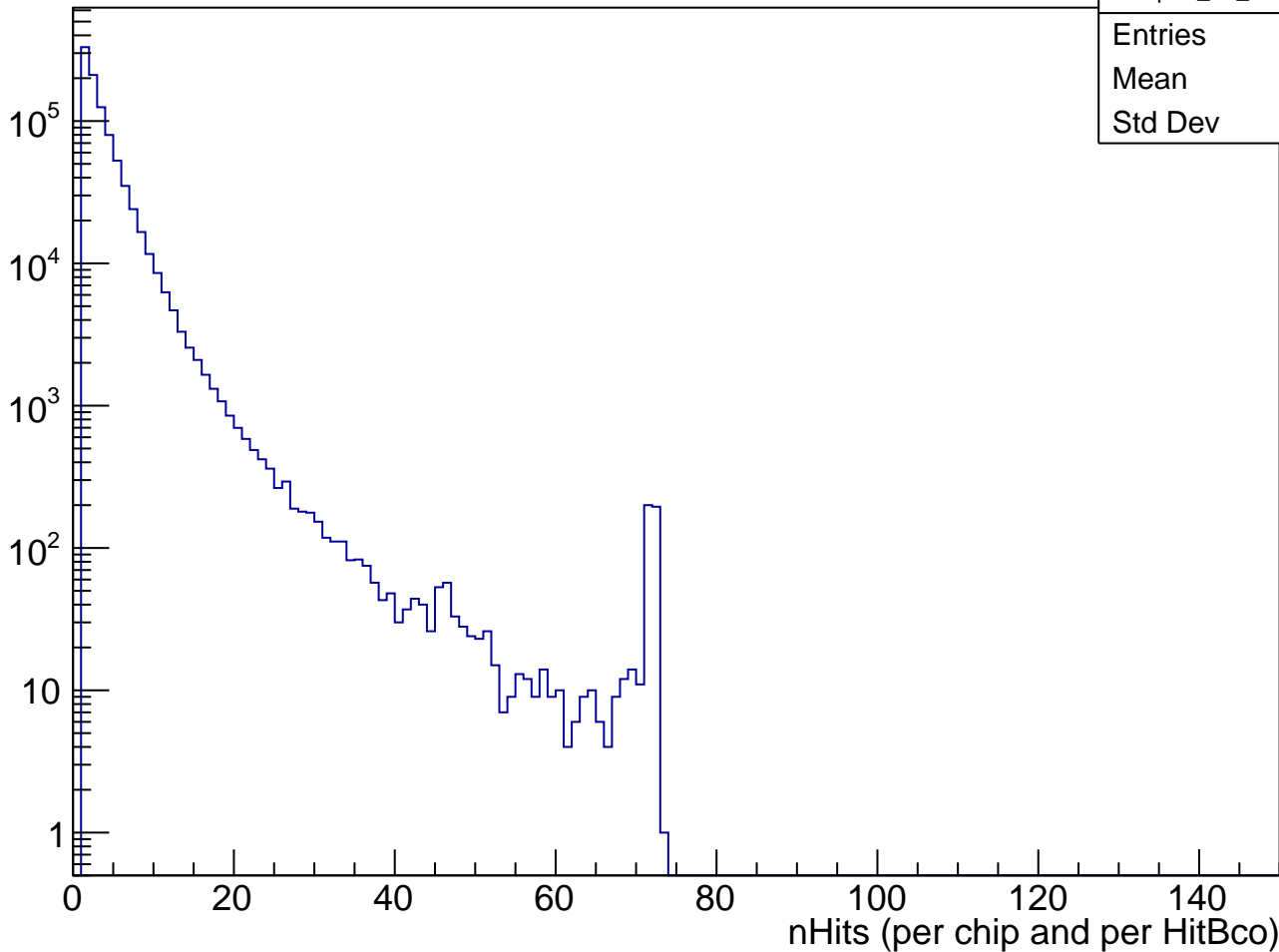
Entries 923228

Mean 3.064

Std Dev 3.543

nChipHit_F4_Fch2_Chip24

Entries

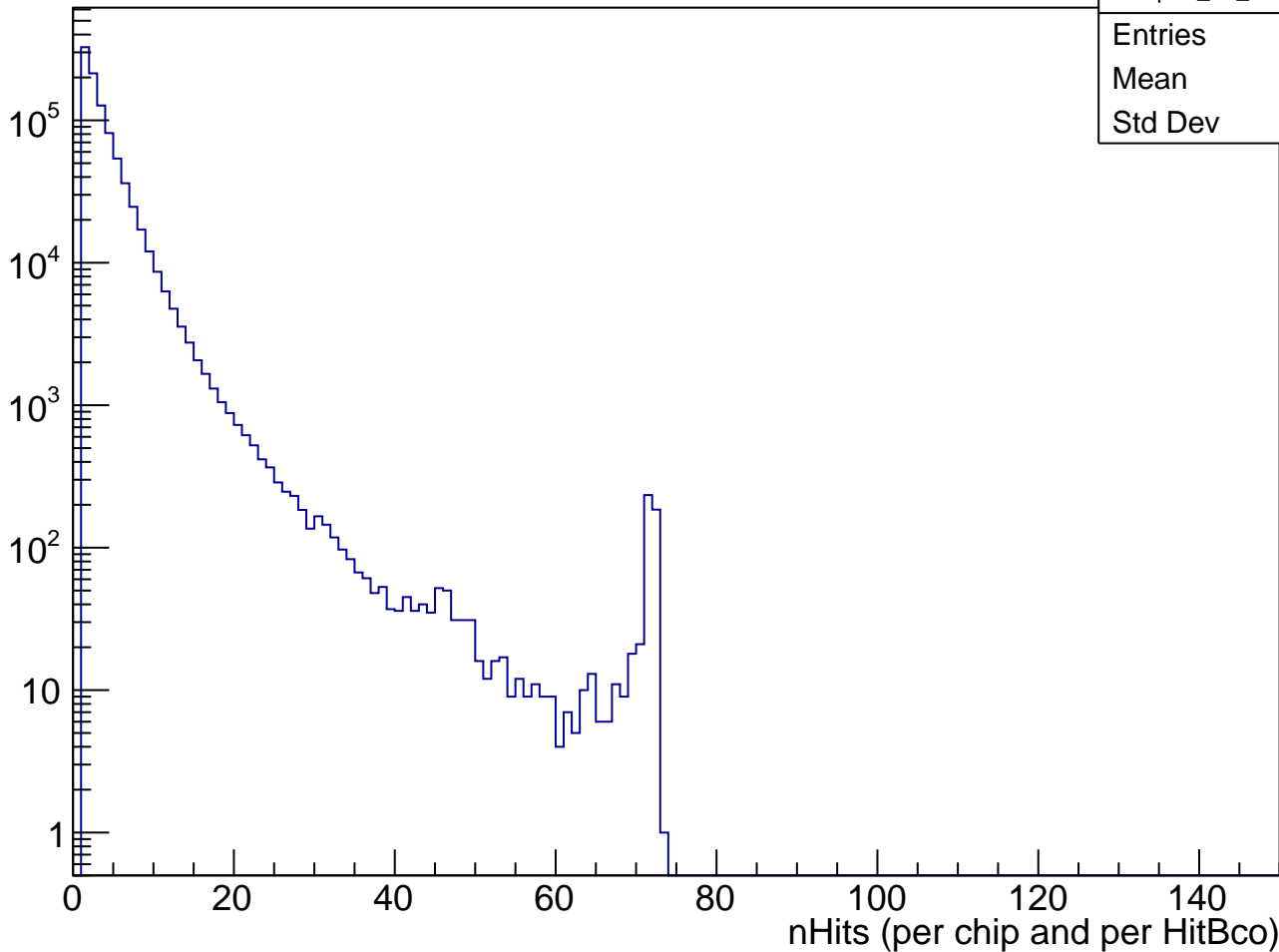


nChipHit_F4_Fch2_Chip24	
Entries	924094
Mean	3.142
Std Dev	3.61

nHits (per chip and per HitBco)

nChipHit_F4_Fch2_Chip25

Entries

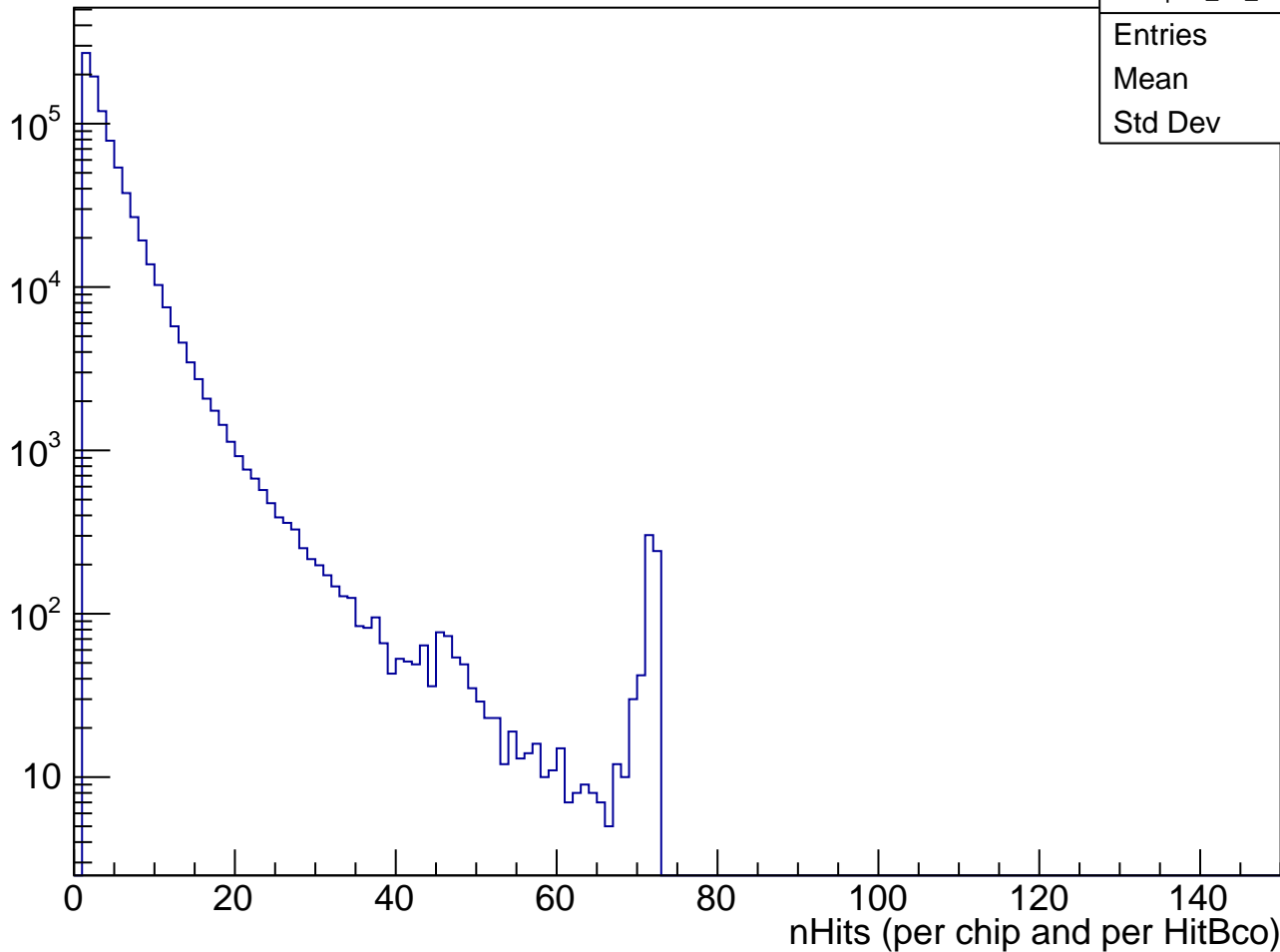


nChipHit_F4_Fch2_Chip25	
Entries	930734
Mean	3.173
Std Dev	3.634

nHits (per chip and per HitBco)

nChipHit_F4_Fch2_Chip3

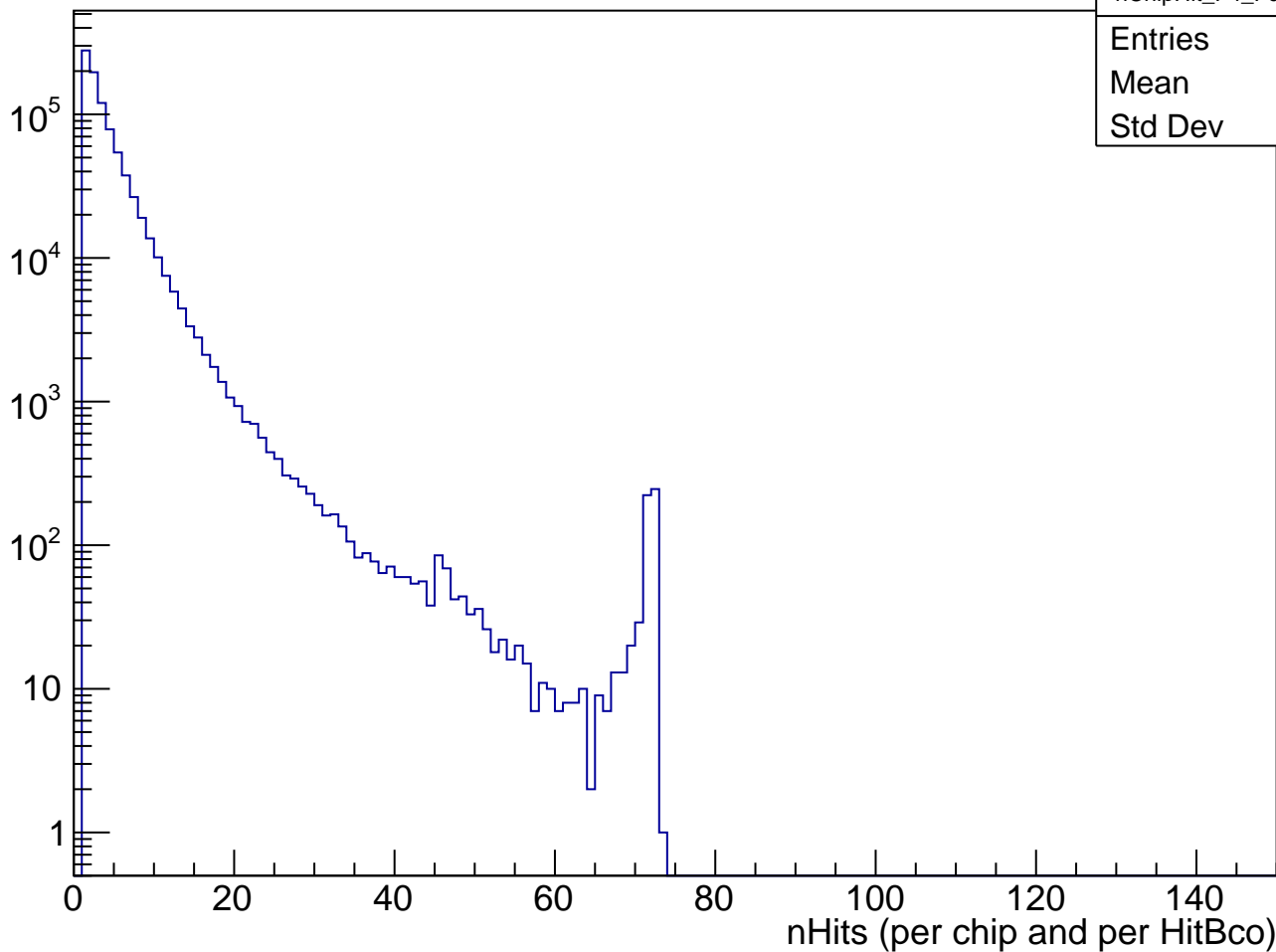
Entries



nChipHit_F4_Fch2_Chip3	
Entries	862903
Mean	3.525
Std Dev	4.137

nChipHit_F4_Fch2_Chip4

Entries



nChipHit_F4_Fch2_Chip4

Entries 872031

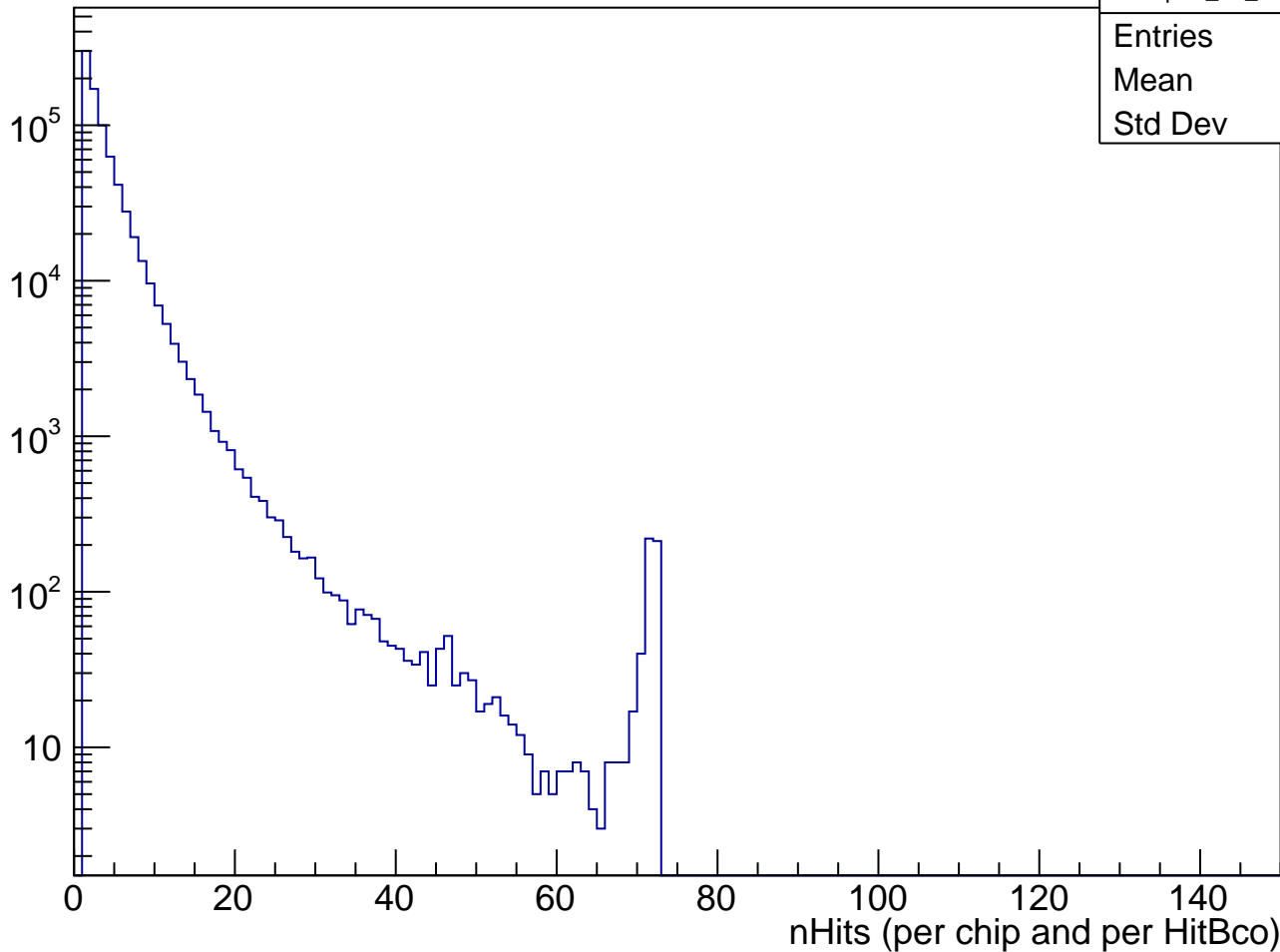
Mean 3.484

Std Dev 4.048

nHits (per chip and per HitBco)

nChipHit_F4_Fch2_Chip5

Entries

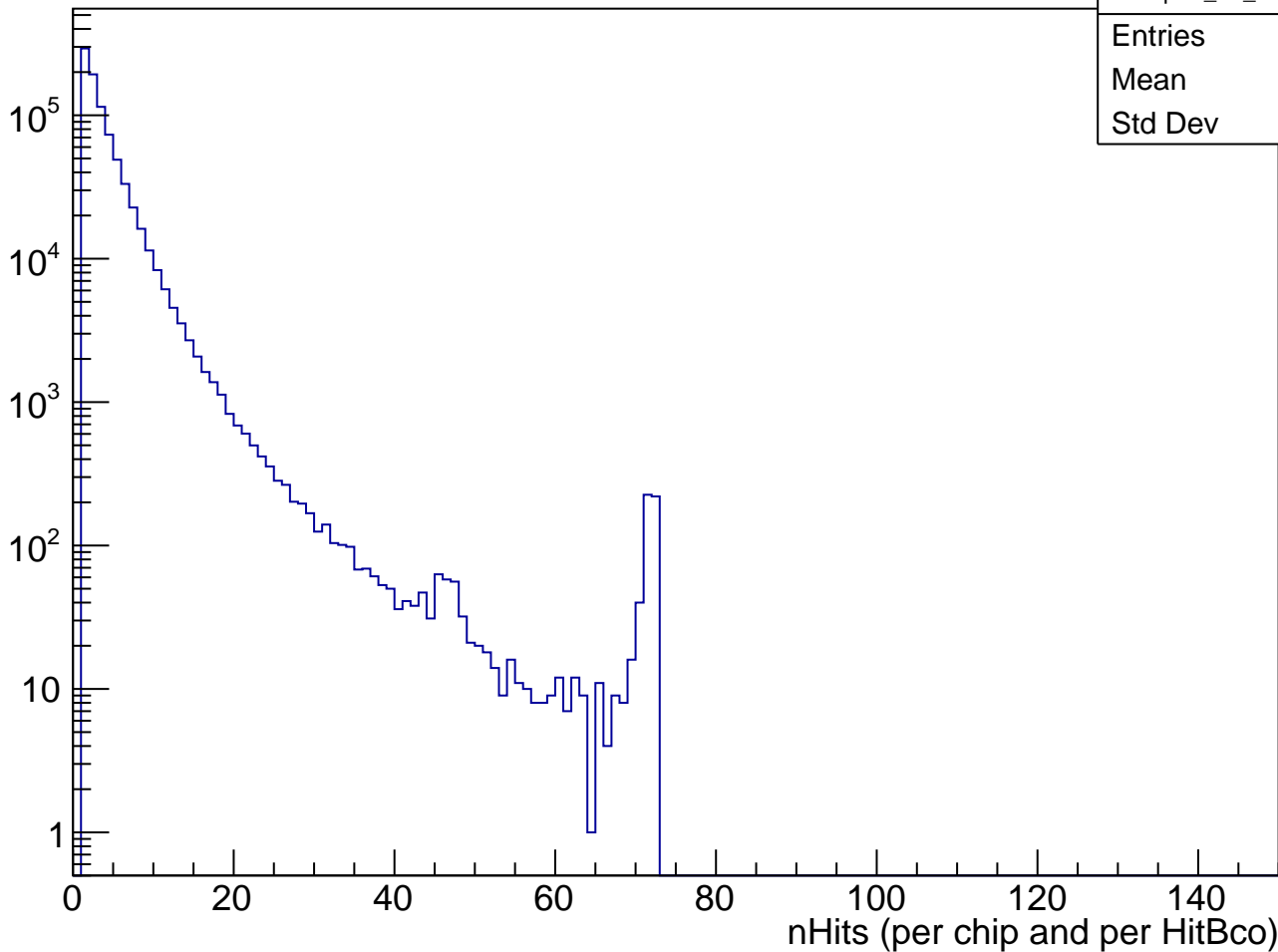


nChipHit_F4_Fch2_Chip5	
Entries	778791
Mean	3.089
Std Dev	3.772

nHits (per chip and per HitBco)

nChipHit_F4_Fch2_Chip6

Entries



nChipHit_F4_Fch2_Chip6

Entries 843585

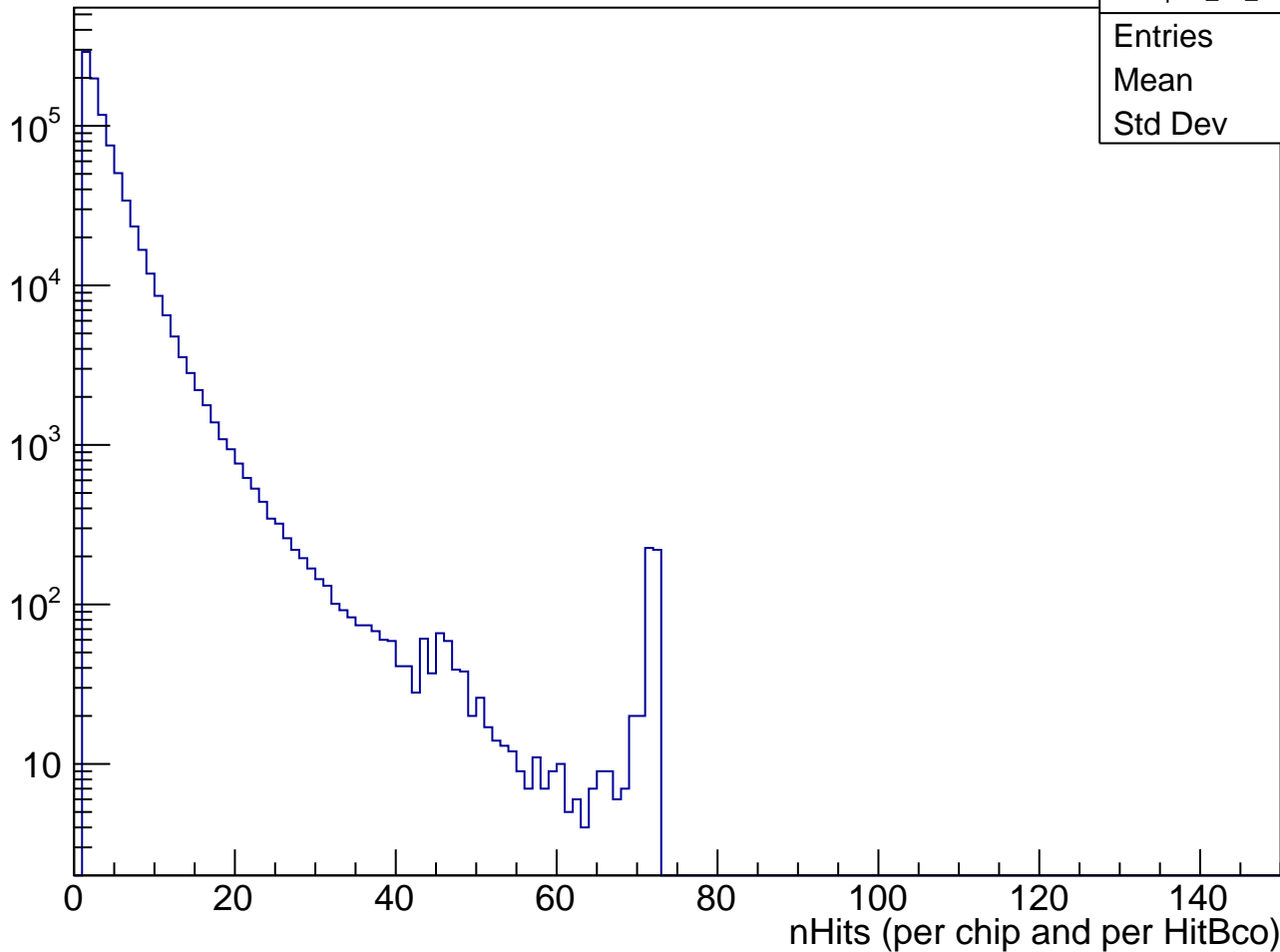
Mean 3.243

Std Dev 3.806

nHits (per chip and per HitBco)

nChipHit_F4_Fch2_Chip7

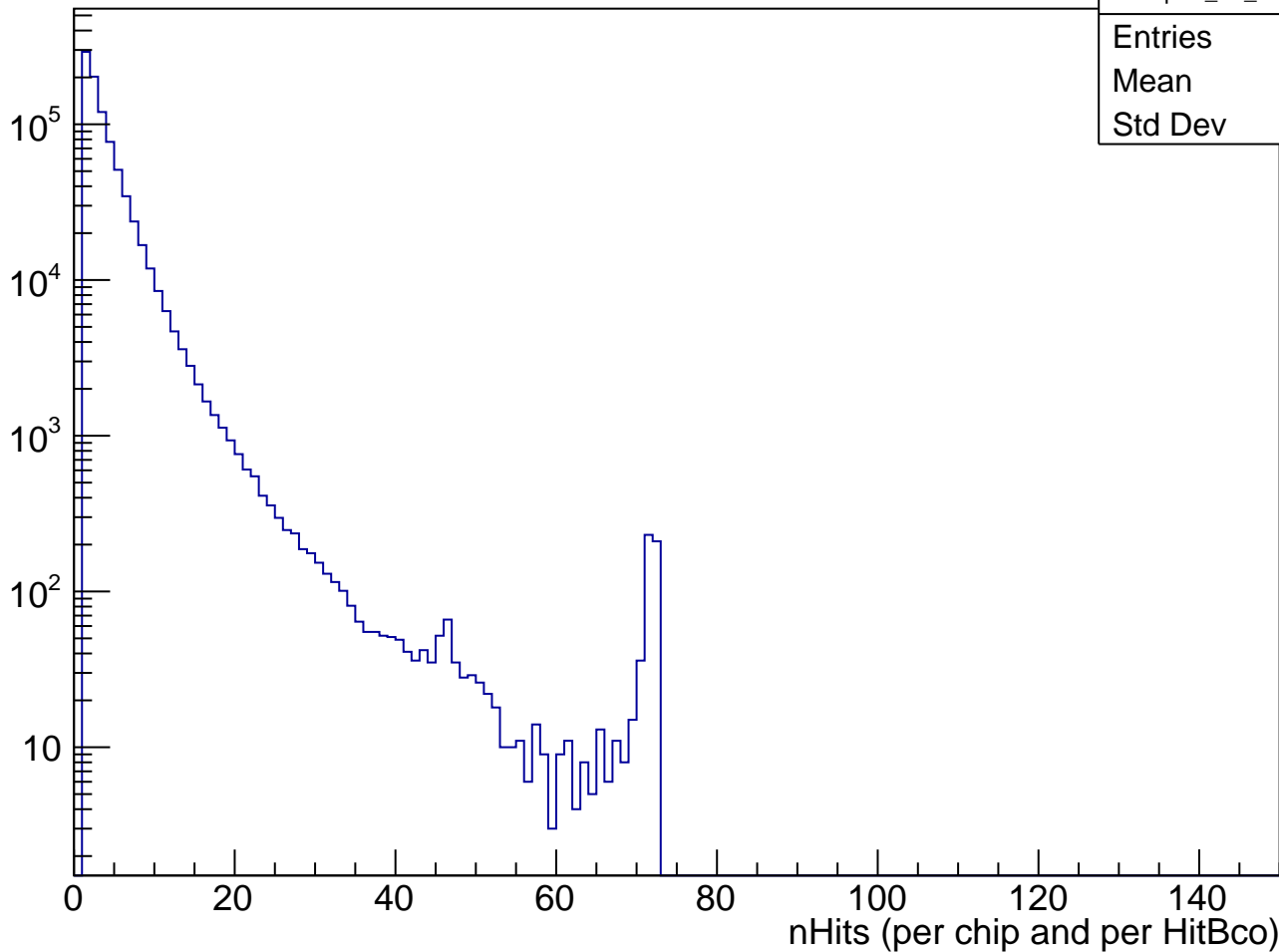
Entries



nChipHit_F4_Fch2_Chip7	
Entries	857851
Mean	3.274
Std Dev	3.801

nChipHit_F4_Fch2_Chip8

Entries



nChipHit_F4_Fch2_Chip8

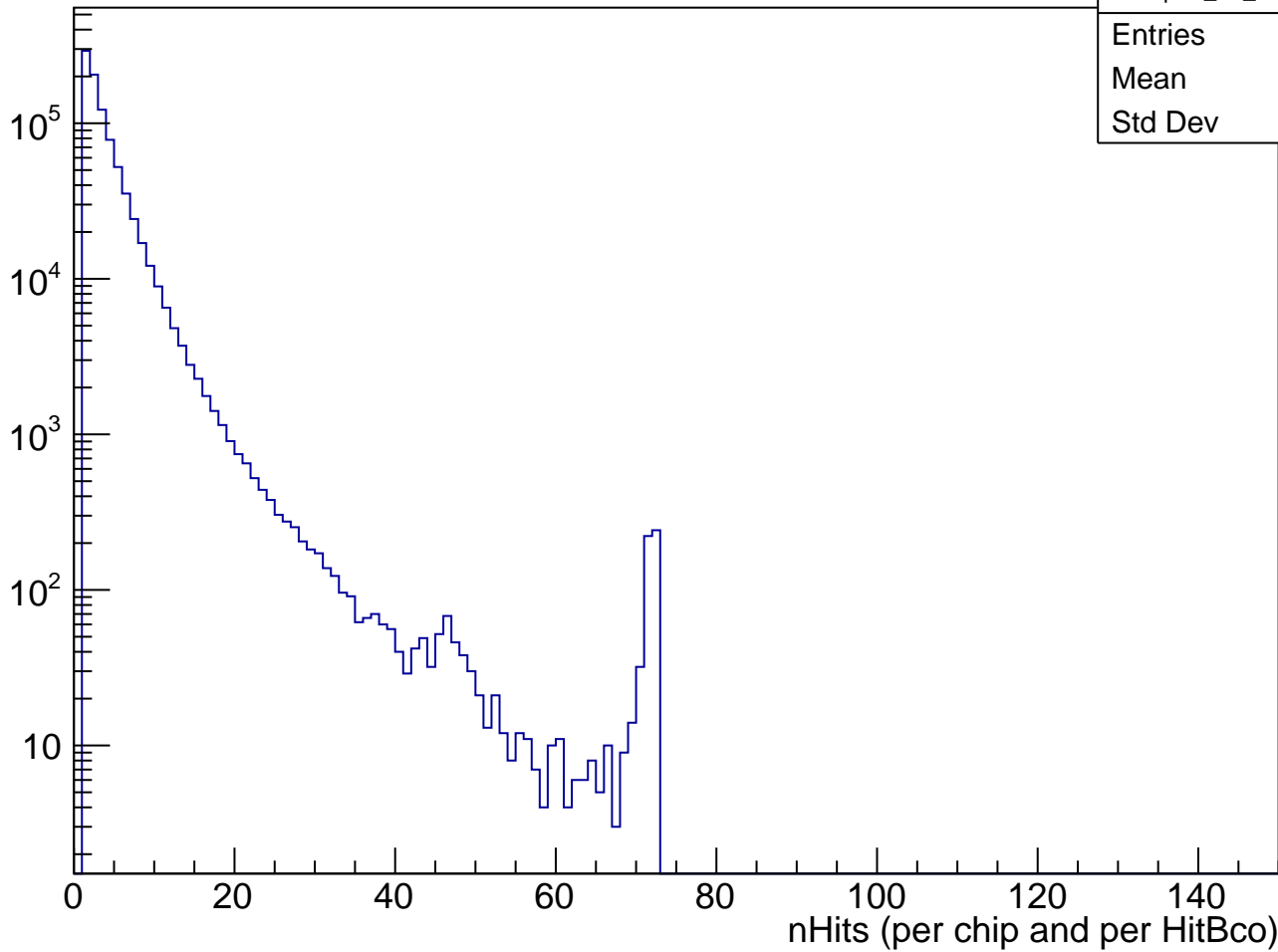
Entries 867701

Mean 3.264

Std Dev 3.775

nChipHit_F4_Fch2_Chip9

Entries



nChipHit_F4_Fch2_Chip9

Entries 879513

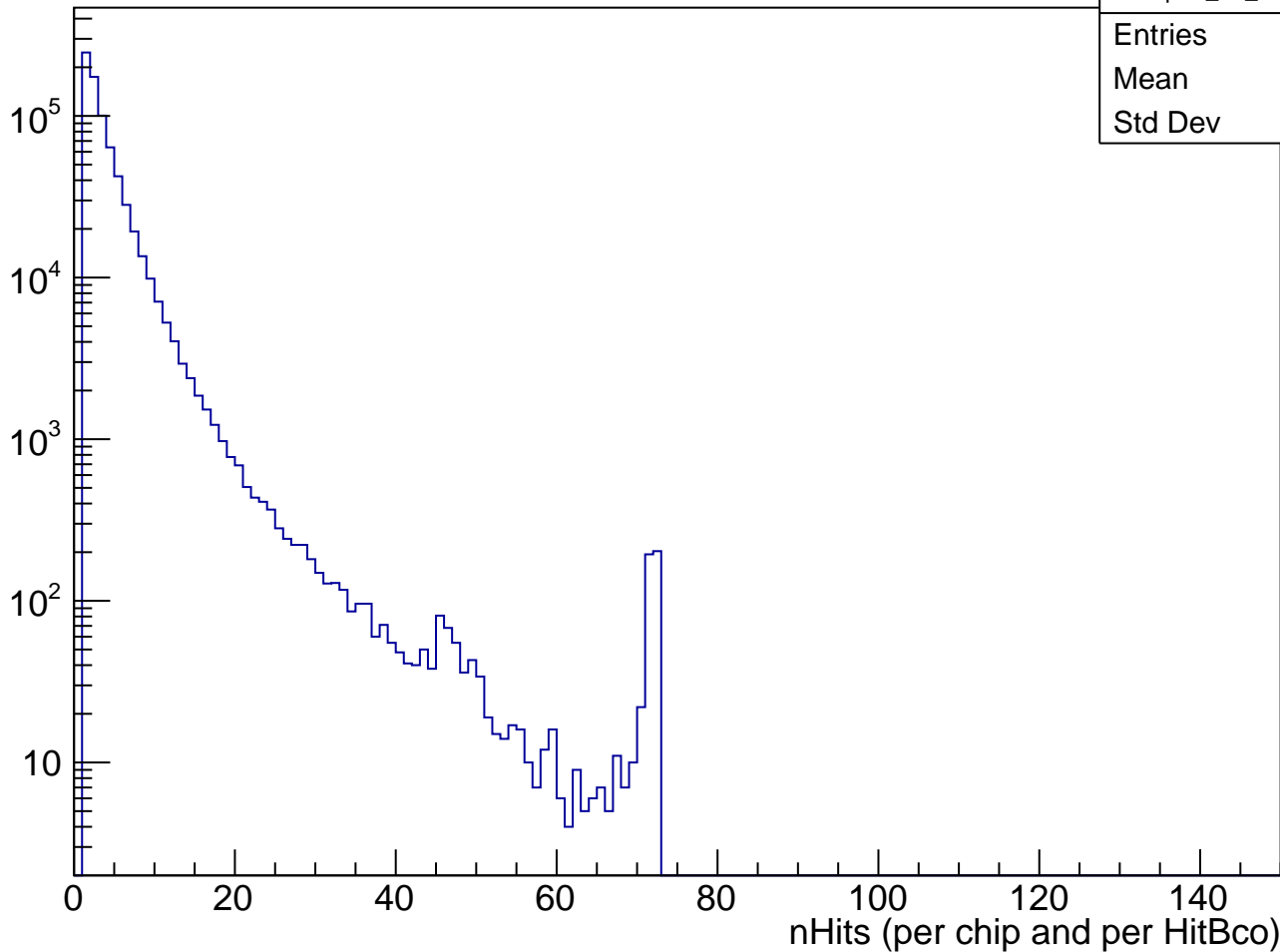
Mean 3.287

Std Dev 3.798

nHits (per chip and per HitBco)

nChipHit_F4_Fch3_Chip0

Entries



nChipHit_F4_Fch3_Chip0

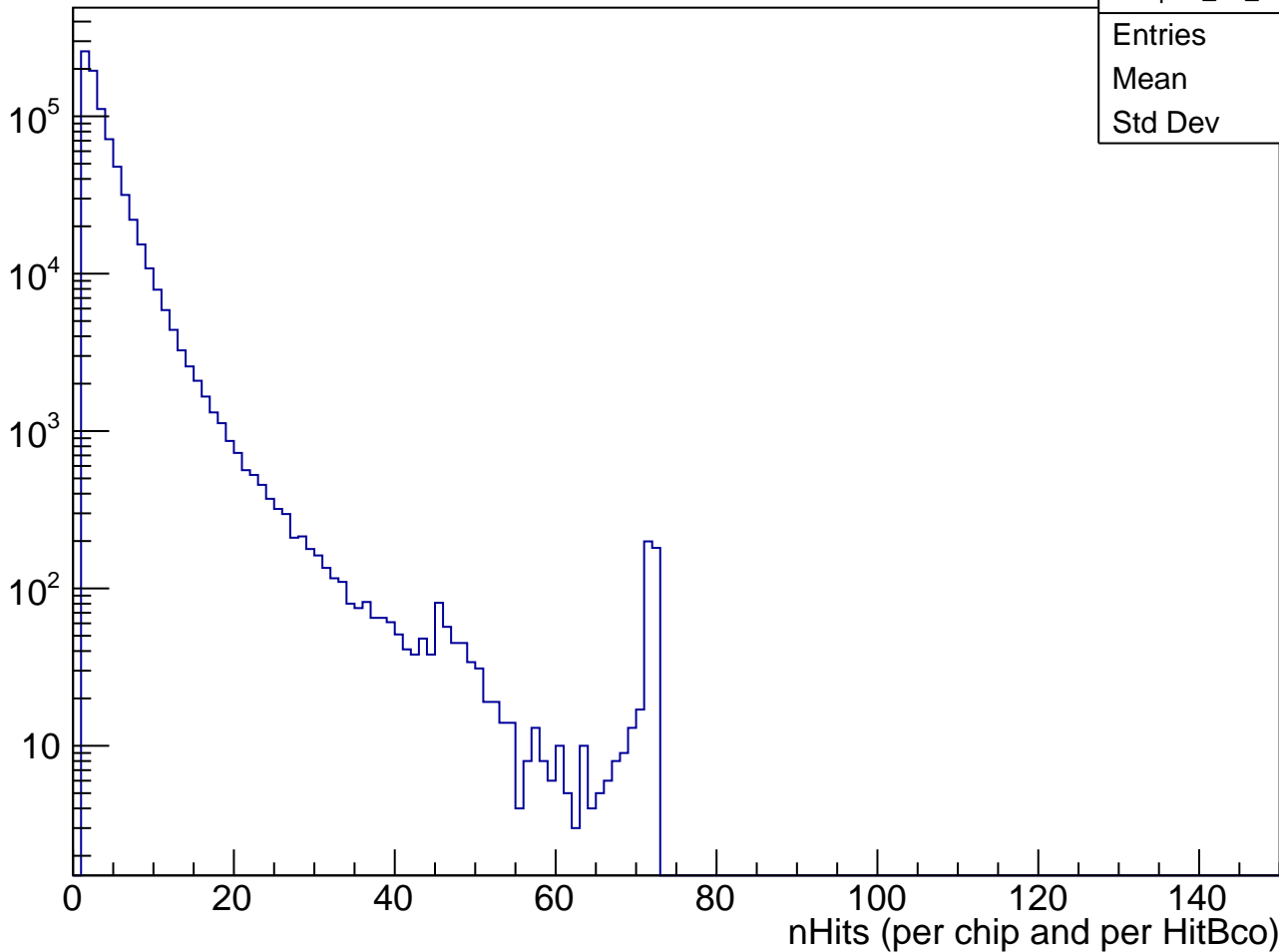
Entries 732659

Mean 3.278

Std Dev 3.93

nChipHit_F4_Fch3_Chip1

Entries

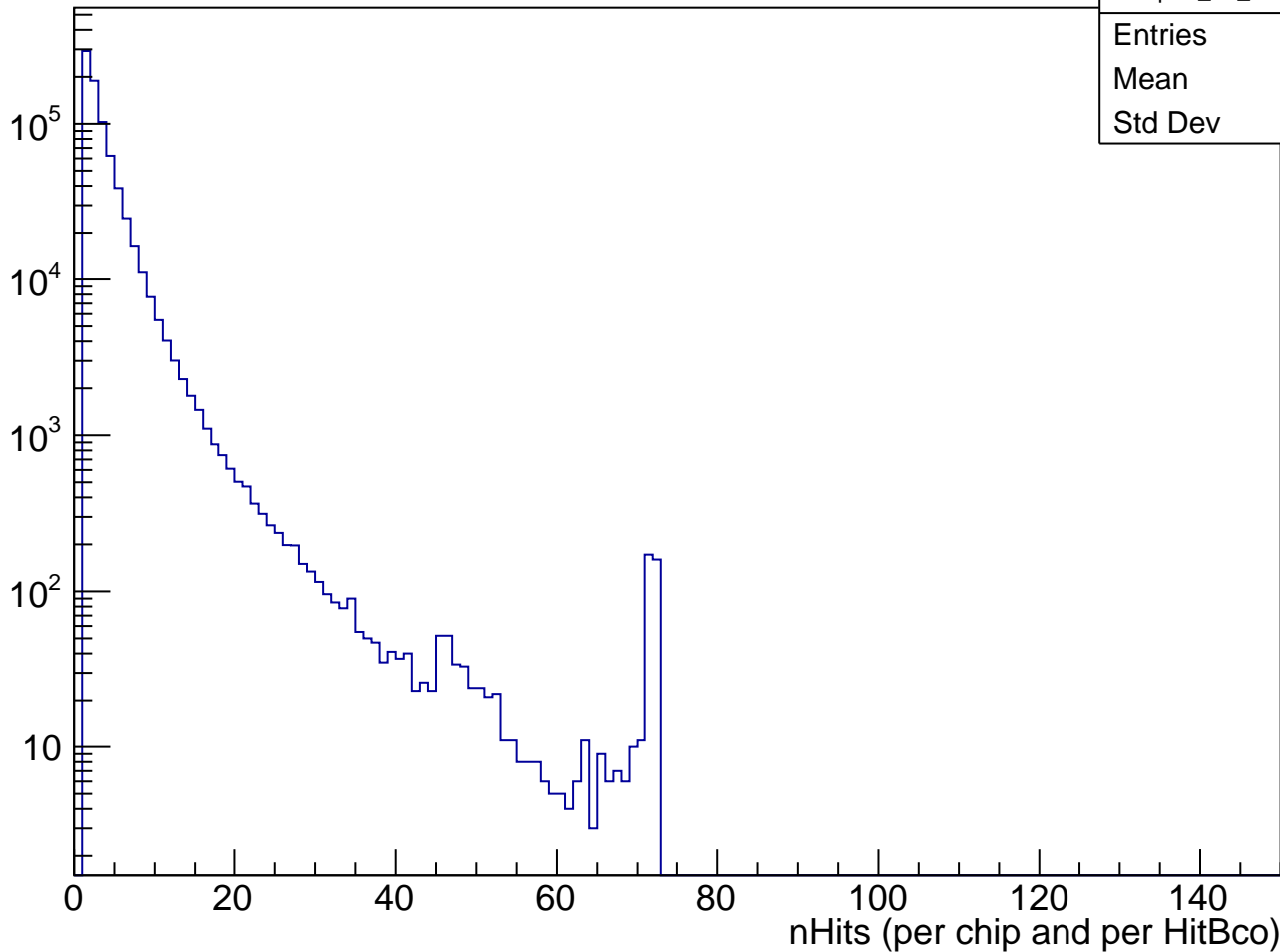


nChipHit_F4_Fch3_Chip1	
Entries	800964
Mean	3.304
Std Dev	3.825

nHits (per chip and per HitBco)

nChipHit_F4_Fch3_Chip10

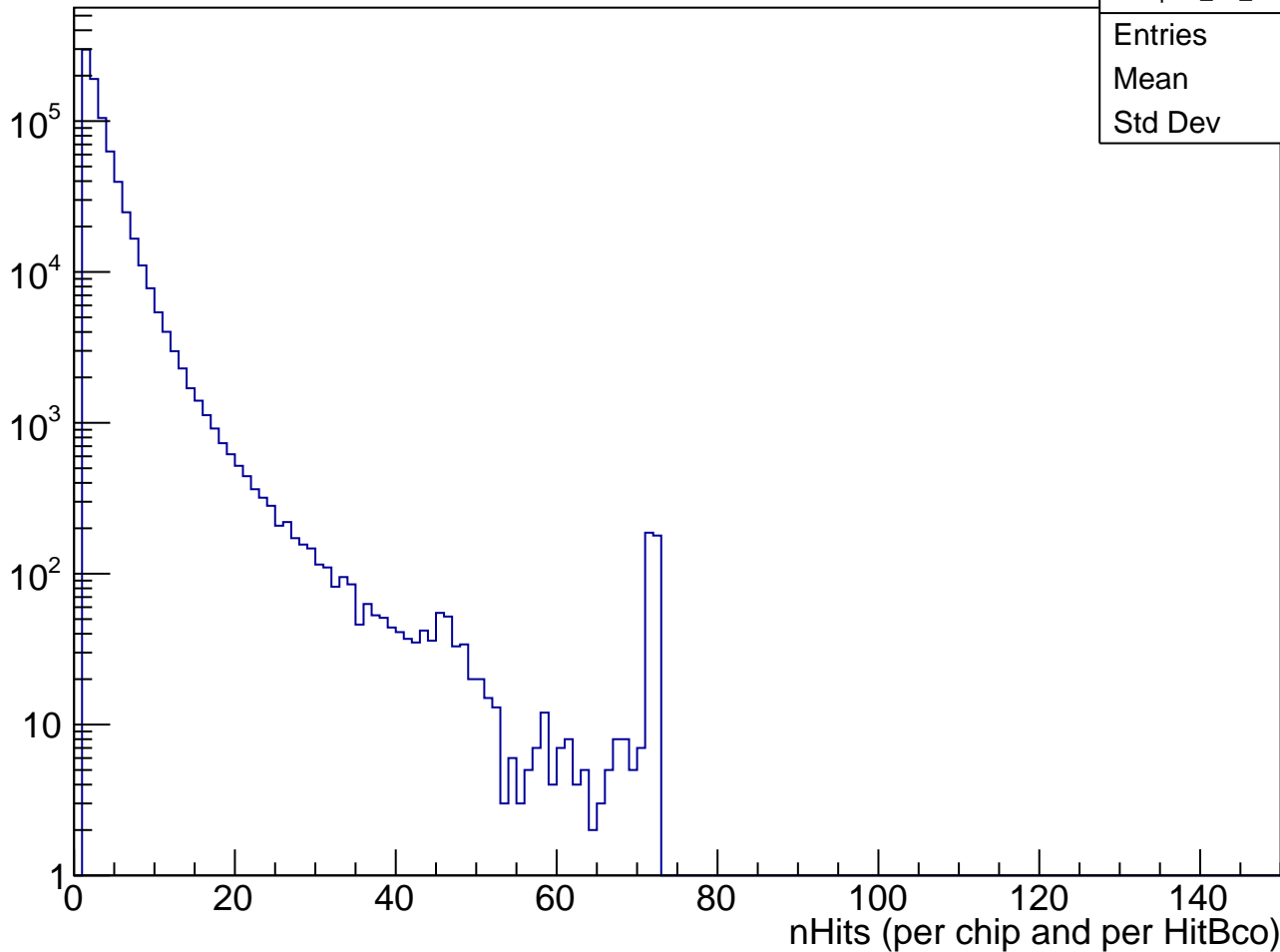
Entries



nChipHit_F4_Fch3_Chip10	
Entries	770974
Mean	2.92
Std Dev	3.478

nChipHit_F4_Fch3_Chip11

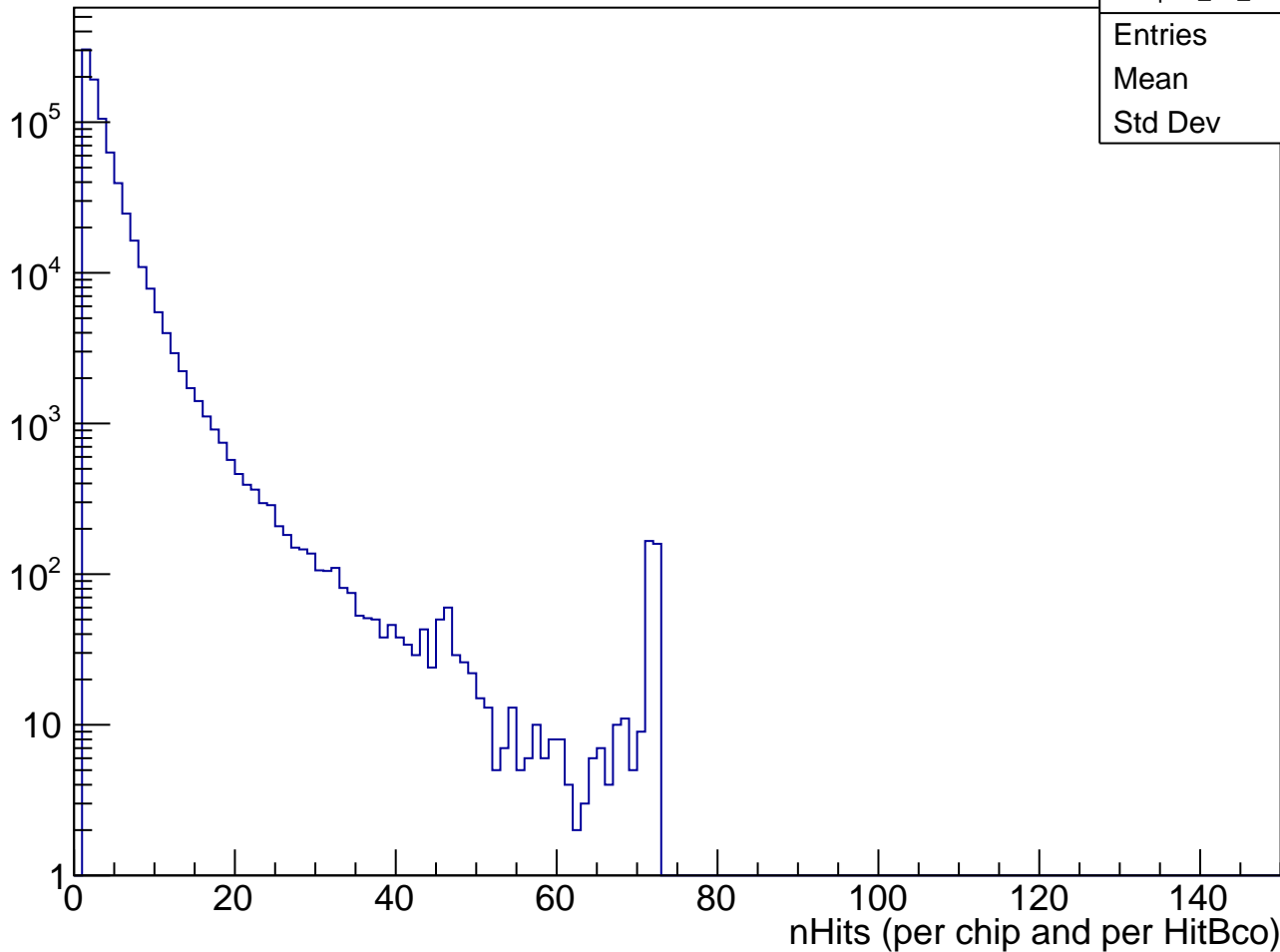
Entries



nChipHit_F4_Fch3_Chip11	
Entries	781514
Mean	2.913
Std Dev	3.481

nChipHit_F4_Fch3_Chip12

Entries

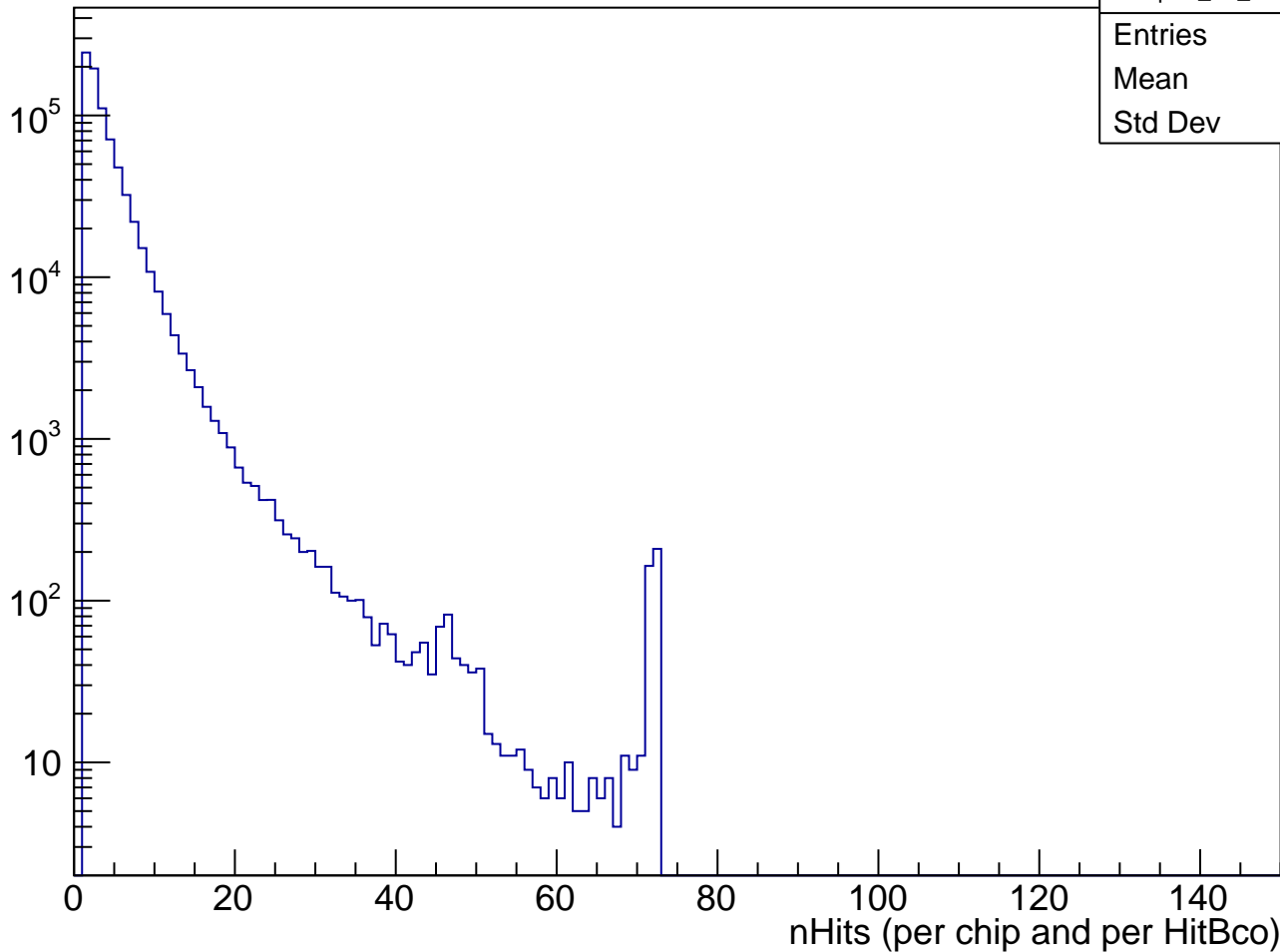


nChipHit_F4_Fch3_Chip12	
Entries	788696
Mean	2.881
Std Dev	3.405

nHits (per chip and per HitBco)

nChipHit_F4_Fch3_Chip13

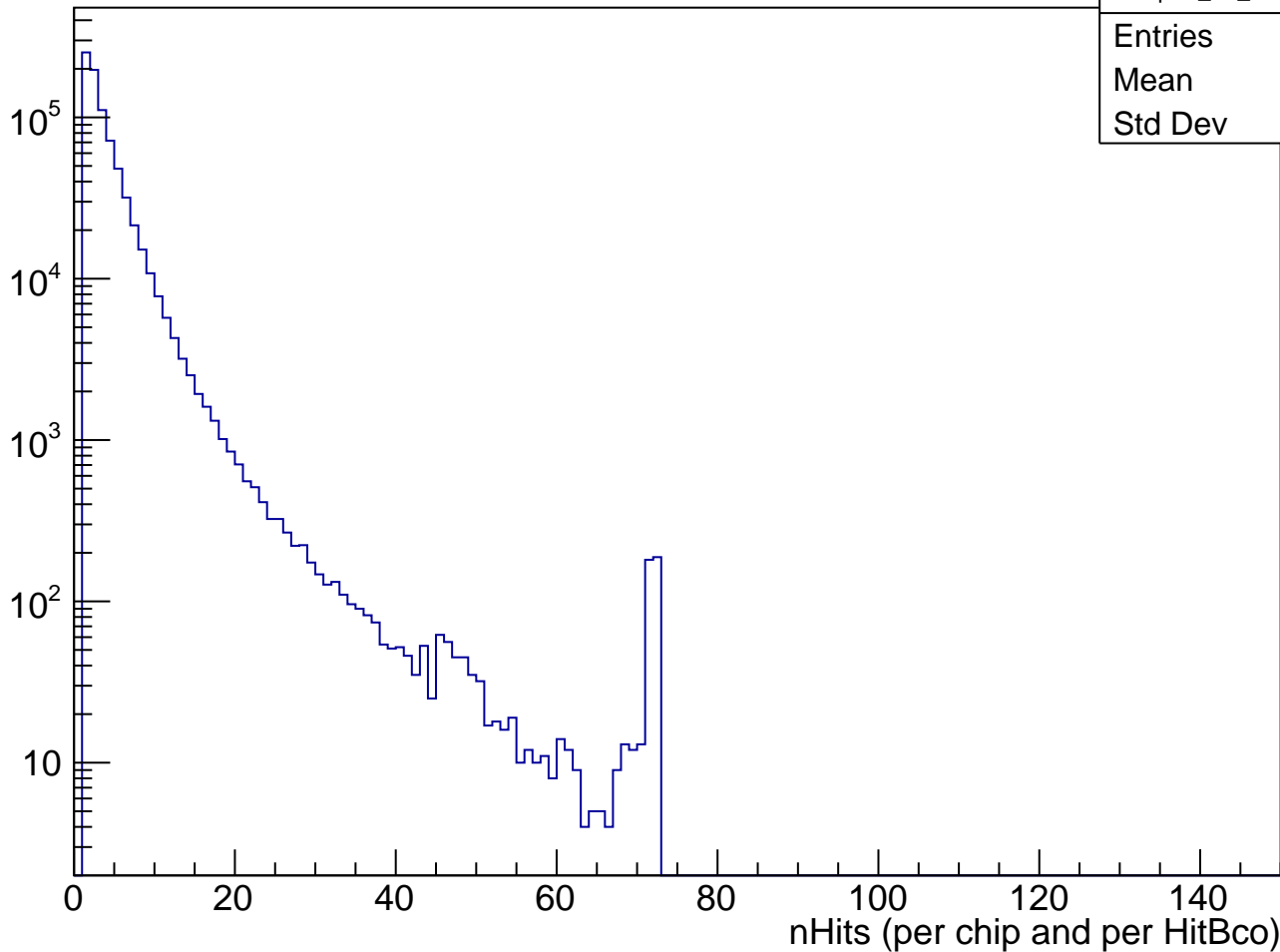
Entries



nChipHit_F4_Fch3_Chip13	
Entries	786920
Mean	3.347
Std Dev	3.845

nChipHit_F4_Fch3_Chip14

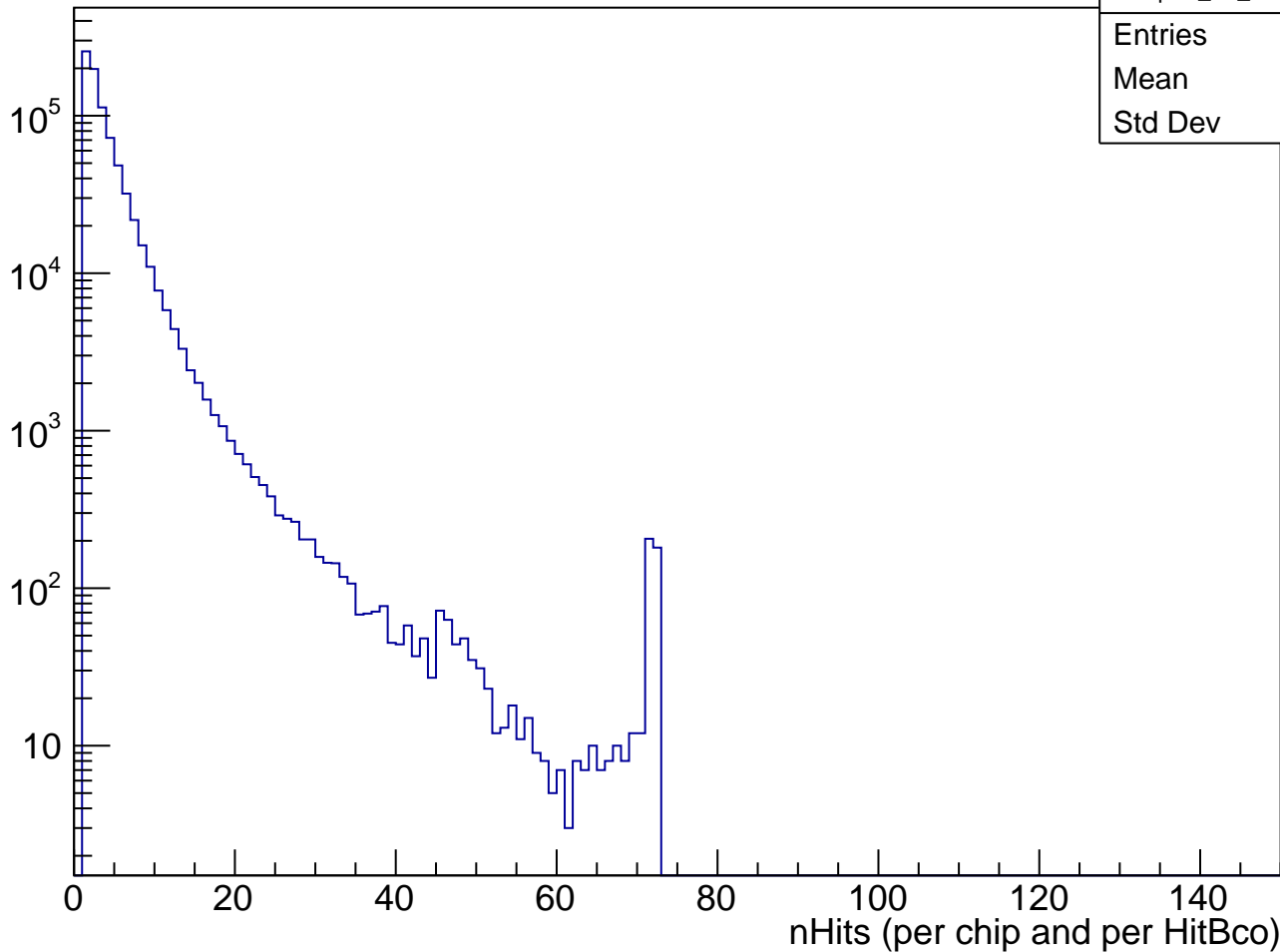
Entries



nChipHit_F4_Fch3_Chip14	
Entries	795711
Mean	3.301
Std Dev	3.806

nChipHit_F4_Fch3_Chip15

Entries

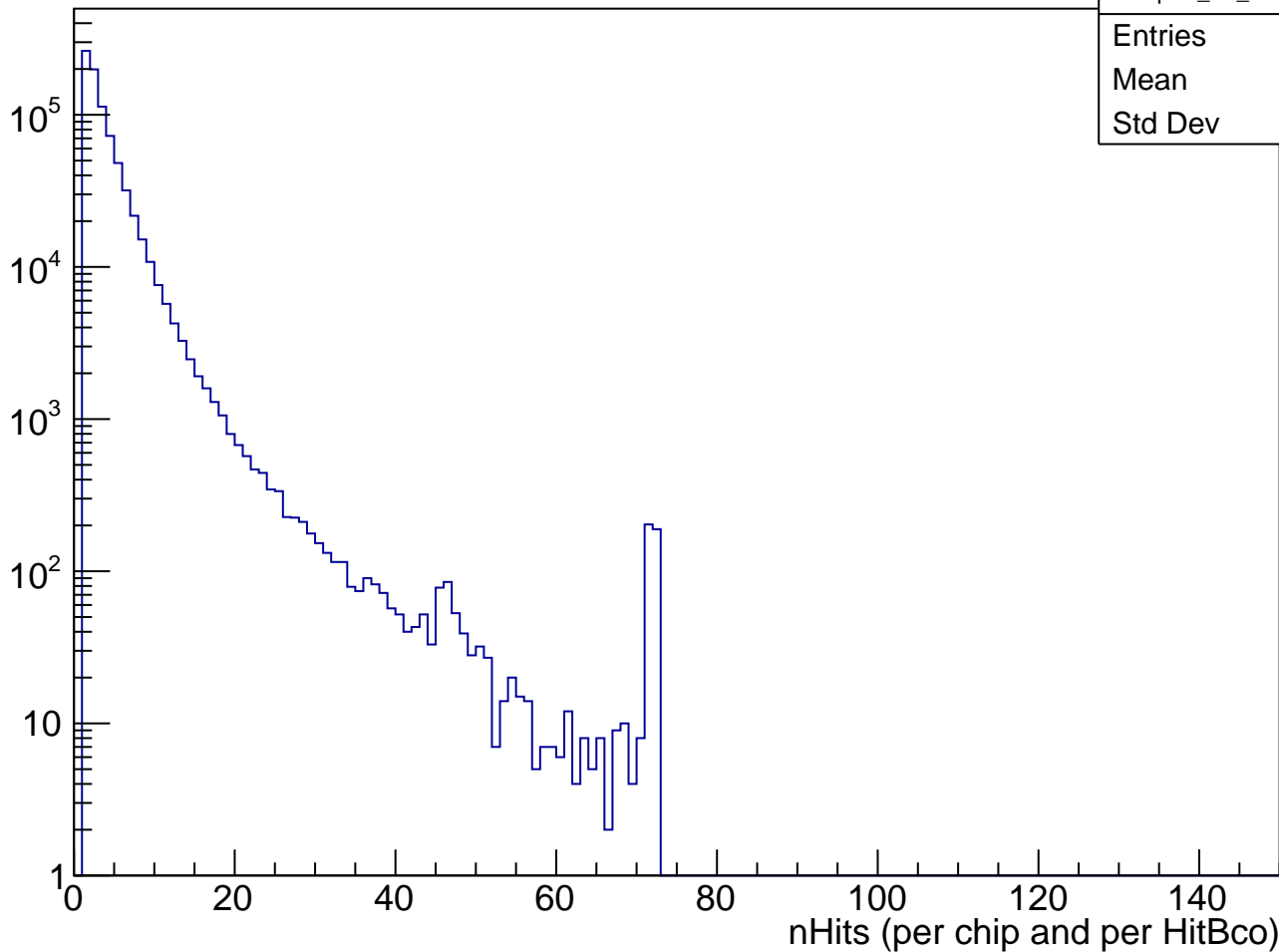


nChipHit_F4_Fch3_Chip15	
Entries	804217
Mean	3.304
Std Dev	3.825

nHits (per chip and per HitBco)

nChipHit_F4_Fch3_Chip16

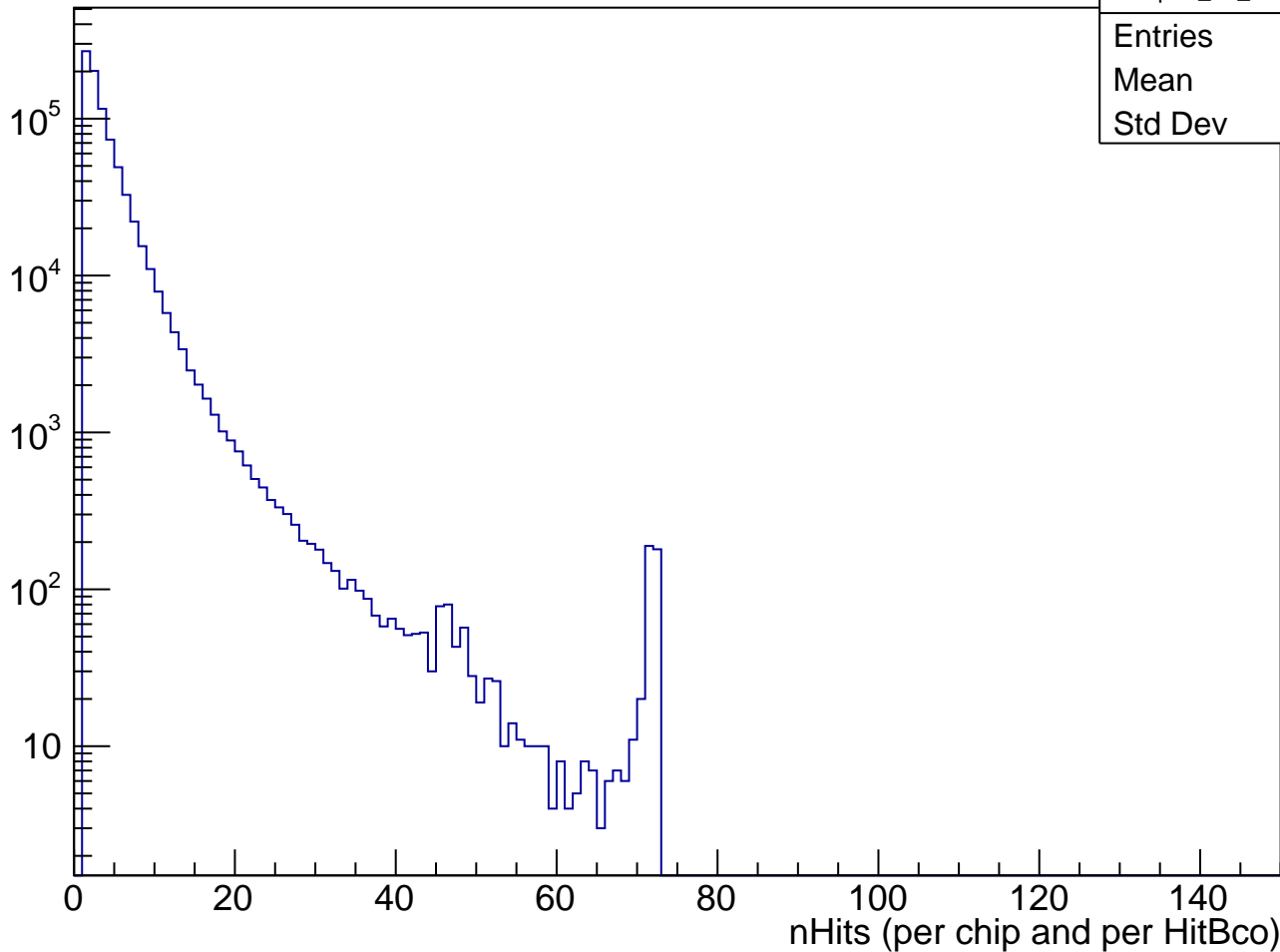
Entries



nChipHit_F4_Fch3_Chip16	
Entries	811169
Mean	3.269
Std Dev	3.789

nChipHit_F4_Fch3_Chip17

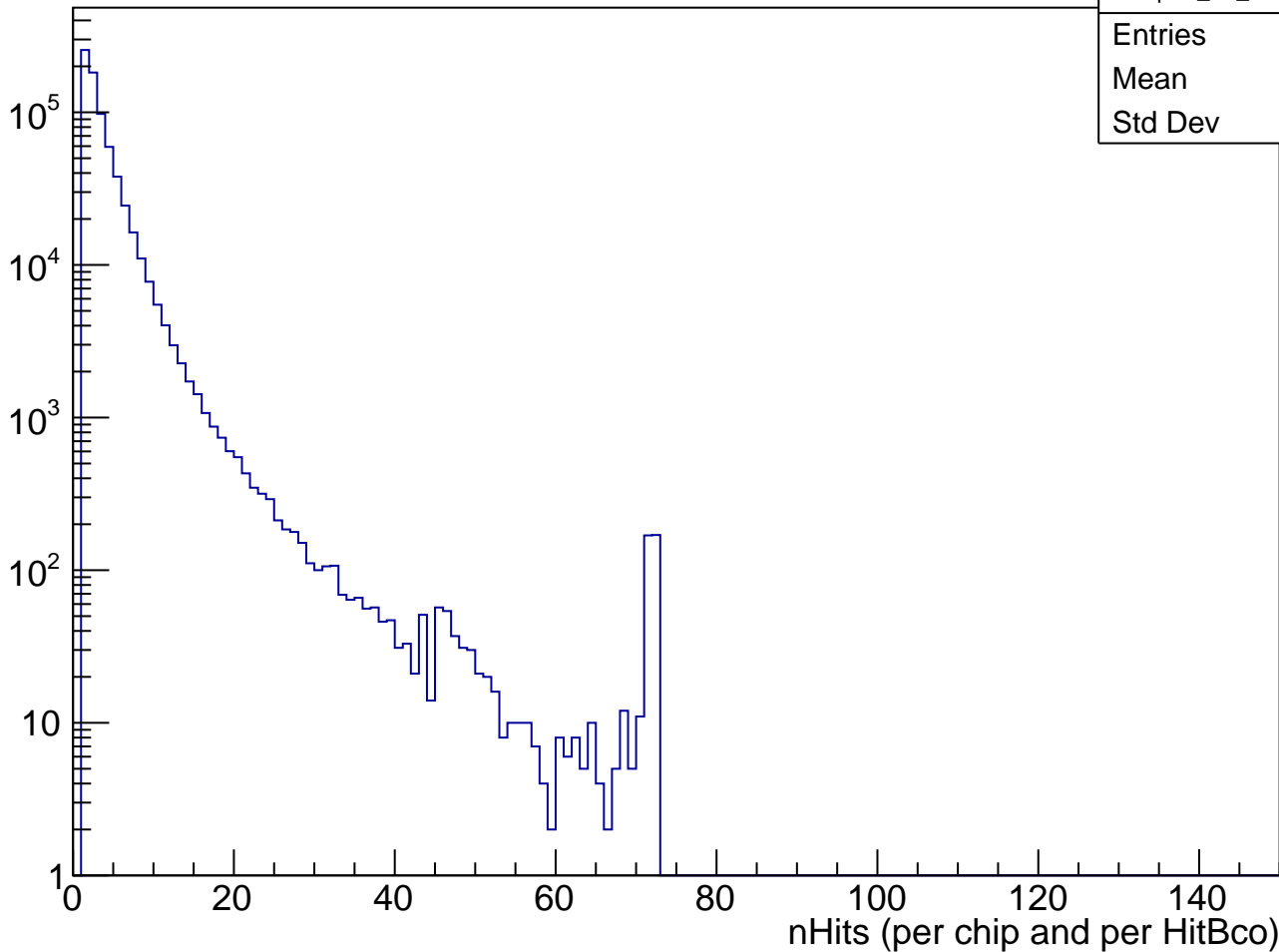
Entries



nChipHit_F4_Fch3_Chip17	
Entries	827171
Mean	3.276
Std Dev	3.798

nChipHit_F4_Fch3_Chip18

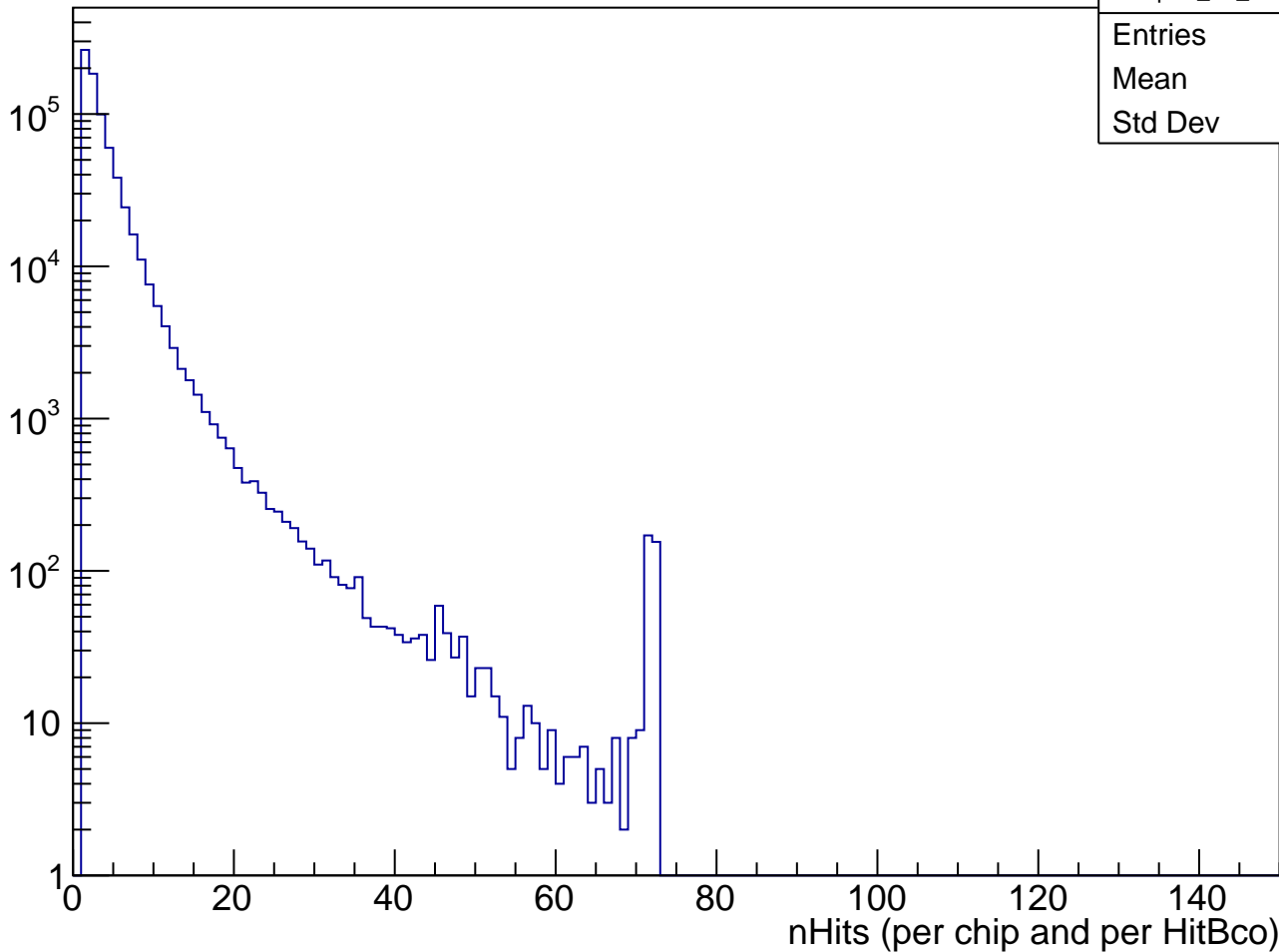
Entries



nChipHit_F4_Fch3_Chip18	
Entries	718124
Mean	3.015
Std Dev	3.569

nChipHit_F4_Fch3_Chip19

Entries

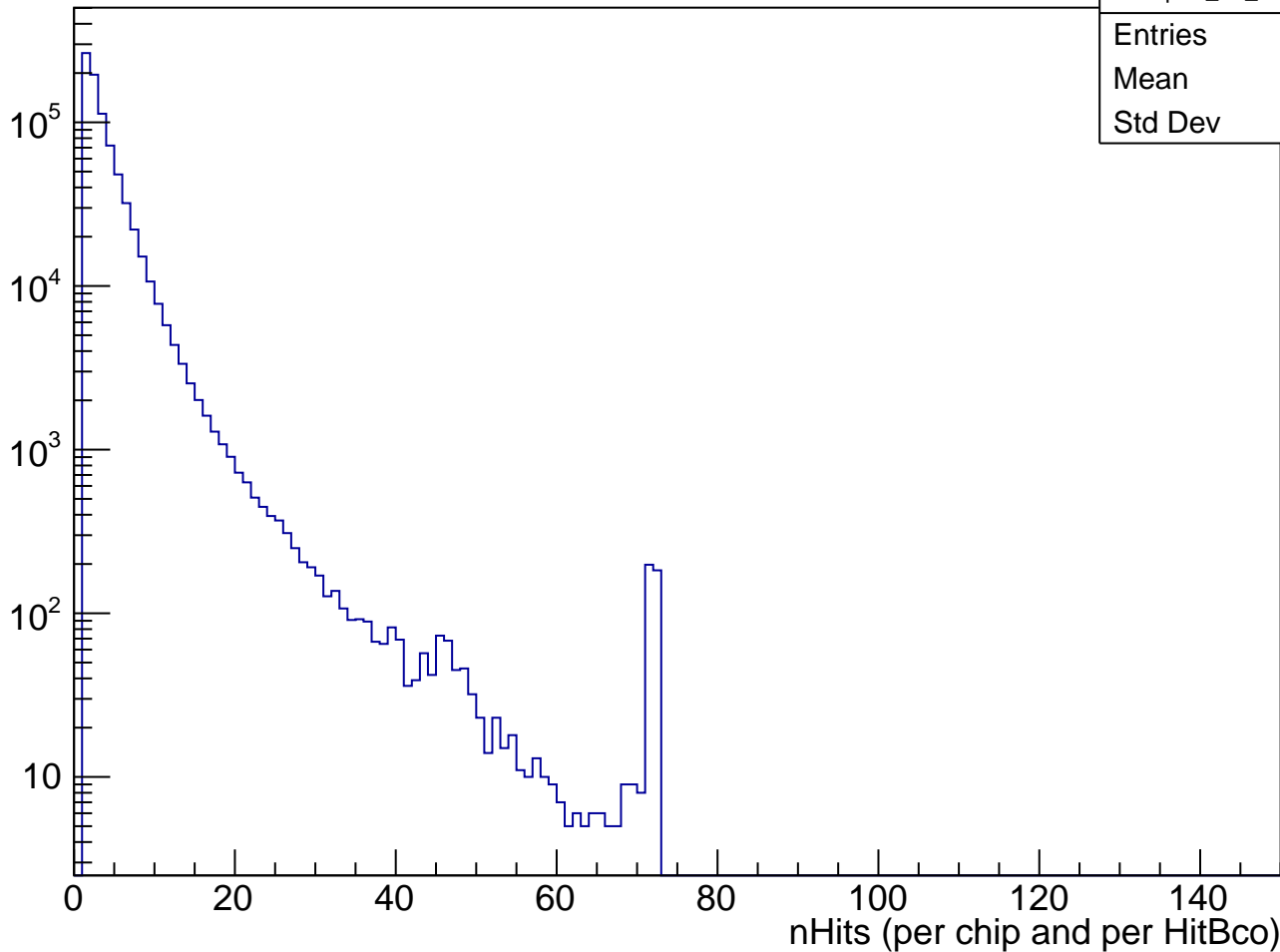


nChipHit_F4_Fch3_Chip19	
Entries	730178
Mean	2.991
Std Dev	3.535

nHits (per chip and per HitBco)

nChipHit_F4_Fch3_Chip2

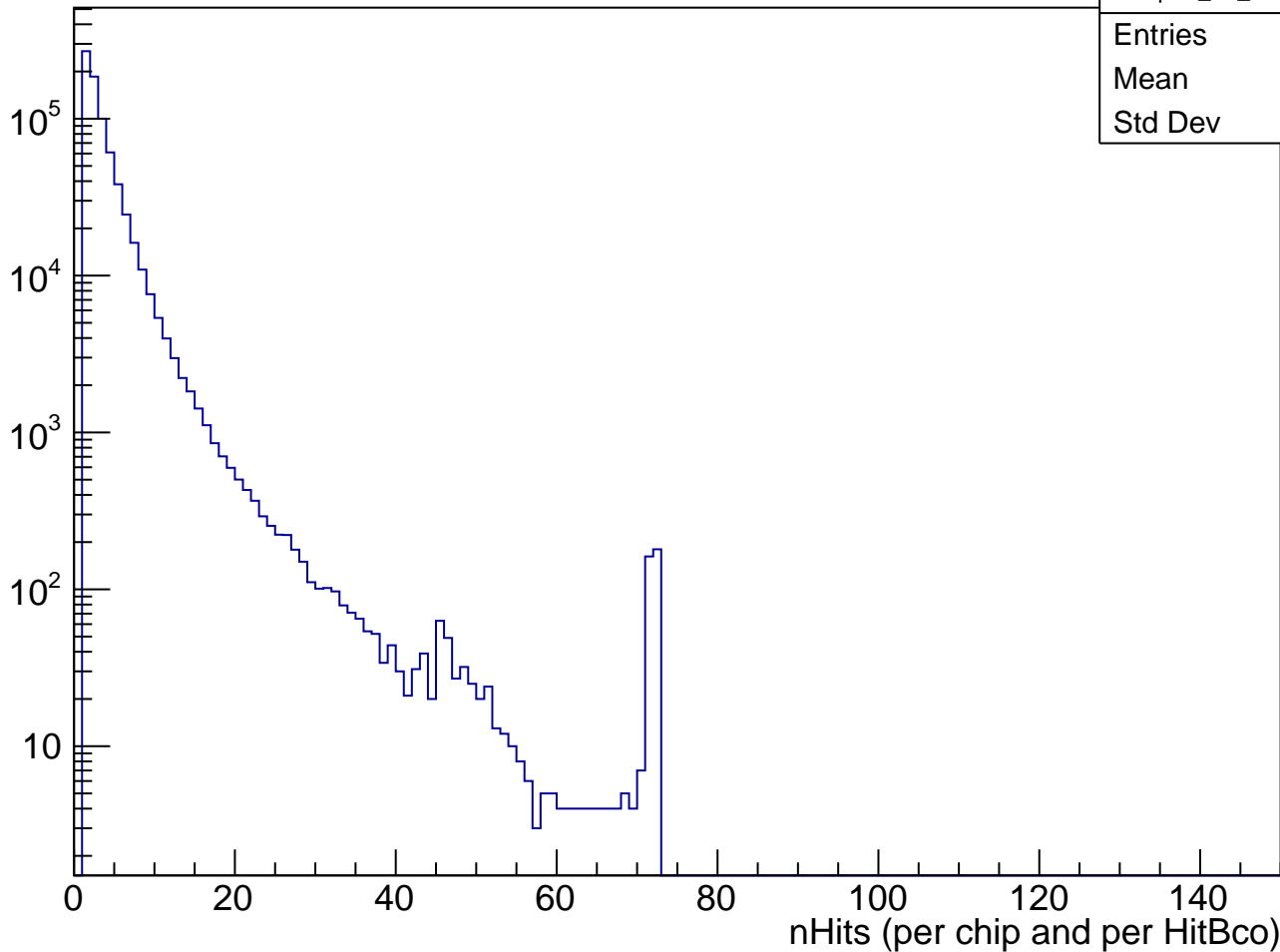
Entries



nChipHit_F4_Fch3_Chip2	
Entries	810285
Mean	3.289
Std Dev	3.827

nChipHit_F4_Fch3_Chip20

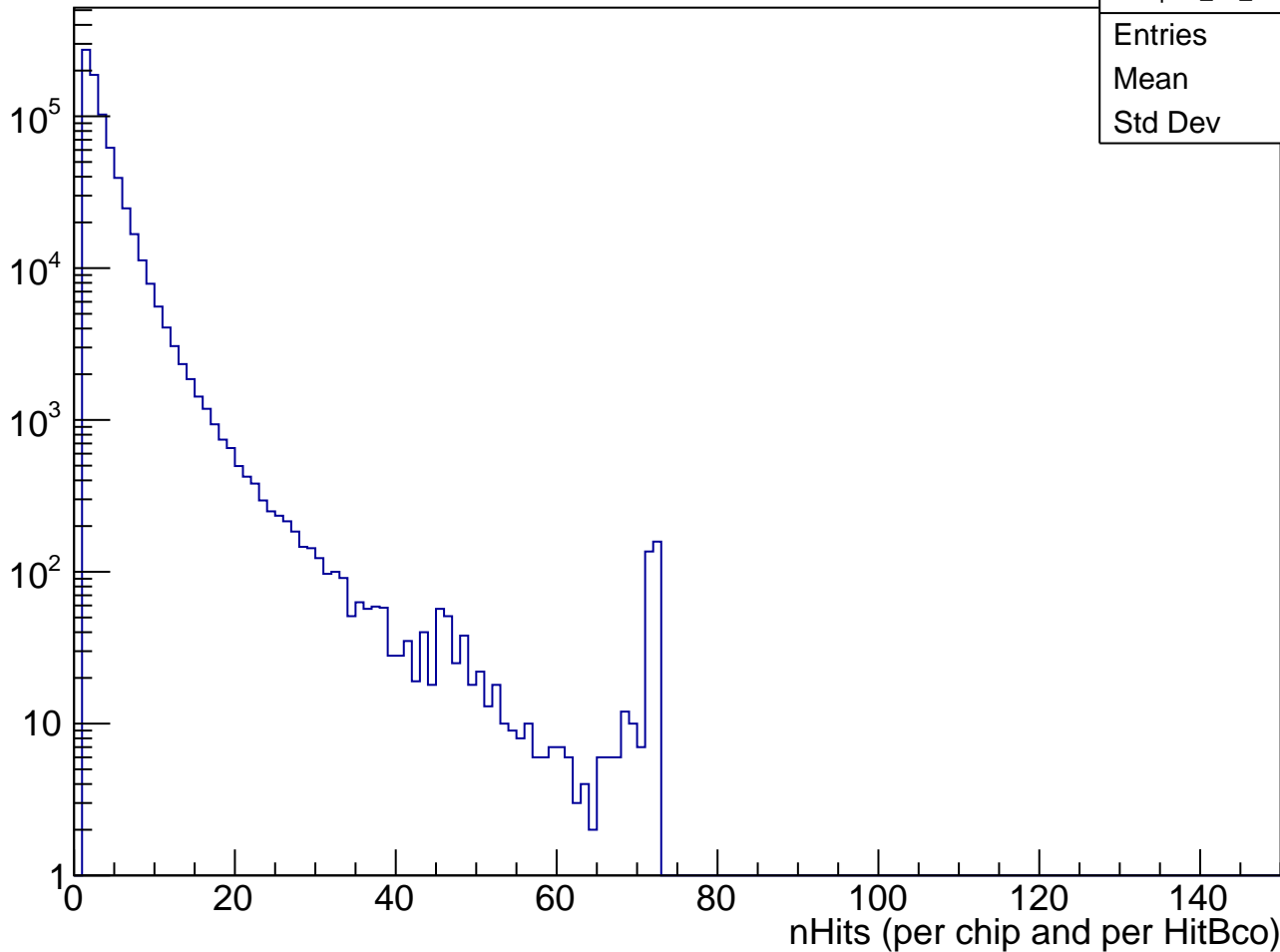
Entries



nChipHit_F4_Fch3_Chip20	
Entries	738833
Mean	2.967
Std Dev	3.494

nChipHit_F4_Fch3_Chip21

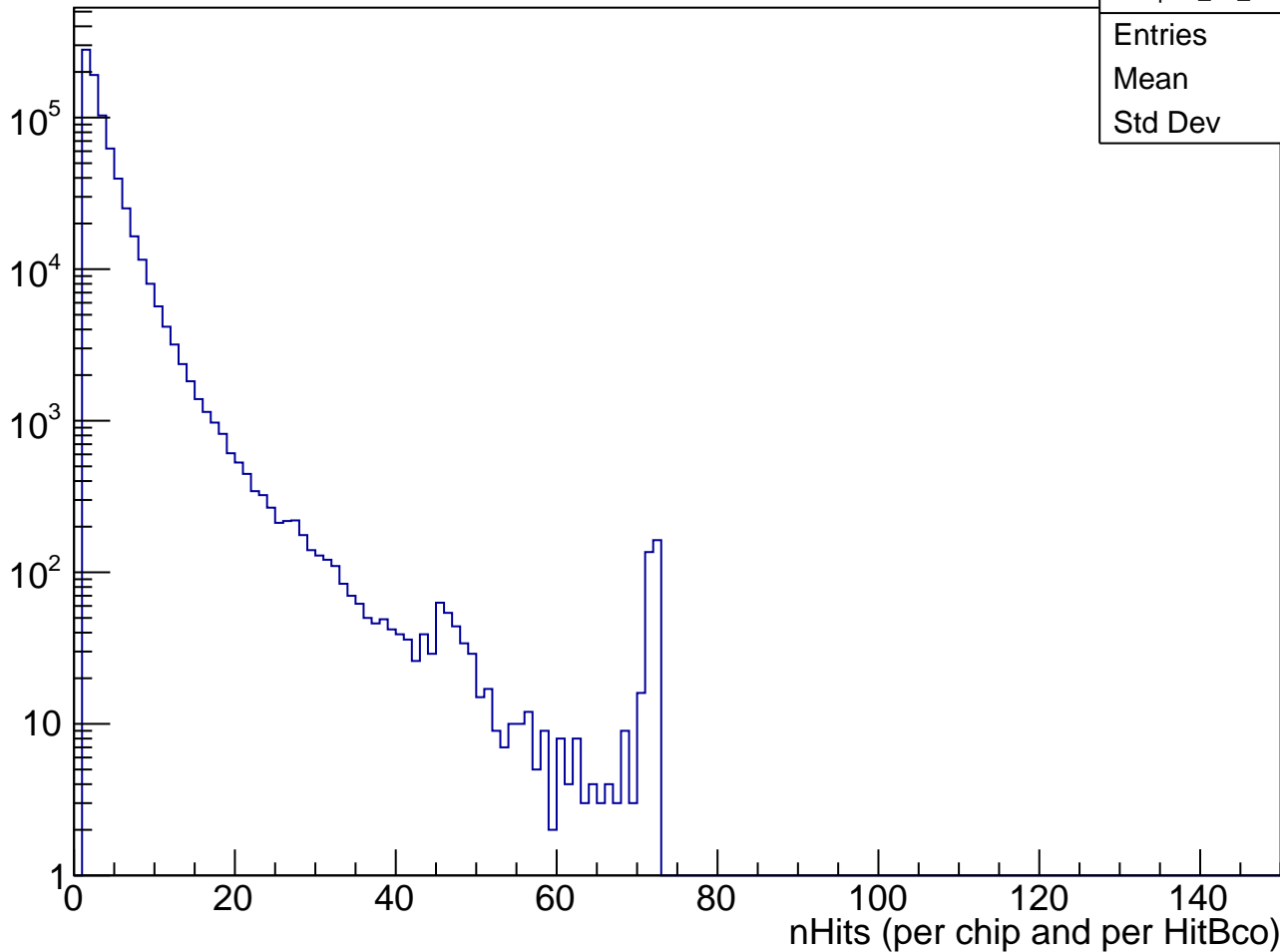
Entries



nChipHit_F4_Fch3_Chip21	
Entries	752255
Mean	2.976
Std Dev	3.473

nChipHit_F4_Fch3_Chip22

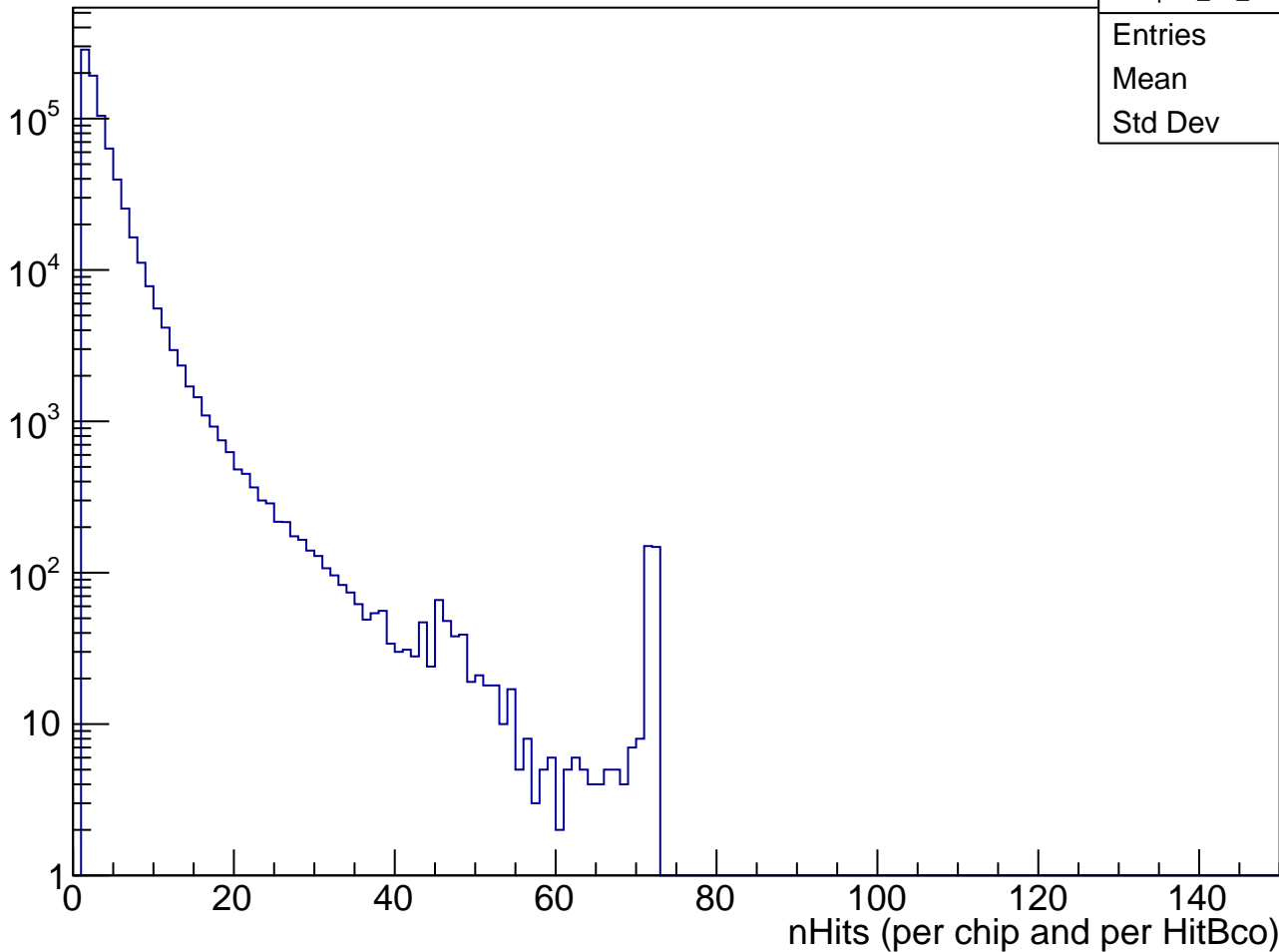
Entries



nChipHit_F4_Fch3_Chip22	
Entries	764373
Mean	2.97
Std Dev	3.488

nChipHit_F4_Fch3_Chip23

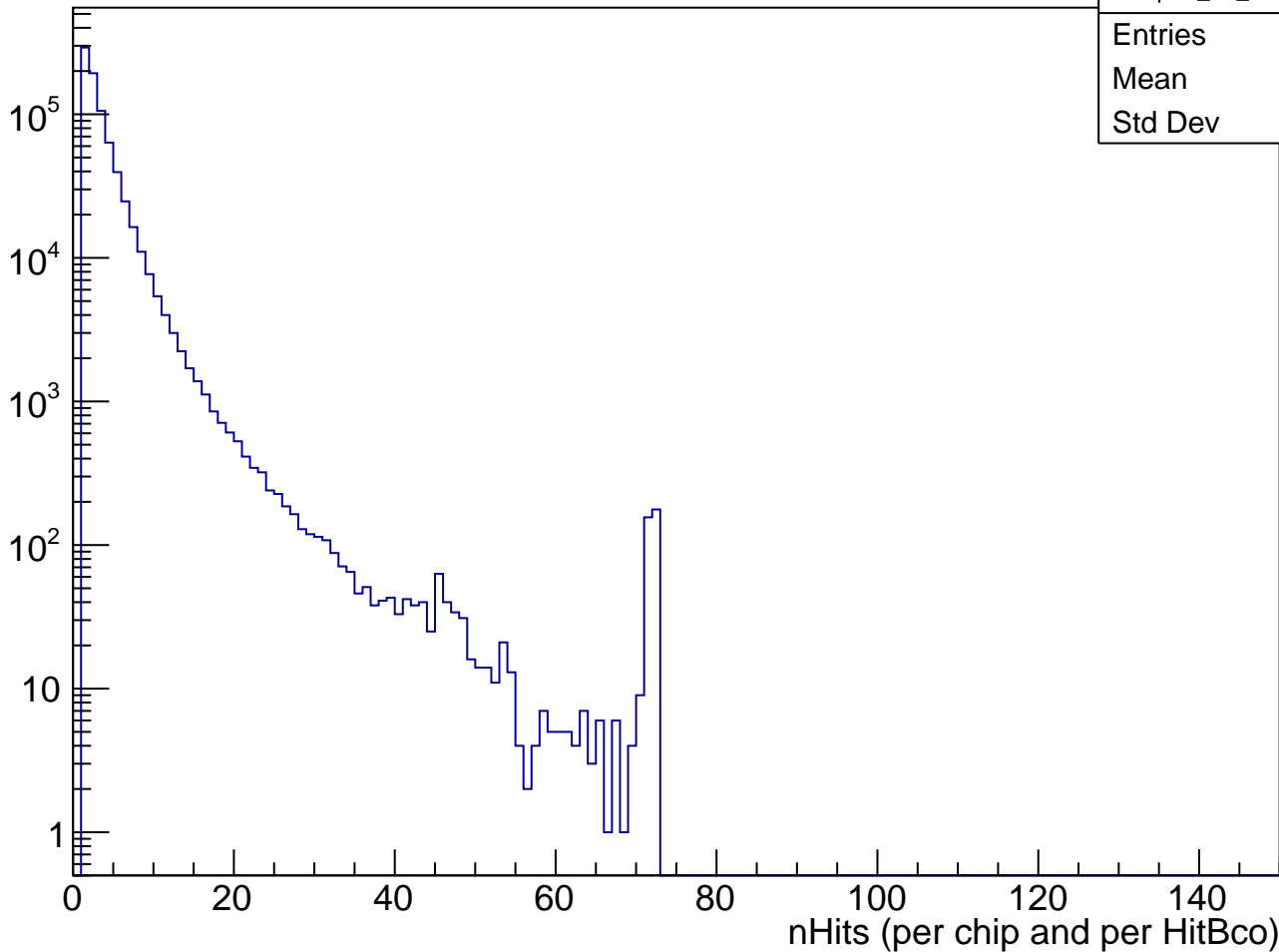
Entries



nChipHit_F4_Fch3_Chip23	
Entries	771616
Mean	2.942
Std Dev	3.441

nChipHit_F4_Fch3_Chip24

Entries

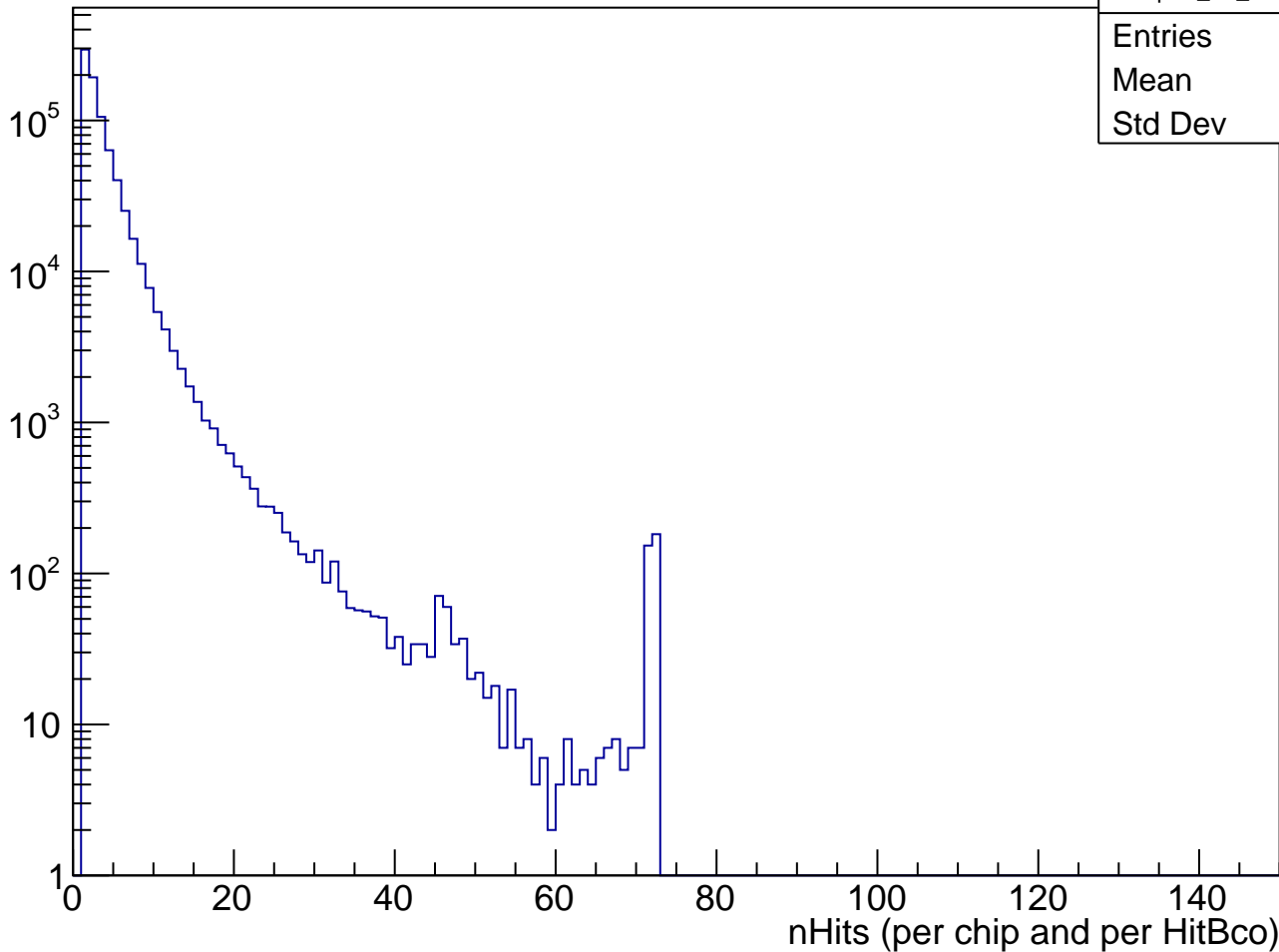


nChipHit_F4_Fch3_Chip24	
Entries	778663
Mean	2.908
Std Dev	3.405

nHits (per chip and per HitBco)

nChipHit_F4_Fch3_Chip25

Entries

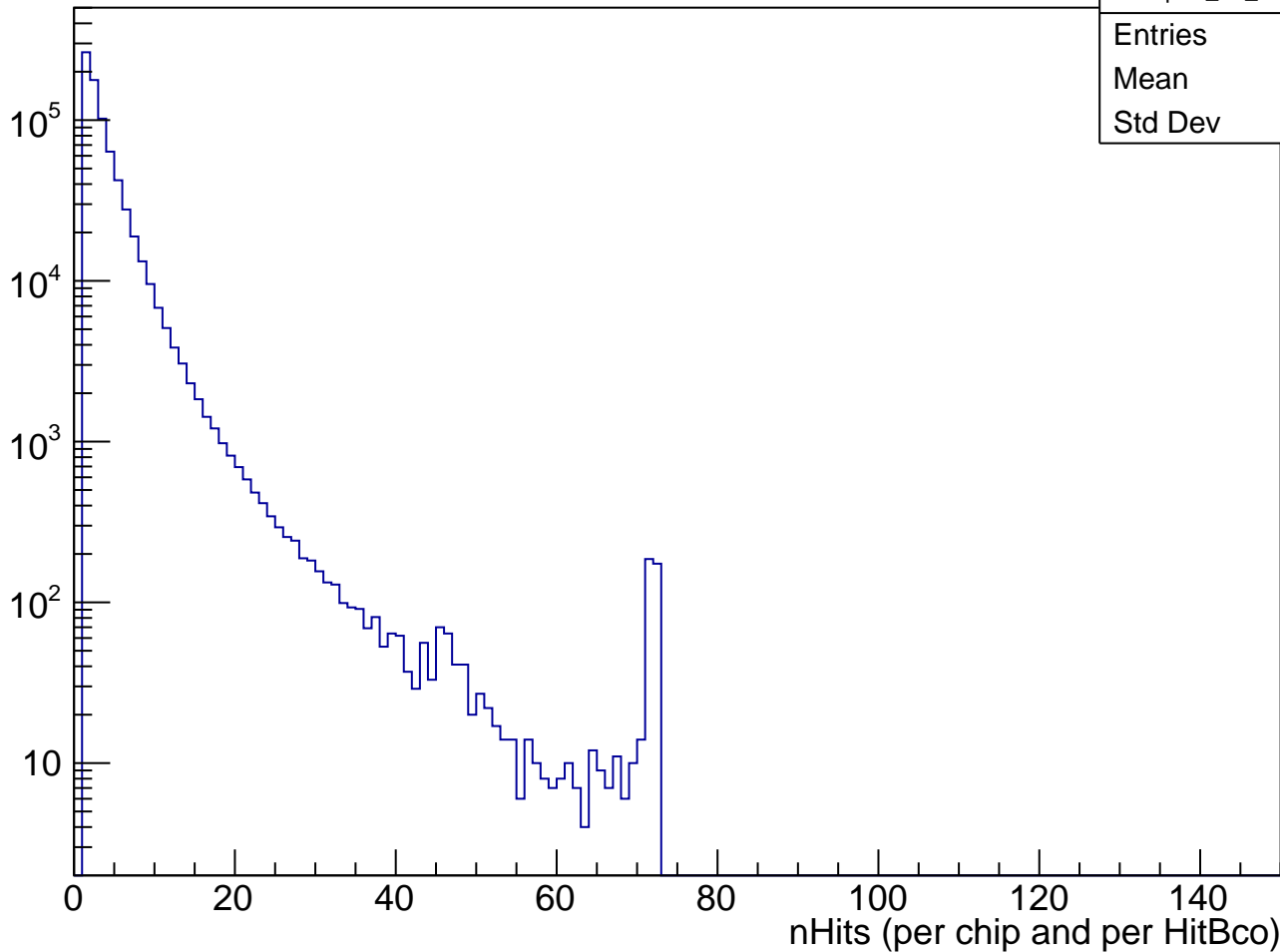


nChipHit_F4_Fch3_Chip25	
Entries	783361
Mean	2.915
Std Dev	3.443

nHits (per chip and per HitBco)

nChipHit_F4_Fch3_Chip3

Entries

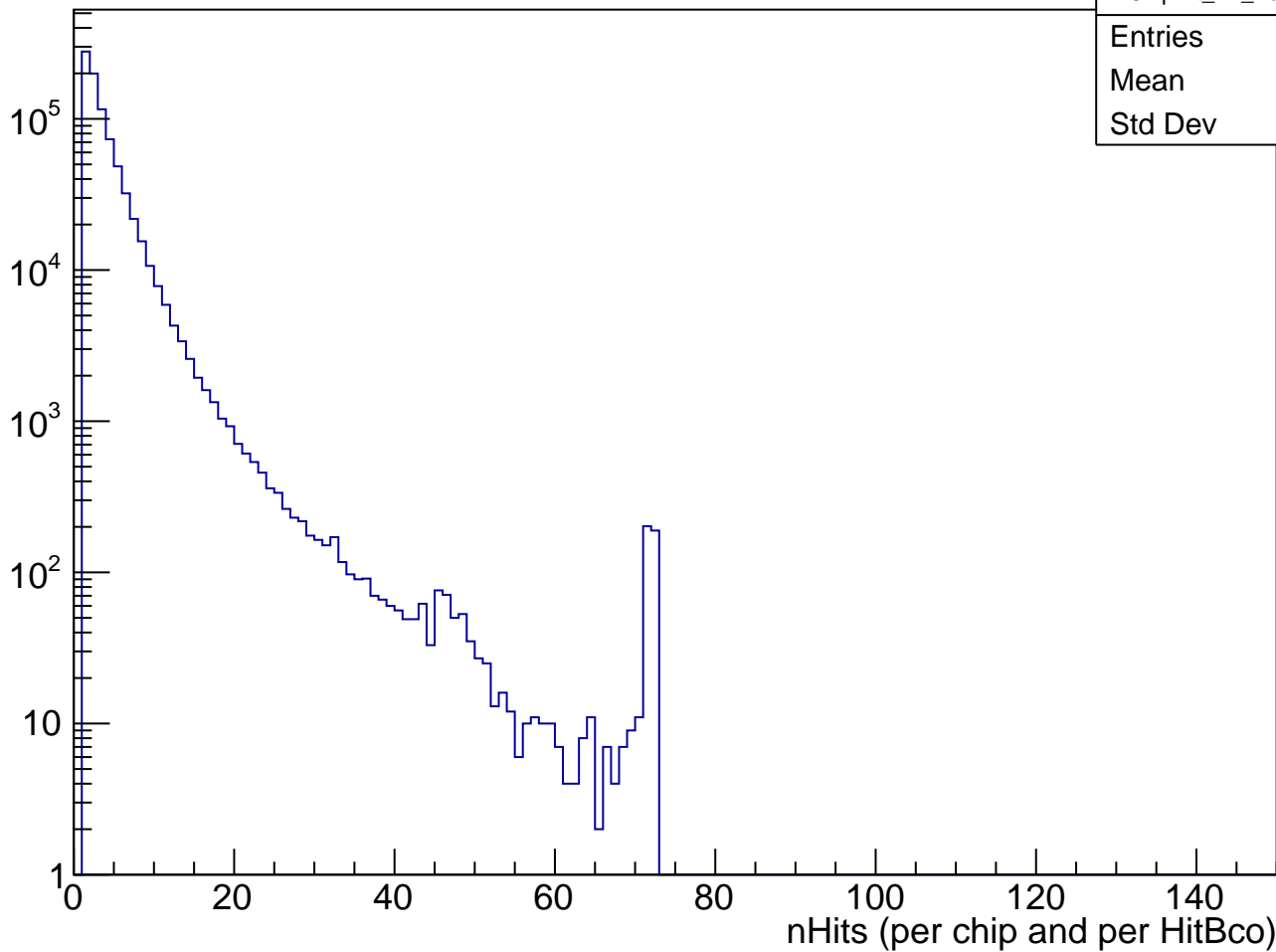


nChipHit_F4_Fch3_Chip3	
Entries	752478
Mean	3.198
Std Dev	3.831

nHits (per chip and per HitBco)

nChipHit_F4_Fch3_Chip4

Entries



nChipHit_F4_Fch3_Chip4

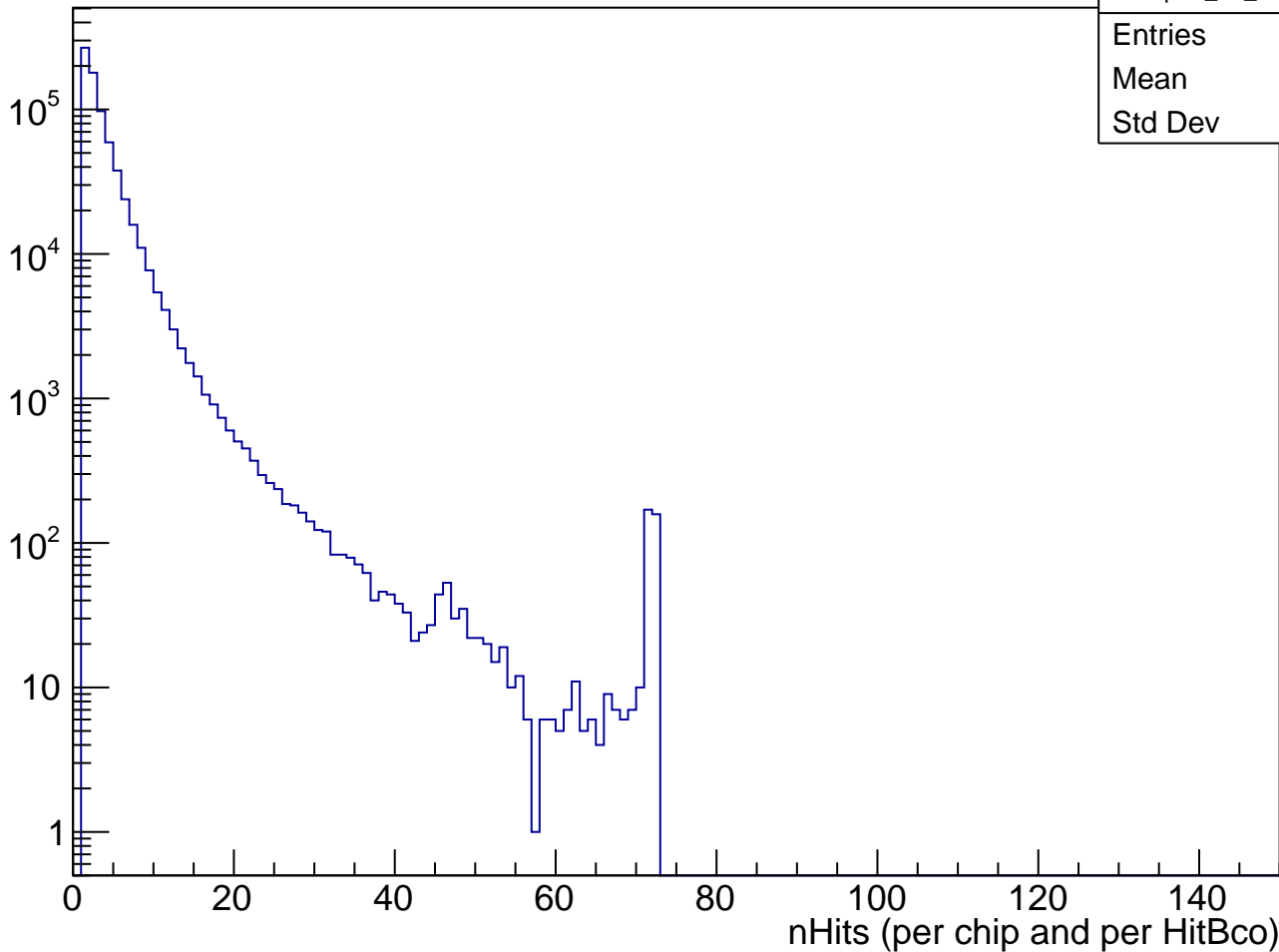
Entries 832794

Mean 3.248

Std Dev 3.799

nChipHit_F4_Fch3_Chip5

Entries



nChipHit_F4_Fch3_Chip5

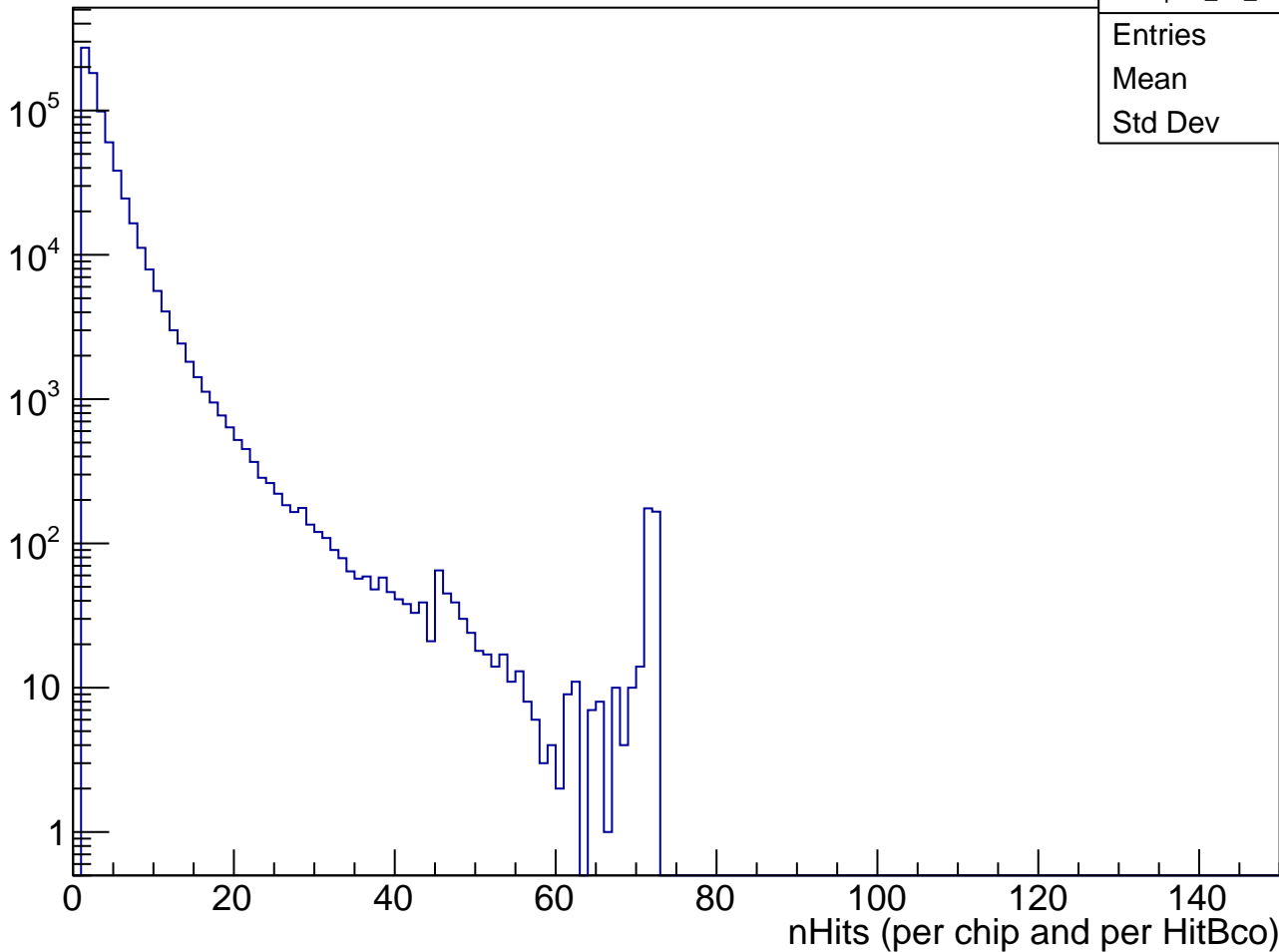
Entries 725455

Mean 2.982

Std Dev 3.55

nChipHit_F4_Fch3_Chip6

Entries



nChipHit_F4_Fch3_Chip6

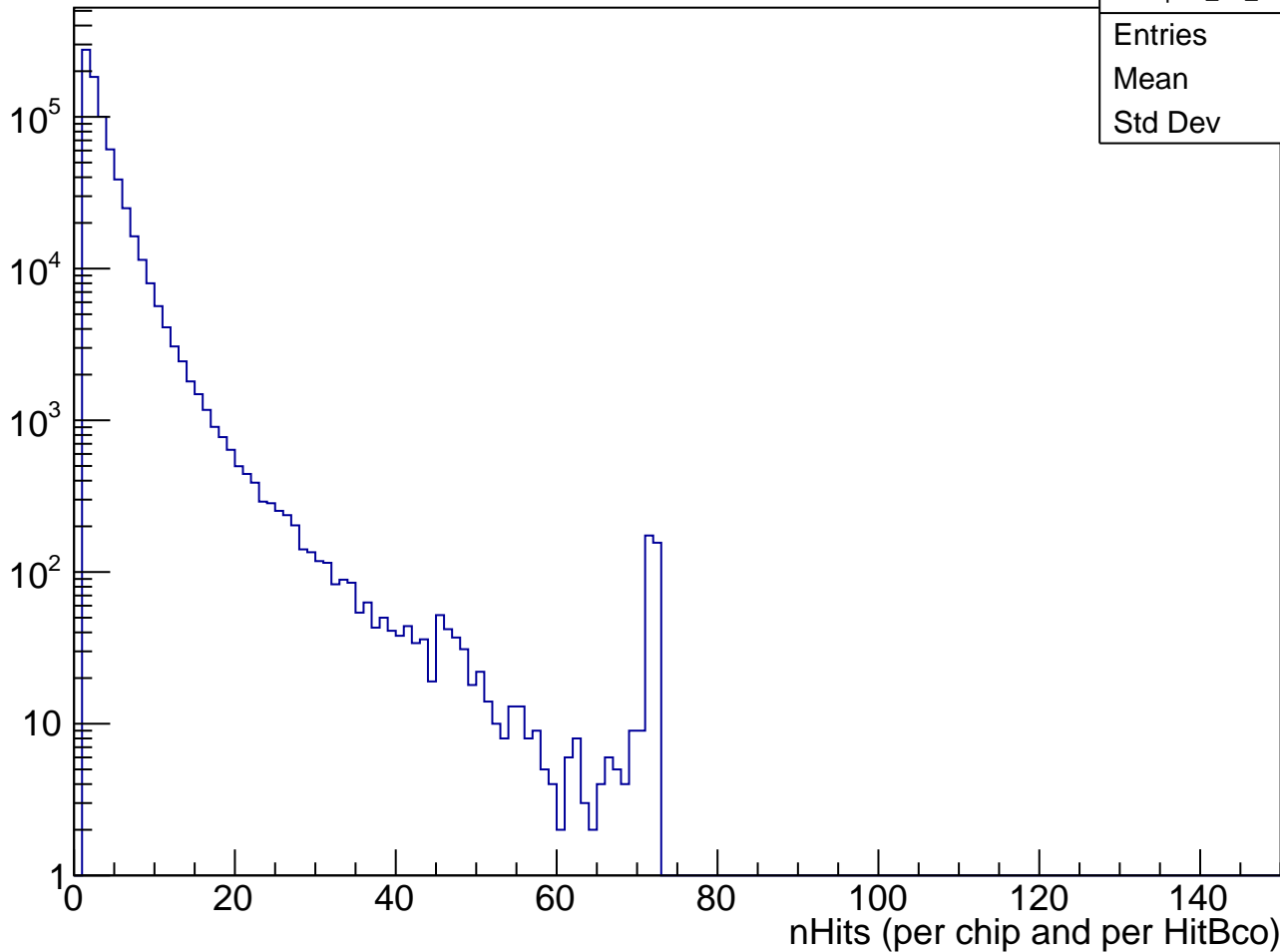
Entries 737709

Mean 2.987

Std Dev 3.563

nChipHit_F4_Fch3_Chip7

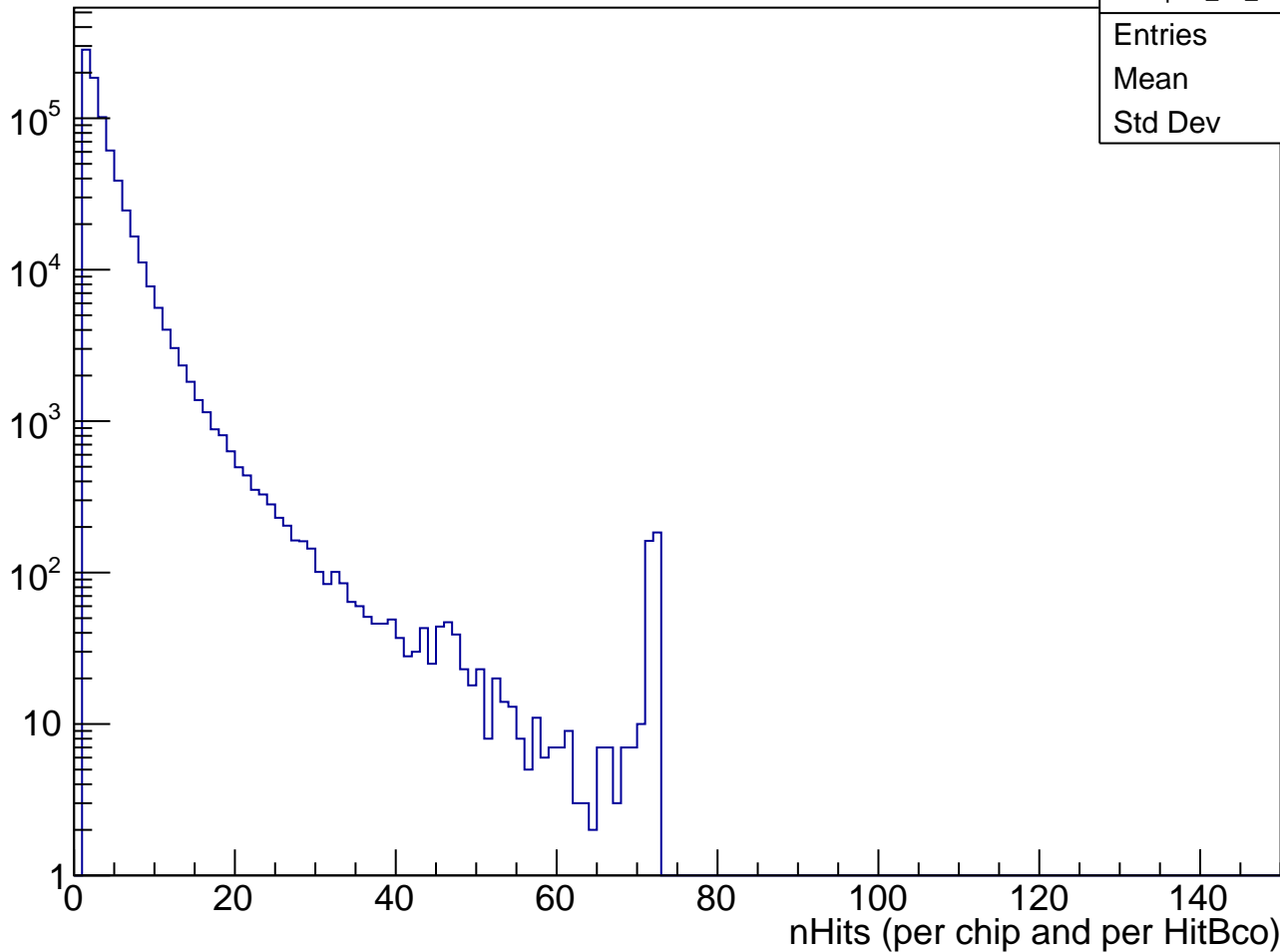
Entries



nChipHit_F4_Fch3_Chip7	
Entries	747446
Mean	2.981
Std Dev	3.527

nChipHit_F4_Fch3_Chip8

Entries



nChipHit_F4_Fch3_Chip8

Entries 756648

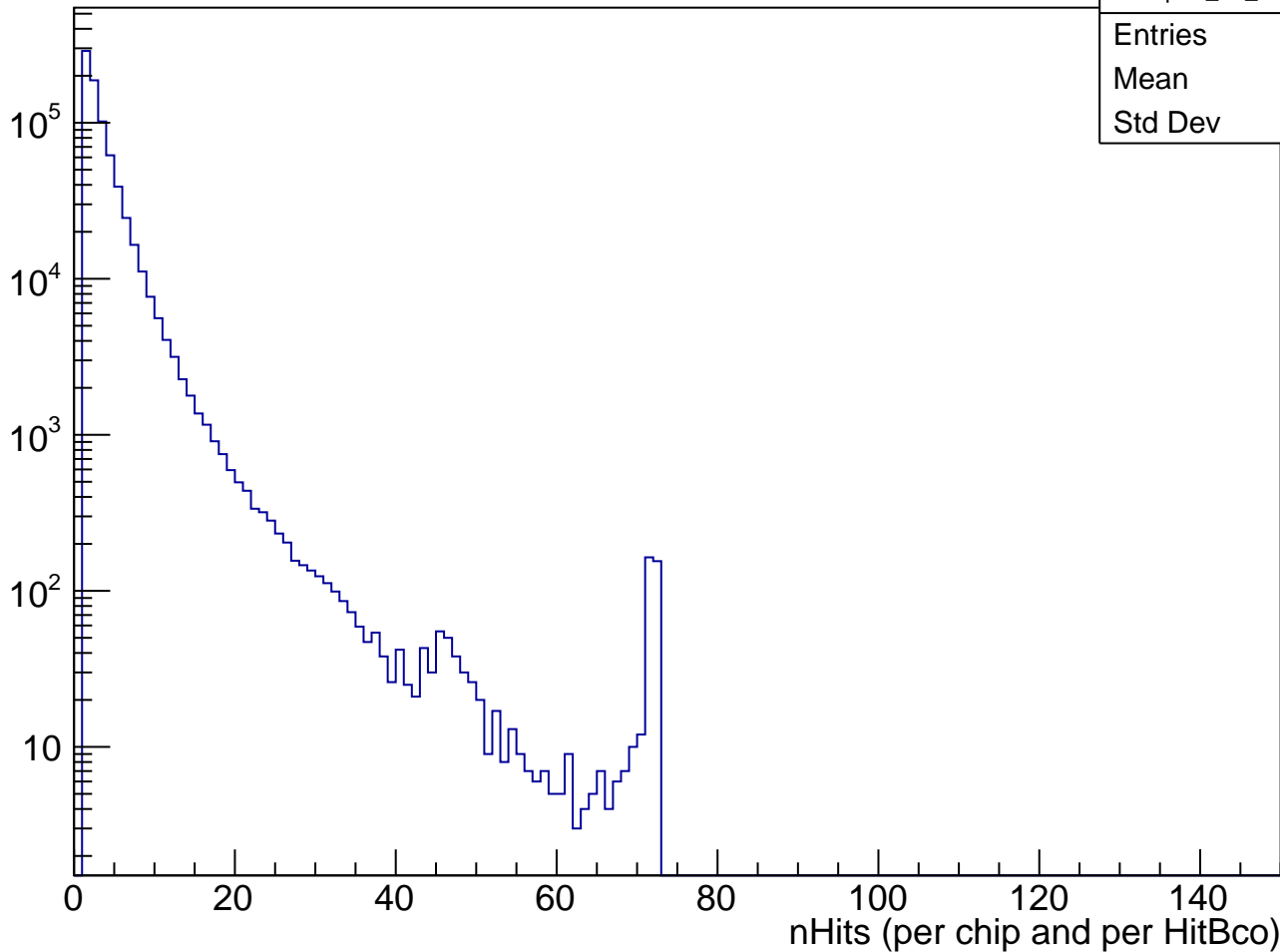
Mean 2.95

Std Dev 3.505

nHits (per chip and per HitBco)

nChipHit_F4_Fch3_Chip9

Entries

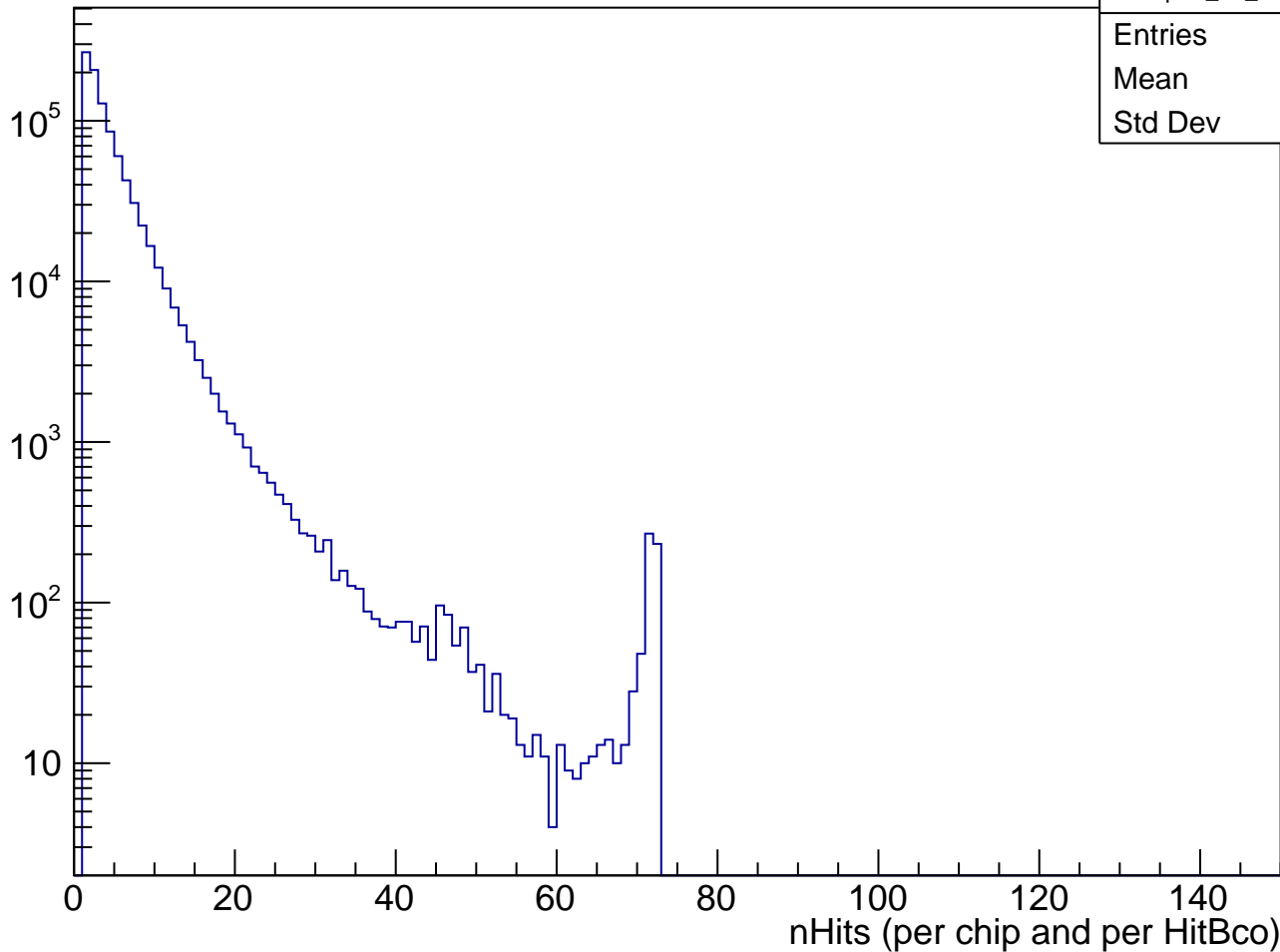


nChipHit_F4_Fch3_Chip9	
Entries	763989
Mean	2.932
Std Dev	3.469

nHits (per chip and per HitBco)

nChipHit_F4_Fch4_Chip0

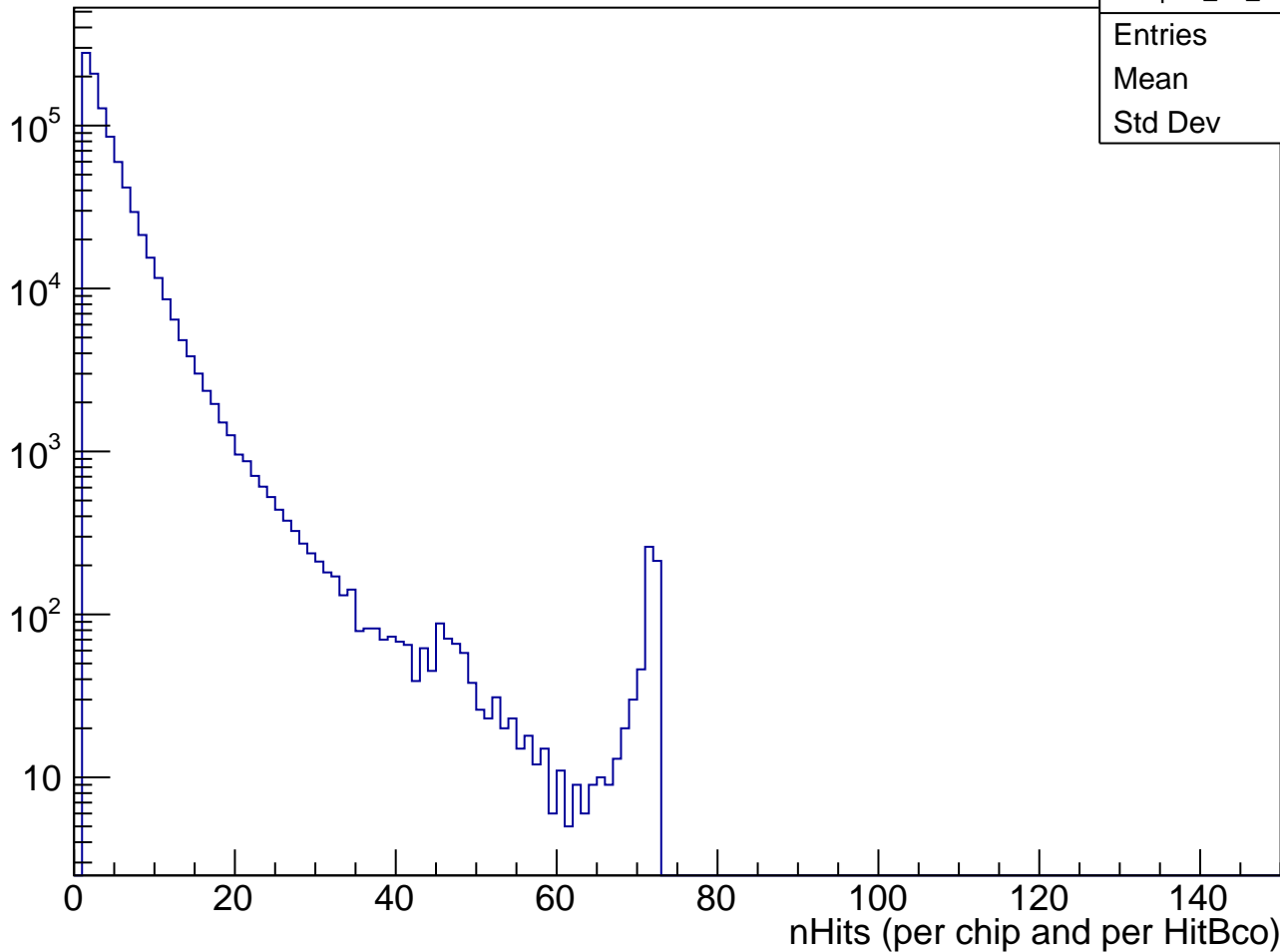
Entries



nChipHit_F4_Fch4_Chip0	
Entries	917818
Mean	3.682
Std Dev	4.201

nChipHit_F4_Fch4_Chip1

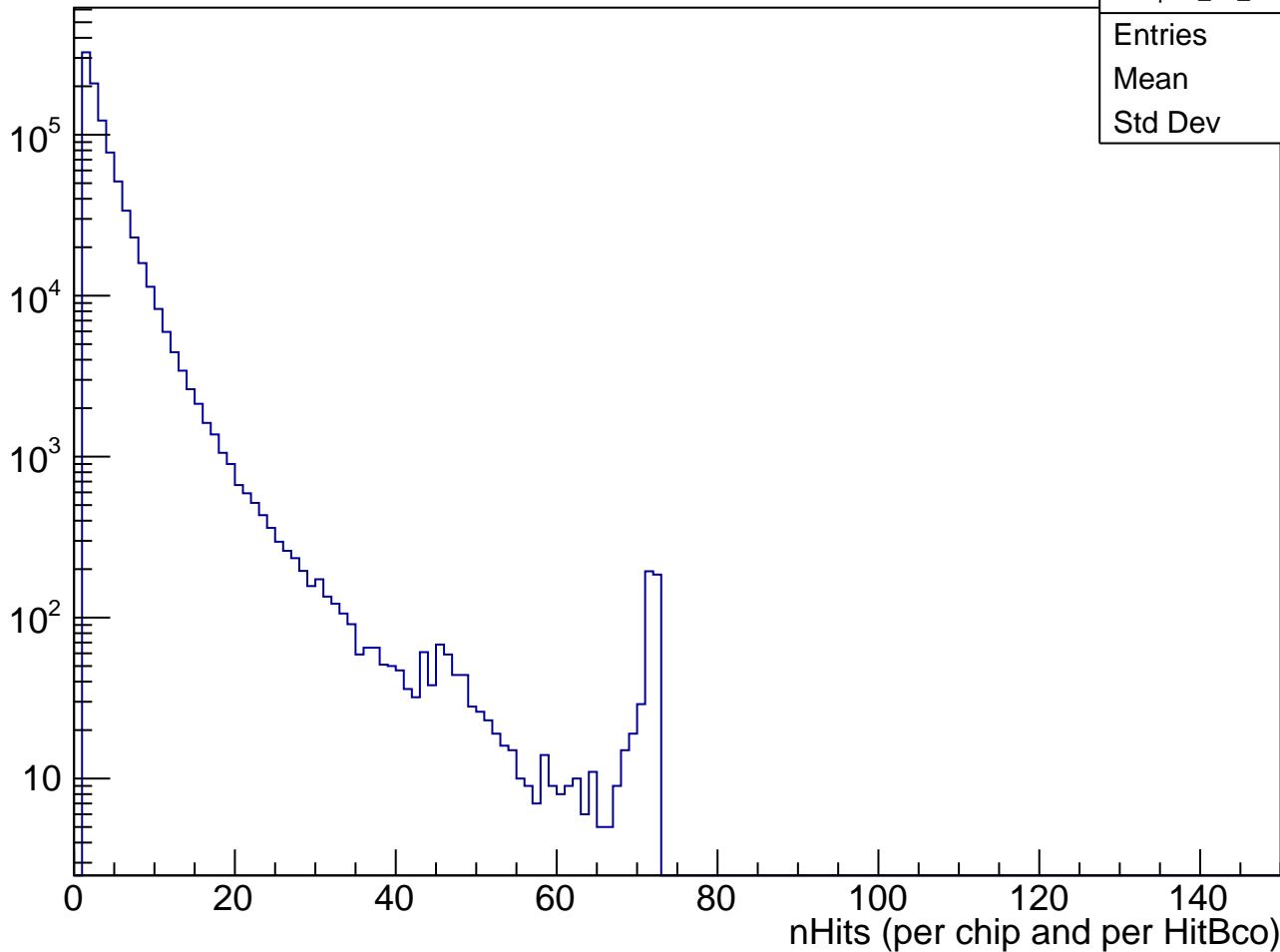
Entries



nChipHit_F4_Fch4_Chip1	
Entries	921664
Mean	3.584
Std Dev	4.103

nChipHit_F4_Fch4_Chip10

Entries

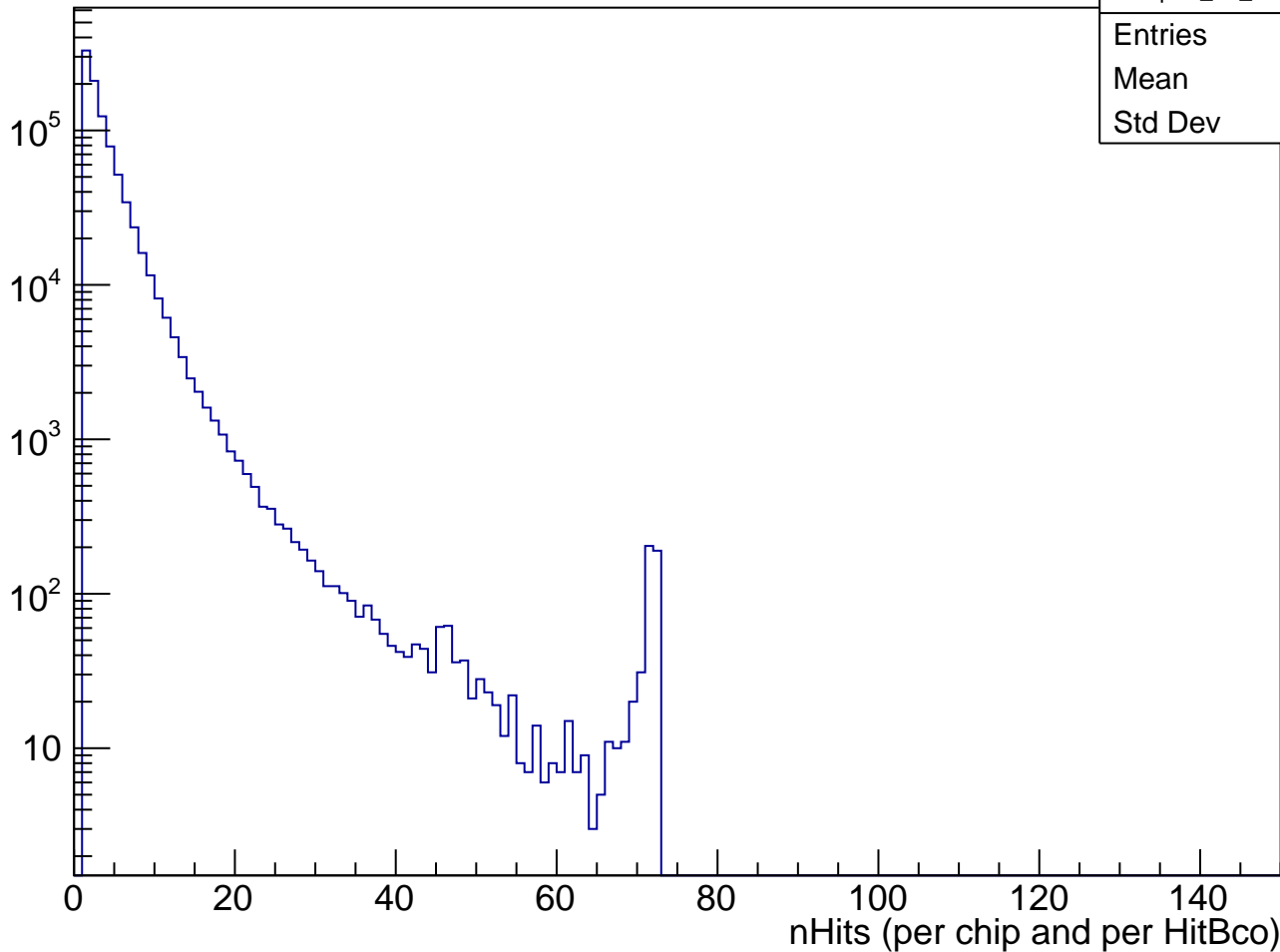


nChipHit_F4_Fch4_Chip10	
Entries	906417
Mean	3.144
Std Dev	3.672

nHits (per chip and per HitBco)

nChipHit_F4_Fch4_Chip11

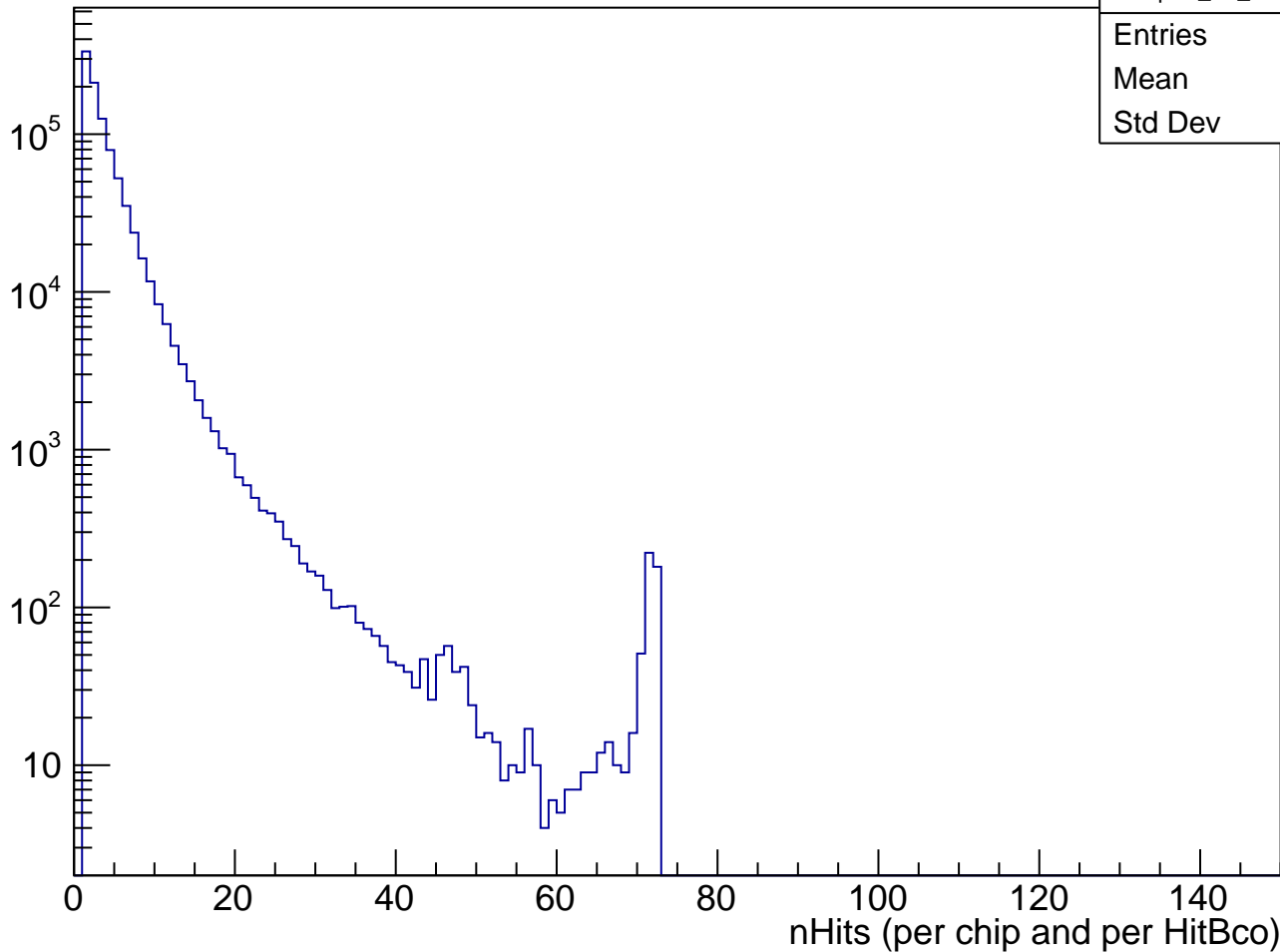
Entries



nChipHit_F4_Fch4_Chip11	
Entries	915370
Mean	3.135
Std Dev	3.648

nChipHit_F4_Fch4_Chip12

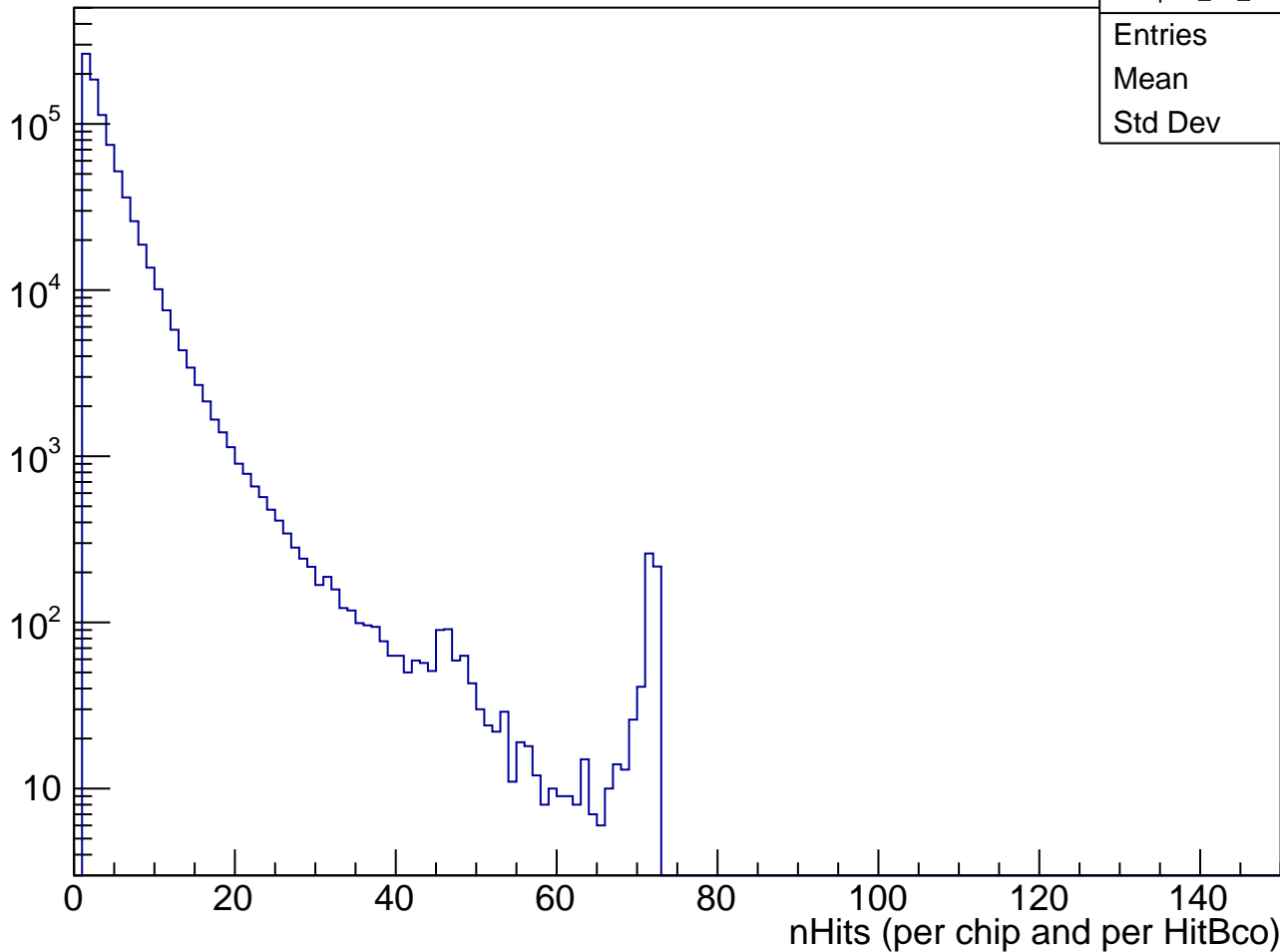
Entries



nChipHit_F4_Fch4_Chip12	
Entries	928578
Mean	3.137
Std Dev	3.655

nChipHit_F4_Fch4_Chip13

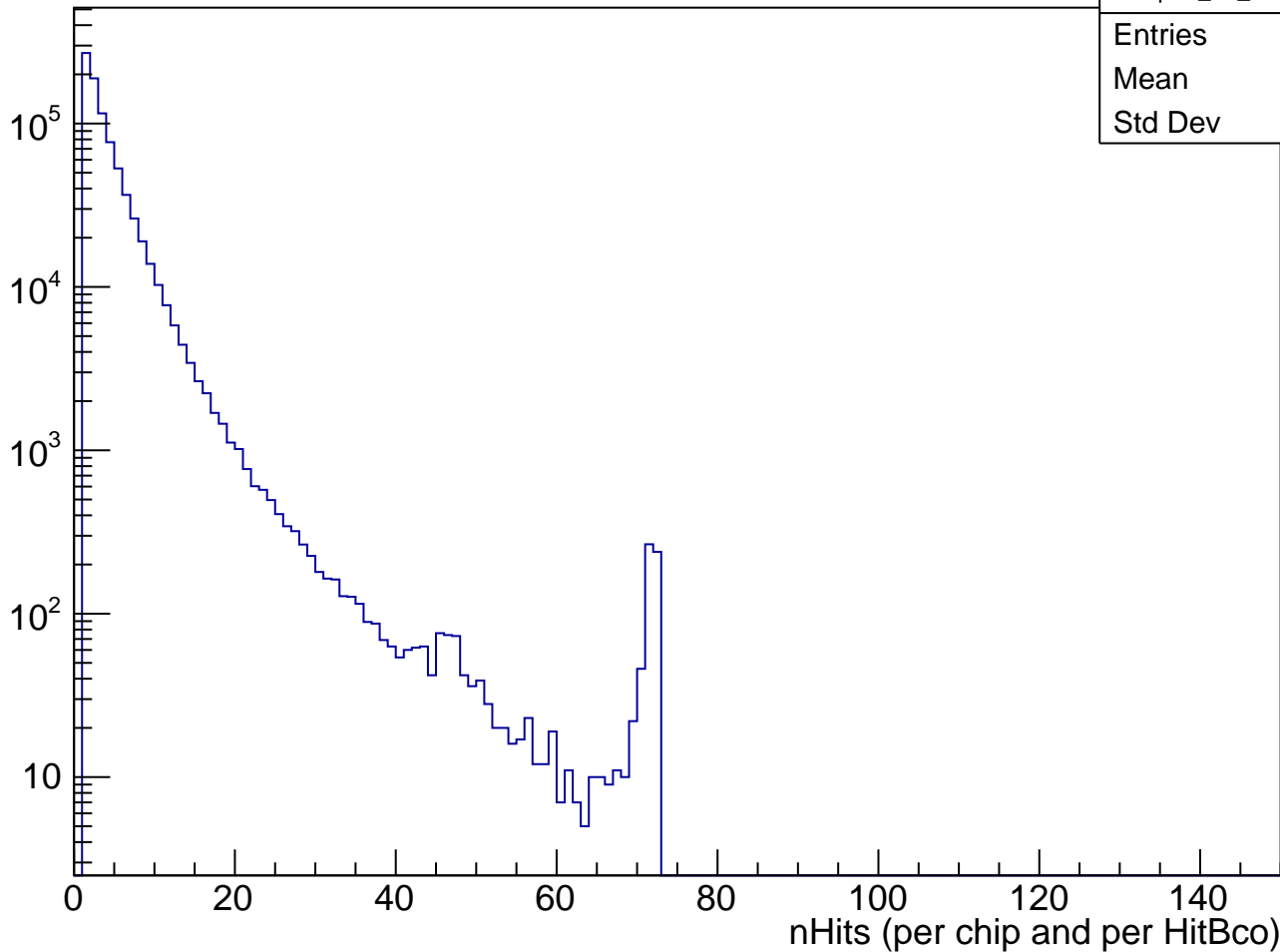
Entries



nChipHit_F4_Fch4_Chip13	
Entries	831489
Mean	3.544
Std Dev	4.18

nChipHit_F4_Fch4_Chip14

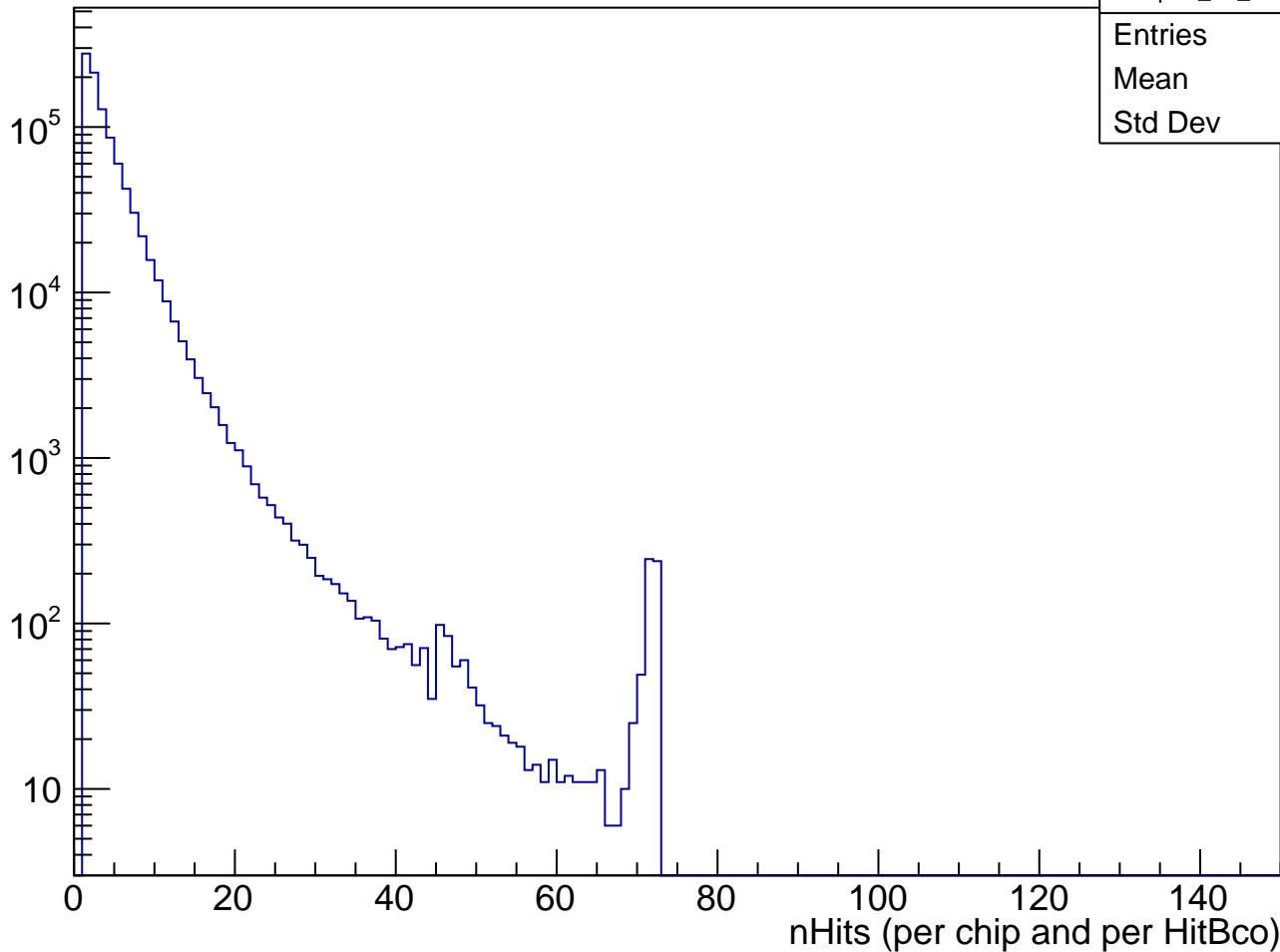
Entries



nChipHit_F4_Fch4_Chip14	
Entries	848940
Mean	3.536
Std Dev	4.172

nChipHit_F4_Fch4_Chip15

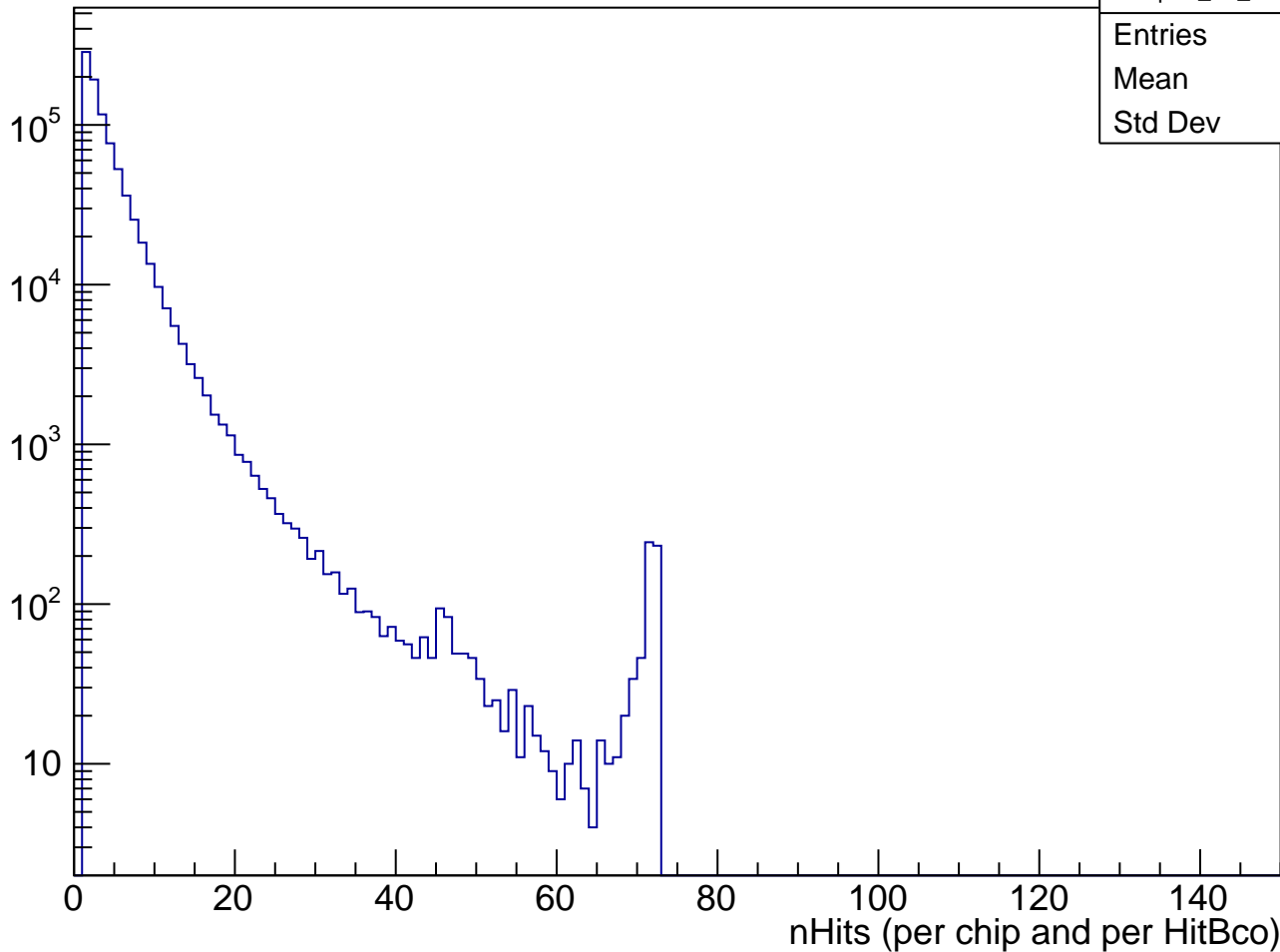
Entries



nChipHit_F4_Fch4_Chip15	
Entries	930856
Mean	3.611
Std Dev	4.137

nChipHit_F4_Fch4_Chip16

Entries

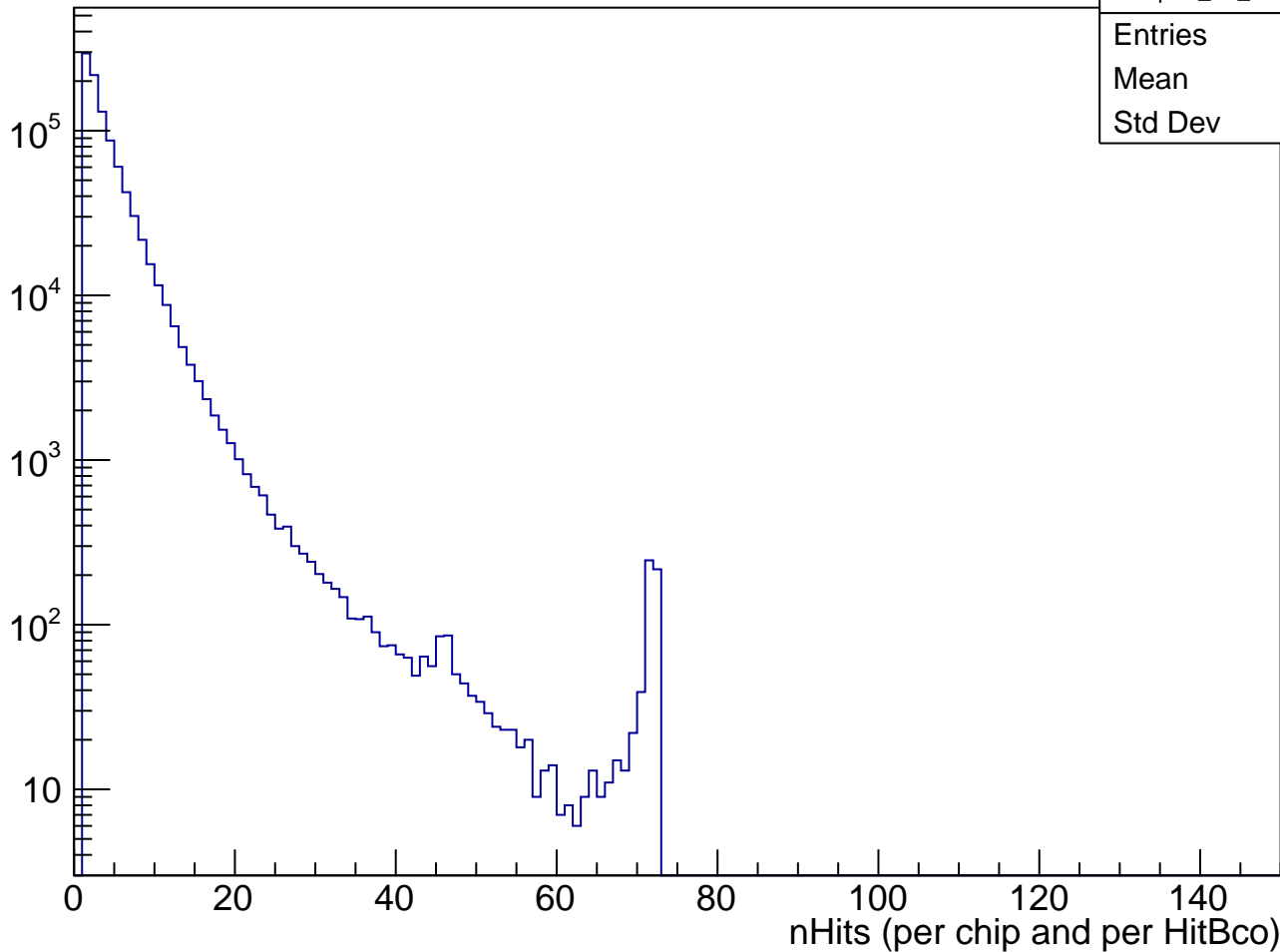


nChipHit_F4_Fch4_Chip16	
Entries	863812
Mean	3.438
Std Dev	4.091

nHits (per chip and per HitBco)

nChipHit_F4_Fch4_Chip17

Entries

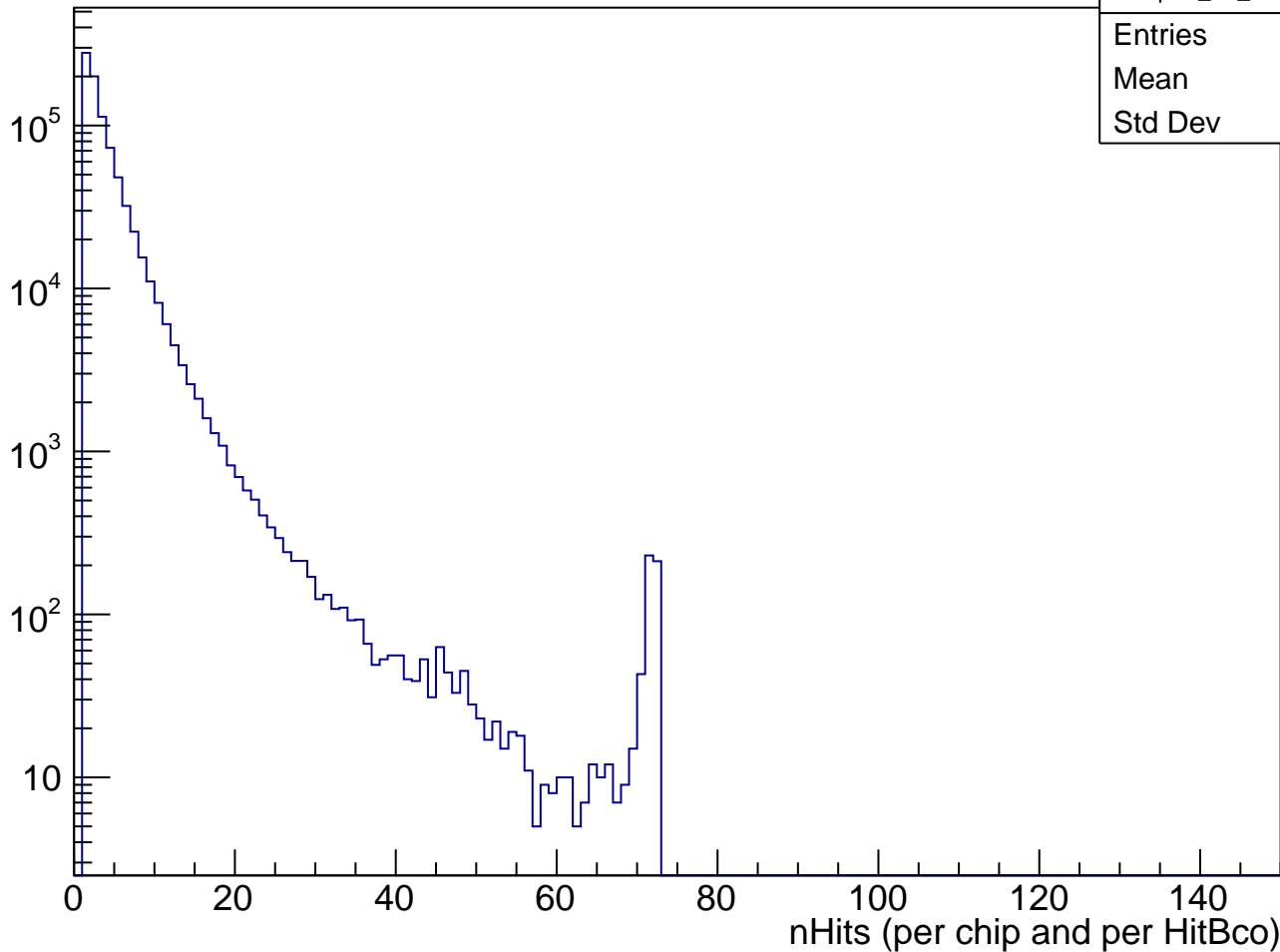


nChipHit_F4_Fch4_Chip17	
Entries	953582
Mean	3.53
Std Dev	4.04

nHits (per chip and per HitBco)

nChipHit_F4_Fch4_Chip18

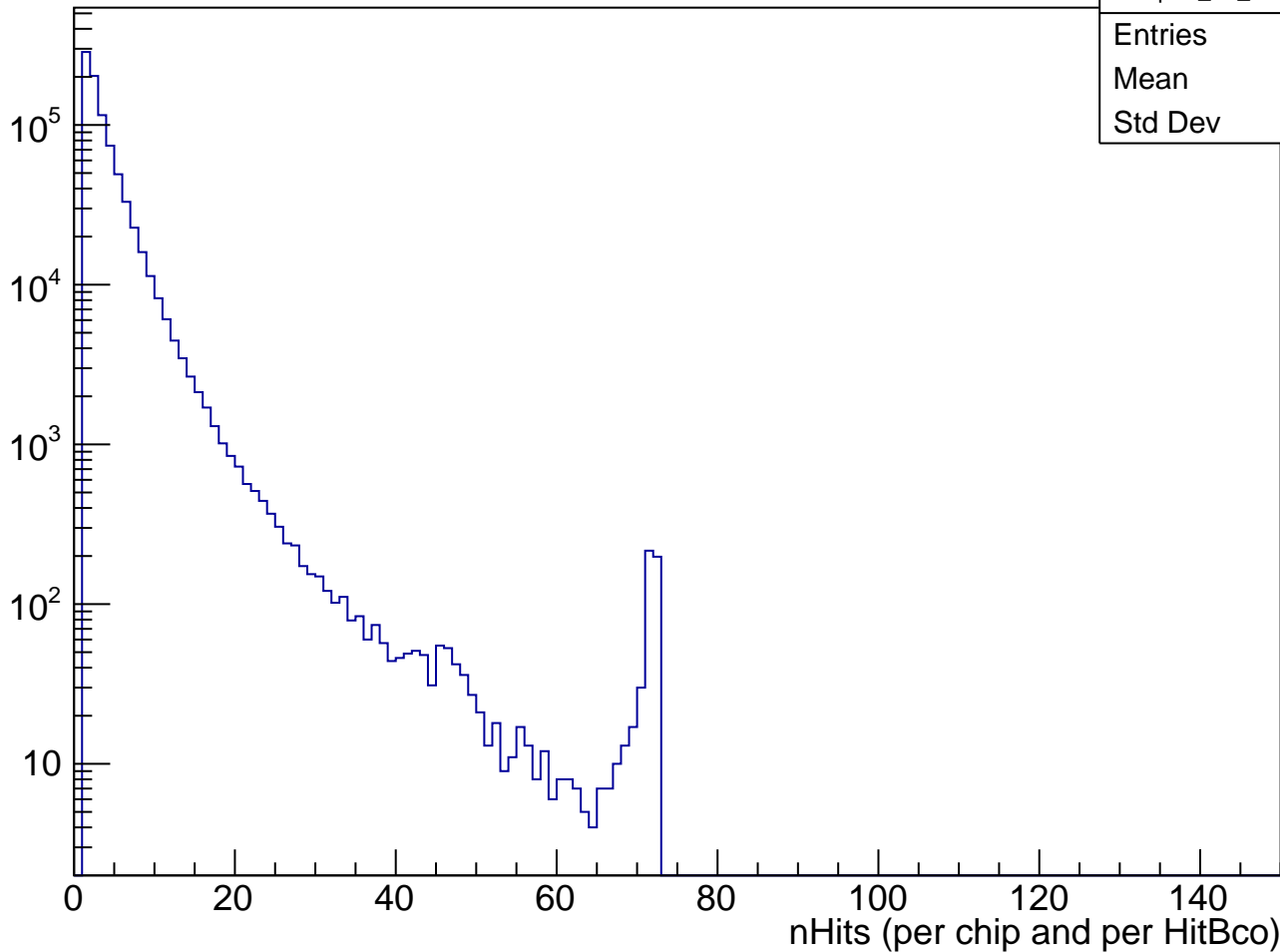
Entries



nChipHit_F4_Fch4_Chip18	
Entries	831926
Mean	3.249
Std Dev	3.822

nChipHit_F4_Fch4_Chip19

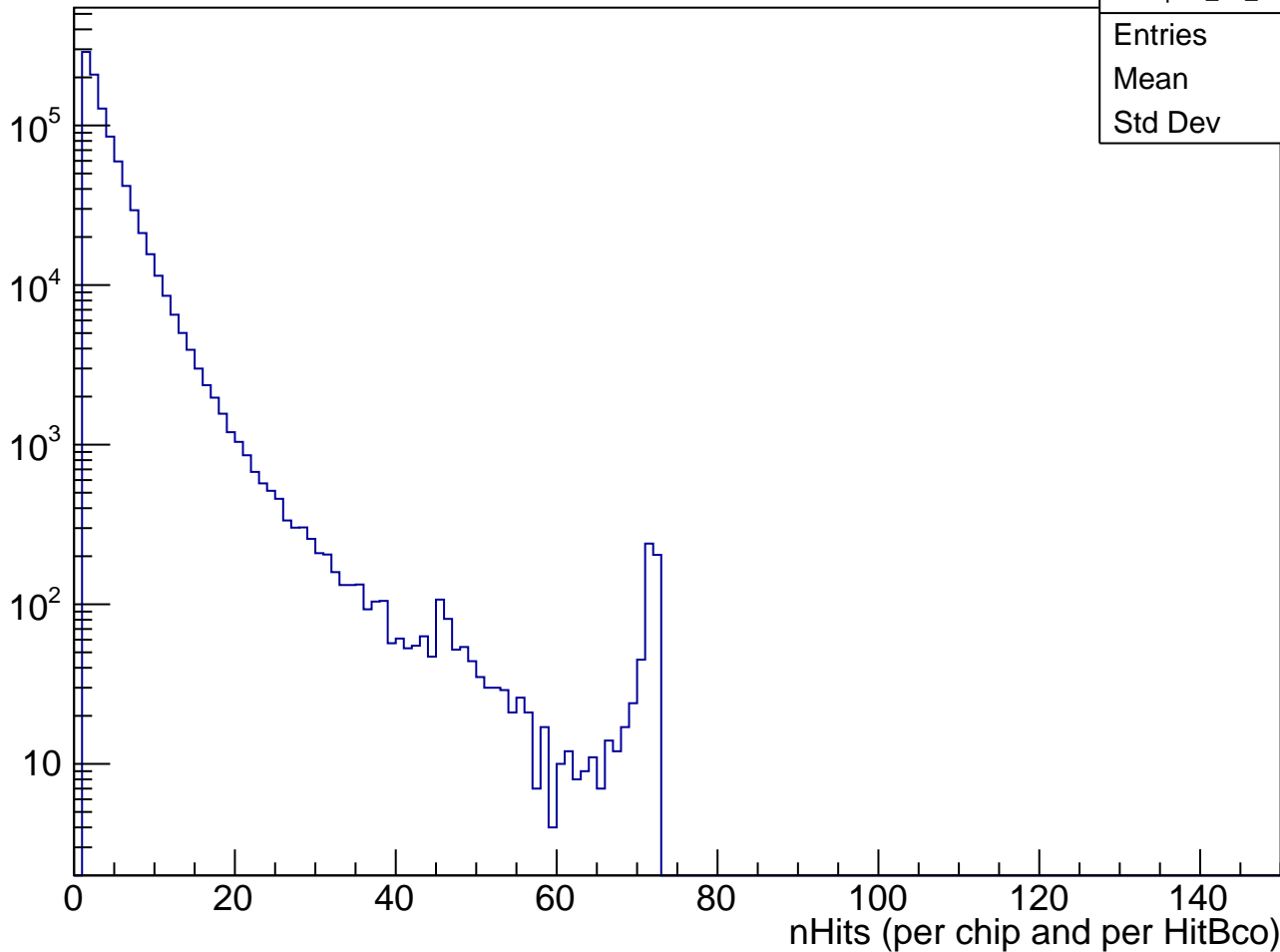
Entries



nChipHit_F4_Fch4_Chip19	
Entries	848553
Mean	3.238
Std Dev	3.762

nChipHit_F4_Fch4_Chip2

Entries



nChipHit_F4_Fch4_Chip2

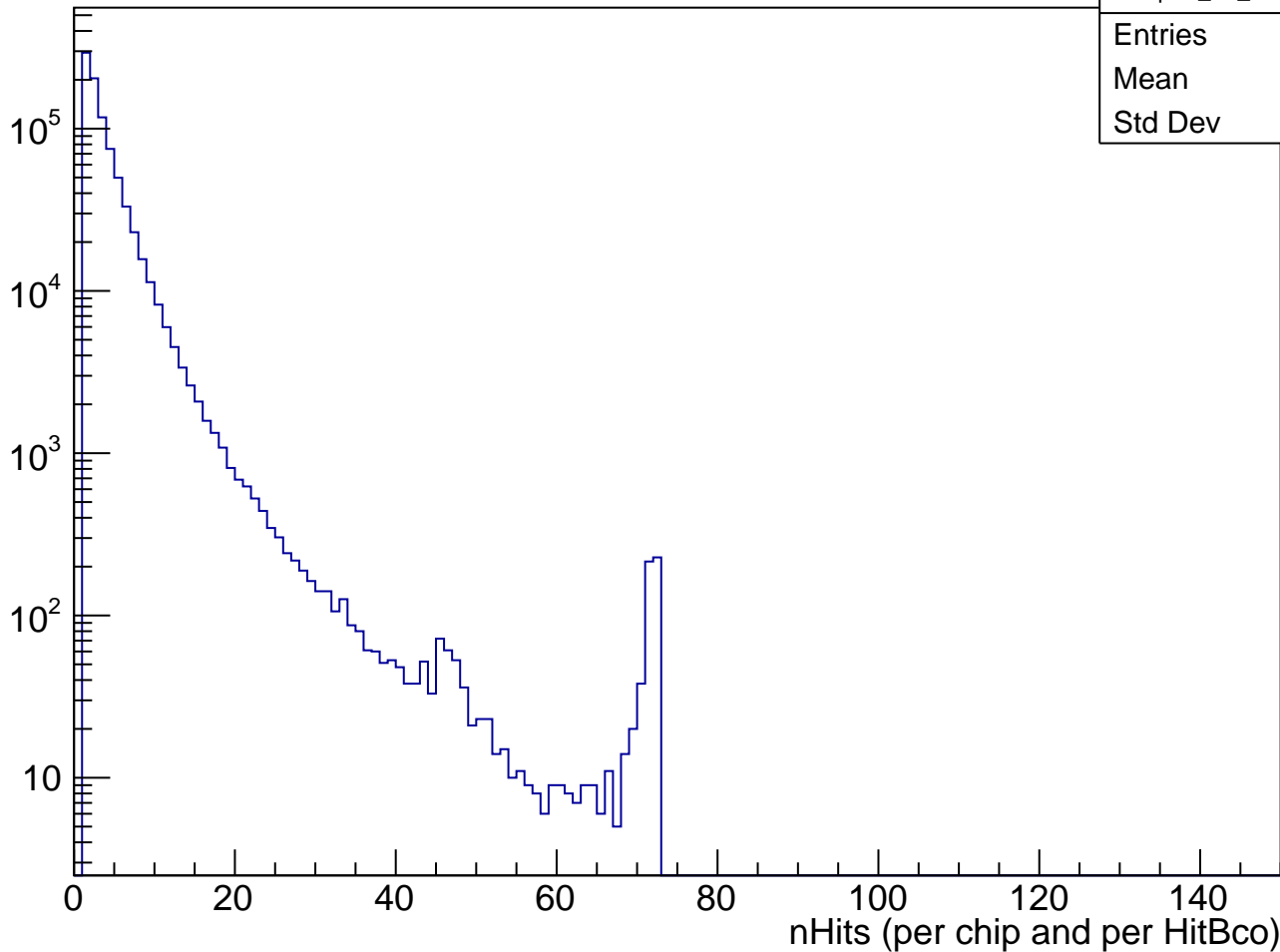
Entries 930777

Mean 3.563

Std Dev 4.103

nChipHit_F4_Fch4_Chip20

Entries

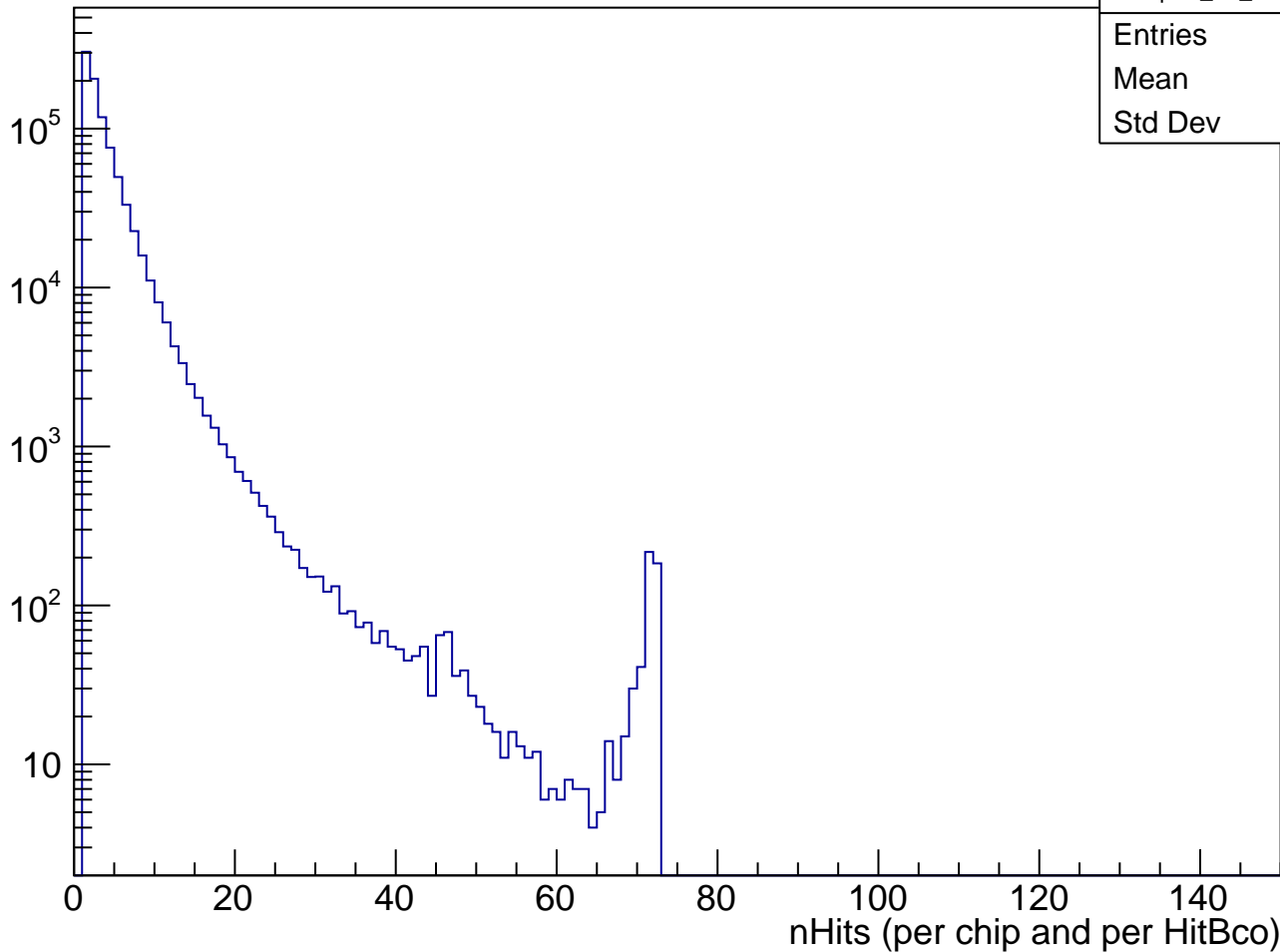


nChipHit_F4_Fch4_Chip20	
Entries	860695
Mean	3.22
Std Dev	3.779

nHits (per chip and per HitBco)

nChipHit_F4_Fch4_Chip21

Entries

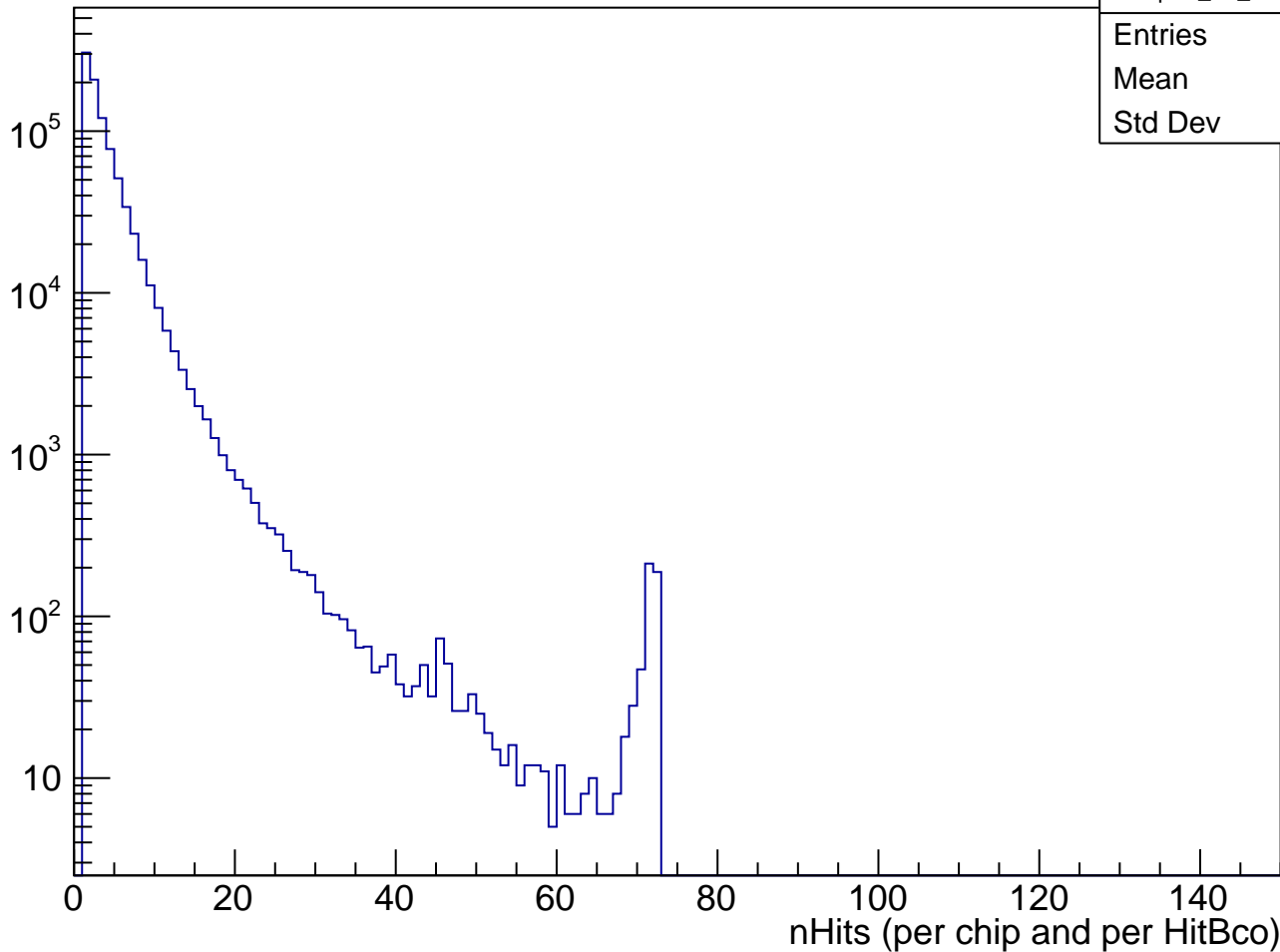


nChipHit_F4_Fch4_Chip21	
Entries	873639
Mean	3.179
Std Dev	3.728

nHits (per chip and per HitBco)

nChipHit_F4_Fch4_Chip22

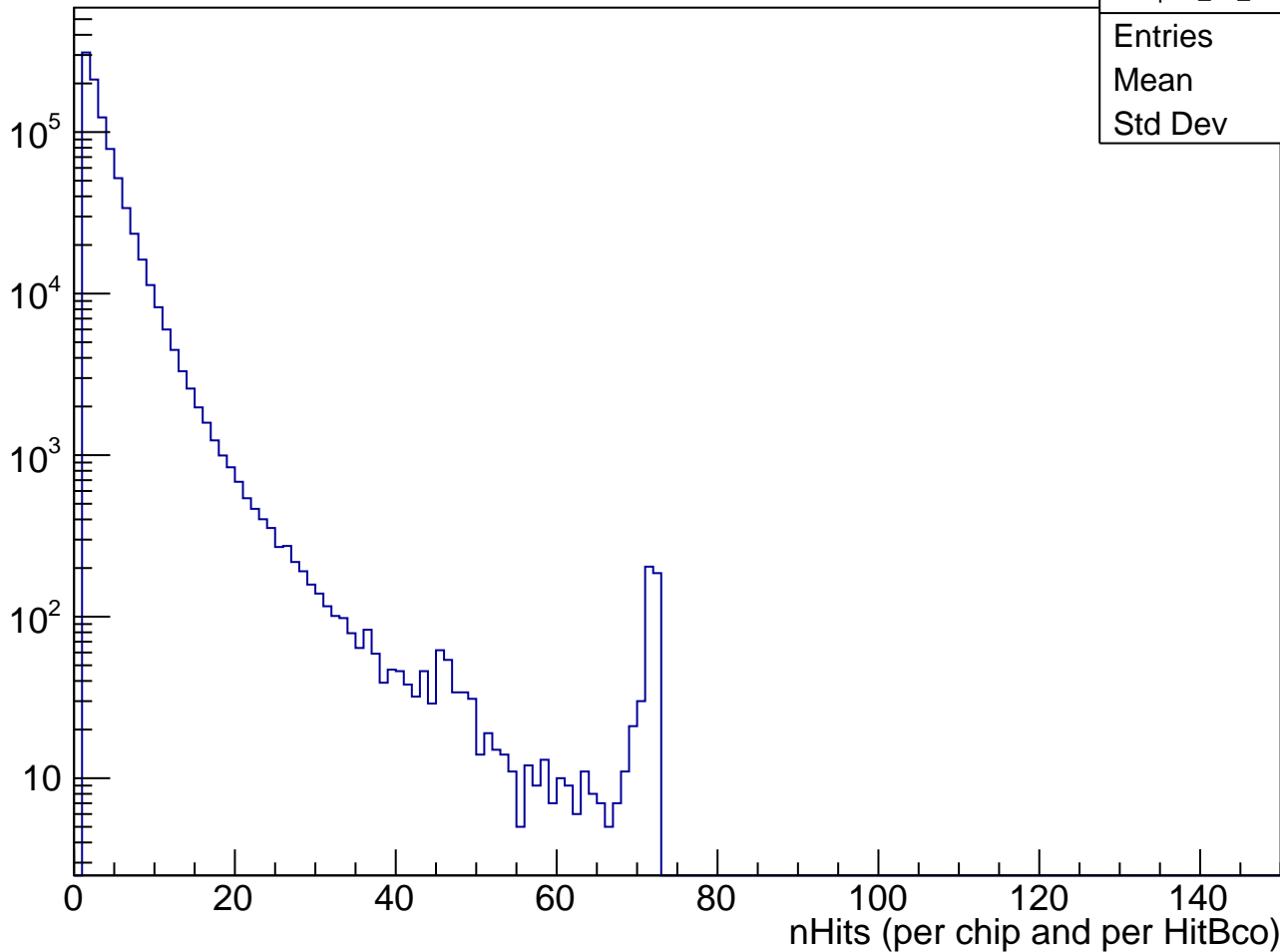
Entries



nChipHit_F4_Fch4_Chip22	
Entries	884190
Mean	3.175
Std Dev	3.684

nChipHit_F4_Fch4_Chip23

Entries

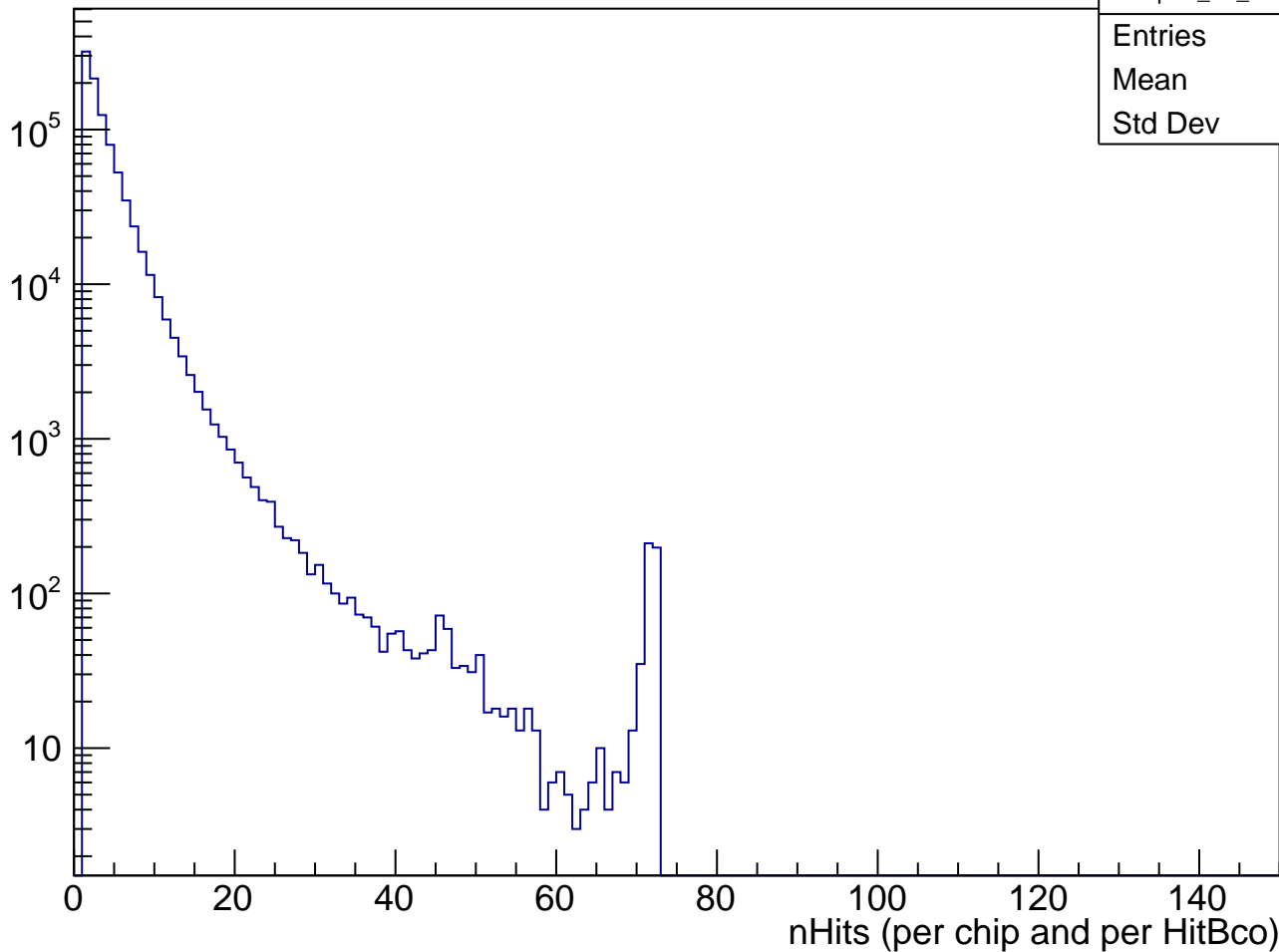


nChipHit_F4_Fch4_Chip23	
Entries	897641
Mean	3.162
Std Dev	3.631

nHits (per chip and per HitBco)

nChipHit_F4_Fch4_Chip24

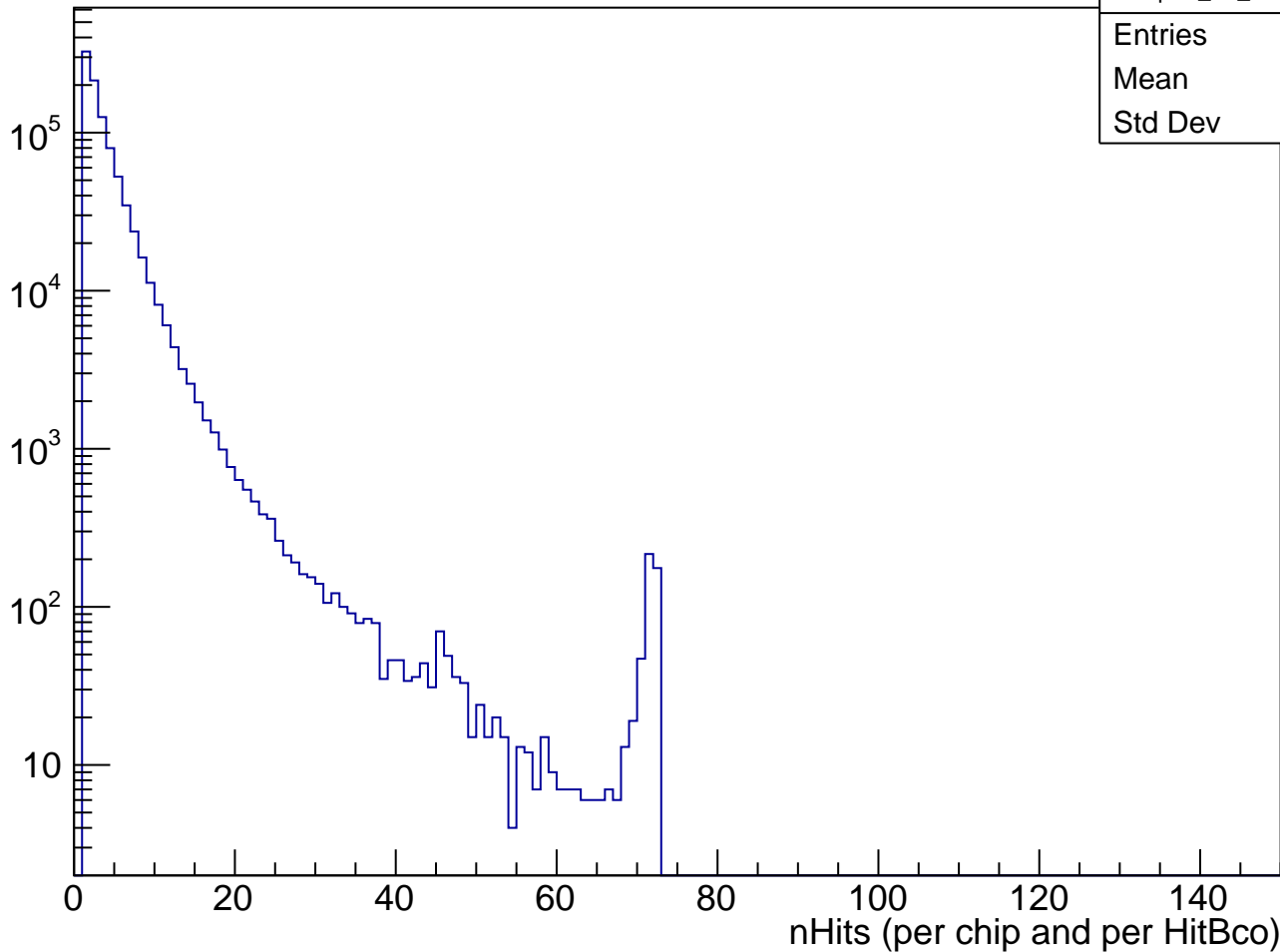
Entries



nChipHit_F4_Fch4_Chip24	
Entries	912874
Mean	3.153
Std Dev	3.641

nChipHit_F4_Fch4_Chip25

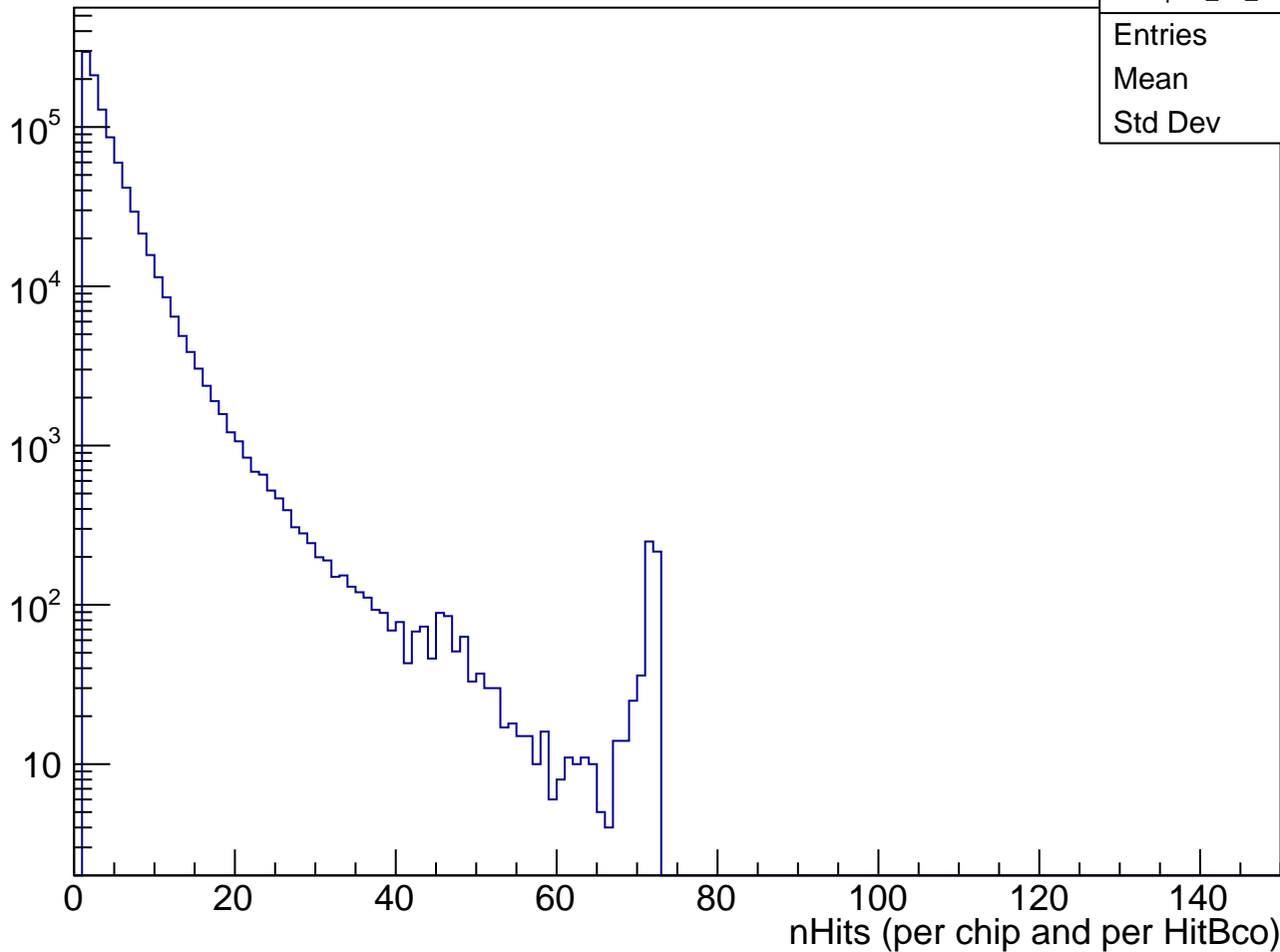
Entries



nChipHit_F4_Fch4_Chip25	
Entries	919676
Mean	3.123
Std Dev	3.596

nChipHit_F4_Fch4_Chip3

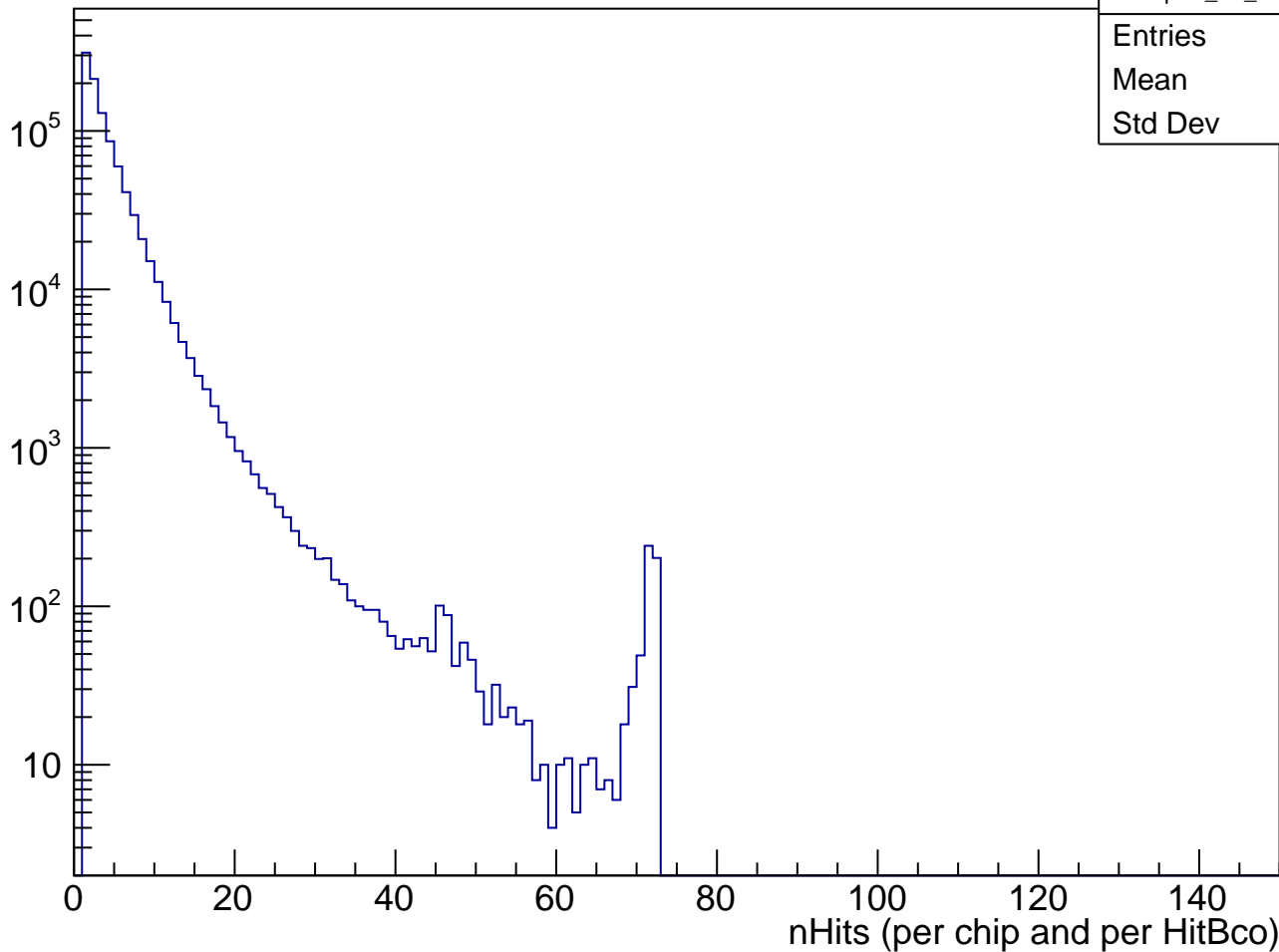
Entries



nChipHit_F4_Fch4_Chip3	
Entries	943857
Mean	3.537
Std Dev	4.079

nChipHit_F4_Fch4_Chip4

Entries



nChipHit_F4_Fch4_Chip4

Entries 958396

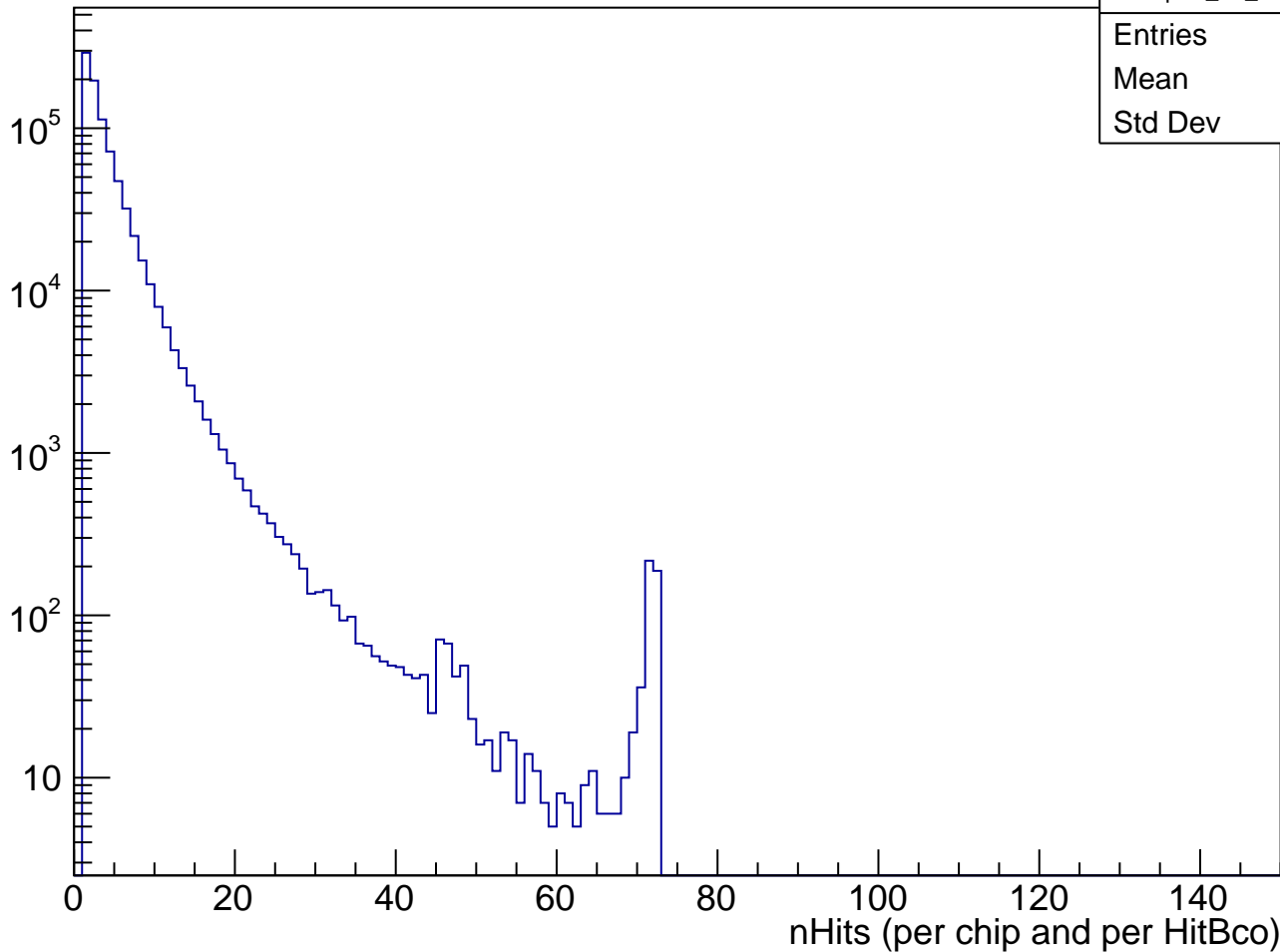
Mean 3.457

Std Dev 3.999

nHits (per chip and per HitBco)

nChipHit_F4_Fch4_Chip5

Entries



nChipHit_F4_Fch4_Chip5

Entries 837269

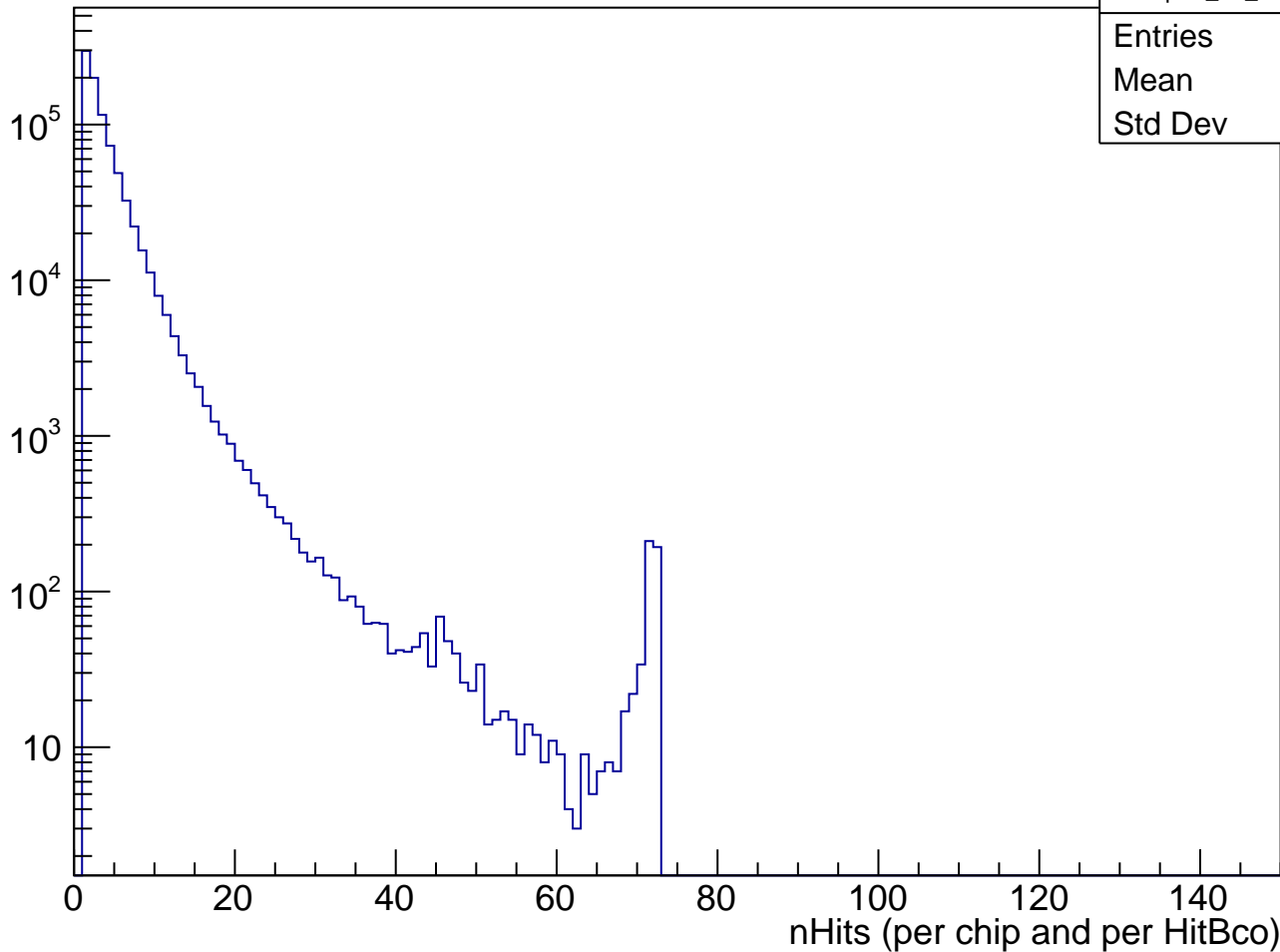
Mean 3.204

Std Dev 3.77

nHits (per chip and per HitBco)

nChipHit_F4_Fch4_Chip6

Entries



nChipHit_F4_Fch4_Chip6

Entries 852551

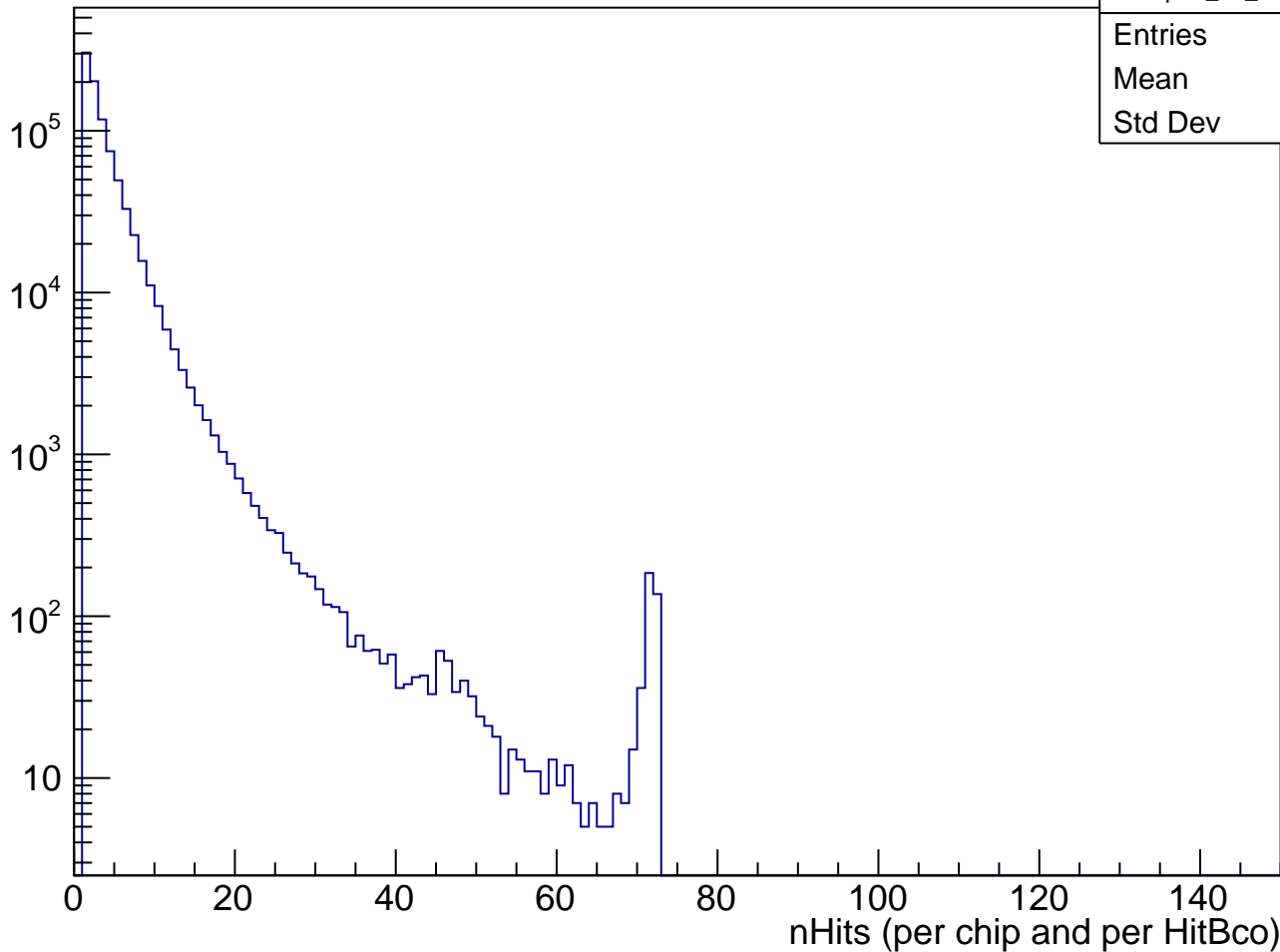
Mean 3.193

Std Dev 3.745

nHits (per chip and per HitBco)

nChipHit_F4_Fch4_Chip7

Entries

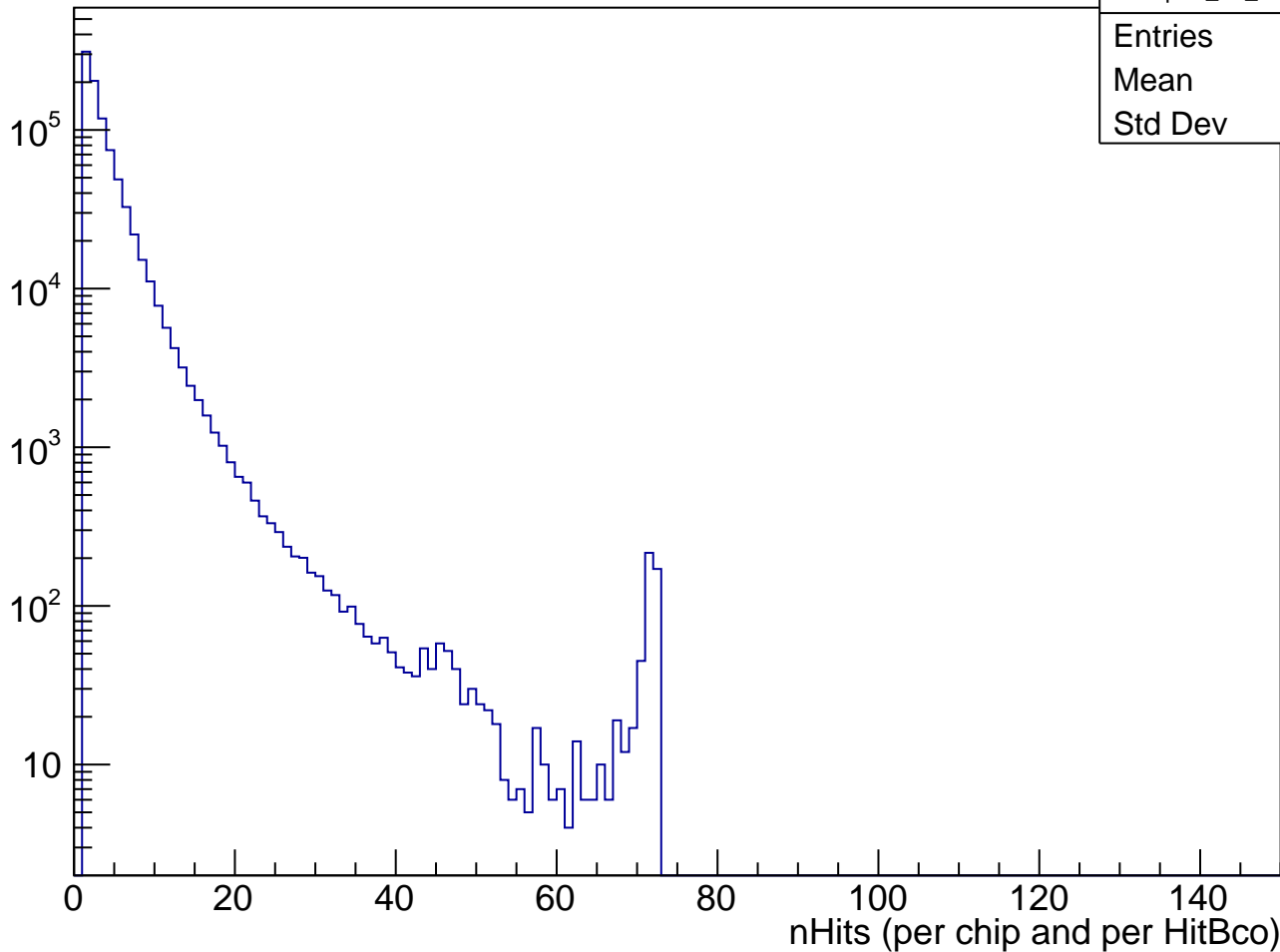


nChipHit_F4_Fch4_Chip7	
Entries	867396
Mean	3.174
Std Dev	3.651

nHits (per chip and per HitBco)

nChipHit_F4_Fch4_Chip8

Entries



nChipHit_F4_Fch4_Chip8

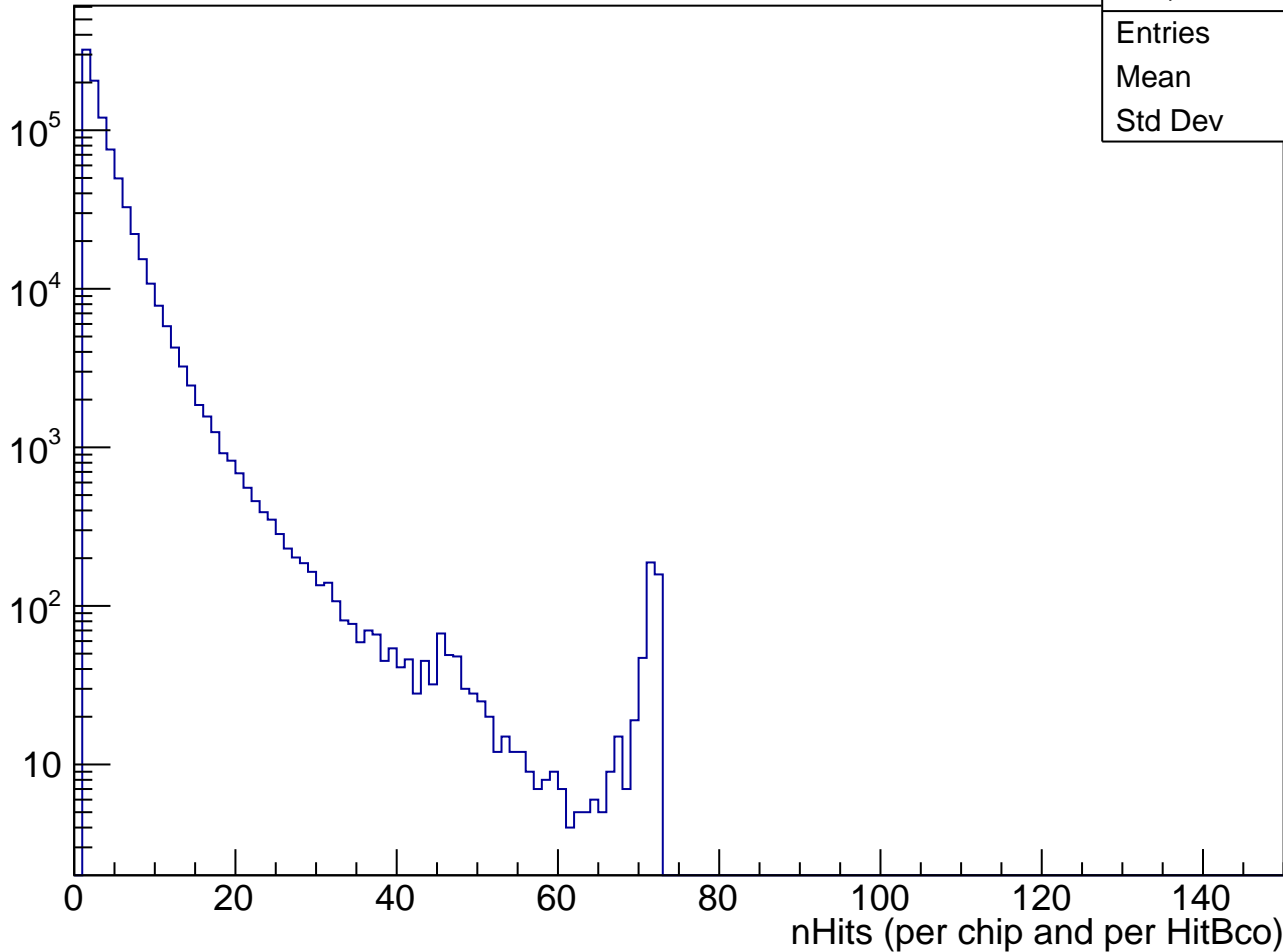
Entries 873121

Mean 3.135

Std Dev 3.676

nChipHit_F4_Fch4_Chip9

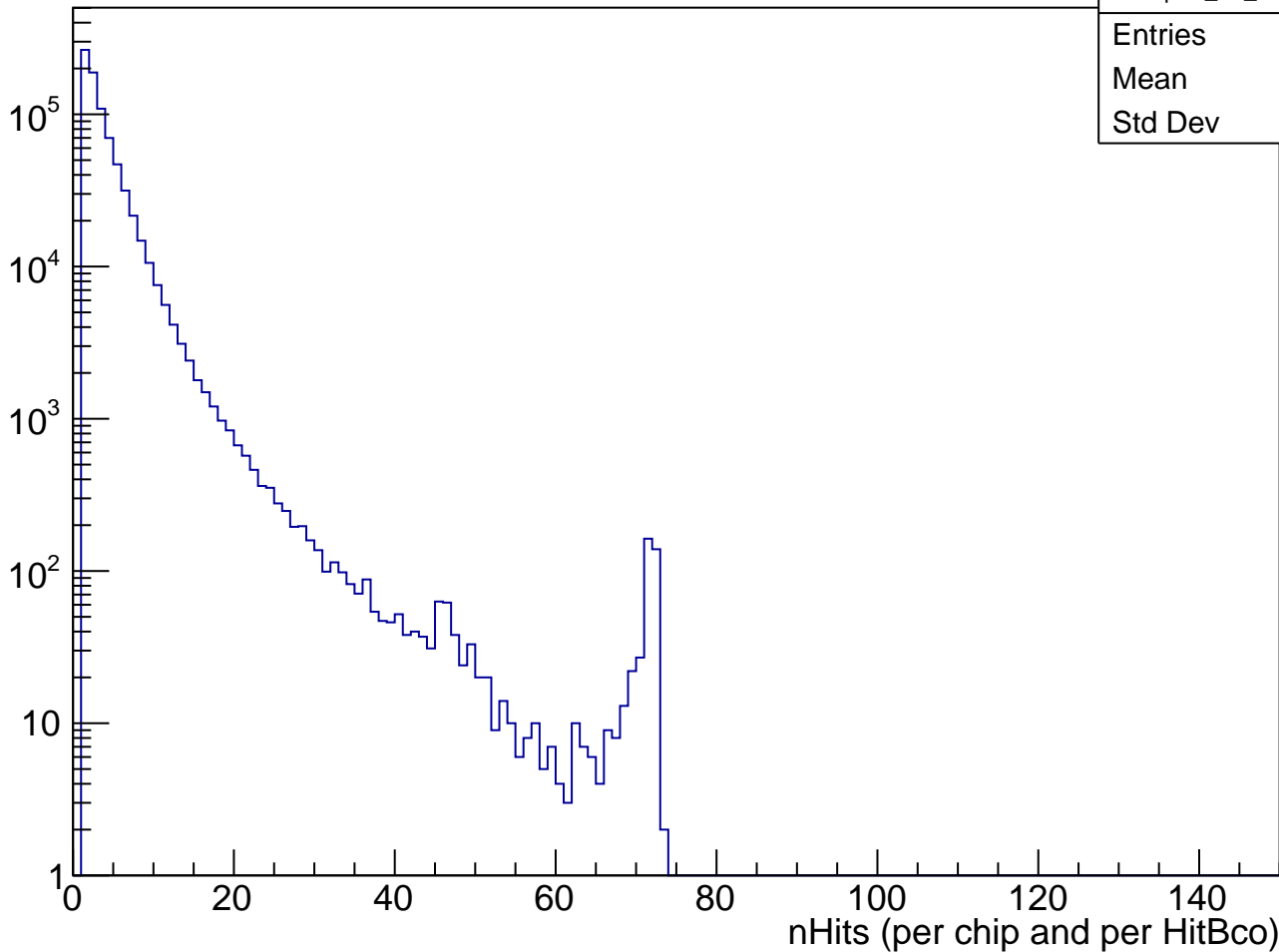
Entries



nChipHit_F4_Fch4_Chip9	
Entries	889328
Mean	3.103
Std Dev	3.601

nChipHit_F4_Fch5_Chip0

Entries



nChipHit_F4_Fch5_Chip0

Entries 791784

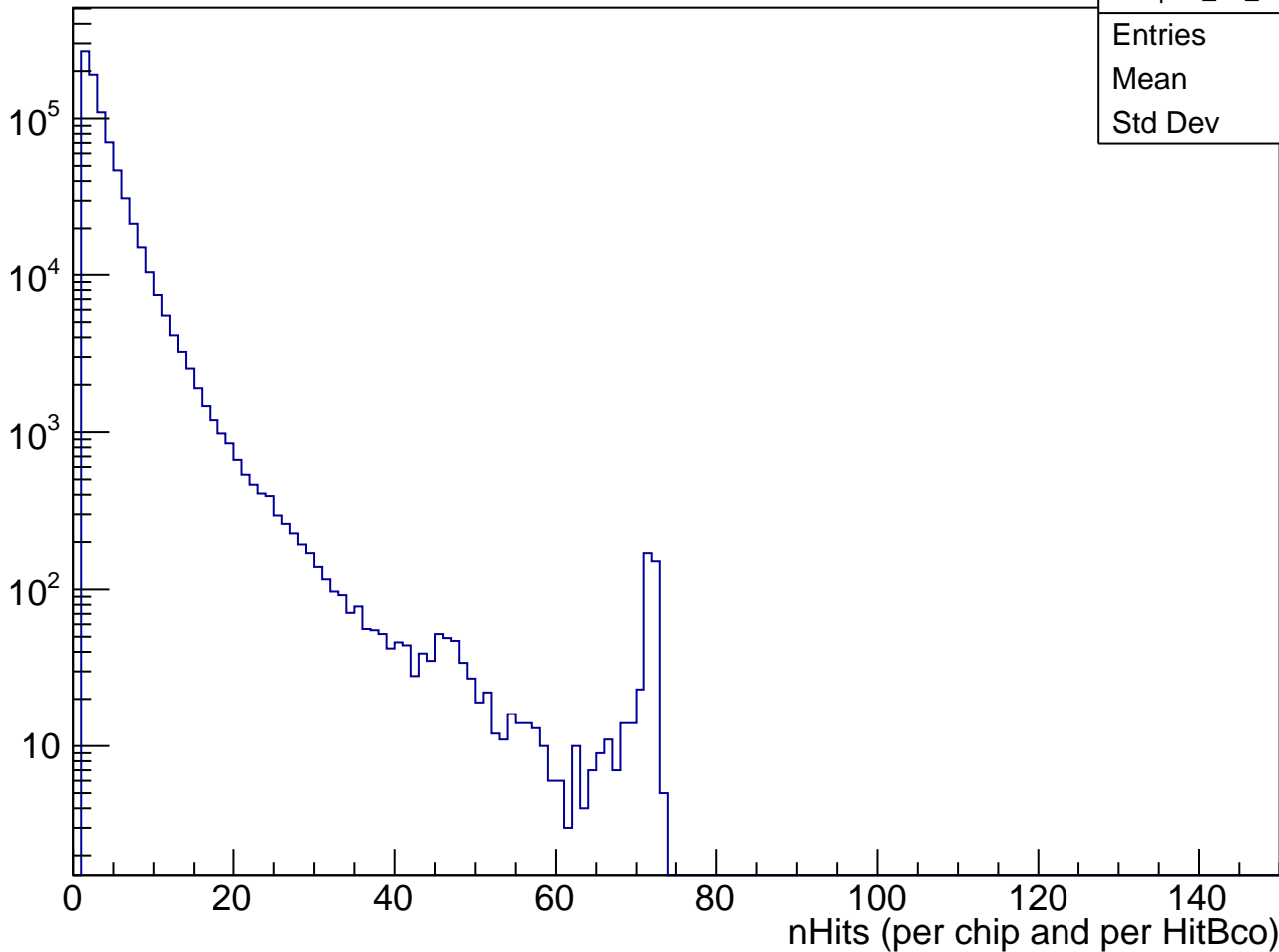
Mean 3.239

Std Dev 3.695

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip1

Entries

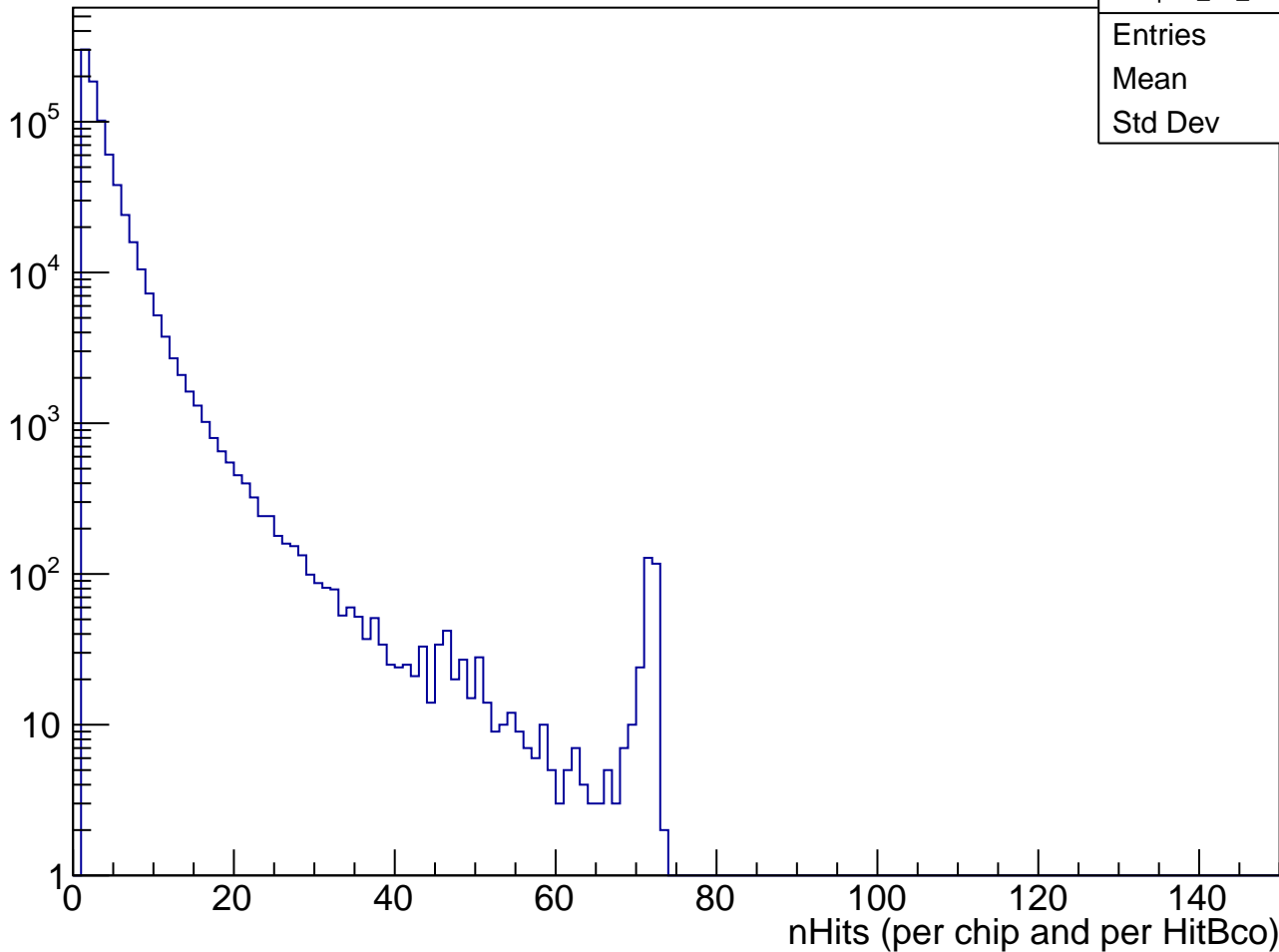


nChipHit_F4_Fch5_Chip1	
Entries	796504
Mean	3.234
Std Dev	3.715

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip10

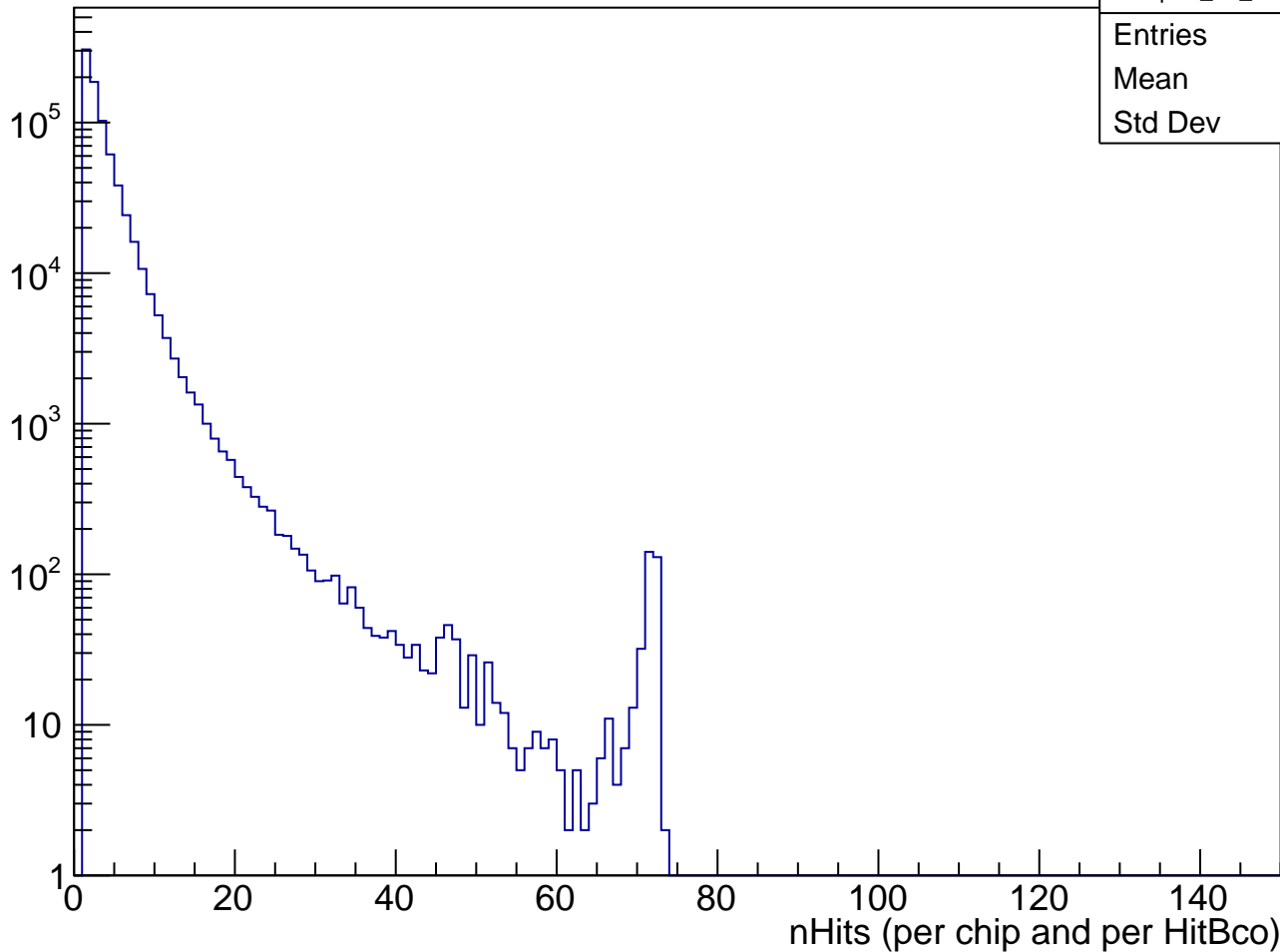
Entries



nChipHit_F4_Fch5_Chip10	
Entries	767984
Mean	2.832
Std Dev	3.266

nChipHit_F4_Fch5_Chip11

Entries

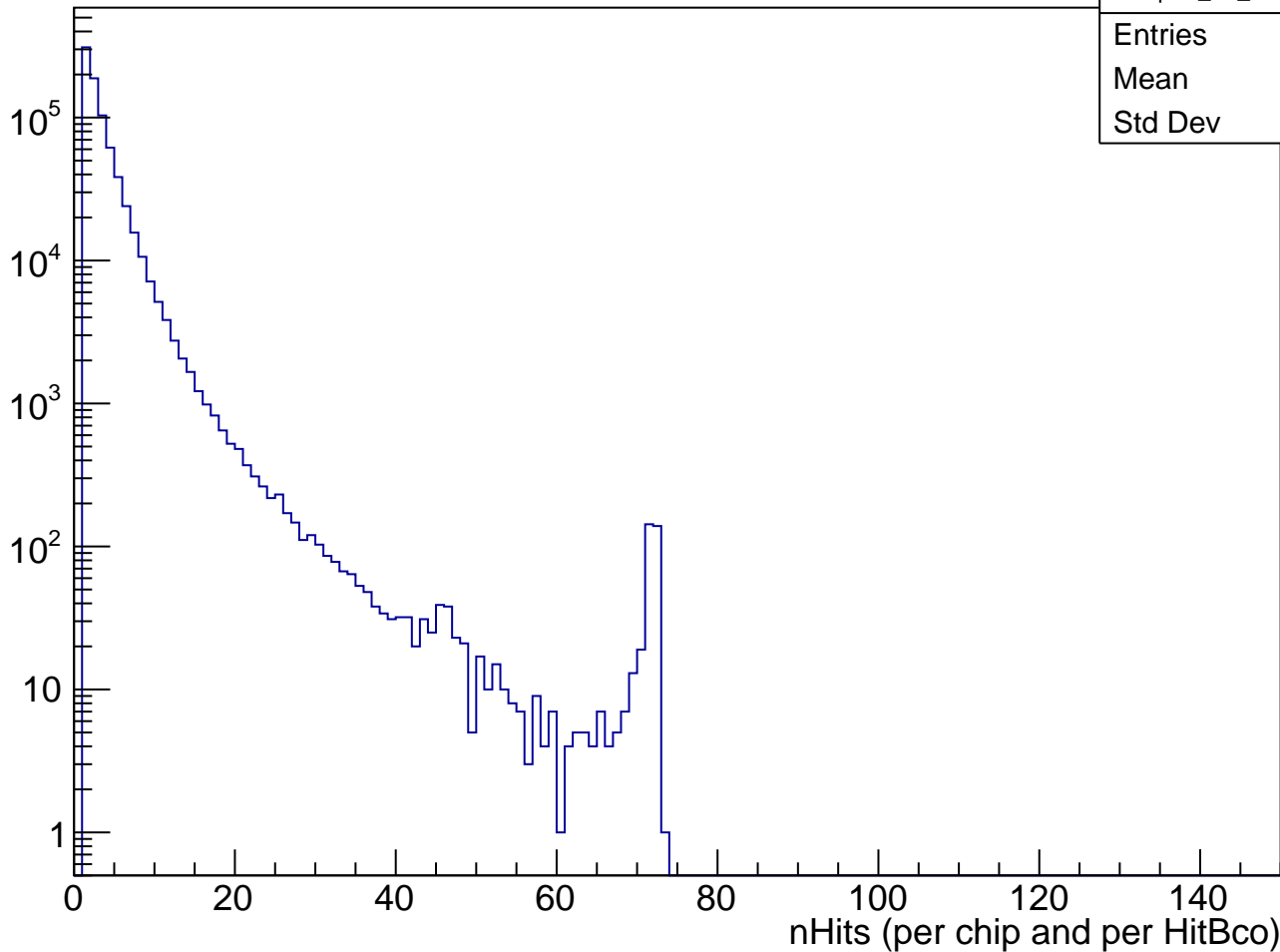


nChipHit_F4_Fch5_Chip11	
Entries	776238
Mean	2.838
Std Dev	3.331

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip12

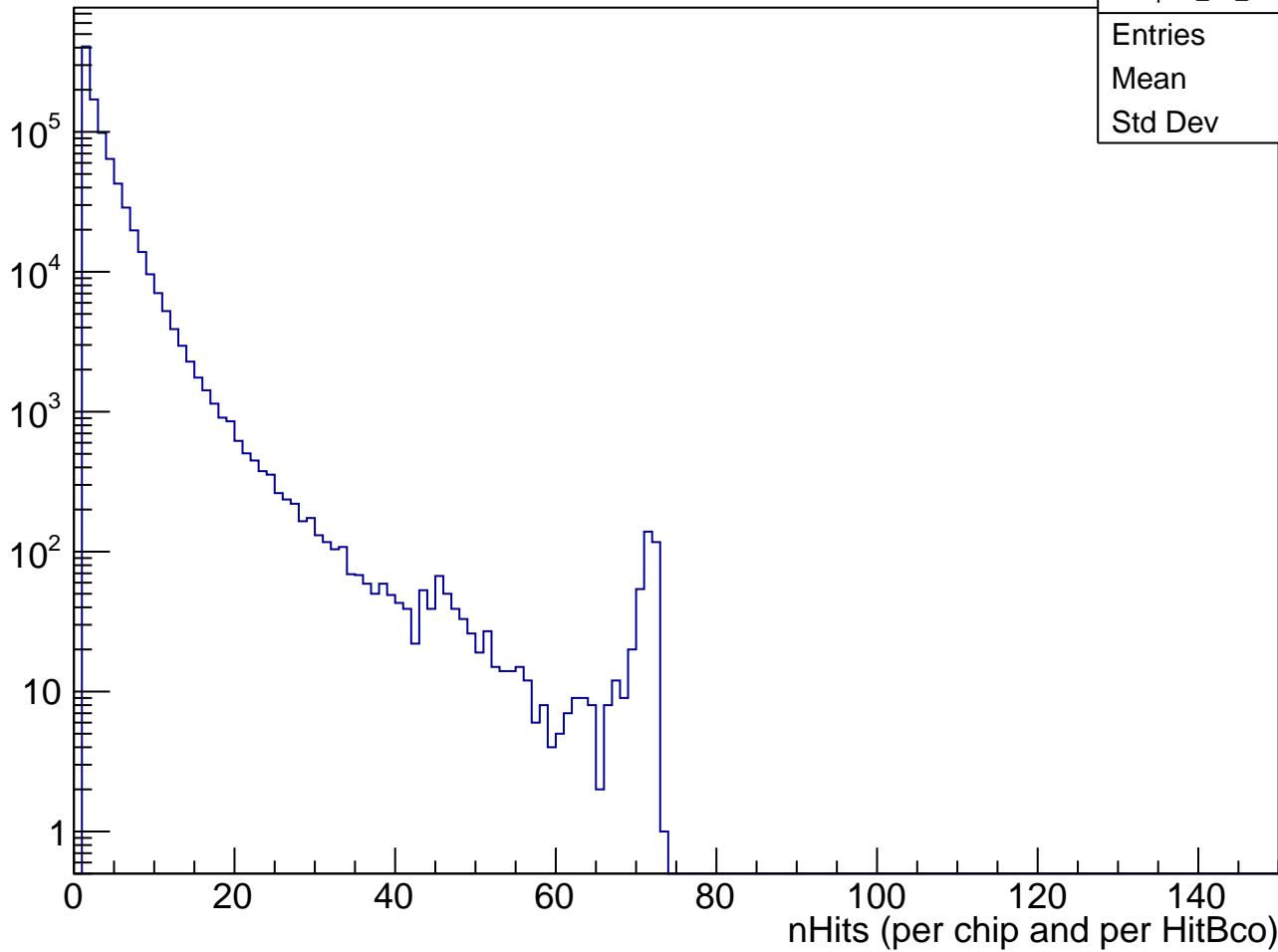
Entries



nChipHit_F4_Fch5_Chip12	
Entries	782138
Mean	2.815
Std Dev	3.276

nChipHit_F4_Fch5_Chip13

Entries

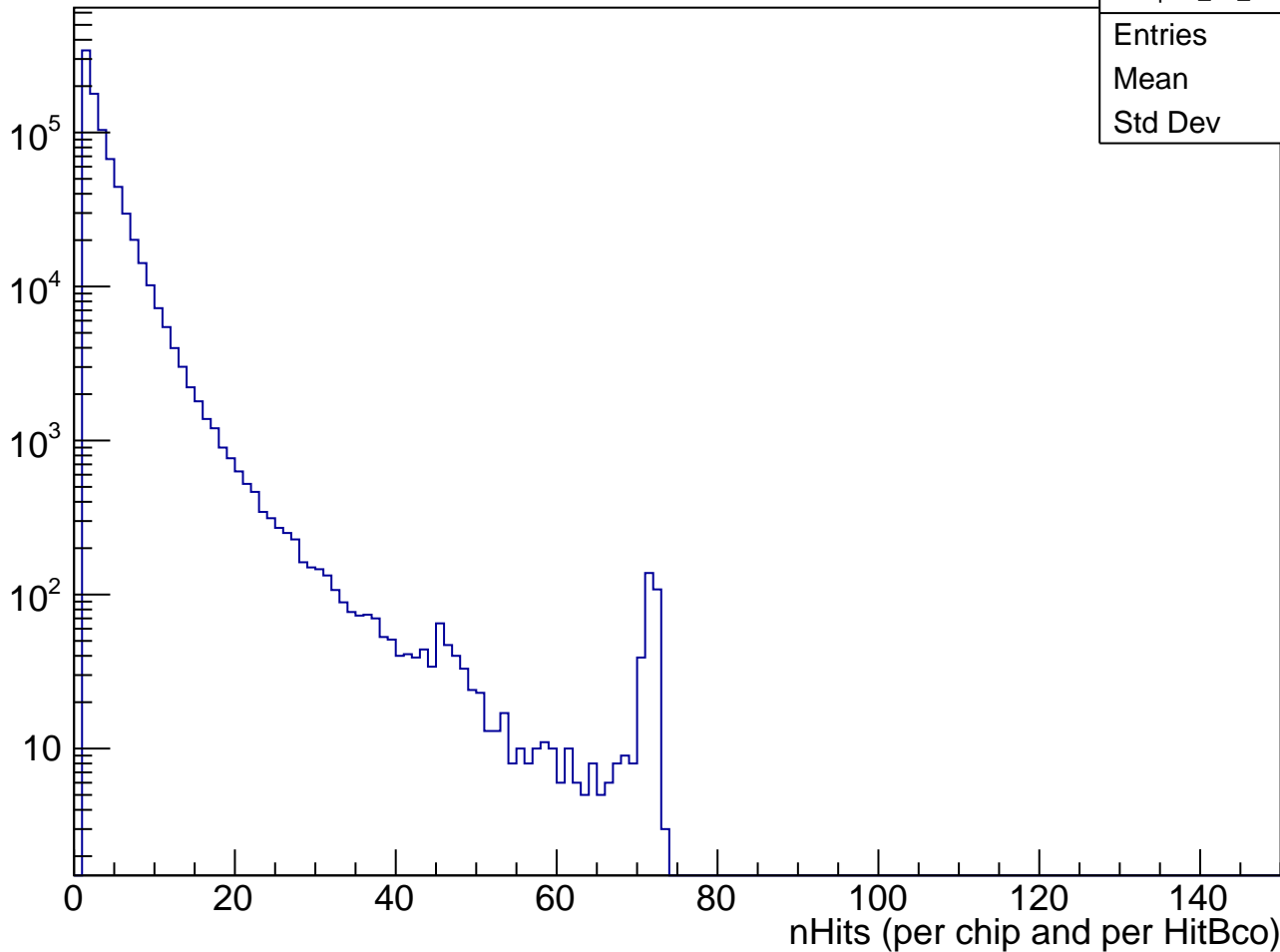


nChipHit_F4_Fch5_Chip13	
Entries	888030
Mean	2.845
Std Dev	3.521

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip14

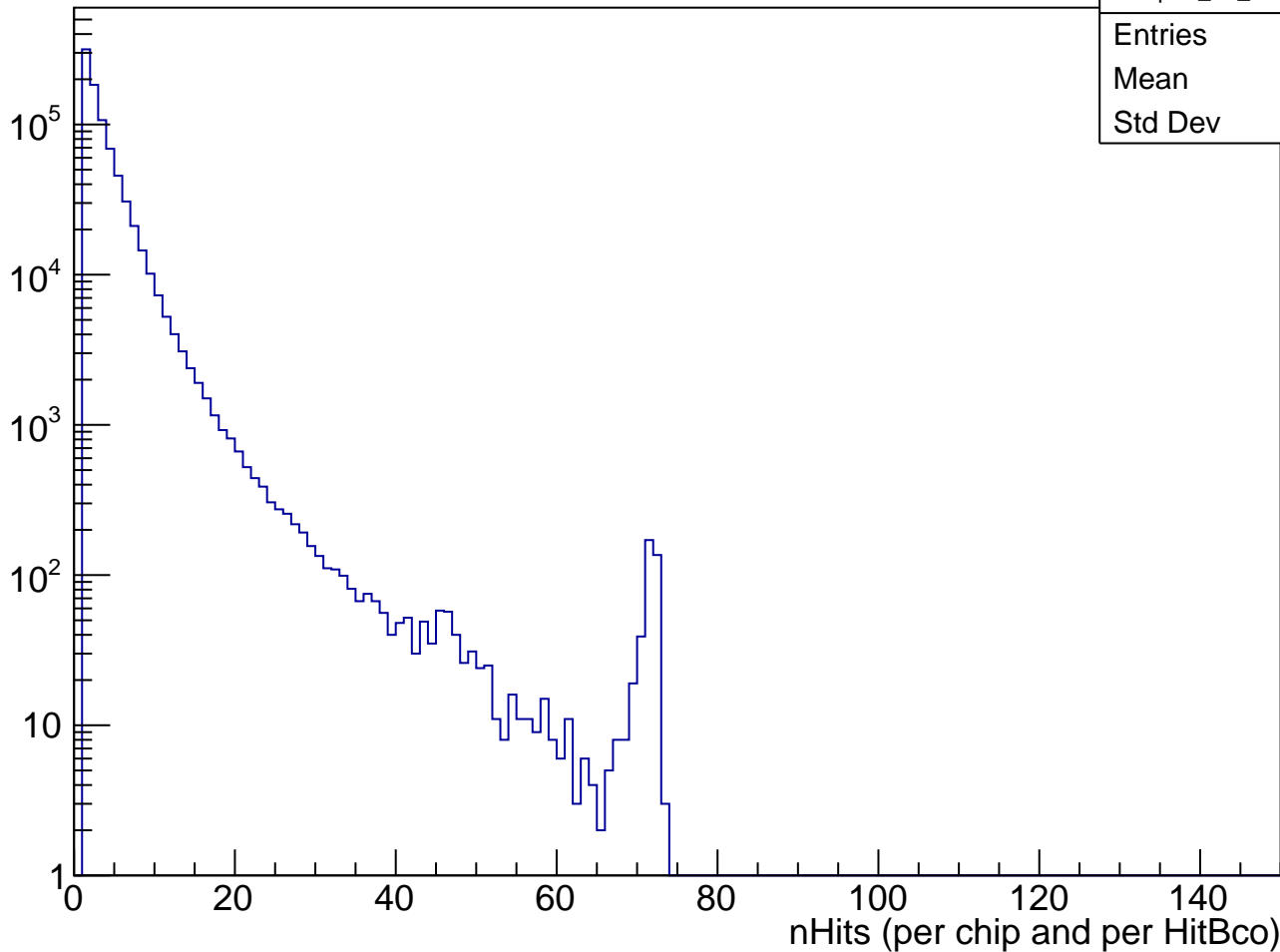
Entries



nChipHit_F4_Fch5_Chip14	
Entries	842353
Mean	3.005
Std Dev	3.558

nChipHit_F4_Fch5_Chip15

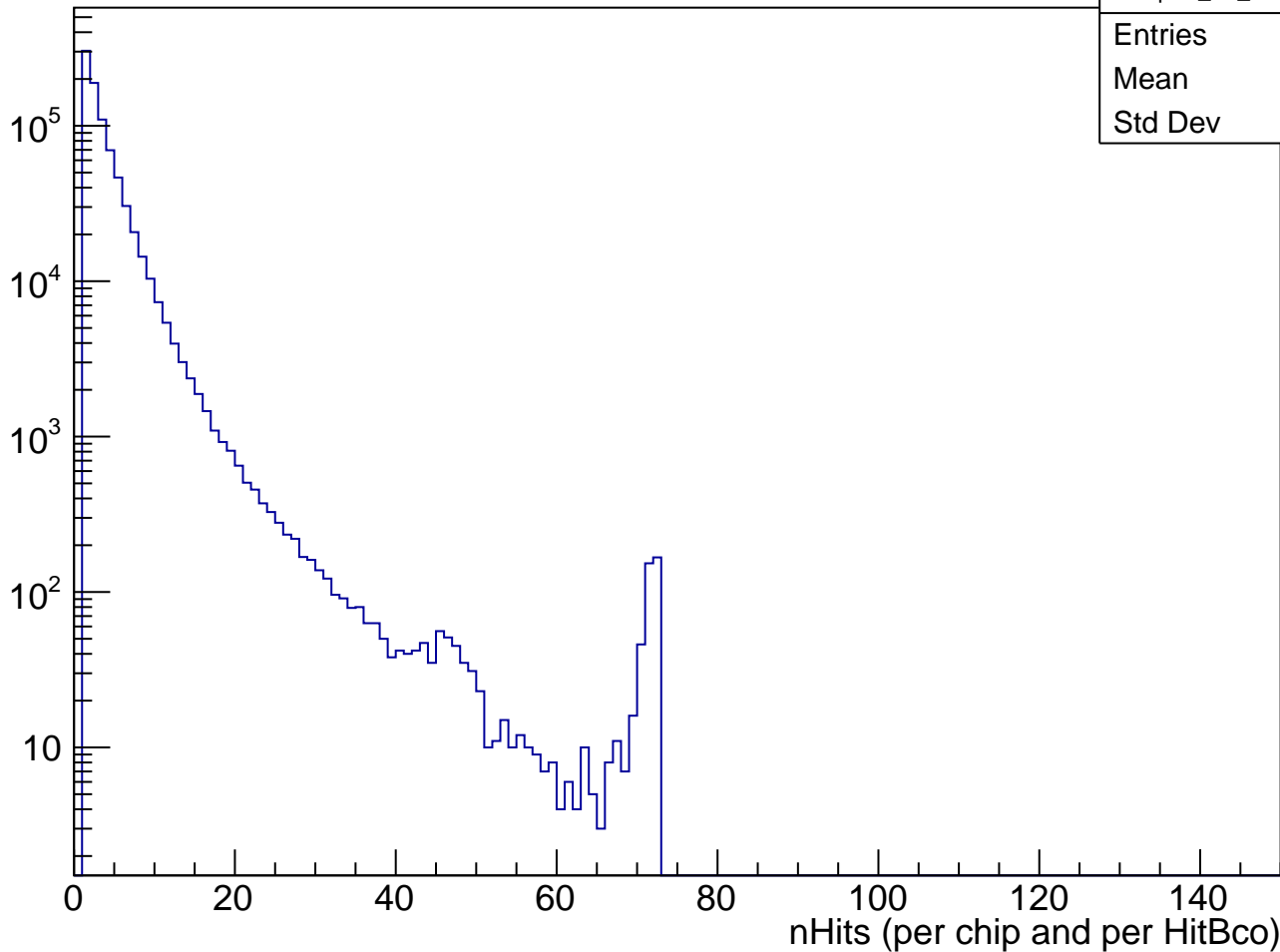
Entries



nChipHit_F4_Fch5_Chip15	
Entries	831707
Mean	3.088
Std Dev	3.642

nChipHit_F4_Fch5_Chip16

Entries

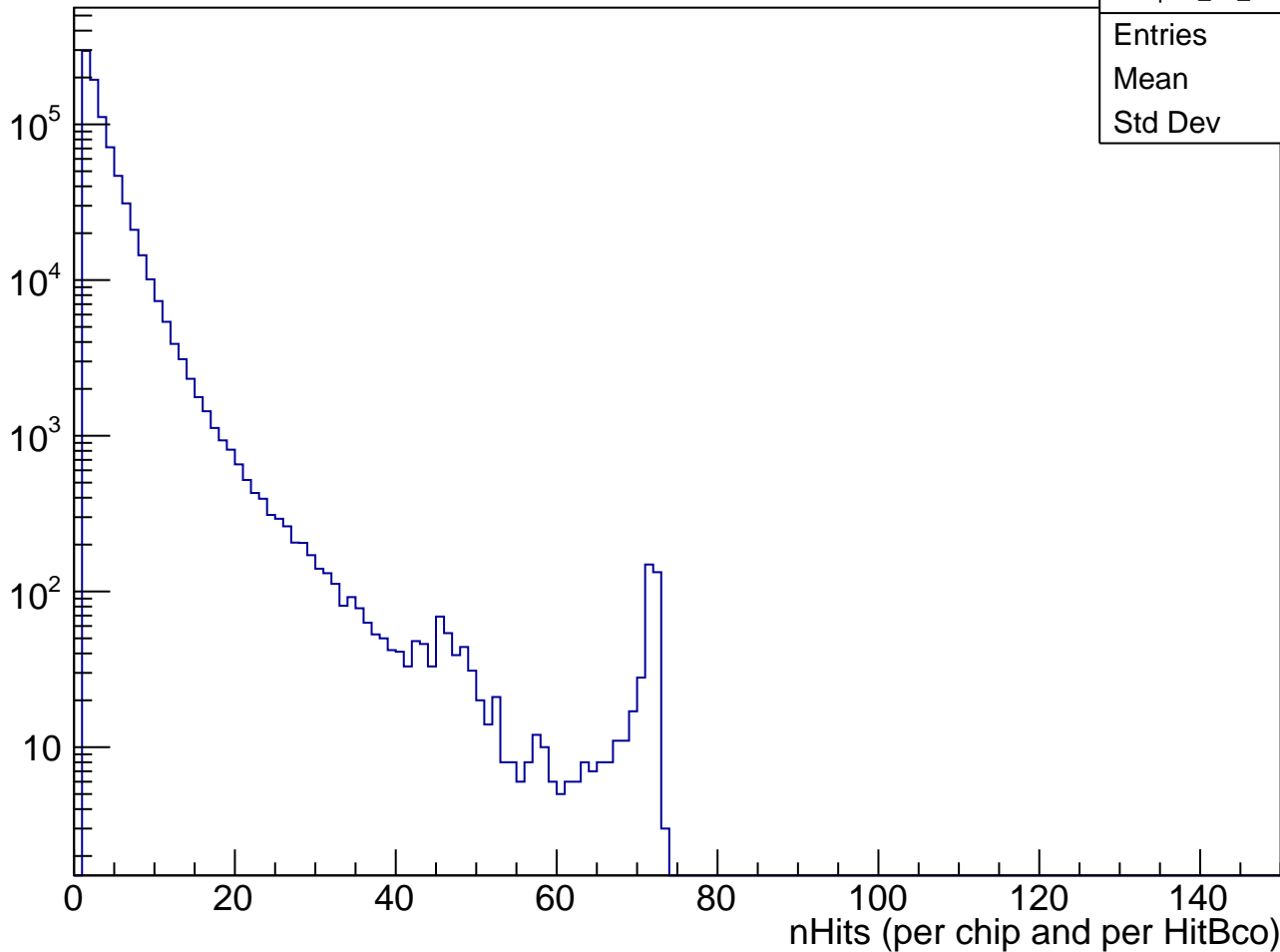


nChipHit_F4_Fch5_Chip16	
Entries	827648
Mean	3.11
Std Dev	3.638

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip17

Entries

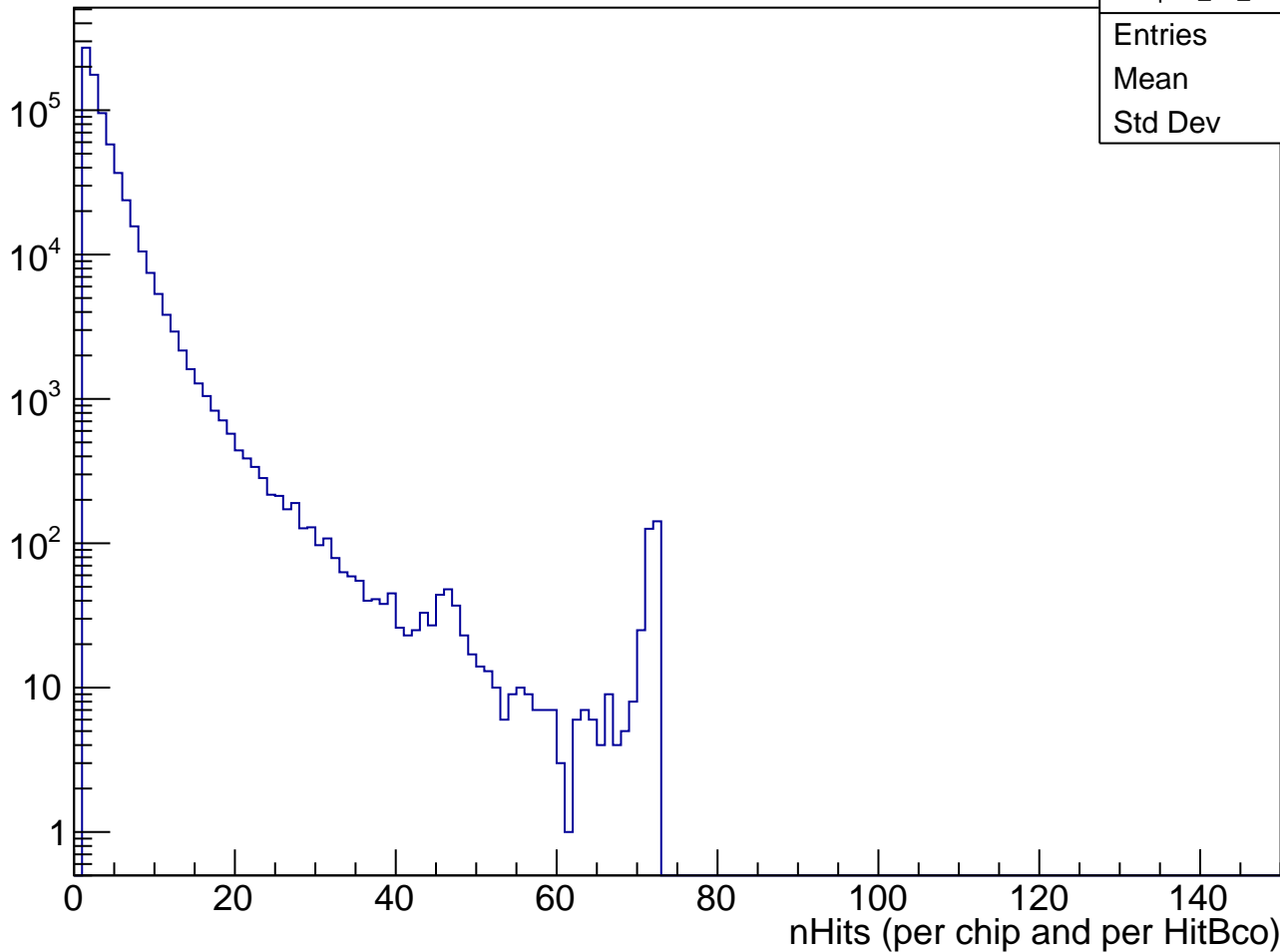


nChipHit_F4_Fch5_Chip17	
Entries	829409
Mean	3.125
Std Dev	3.608

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip18

Entries

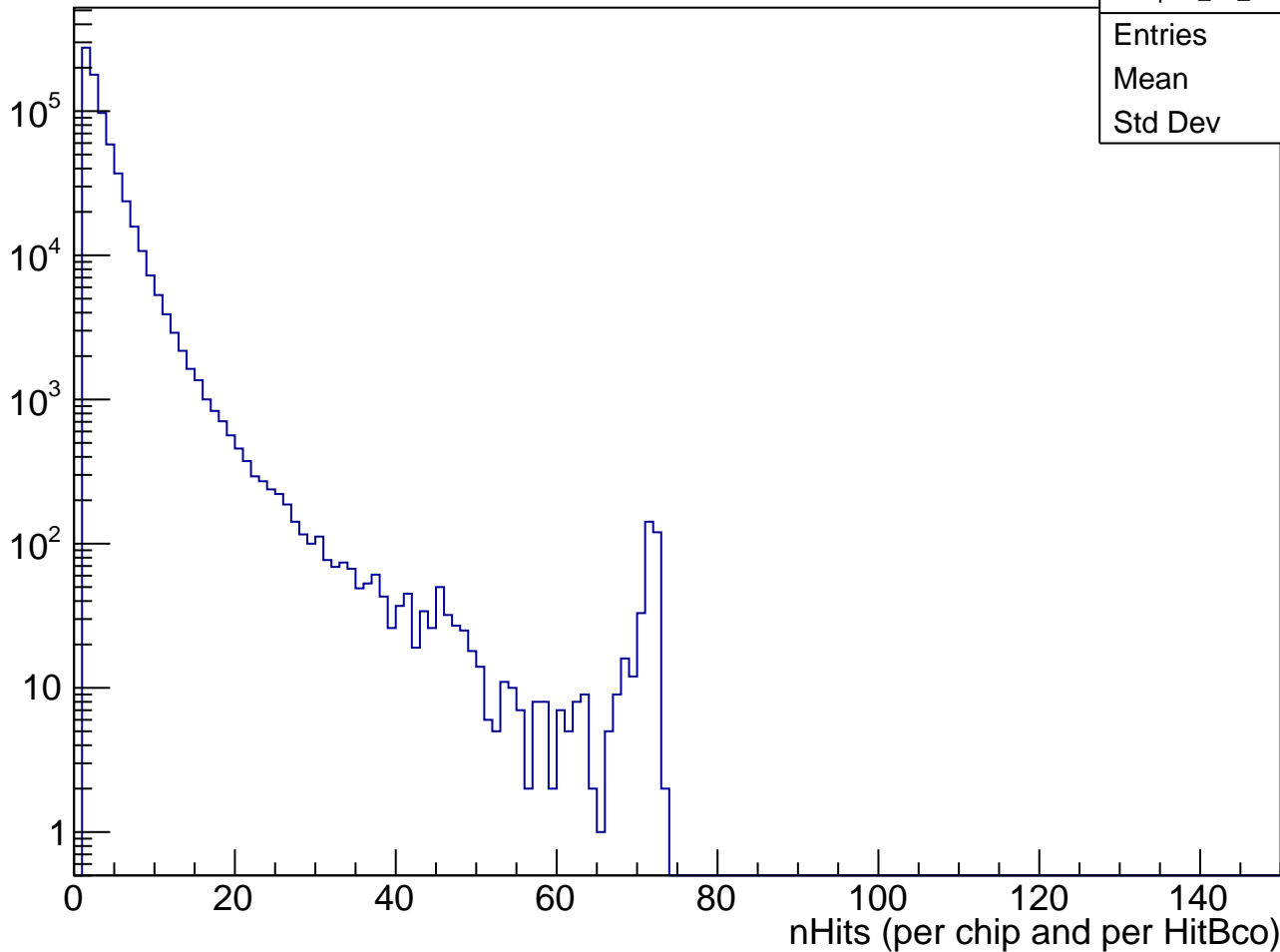


nChipHit_F4_Fch5_Chip18	
Entries	718712
Mean	2.93
Std Dev	3.424

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip19

Entries

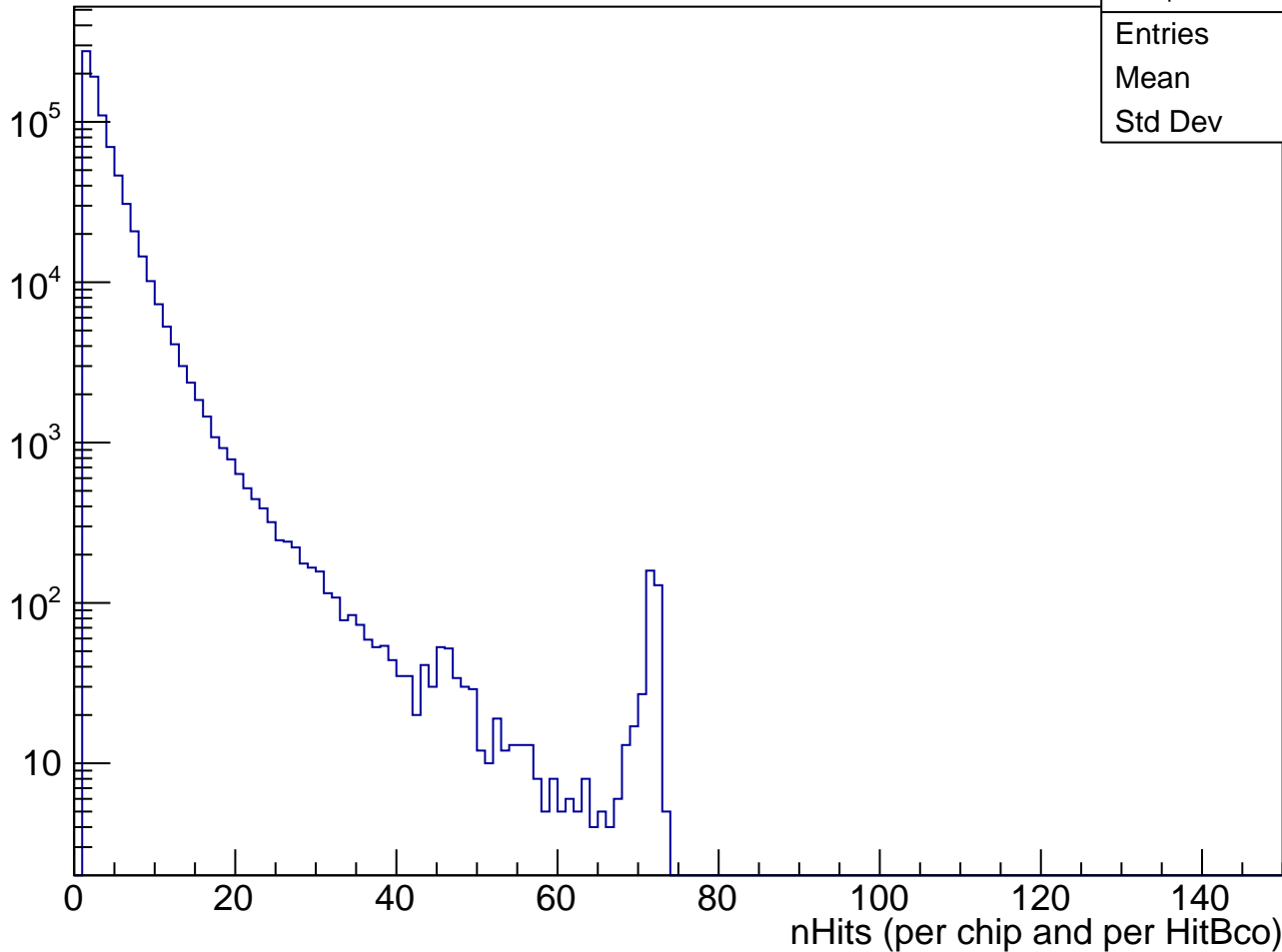


nChipHit_F4_Fch5_Chip19	
Entries	728855
Mean	2.915
Std Dev	3.406

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip2

Entries



nChipHit_F4_Fch5_Chip2

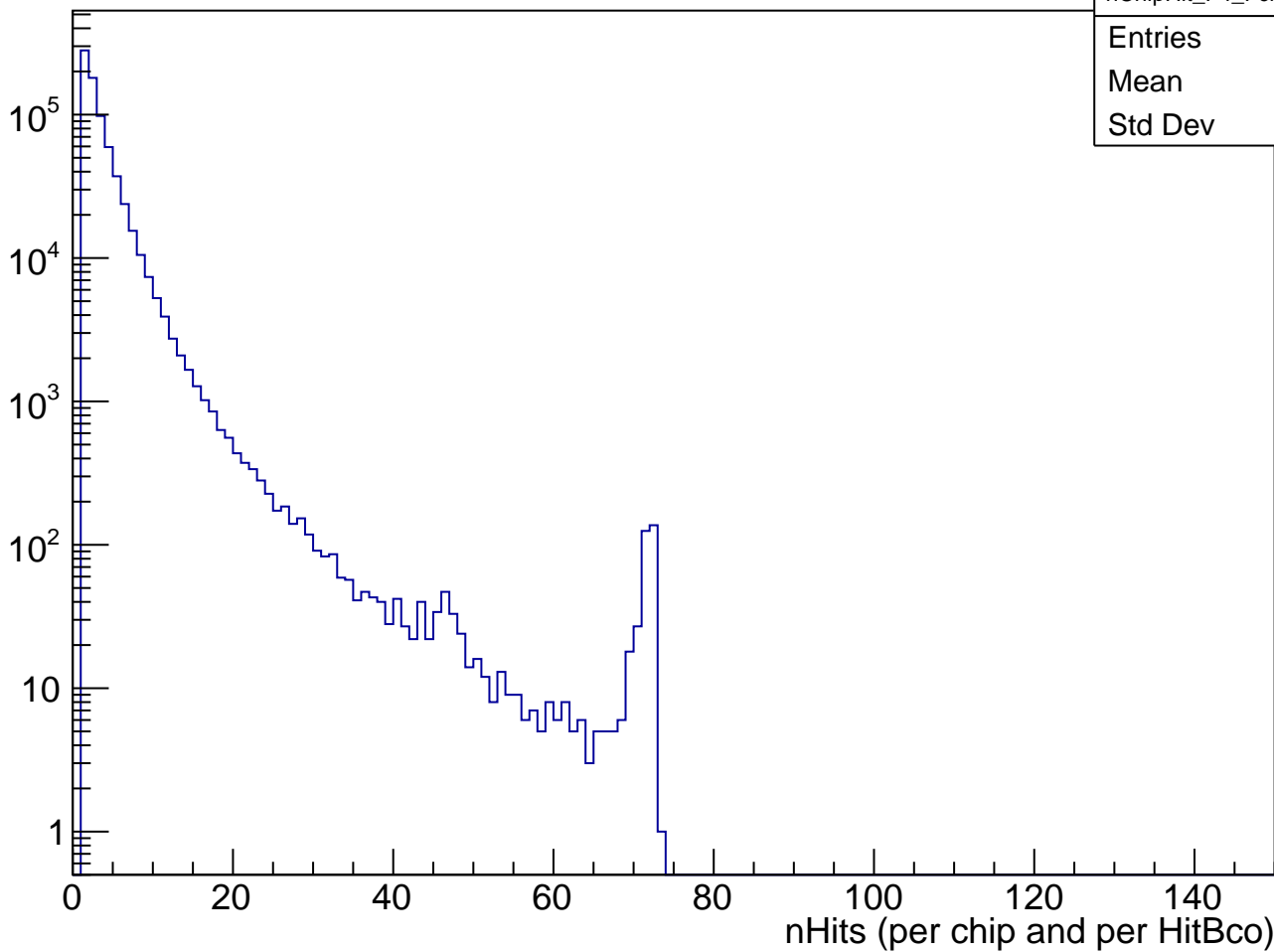
Entries 802549

Mean 3.171

Std Dev 3.614

nChipHit_F4_Fch5_Chip20

Entries



nChipHit_F4_Fch5_Chip20

Entries 736296

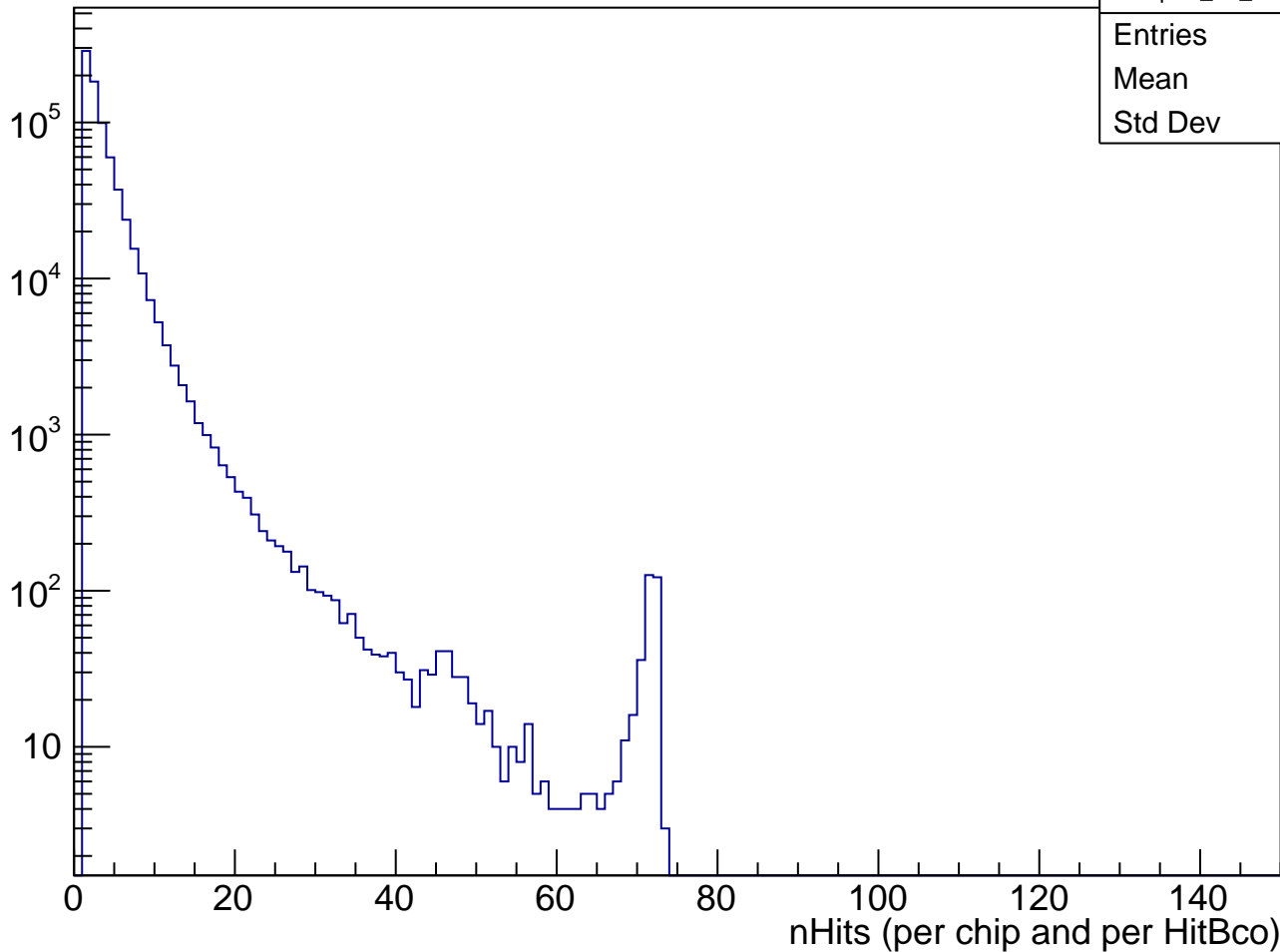
Mean 2.893

Std Dev 3.372

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip21

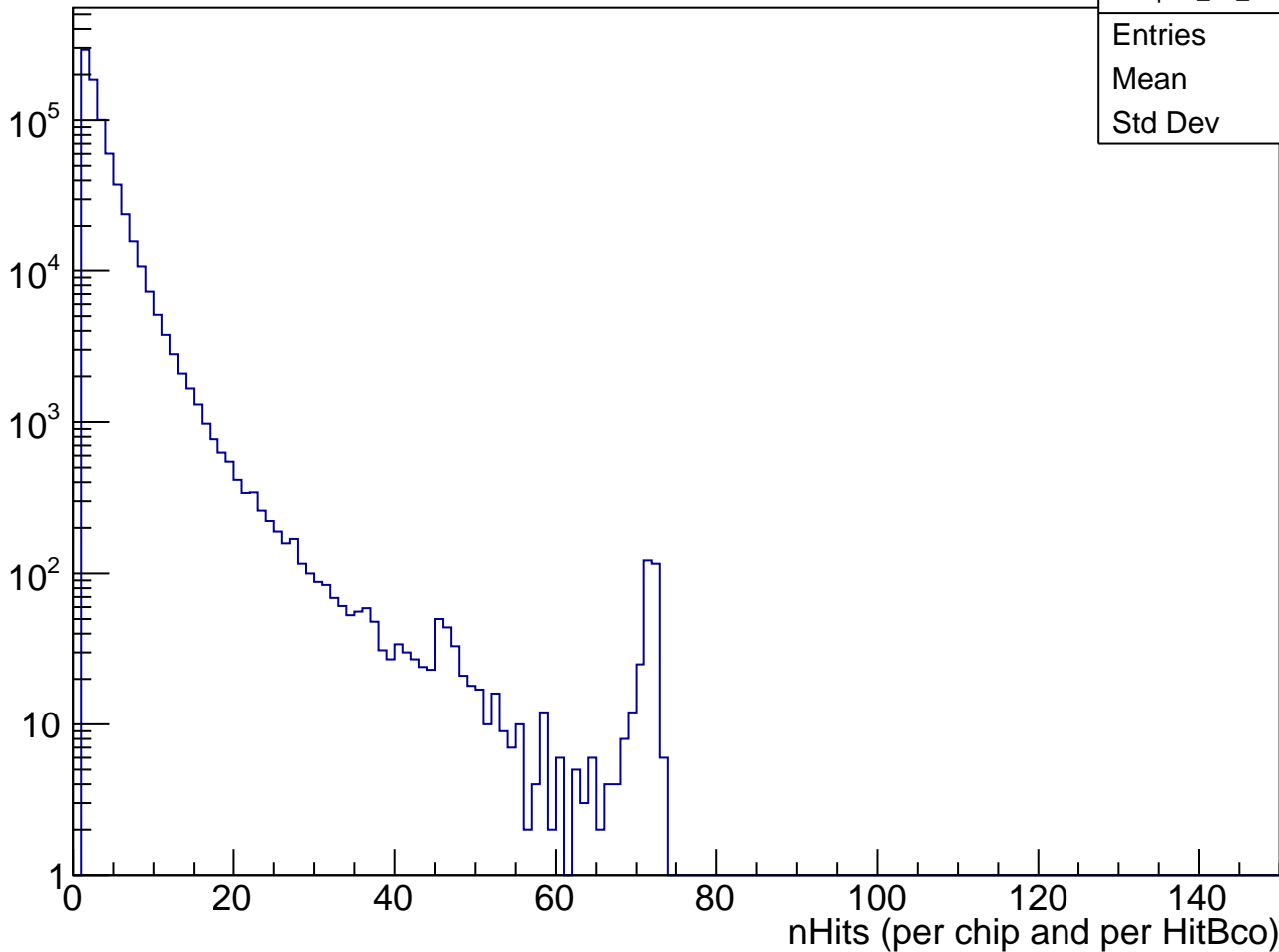
Entries



nChipHit_F4_Fch5_Chip21	
Entries	746476
Mean	2.869
Std Dev	3.339

nChipHit_F4_Fch5_Chip22

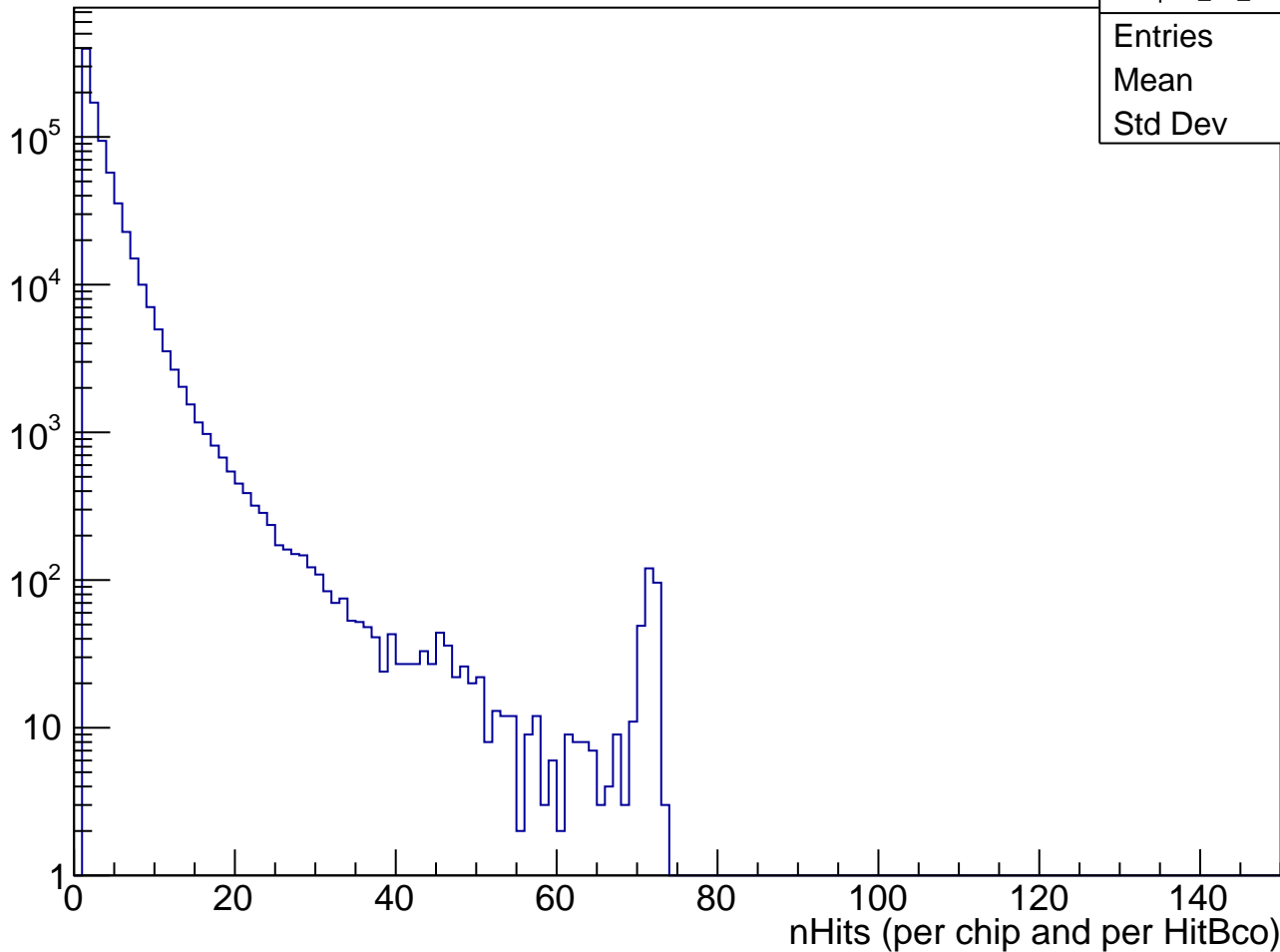
Entries



nChipHit_F4_Fch5_Chip22	
Entries	755892
Mean	2.852
Std Dev	3.287

nChipHit_F4_Fch5_Chip23

Entries

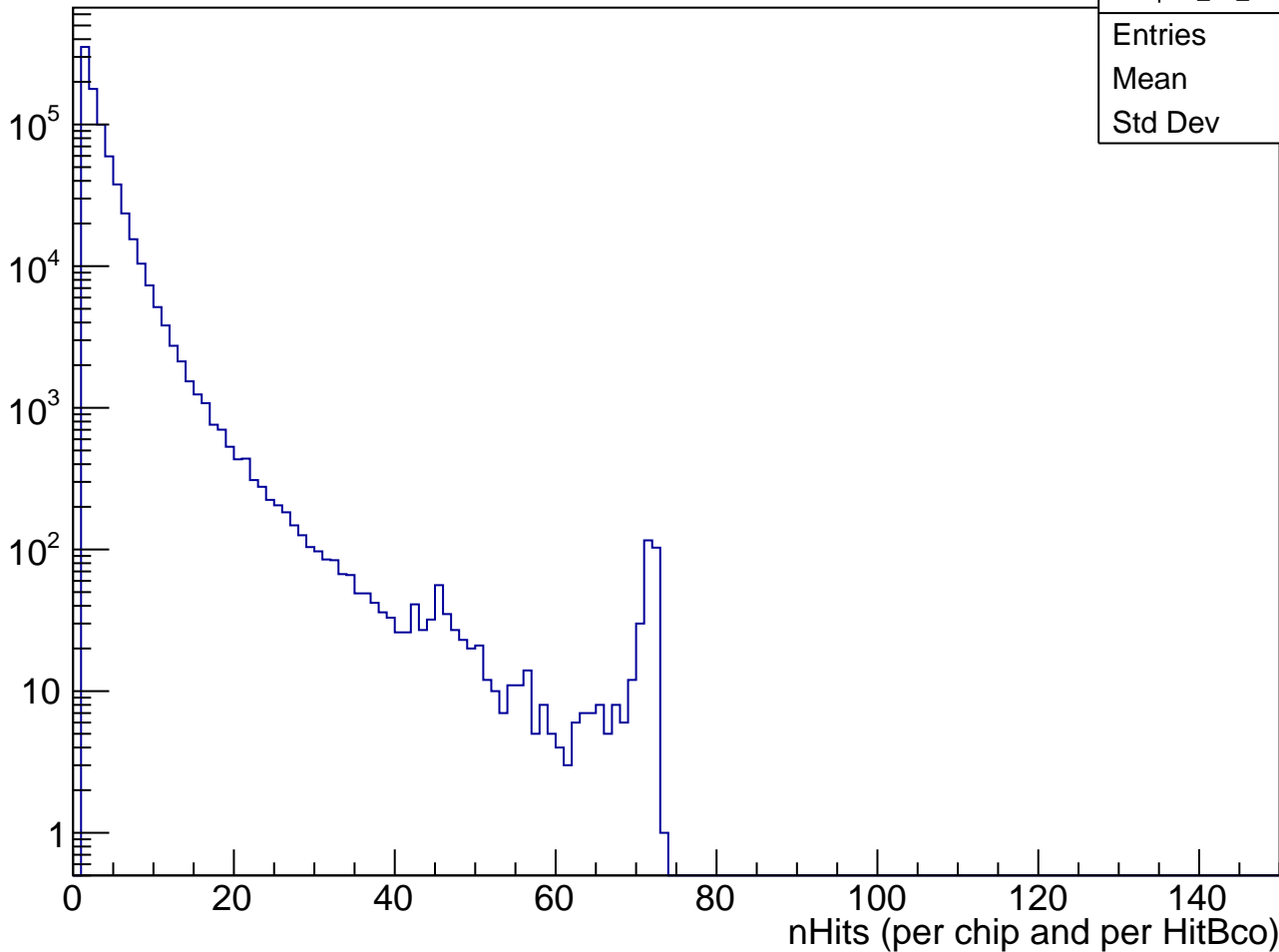


nChipHit_F4_Fch5_Chip23	
Entries	831371
Mean	2.608
Std Dev	3.189

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip24

Entries

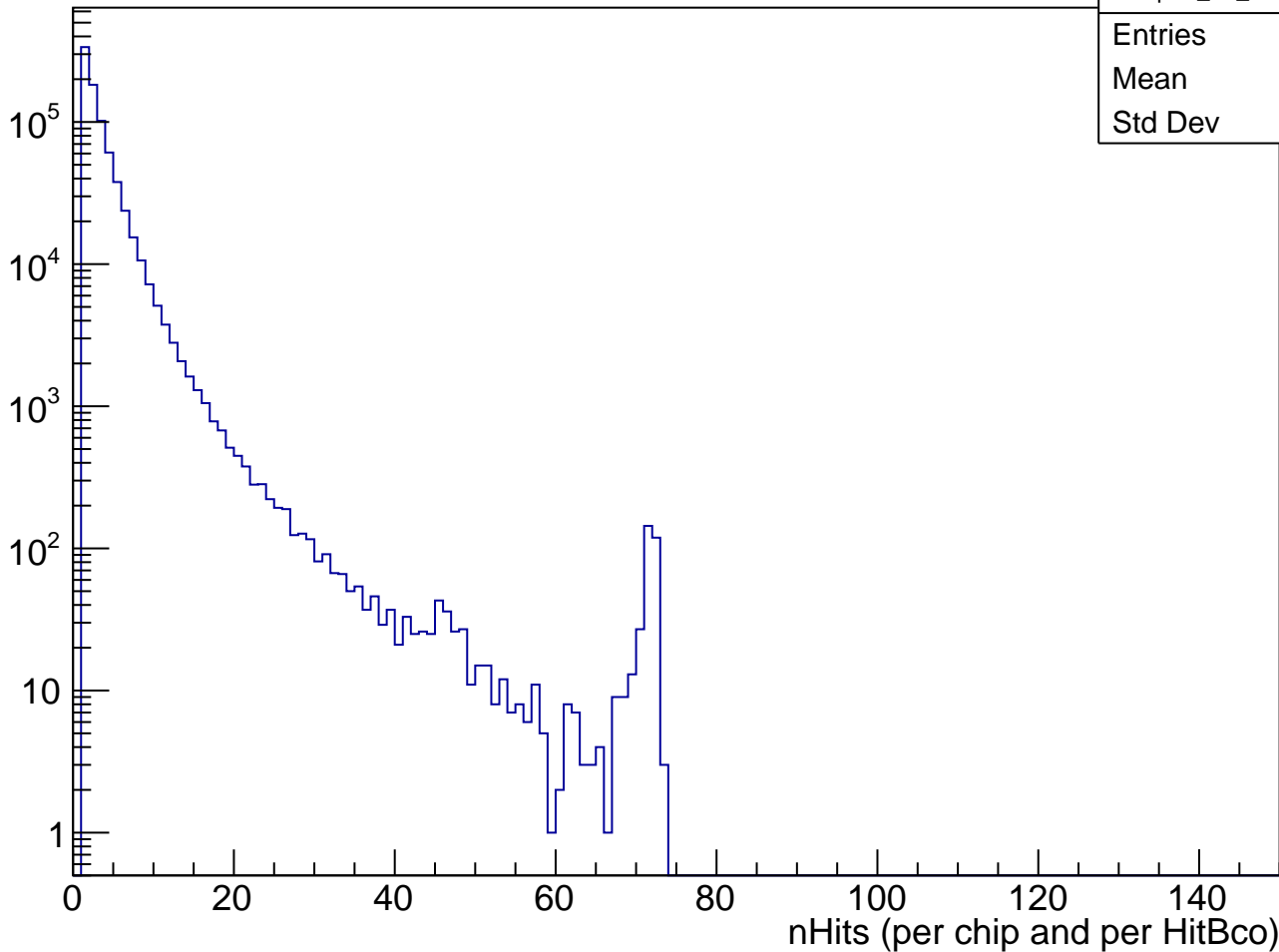


nChipHit_F4_Fch5_Chip24	
Entries	808035
Mean	2.722
Std Dev	3.235

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip25

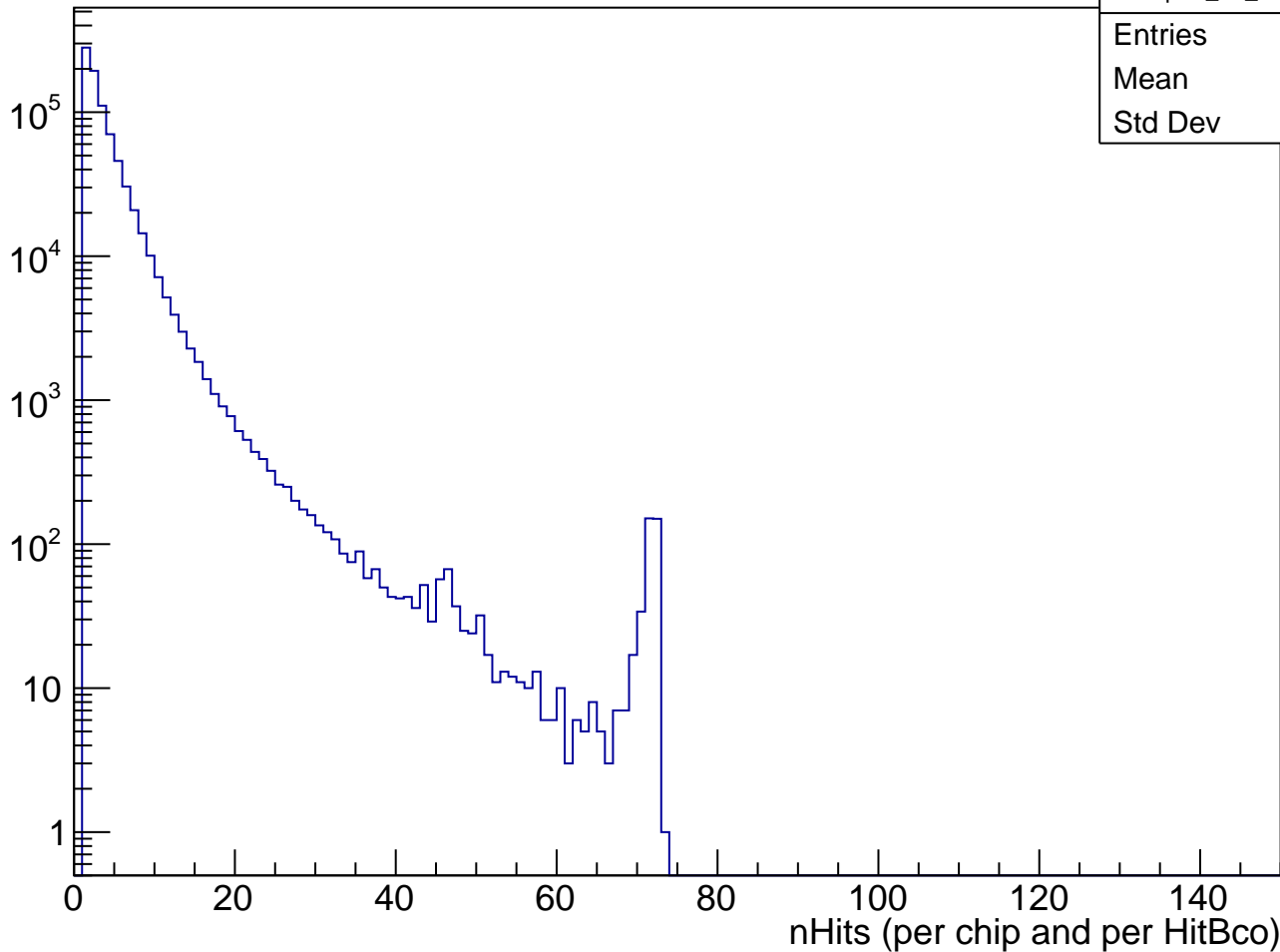
Entries



nChipHit_F4_Fch5_Chip25	
Entries	800195
Mean	2.753
Std Dev	3.241

nChipHit_F4_Fch5_Chip3

Entries

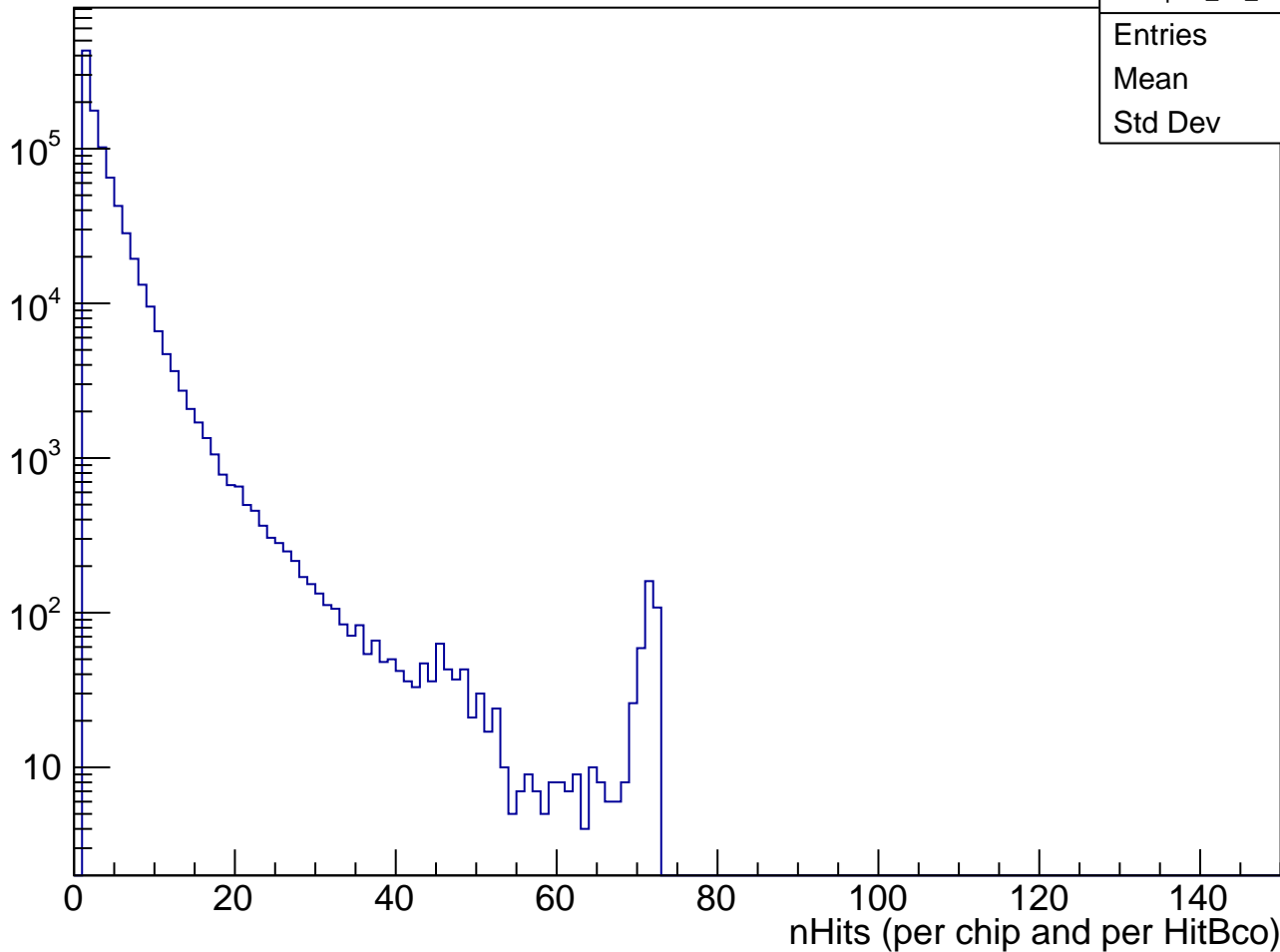


nChipHit_F4_Fch5_Chip3	
Entries	810335
Mean	3.15
Std Dev	3.623

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip4

Entries



nChipHit_F4_Fch5_Chip4

Entries 917838

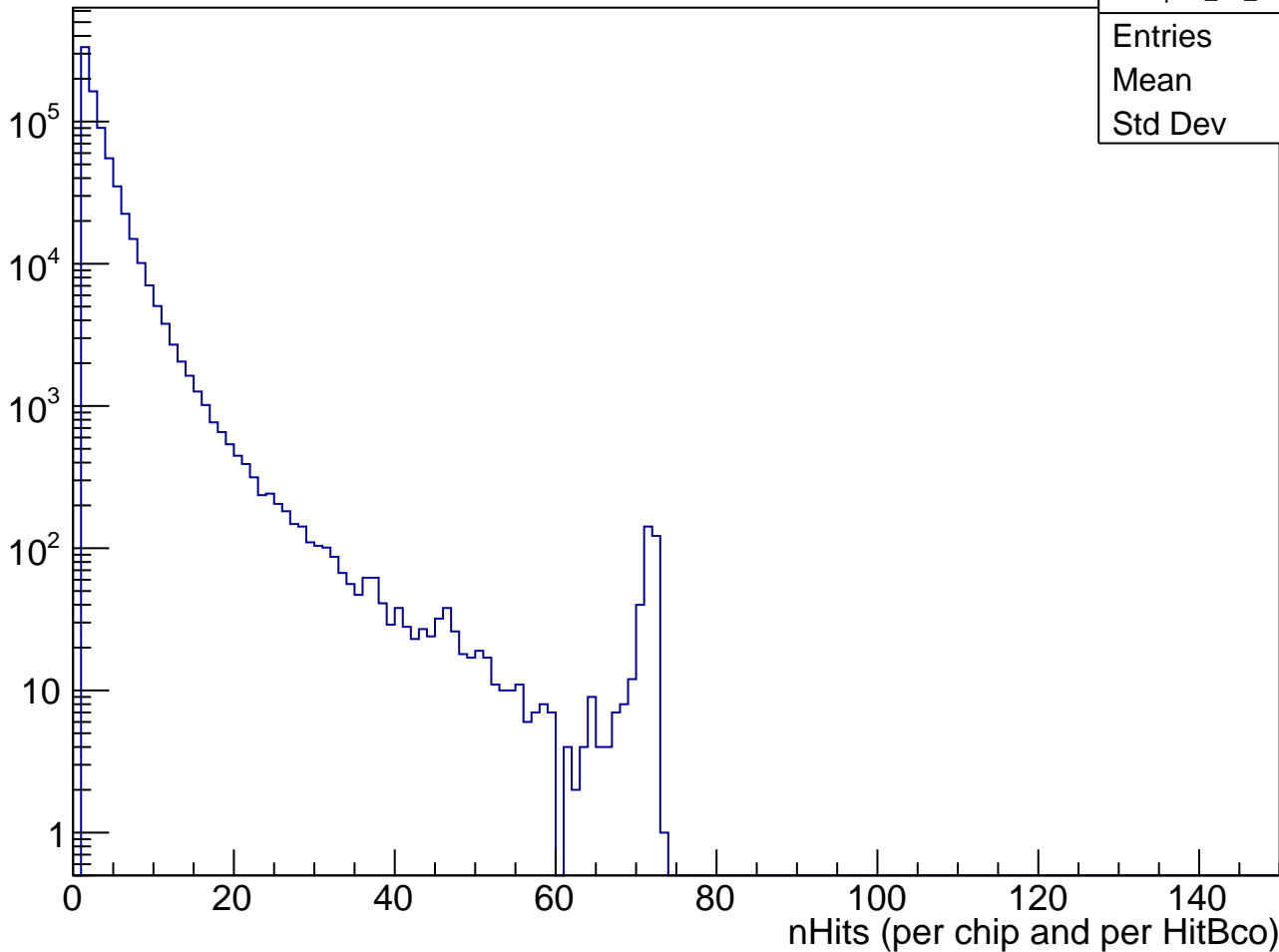
Mean 2.764

Std Dev 3.434

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip5

Entries

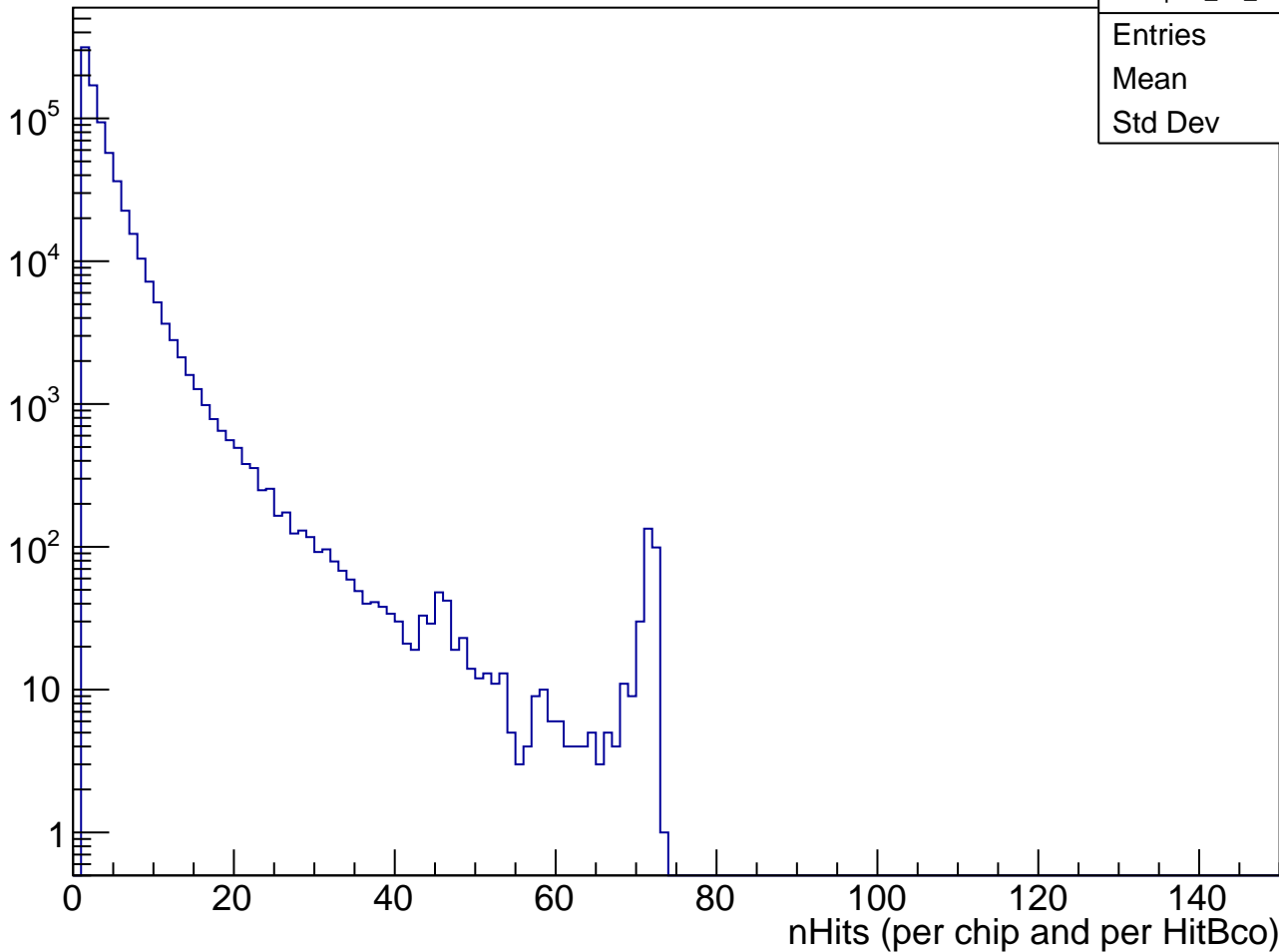


nChipHit_F4_Fch5_Chip5	
Entries	755201
Mean	2.749
Std Dev	3.354

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip6

Entries

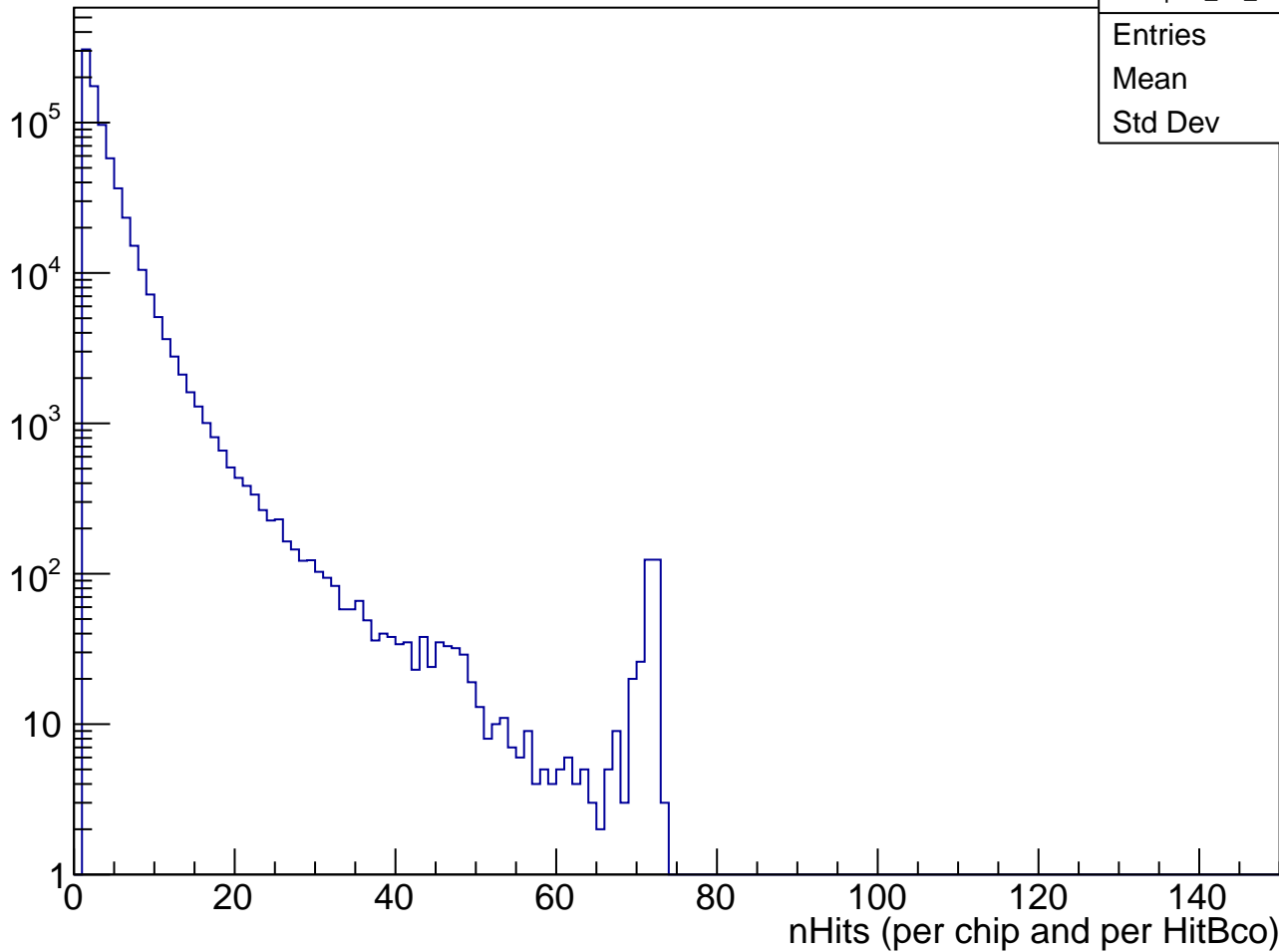


nChipHit_F4_Fch5_Chip6	
Entries	751500
Mean	2.796
Std Dev	3.302

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip7

Entries



nChipHit_F4_Fch5_Chip7

Entries 751635

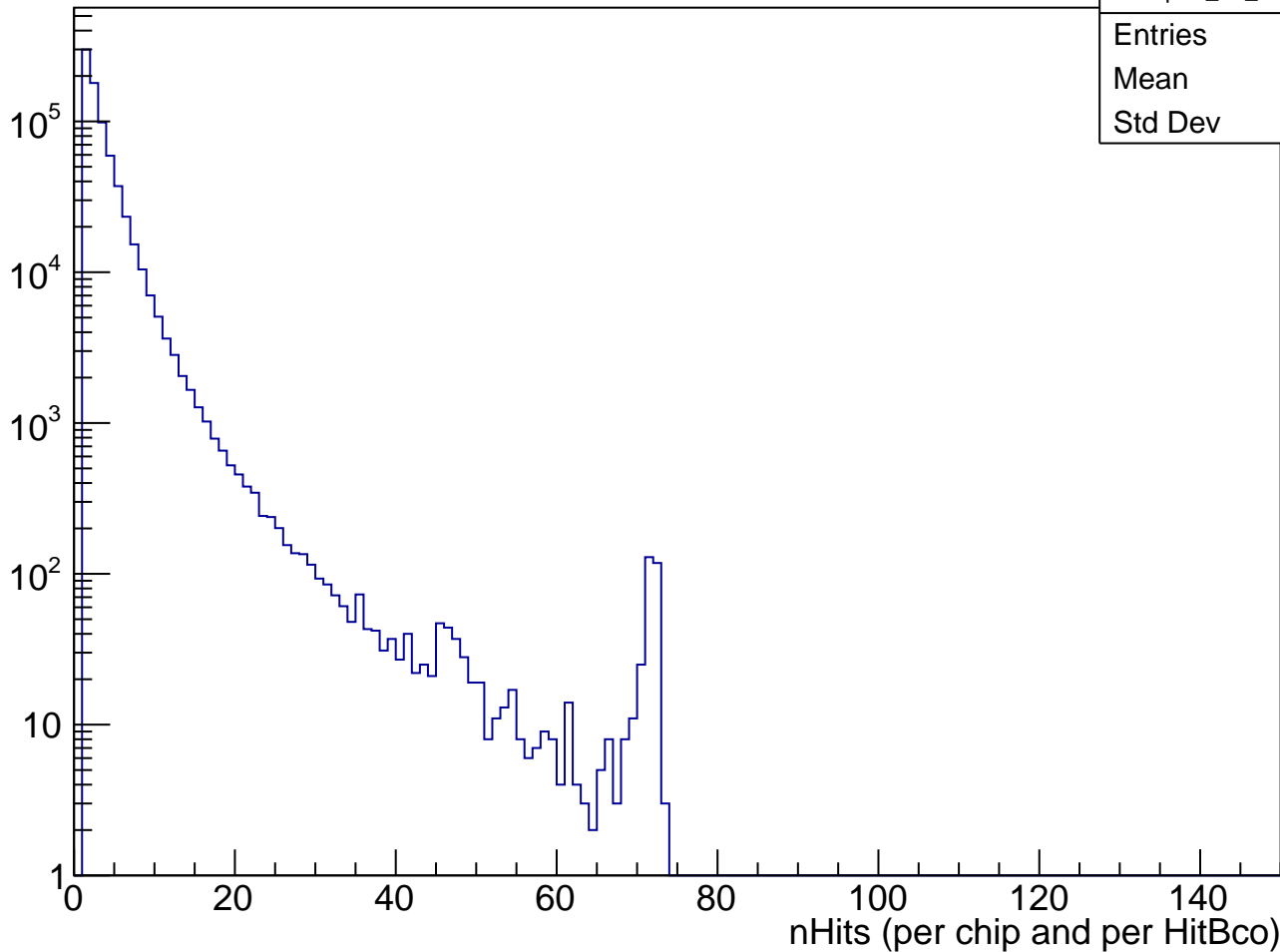
Mean 2.818

Std Dev 3.327

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip8

Entries



nChipHit_F4_Fch5_Chip8

Entries 753573

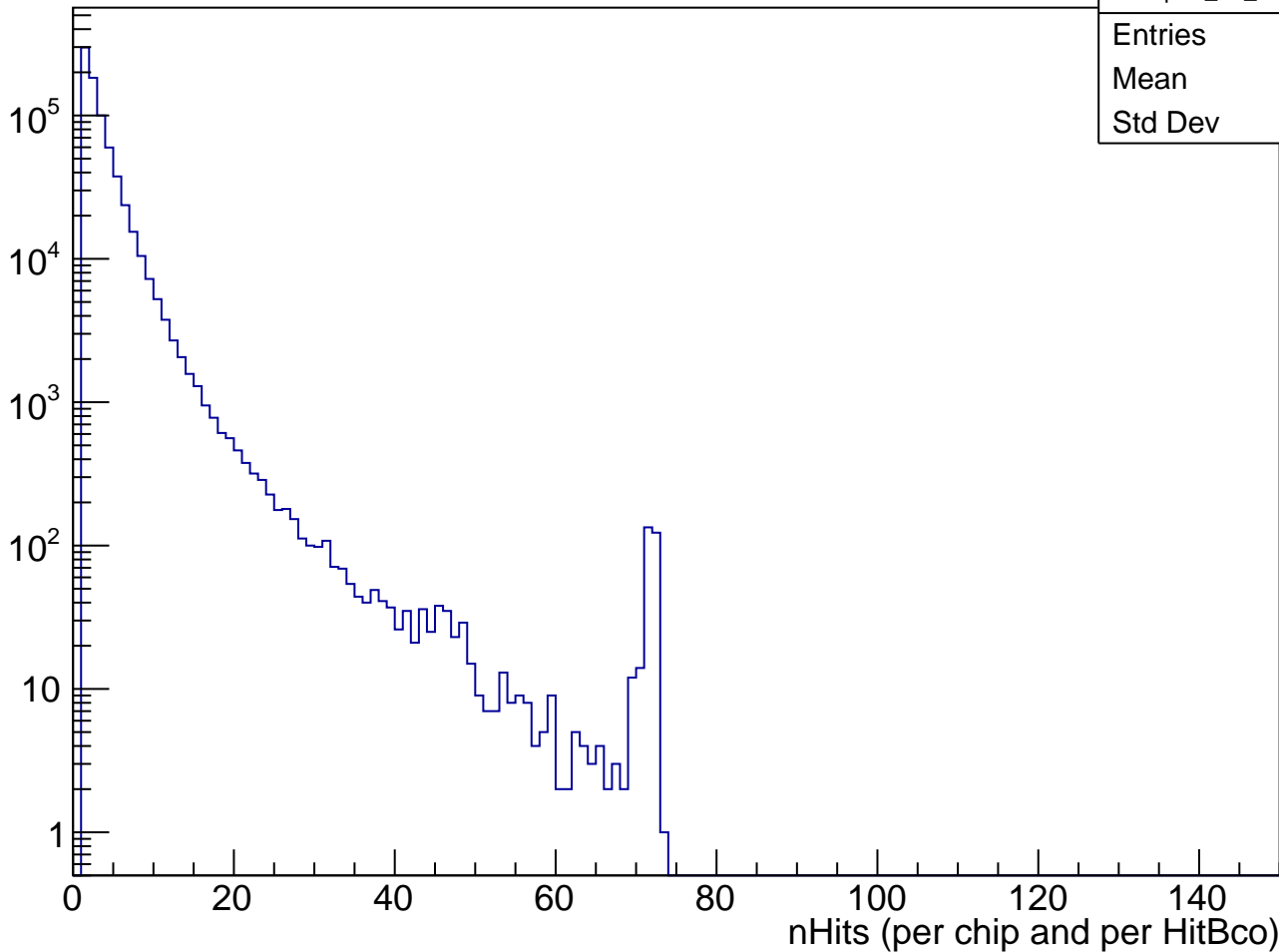
Mean 2.833

Std Dev 3.324

nHits (per chip and per HitBco)

nChipHit_F4_Fch5_Chip9

Entries



nChipHit_F4_Fch5_Chip9

Entries 758757

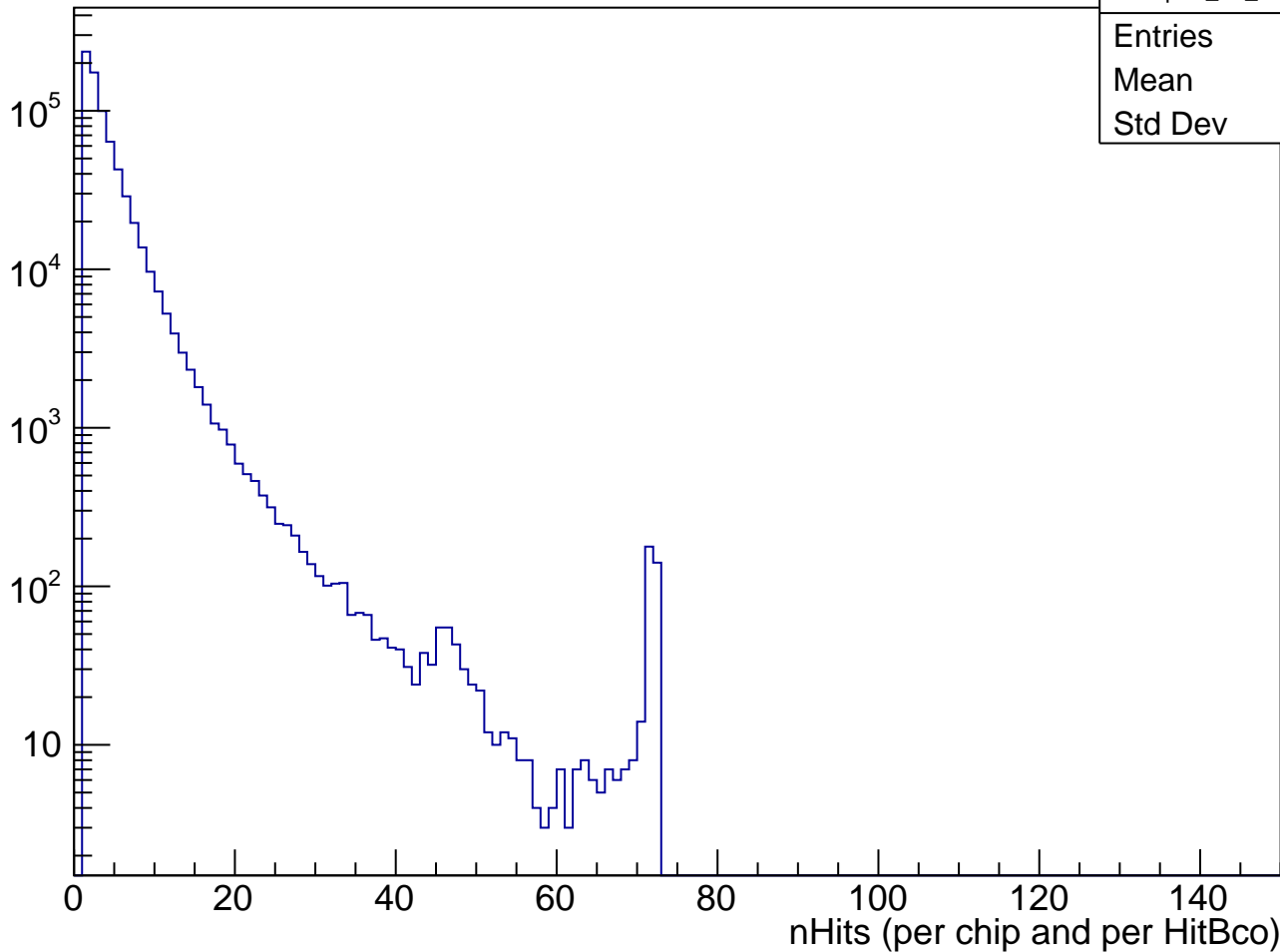
Mean 2.833

Std Dev 3.272

nHits (per chip and per HitBco)

nChipHit_F4_Fch6_Chip0

Entries



nChipHit_F4_Fch6_Chip0

Entries 720931

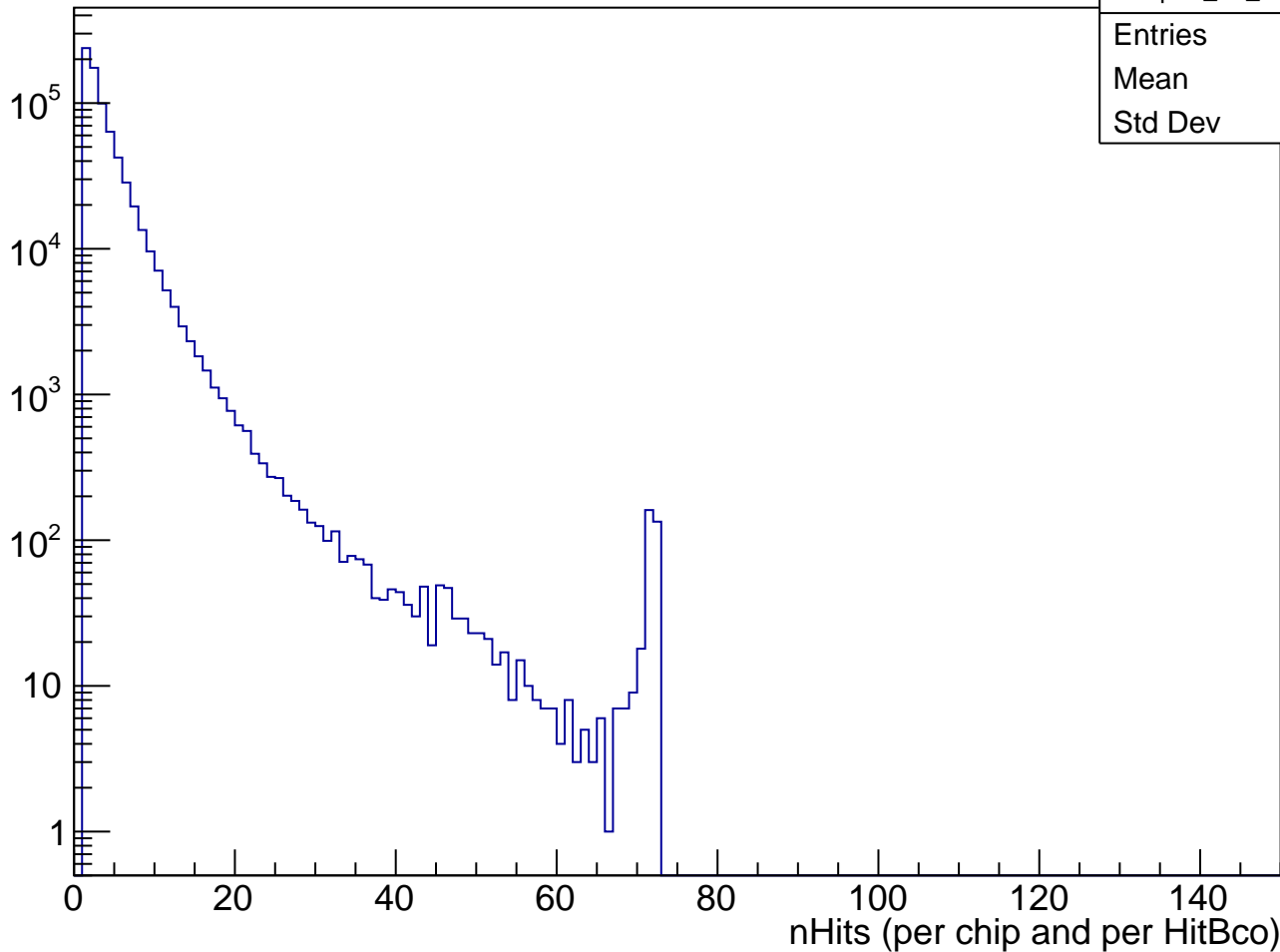
Mean 3.271

Std Dev 3.73

nHits (per chip and per HitBco)

nChipHit_F4_Fch6_Chip1

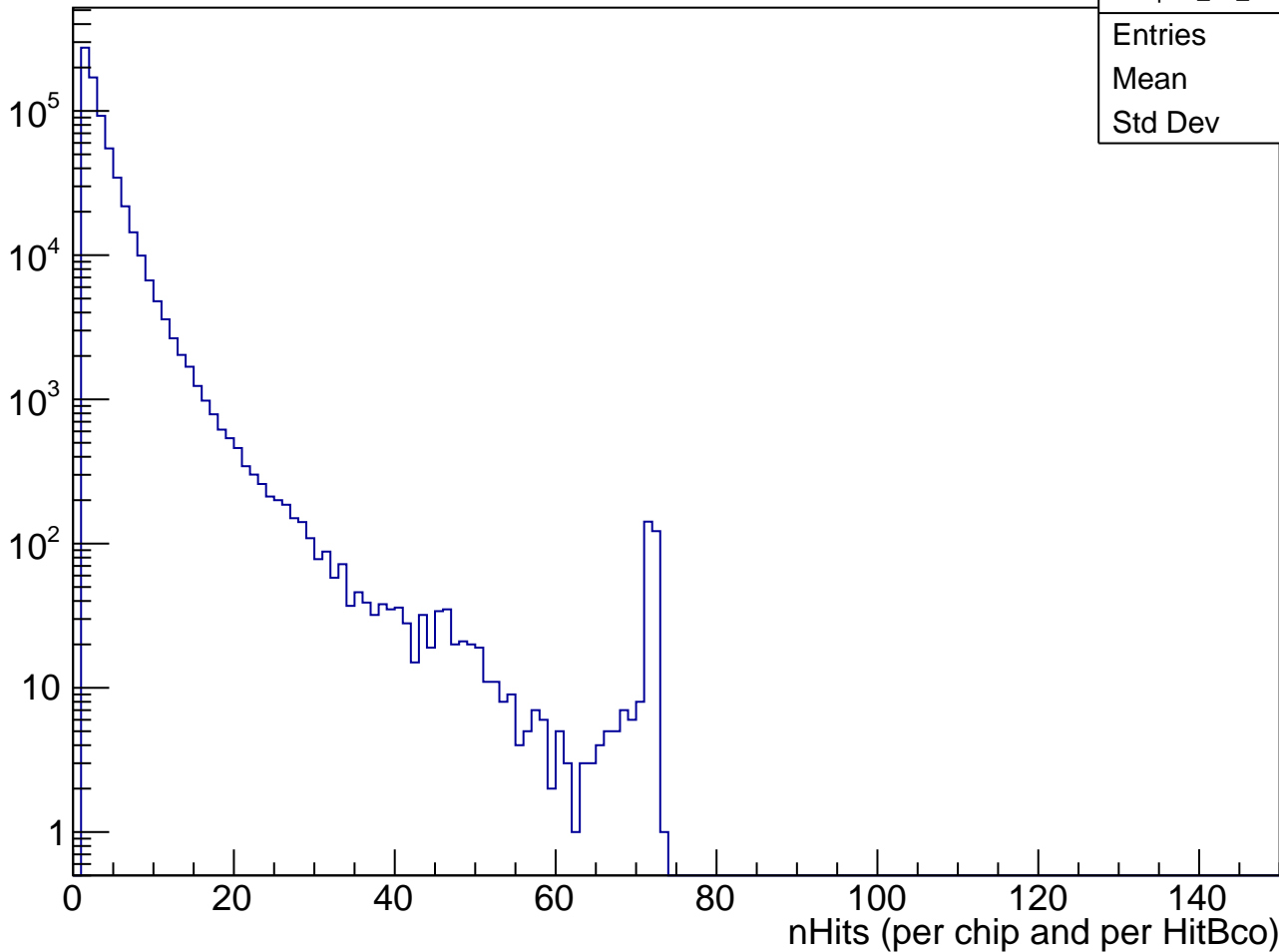
Entries



nChipHit_F4_Fch6_Chip1	
Entries	721954
Mean	3.252
Std Dev	3.7

nChipHit_F4_Fch6_Chip10

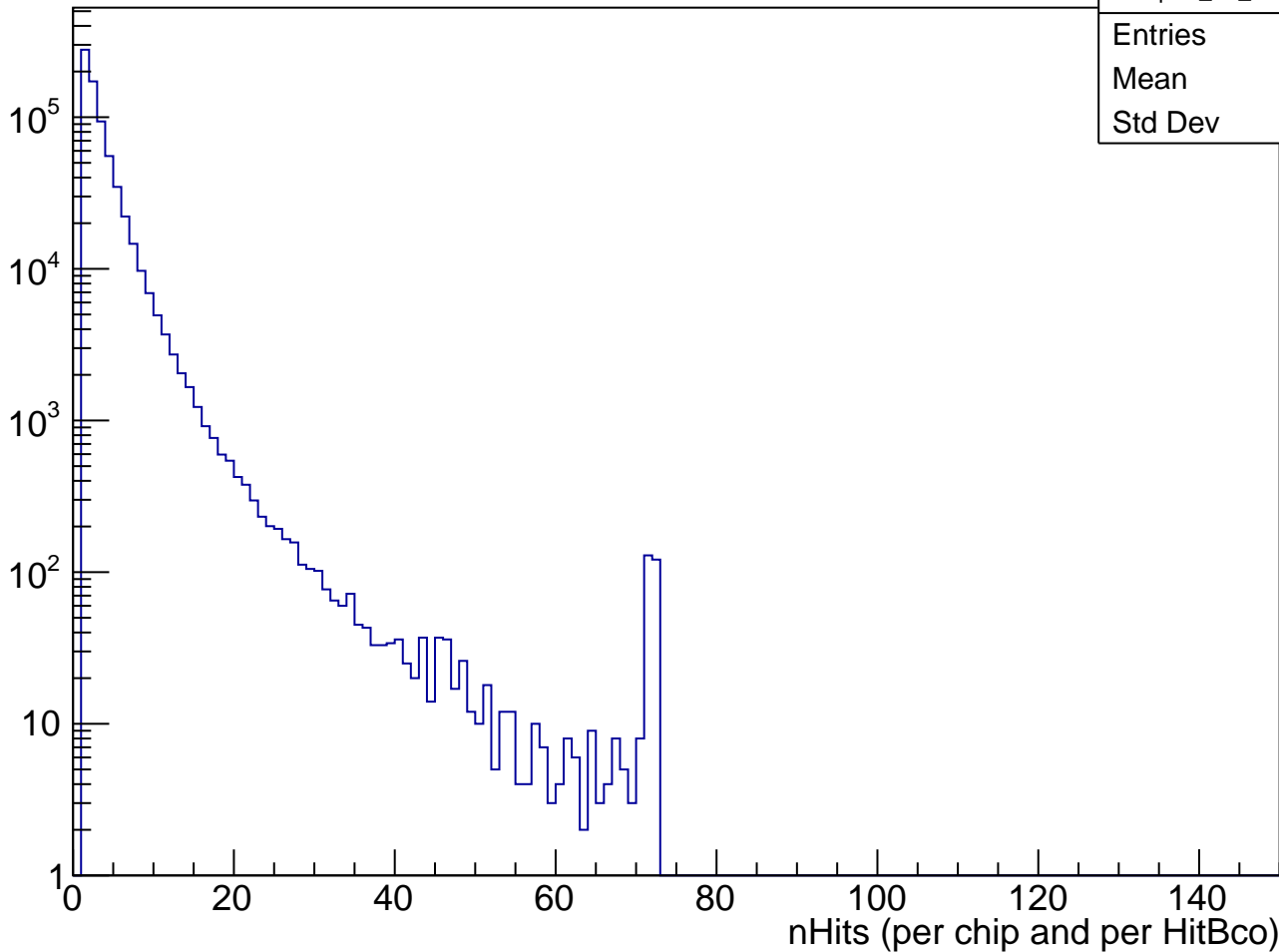
Entries



nChipHit_F4_Fch6_Chip10	
Entries	702477
Mean	2.855
Std Dev	3.333

nChipHit_F4_Fch6_Chip11

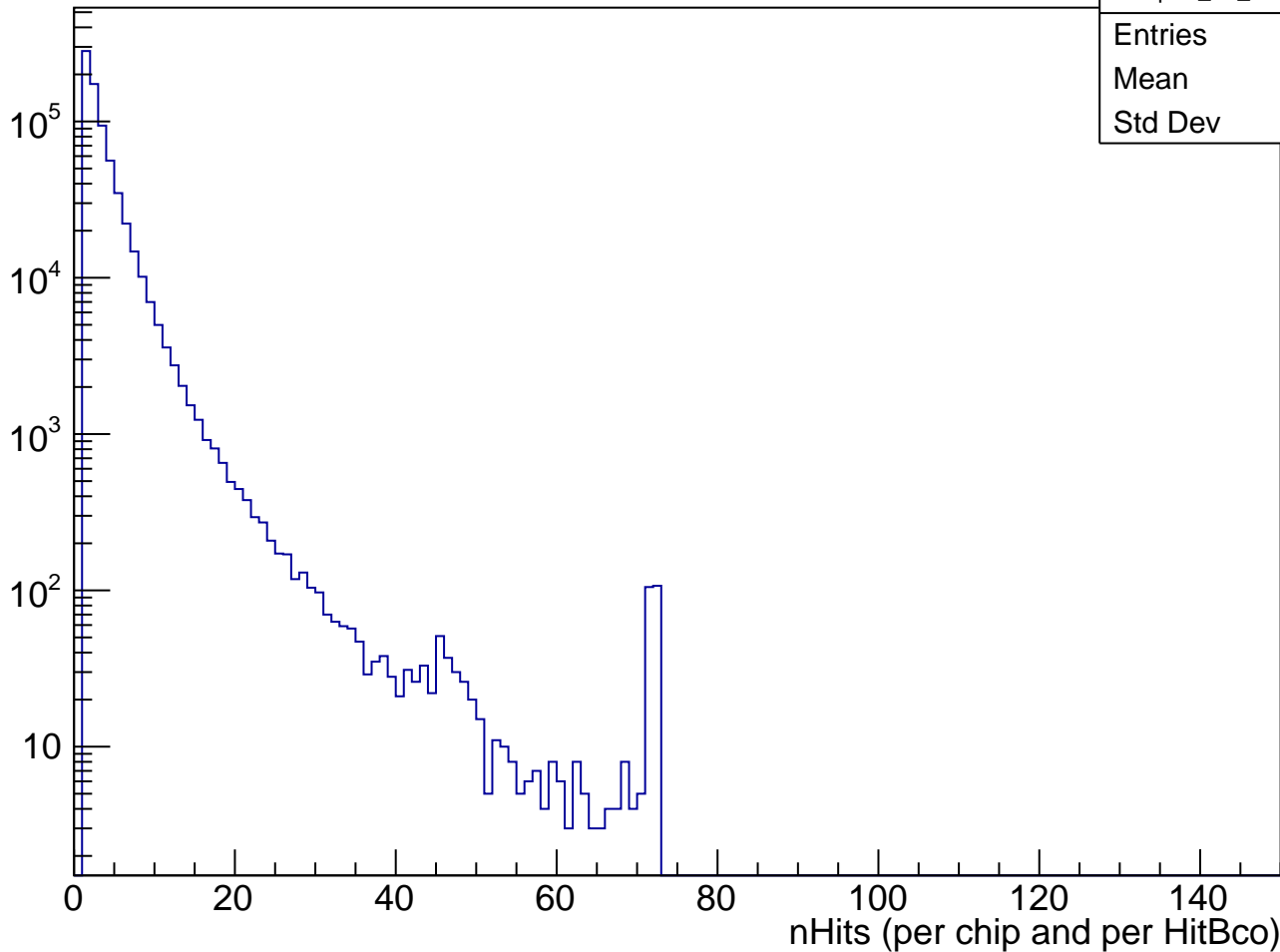
Entries



nChipHit_F4_Fch6_Chip11	
Entries	710568
Mean	2.848
Std Dev	3.31

nChipHit_F4_Fch6_Chip12

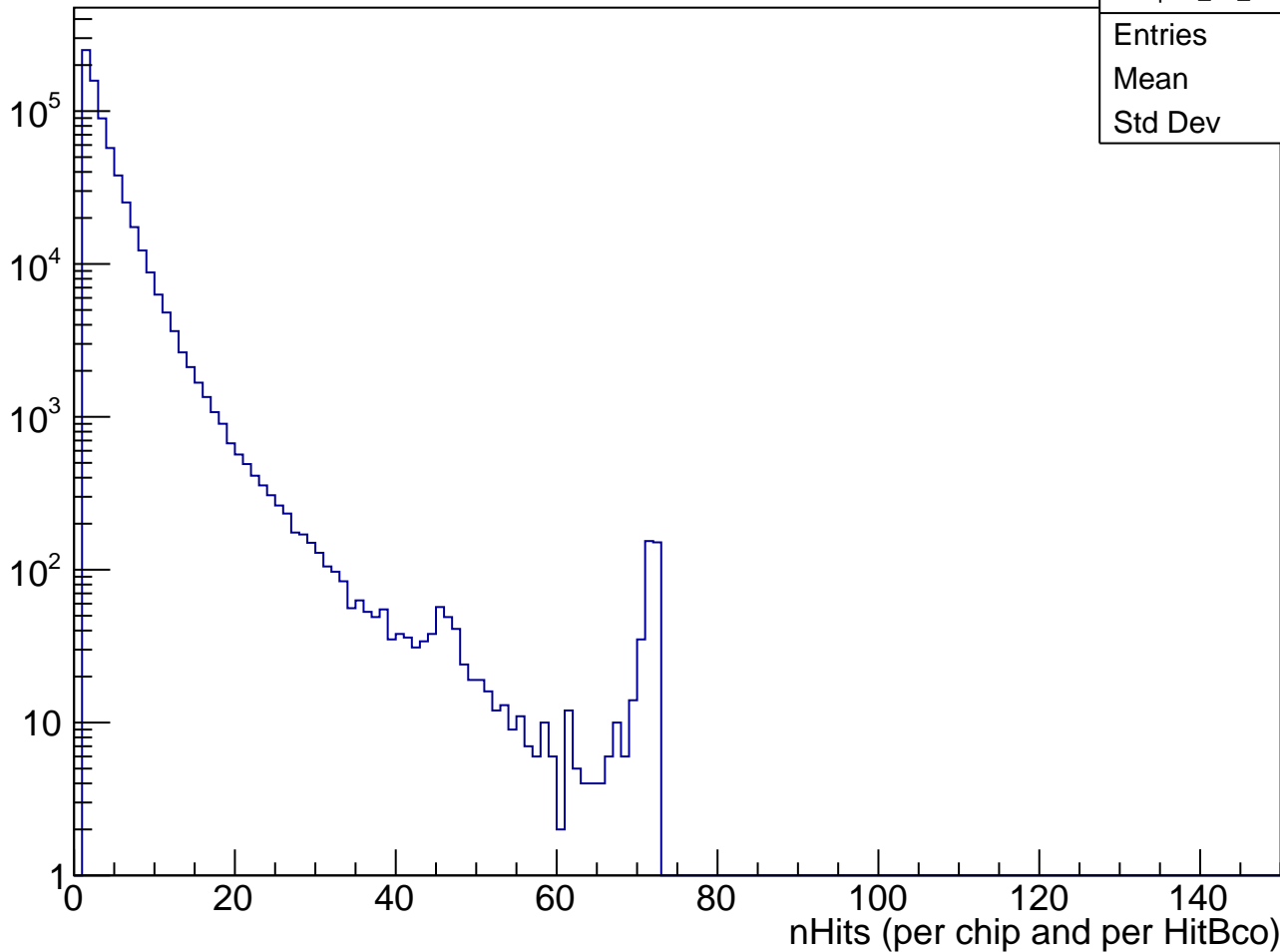
Entries



nChipHit_F4_Fch6_Chip12	
Entries	717302
Mean	2.838
Std Dev	3.261

nChipHit_F4_Fch6_Chip13

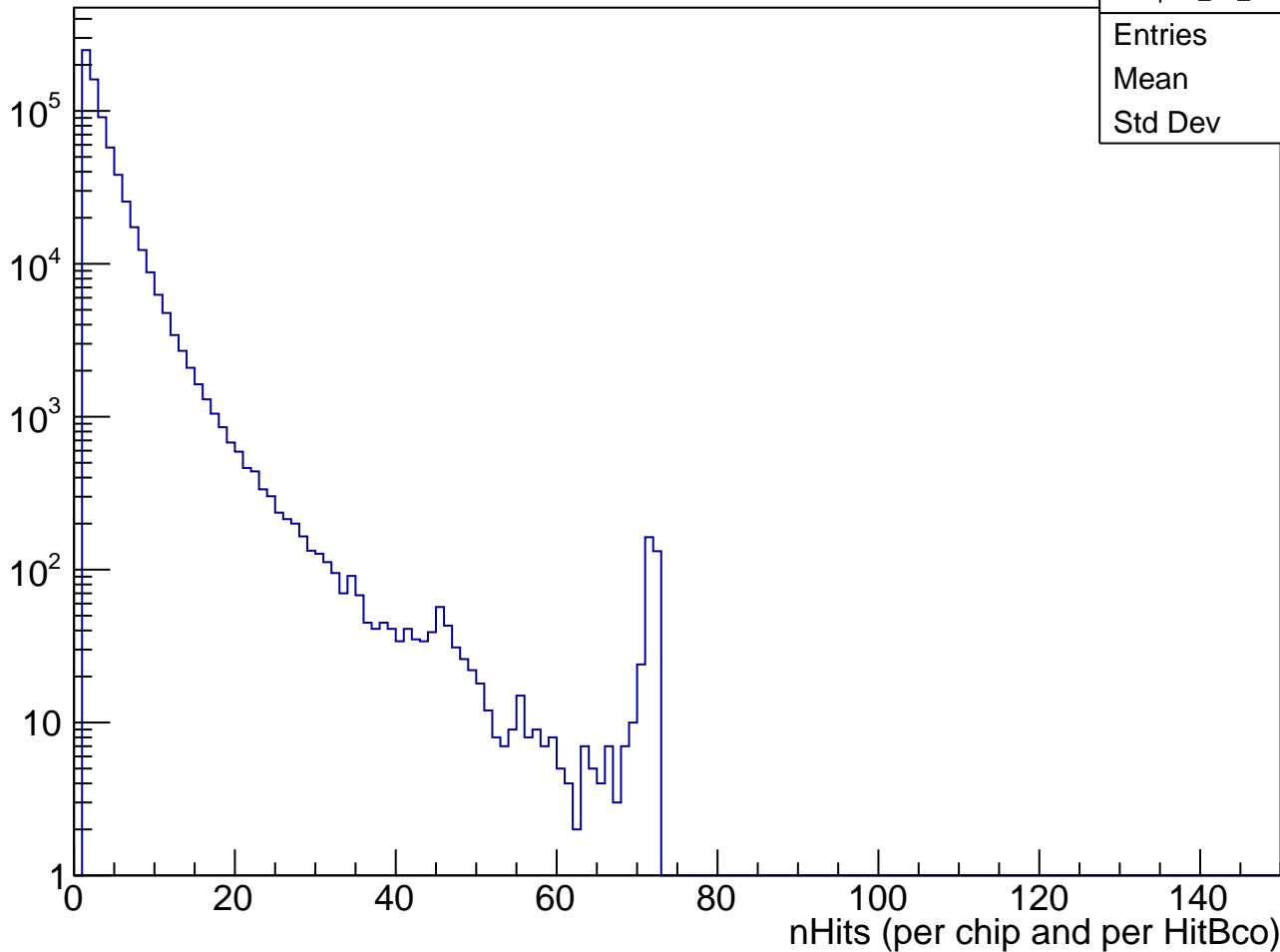
Entries



nChipHit_F4_Fch6_Chip13	
Entries	687395
Mean	3.158
Std Dev	3.759

nChipHit_F4_Fch6_Chip14

Entries

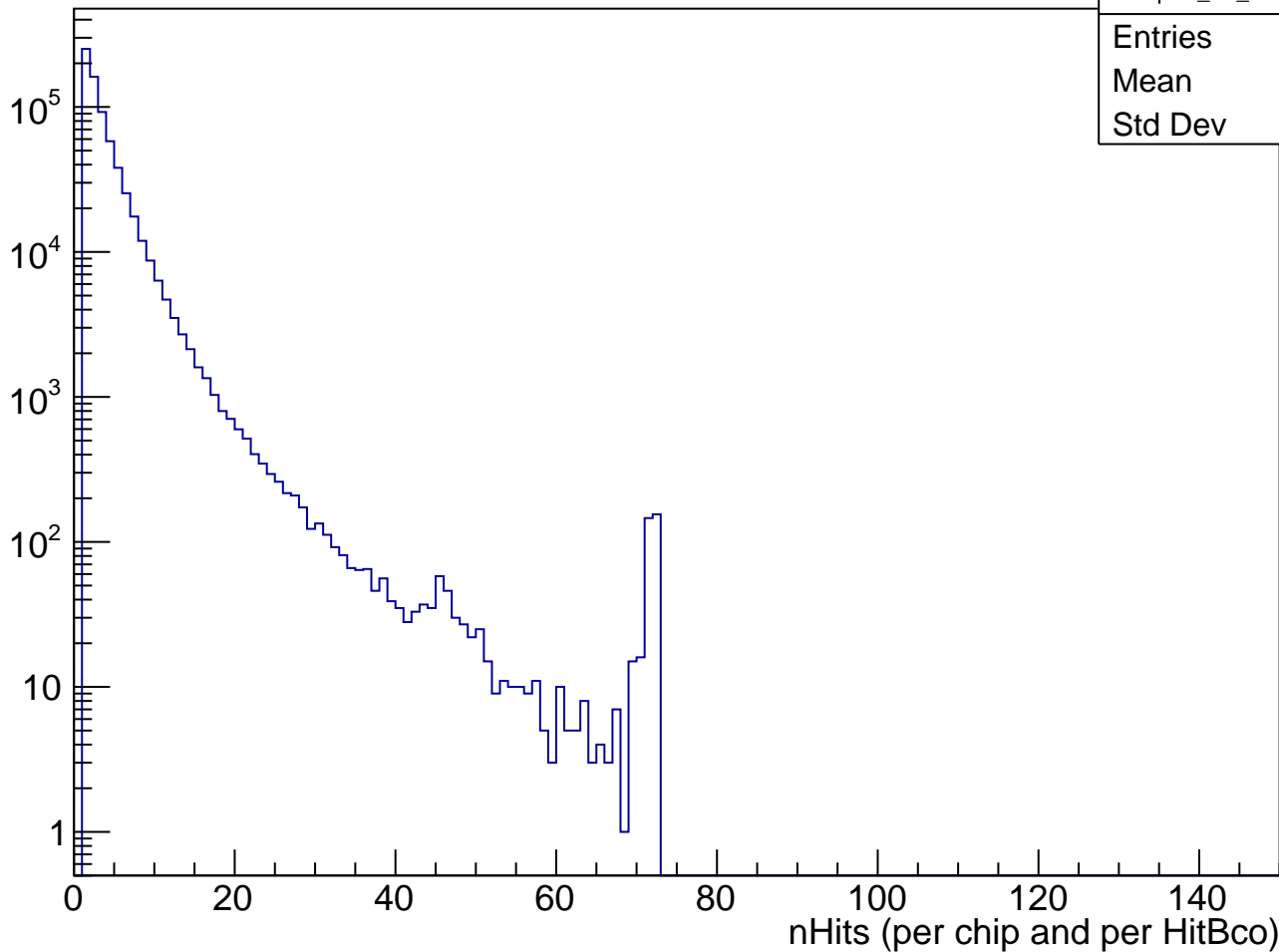


nChipHit_F4_Fch6_Chip14	
Entries	690885
Mean	3.146
Std Dev	3.703

nHits (per chip and per HitBco)

nChipHit_F4_Fch6_Chip15

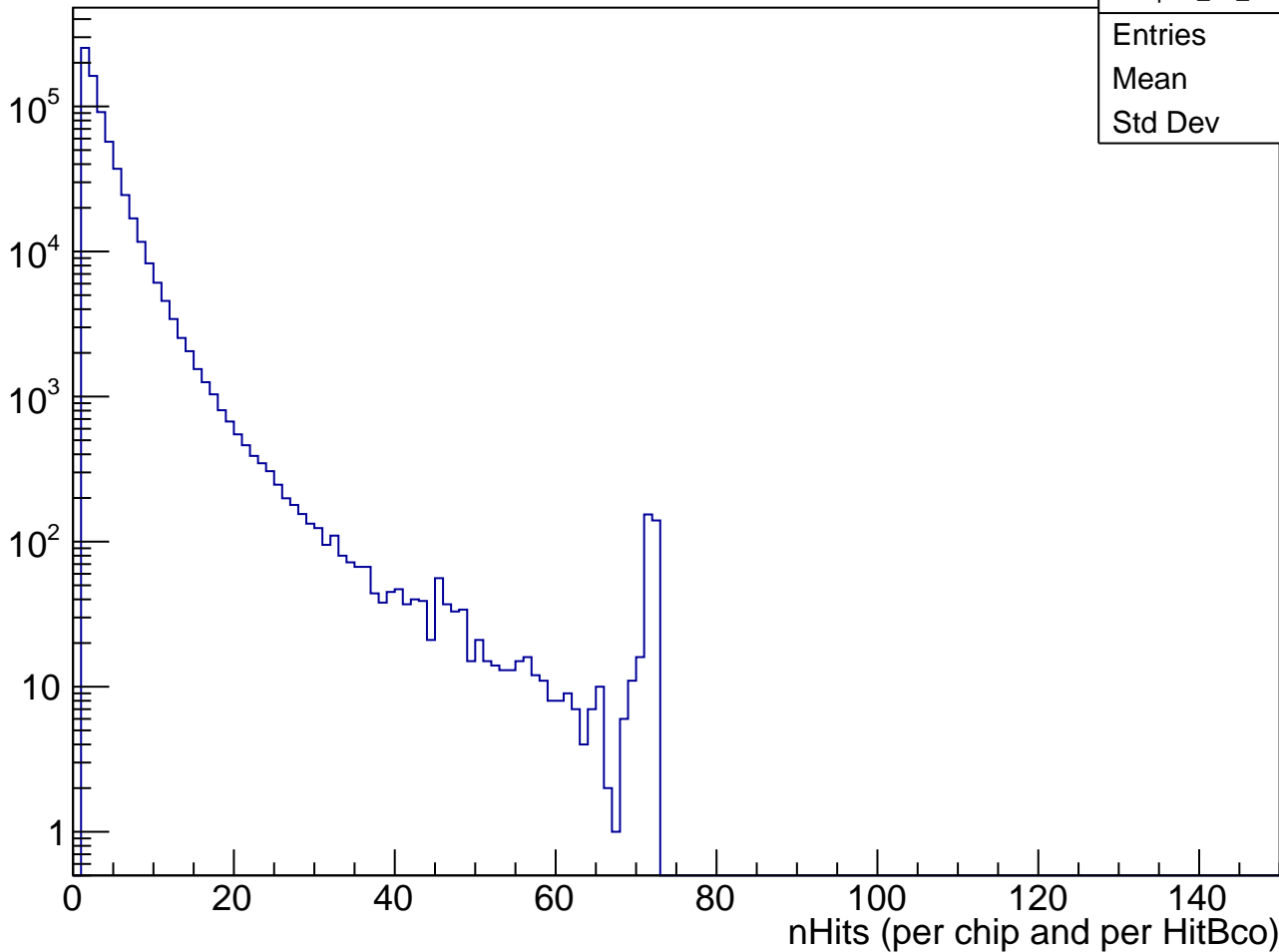
Entries



nChipHit_F4_Fch6_Chip15	
Entries	694781
Mean	3.141
Std Dev	3.705

nChipHit_F4_Fch6_Chip16

Entries

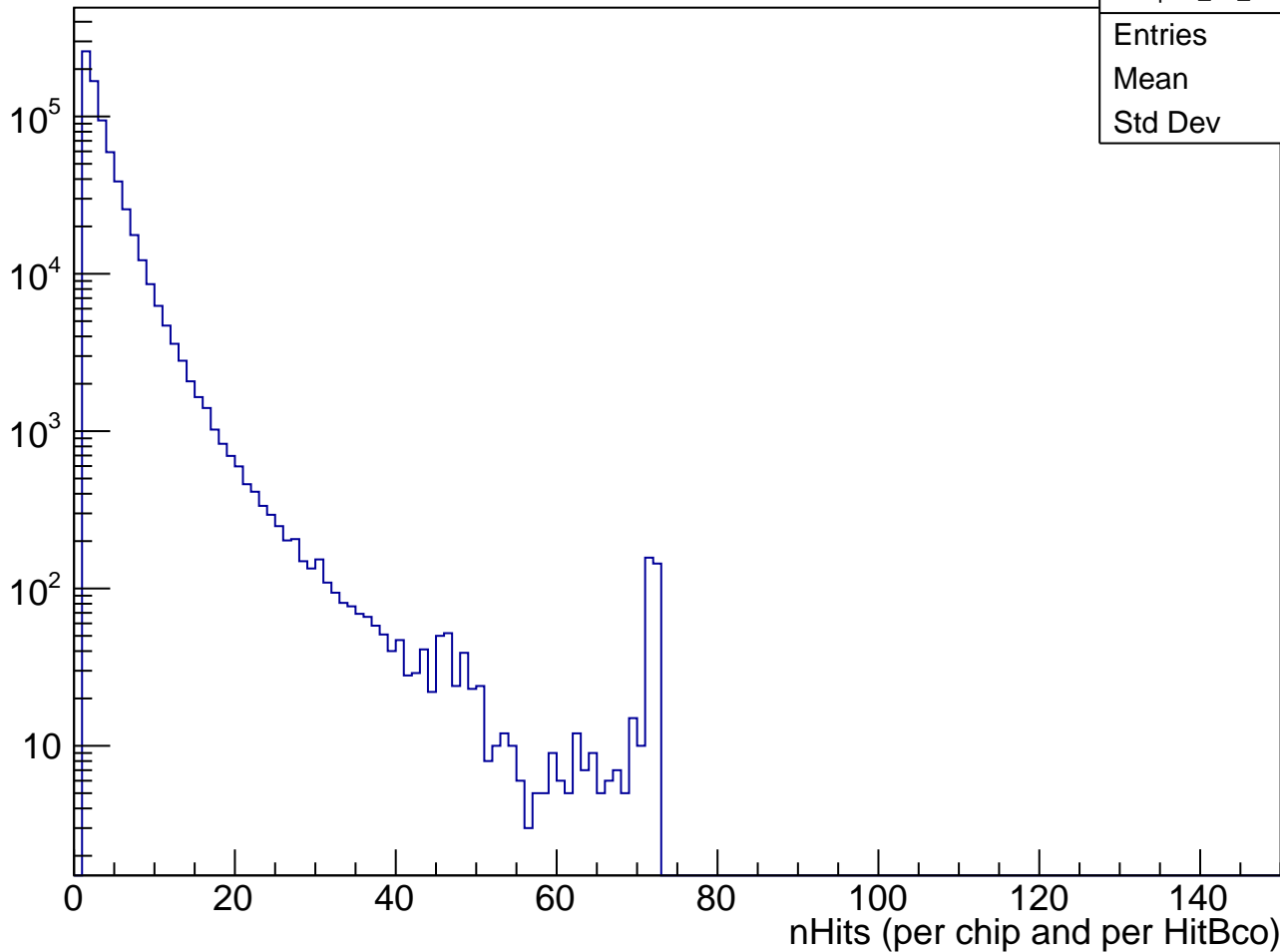


nChipHit_F4_Fch6_Chip16	
Entries	690996
Mean	3.104
Std Dev	3.691

nHits (per chip and per HitBco)

nChipHit_F4_Fch6_Chip17

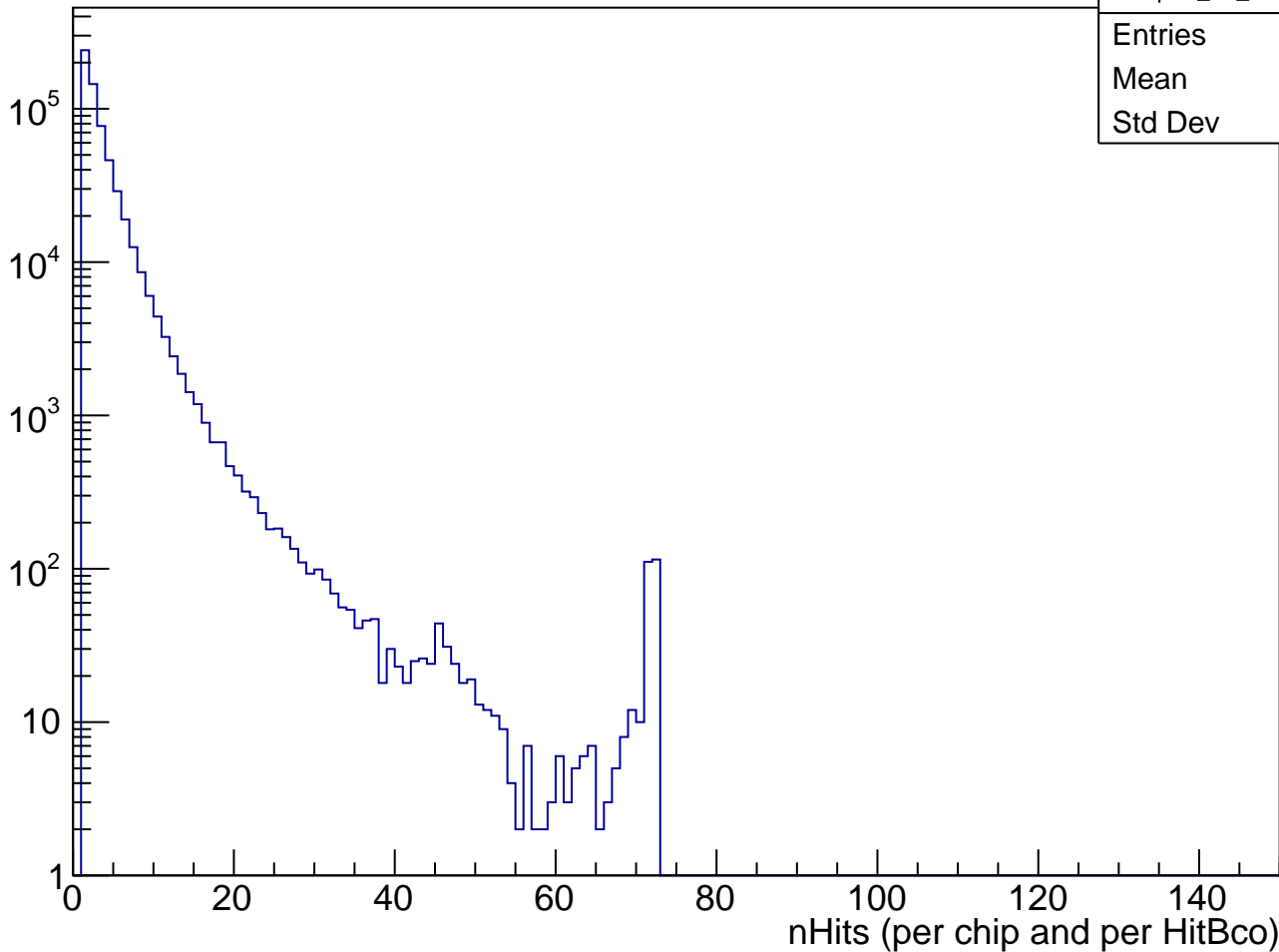
Entries



nChipHit_F4_Fch6_Chip17	
Entries	713393
Mean	3.114
Std Dev	3.671

nChipHit_F4_Fch6_Chip18

Entries

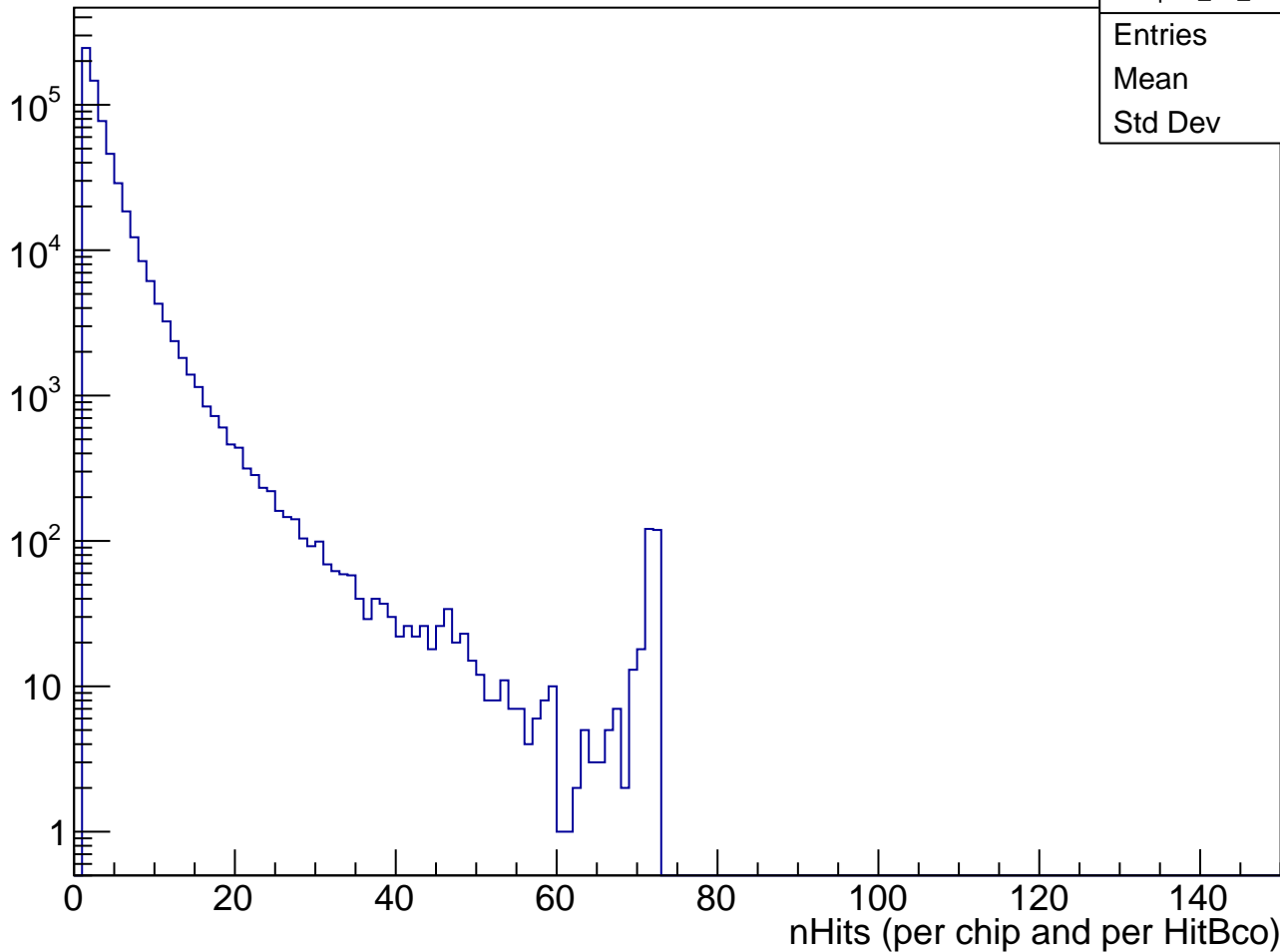


nChipHit_F4_Fch6_Chip18	
Entries	604968
Mean	2.871
Std Dev	3.421

nHits (per chip and per HitBco)

nChipHit_F4_Fch6_Chip19

Entries

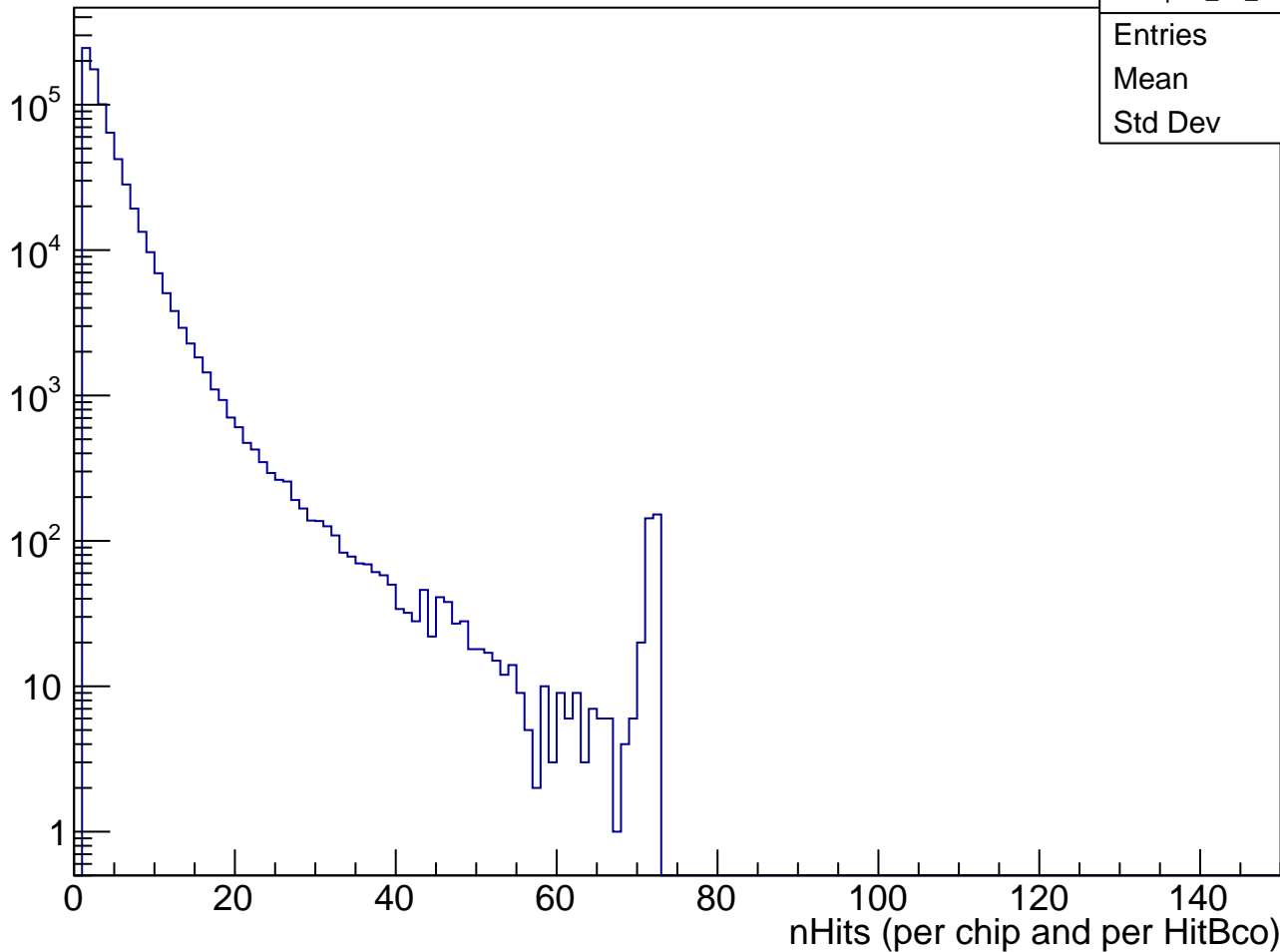


nChipHit_F4_Fch6_Chip19	
Entries	610259
Mean	2.842
Std Dev	3.409

nHits (per chip and per HitBco)

nChipHit_F4_Fch6_Chip2

Entries



nChipHit_F4_Fch6_Chip2

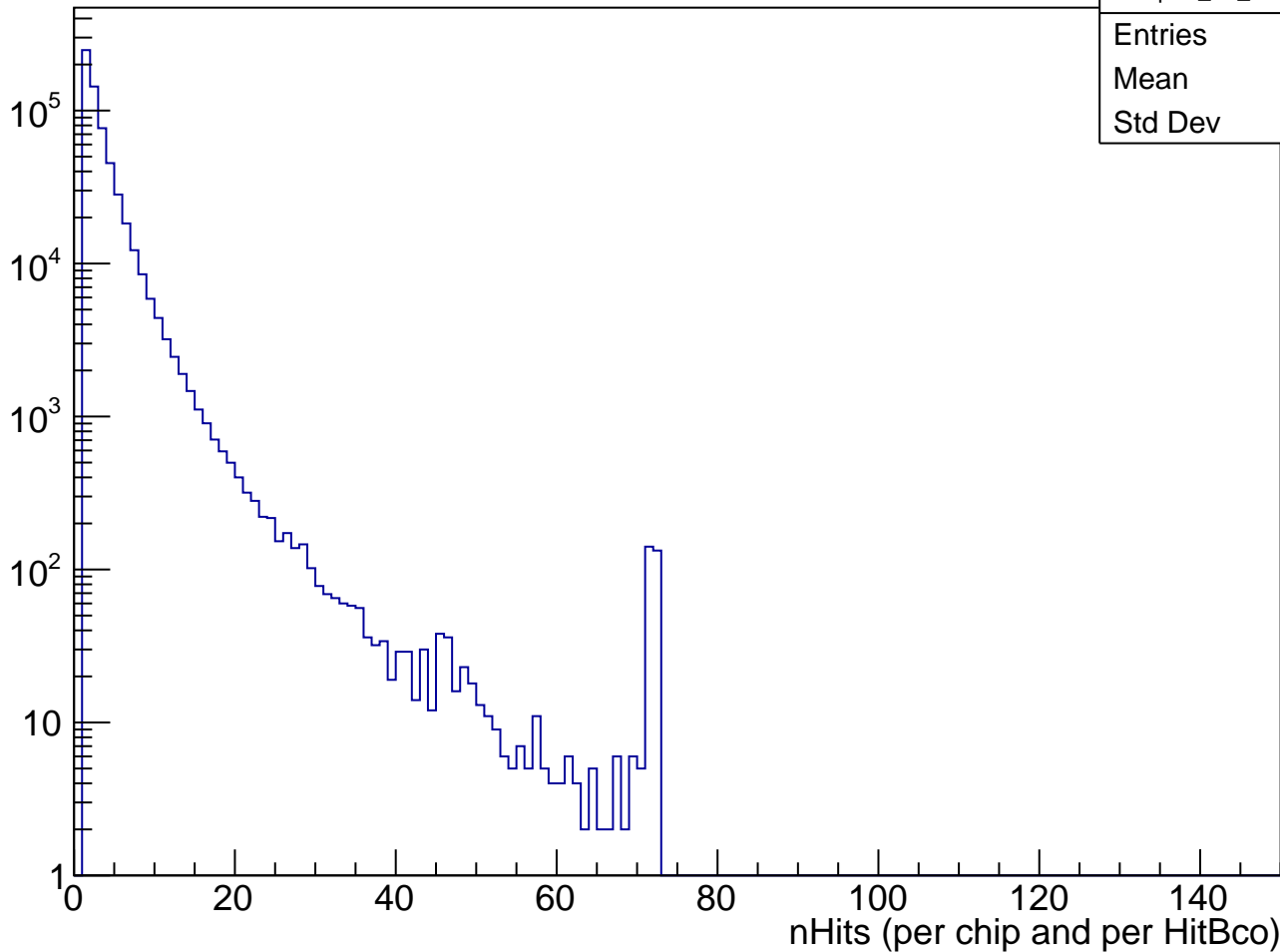
Entries 730016

Mean 3.221

Std Dev 3.674

nChipHit_F4_Fch6_Chip20

Entries

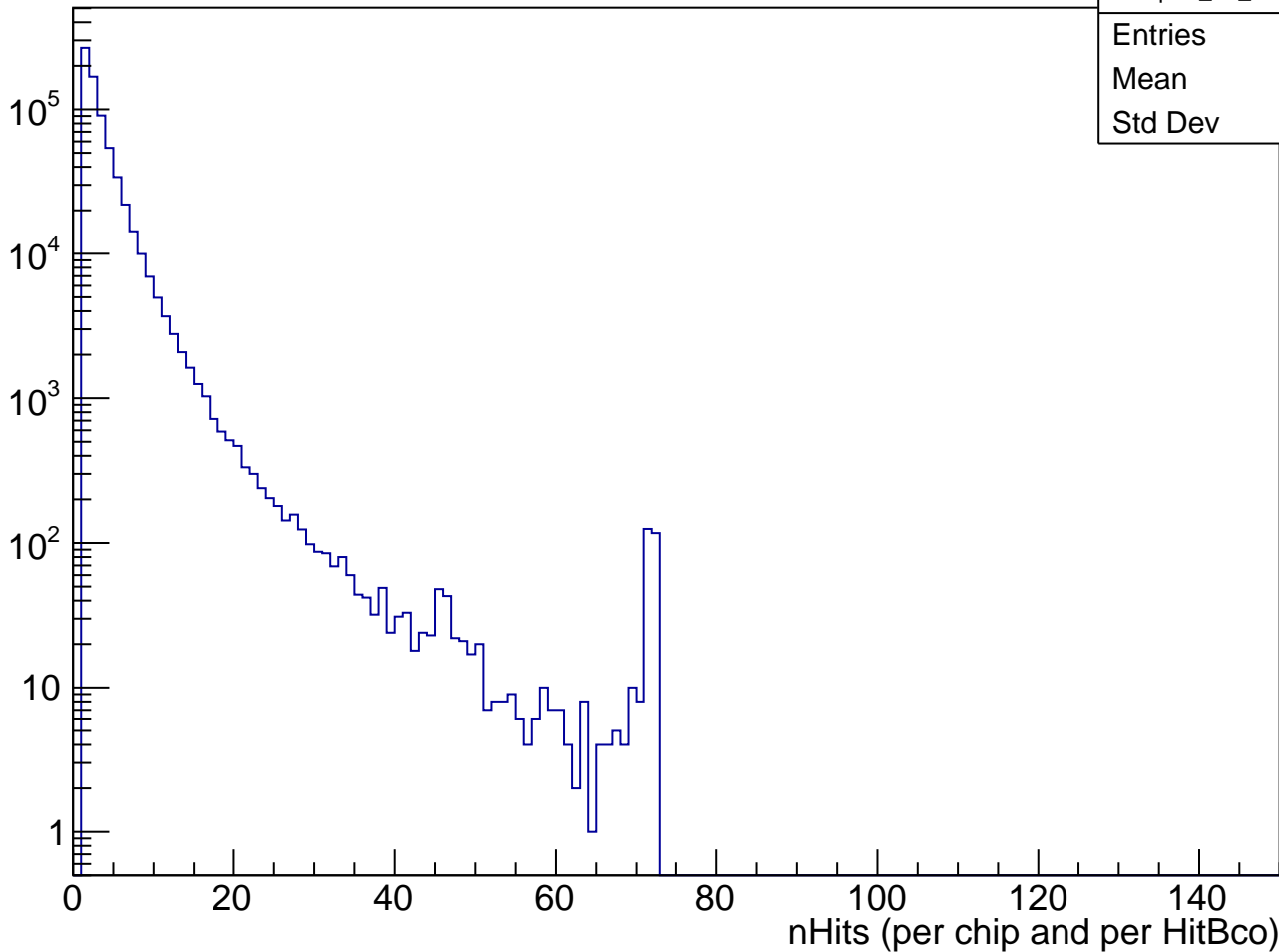


nChipHit_F4_Fch6_Chip20	
Entries	607697
Mean	2.841
Std Dev	3.447

nHits (per chip and per HitBco)

nChipHit_F4_Fch6_Chip21

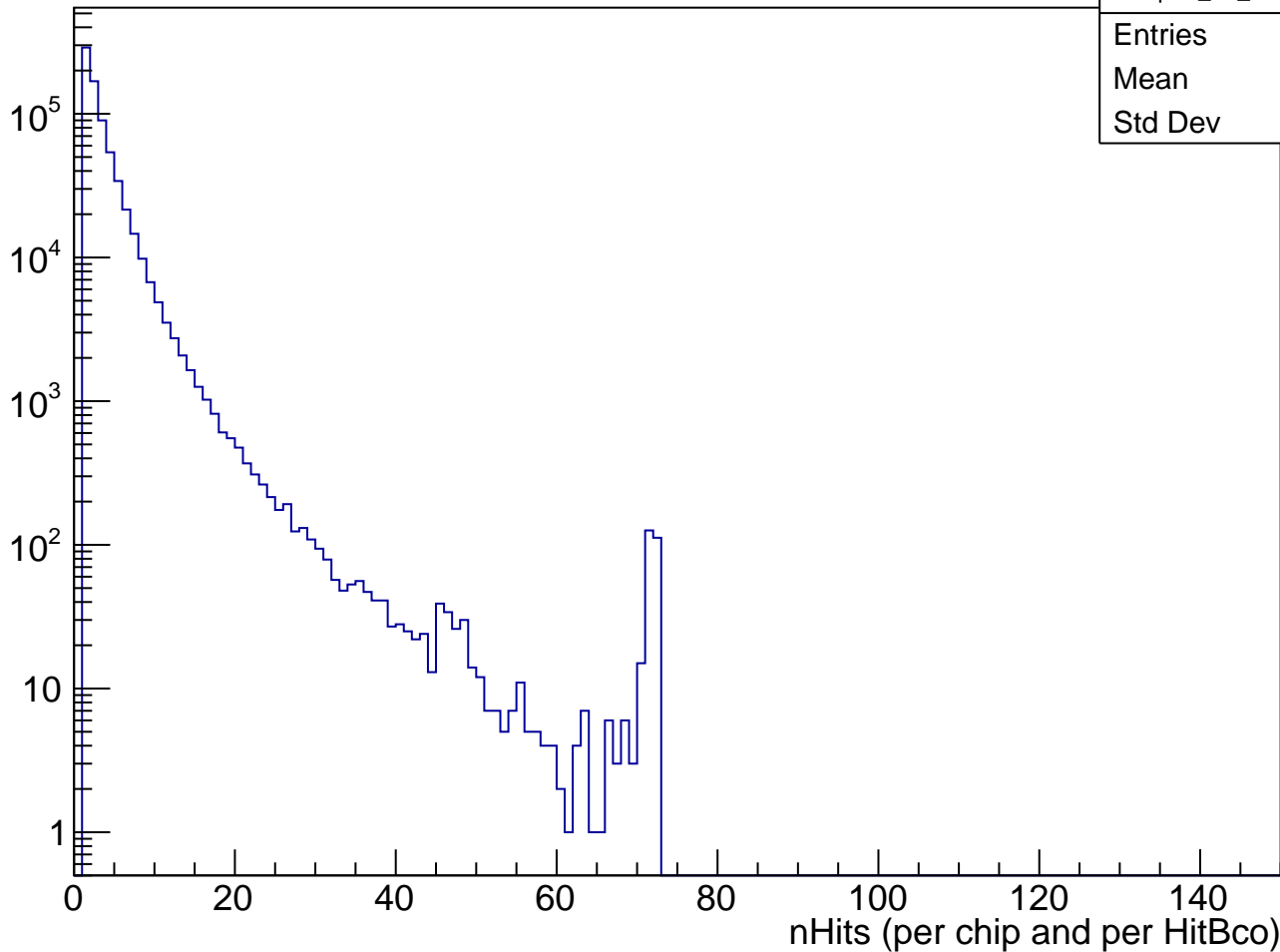
Entries



nChipHit_F4_Fch6_Chip21	
Entries	689150
Mean	2.881
Std Dev	3.35

nChipHit_F4_Fch6_Chip22

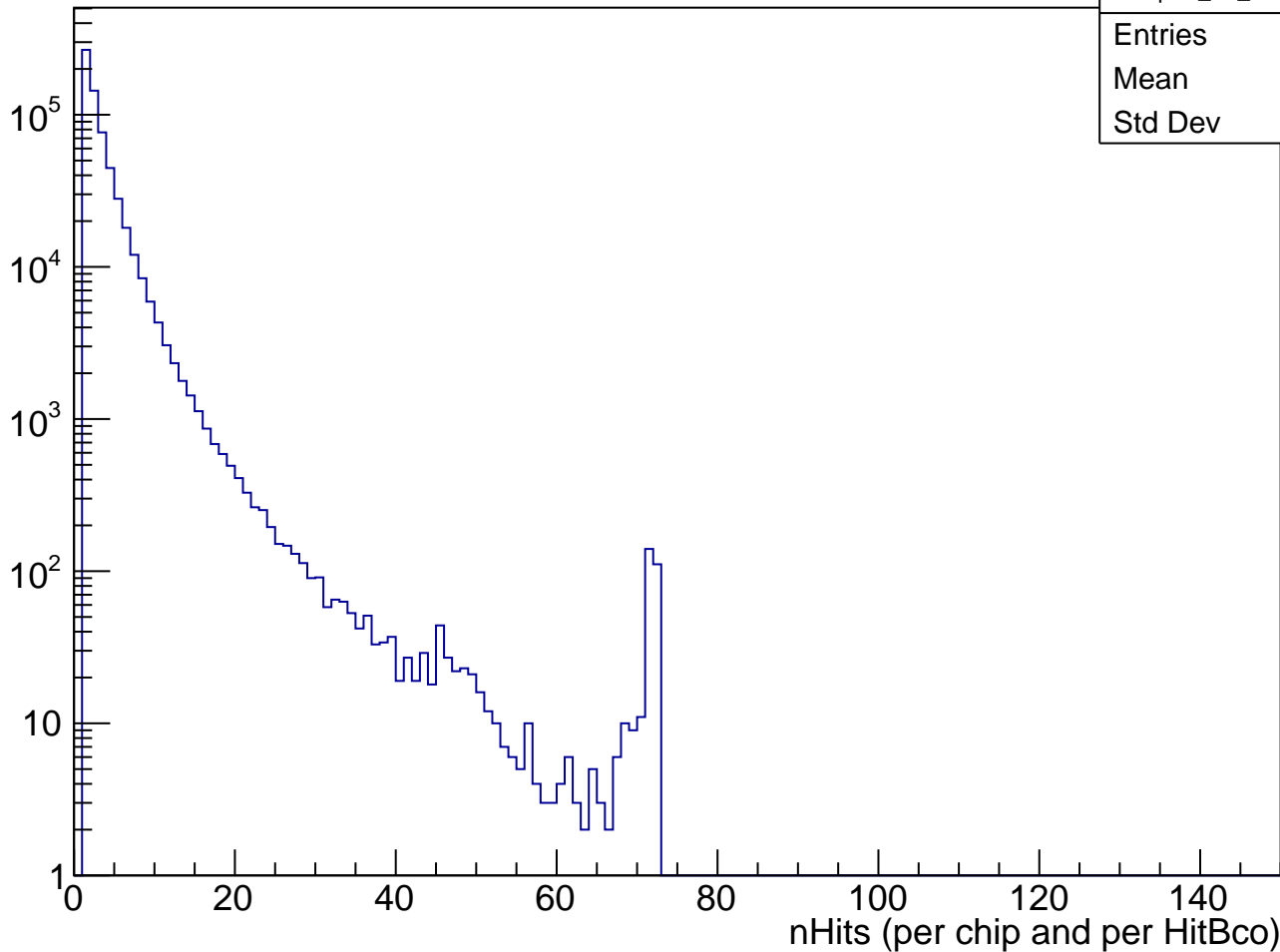
Entries



nChipHit_F4_Fch6_Chip22	
Entries	711112
Mean	2.816
Std Dev	3.294

nChipHit_F4_Fch6_Chip23

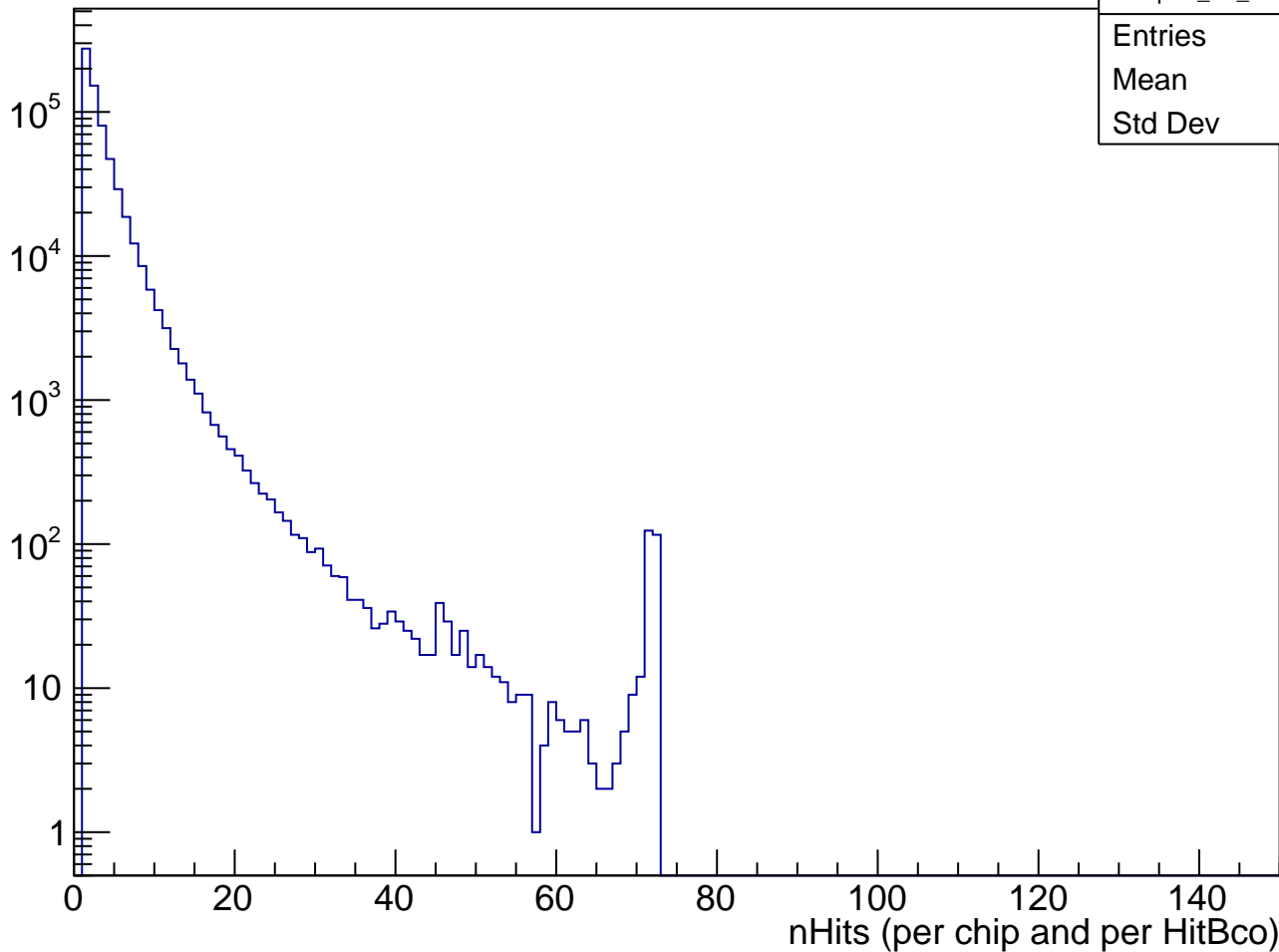
Entries



nChipHit_F4_Fch6_Chip23	
Entries	624459
Mean	2.77
Std Dev	3.388

nChipHit_F4_Fch6_Chip24

Entries

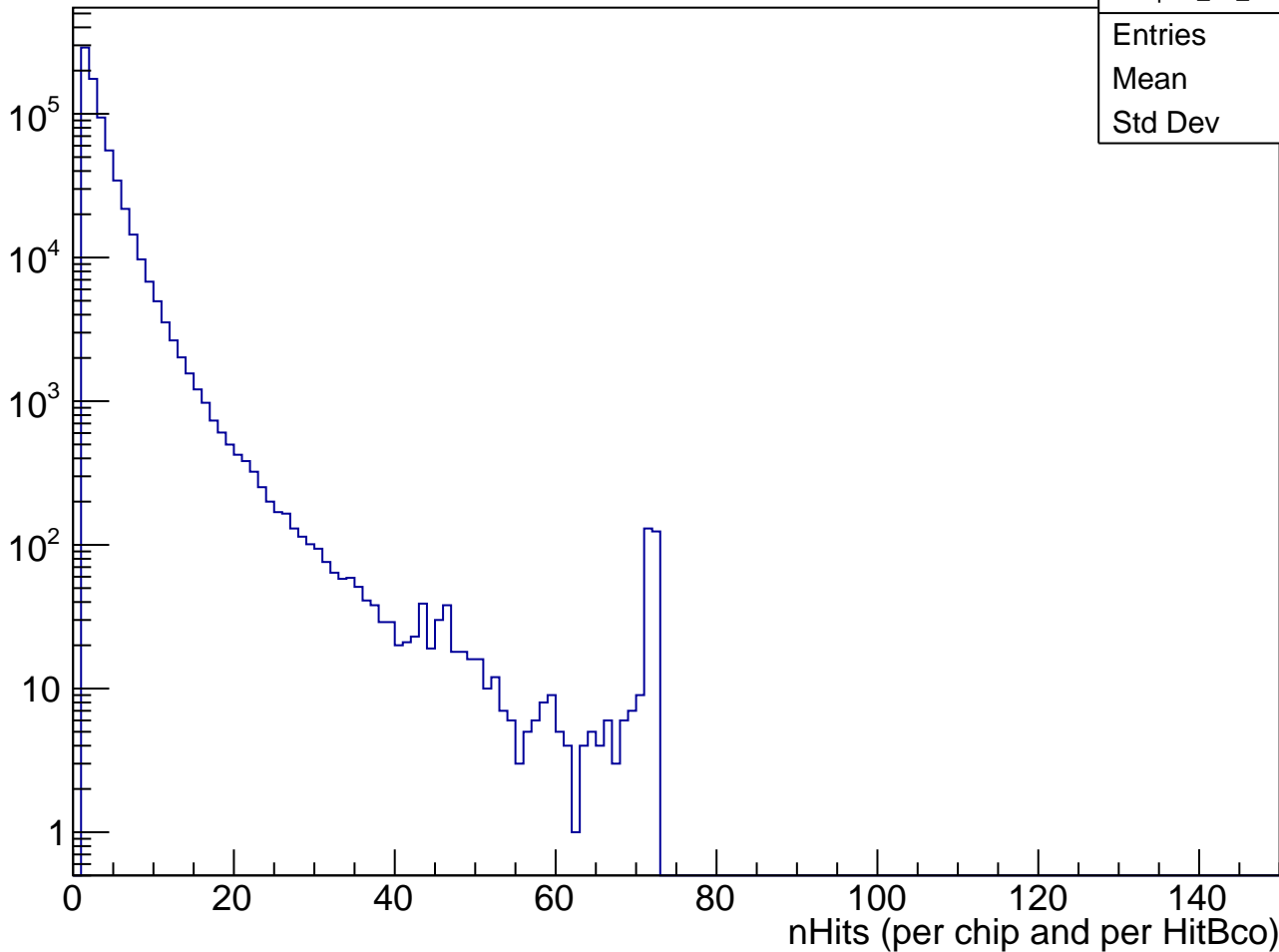


nChipHit_F4_Fch6_Chip24	
Entries	649017
Mean	2.743
Std Dev	3.301

nHits (per chip and per HitBco)

nChipHit_F4_Fch6_Chip25

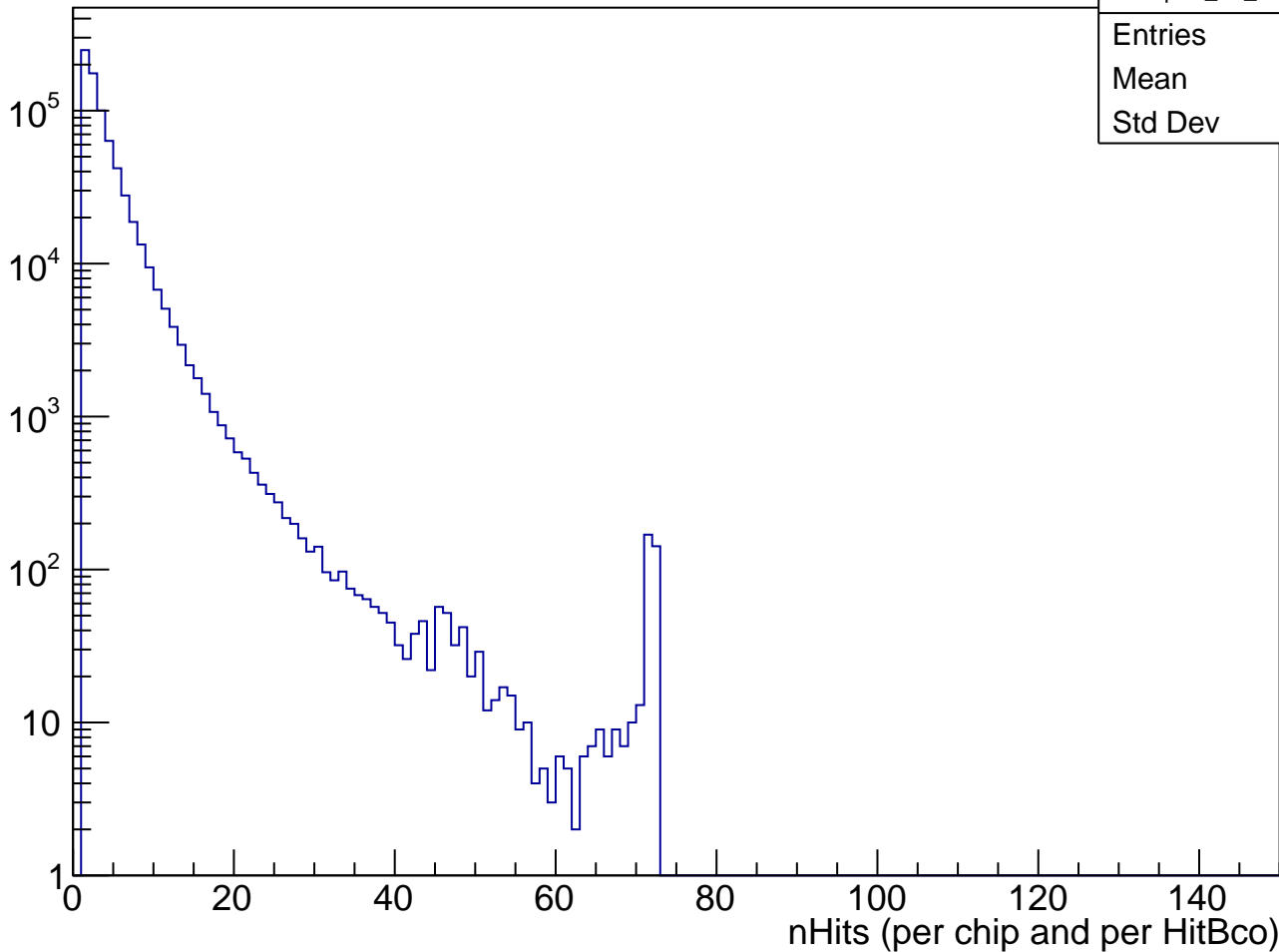
Entries



nChipHit_F4_Fch6_Chip25	
Entries	723880
Mean	2.804
Std Dev	3.263

nChipHit_F4_Fch6_Chip3

Entries



nChipHit_F4_Fch6_Chip3

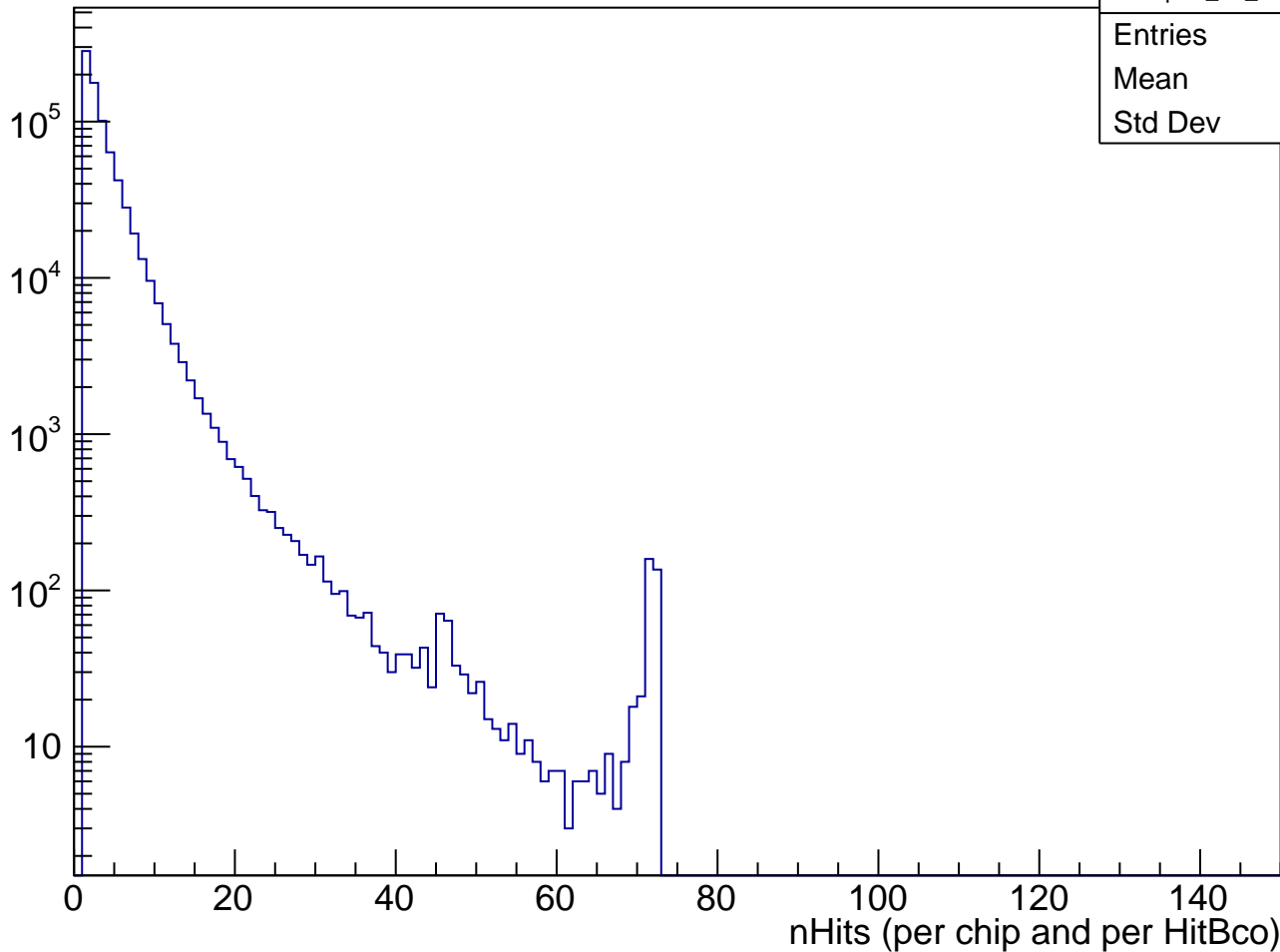
Entries 730810

Mean 3.202

Std Dev 3.695

nChipHit_F4_Fch6_Chip4

Entries



nChipHit_F4_Fch6_Chip4

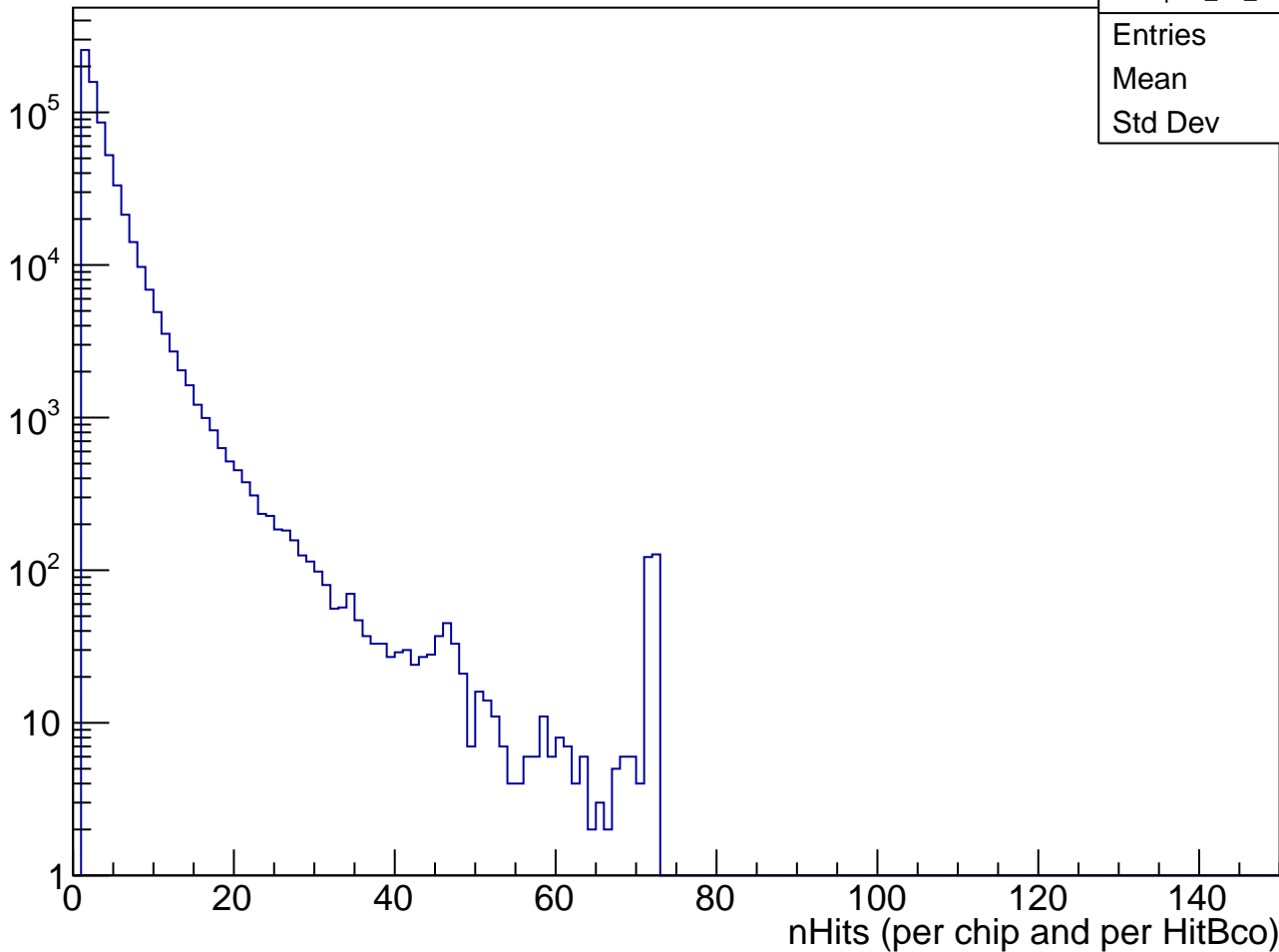
Entries 768502

Mean 3.105

Std Dev 3.637

nChipHit_F4_Fch6_Chip5

Entries

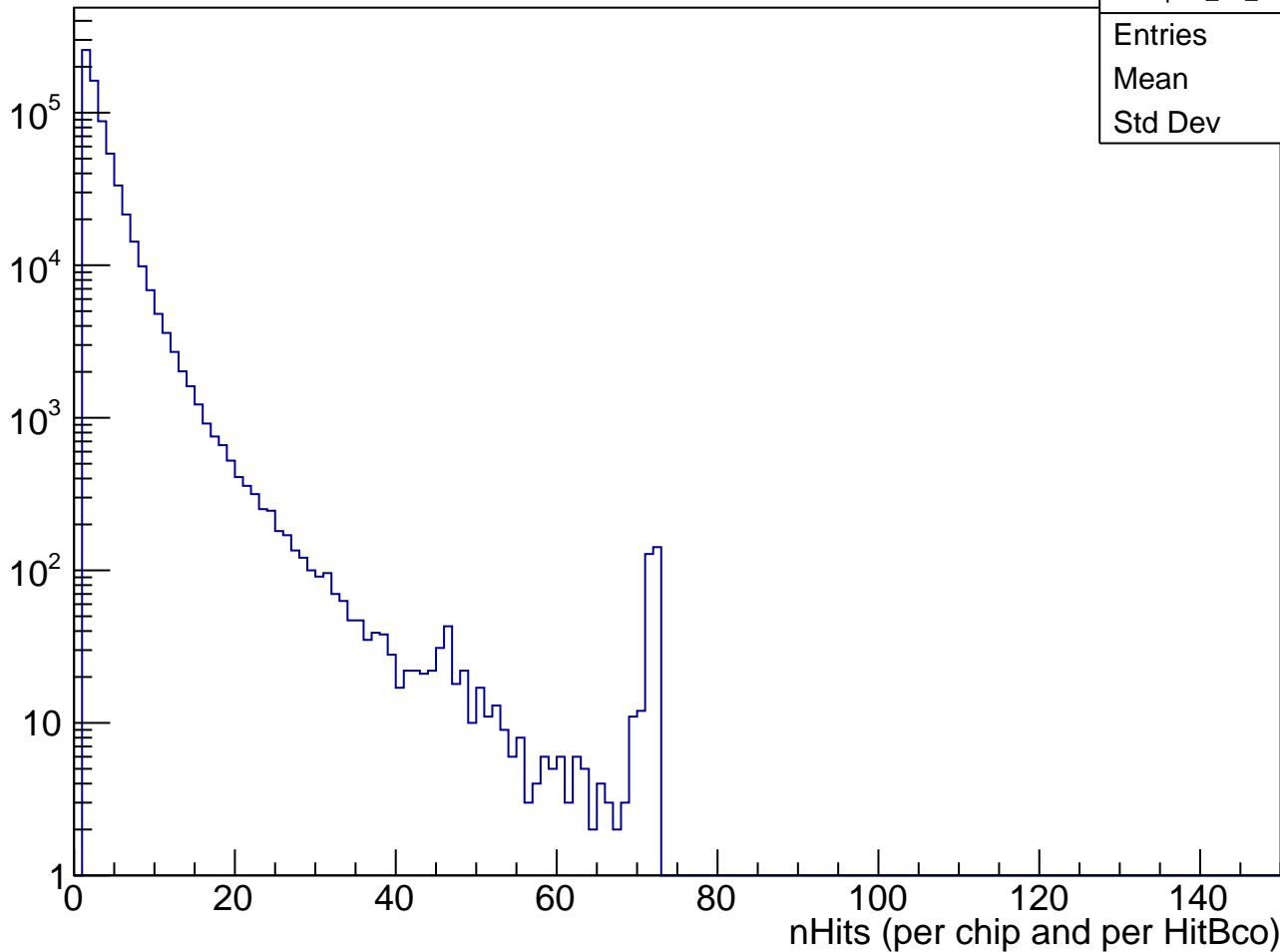


nChipHit_F4_Fch6_Chip5

Entries	660624
Mean	2.911
Std Dev	3.414

nChipHit_F4_Fch6_Chip6

Entries



nChipHit_F4_Fch6_Chip6

Entries 669593

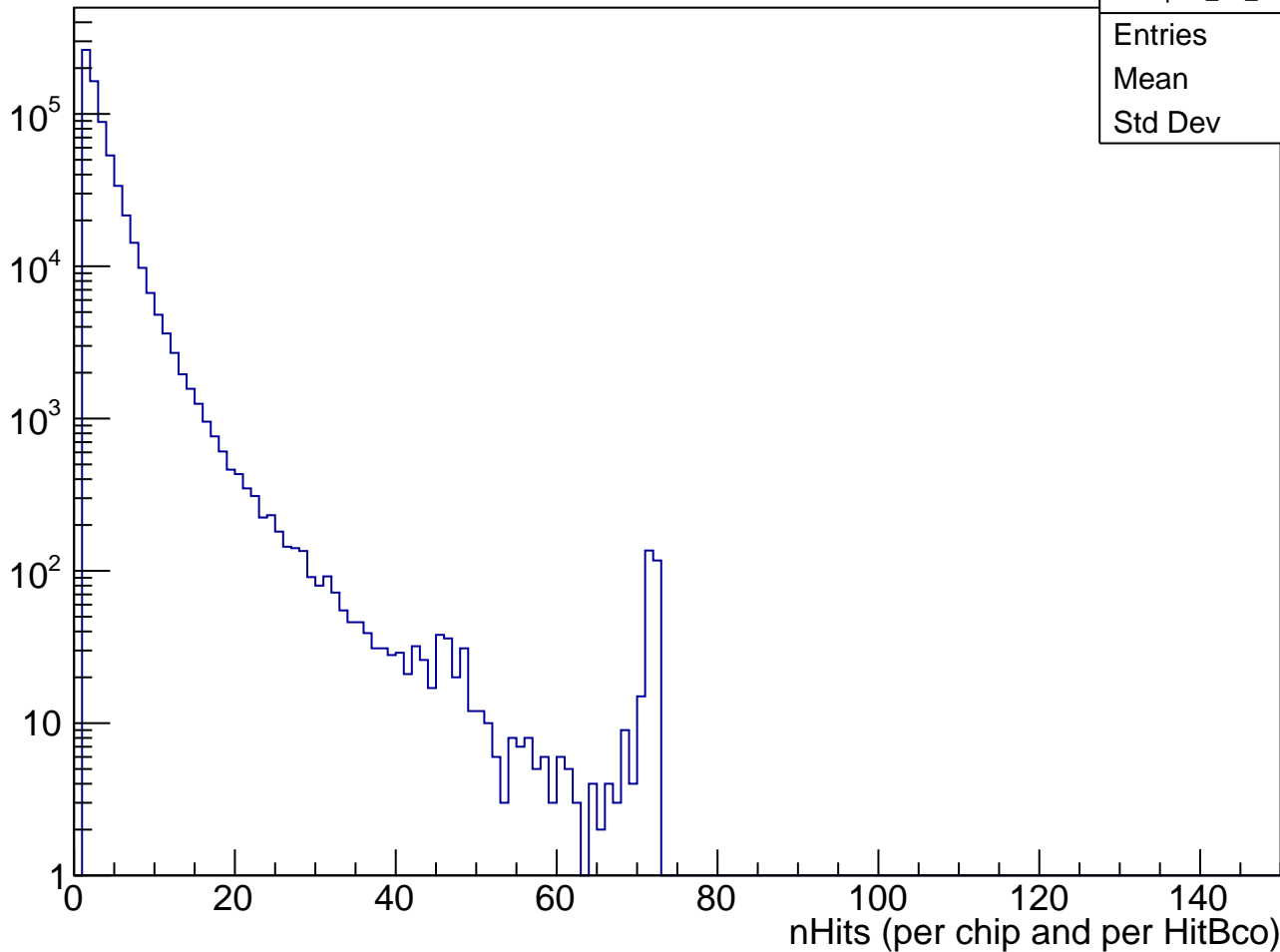
Mean 2.902

Std Dev 3.391

nHits (per chip and per HitBco)

nChipHit_F4_Fch6_Chip7

Entries



nChipHit_F4_Fch6_Chip7

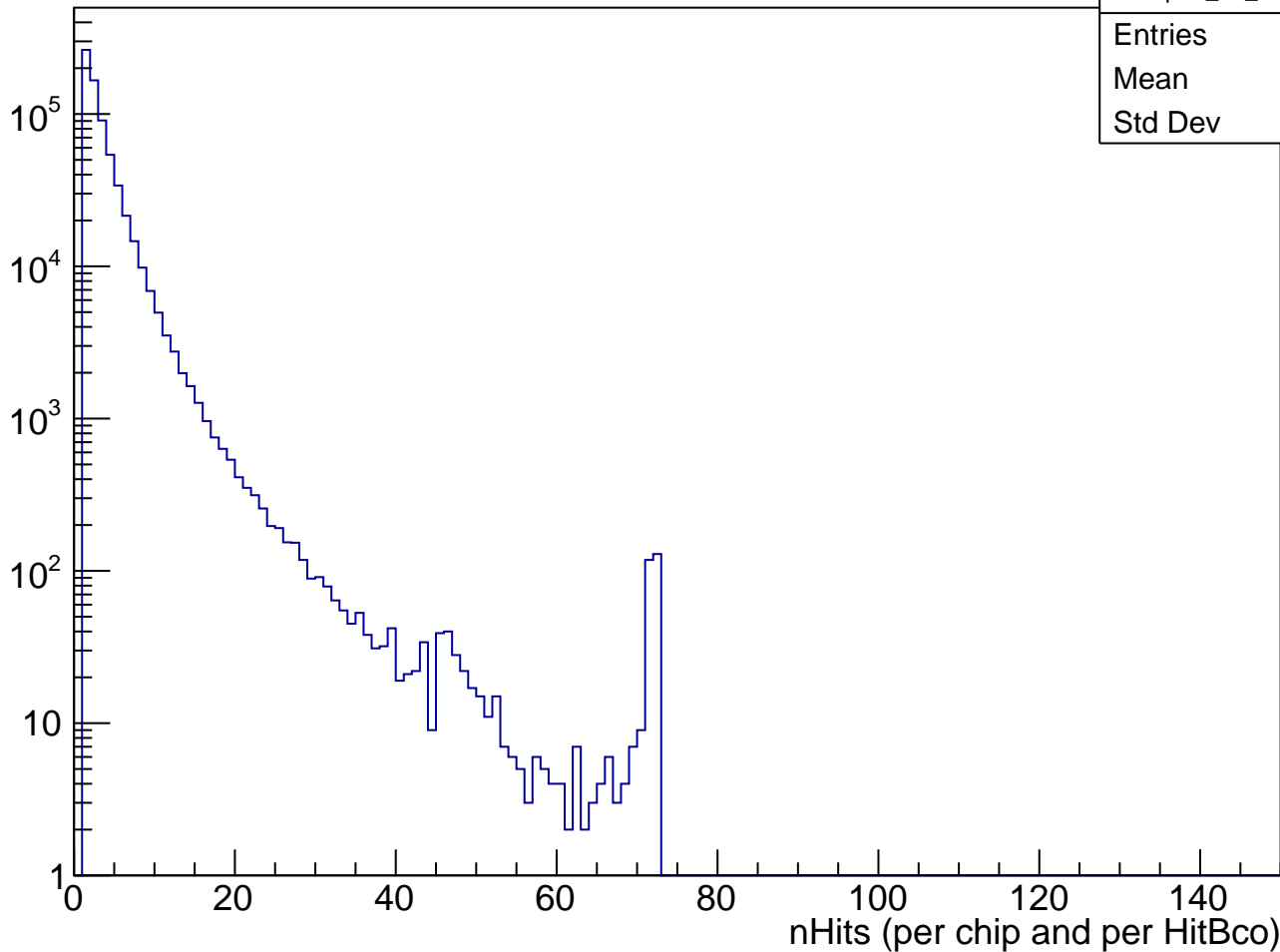
Entries 677564

Mean 2.875

Std Dev 3.342

nChipHit_F4_Fch6_Chip8

Entries



nChipHit_F4_Fch6_Chip8

Entries 683789

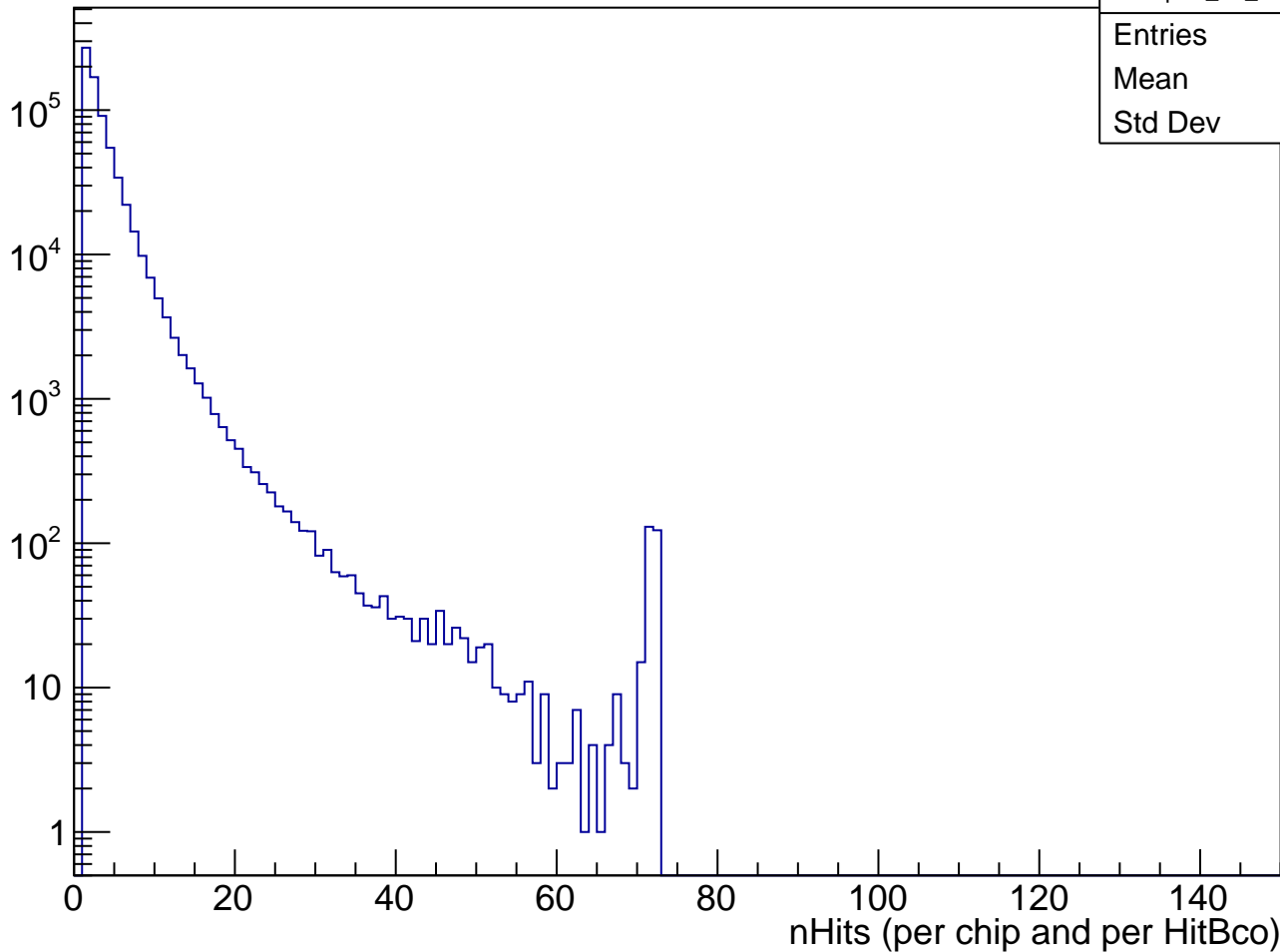
Mean 2.882

Std Dev 3.335

nHits (per chip and per HitBco)

nChipHit_F4_Fch6_Chip9

Entries



nChipHit_F4_Fch6_Chip9

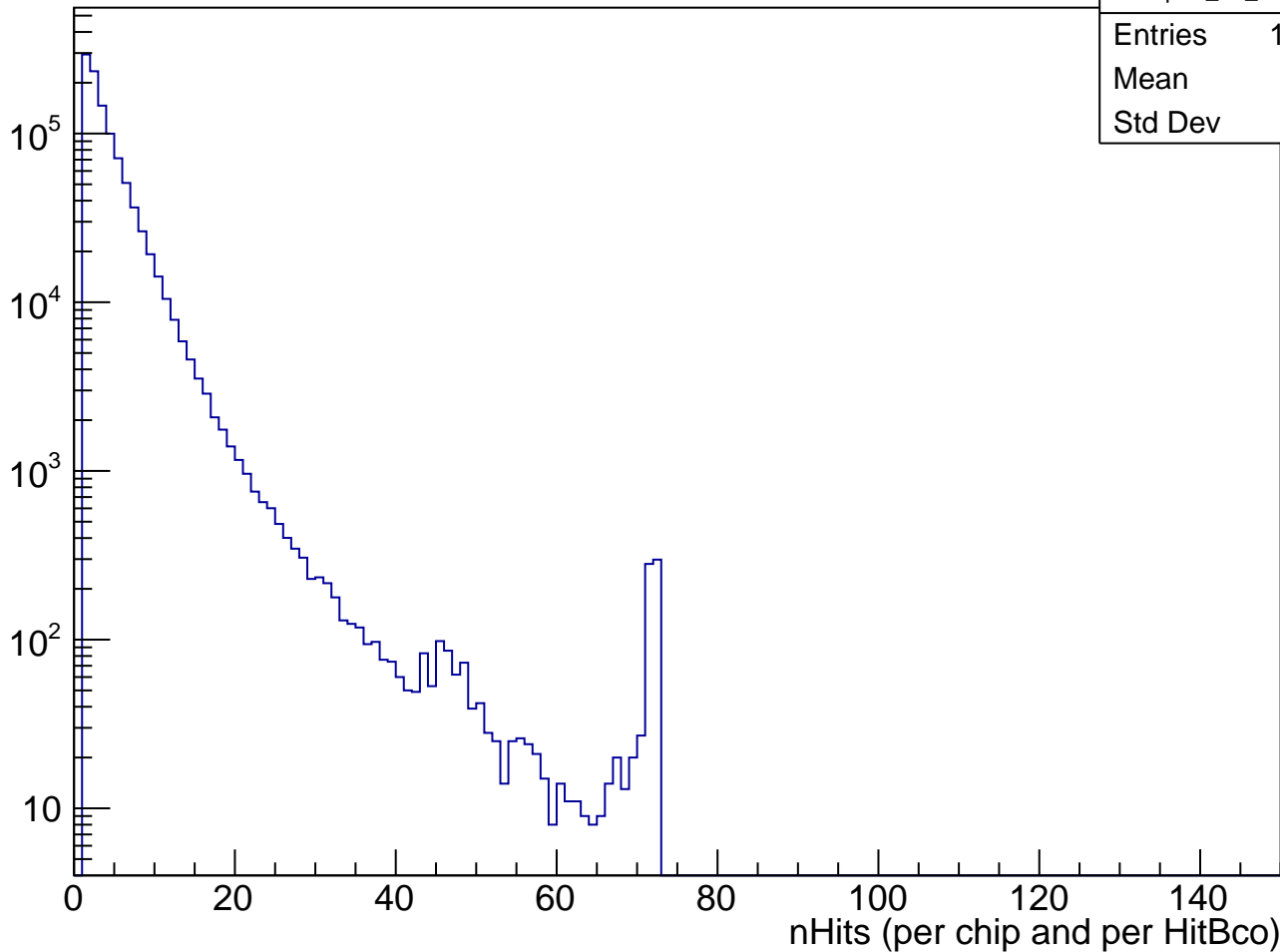
Entries 695444

Mean 2.87

Std Dev 3.345

nChipHit_F4_Fch7_Chip0

Entries



nChipHit_F4_Fch7_Chip0

Entries 1042220

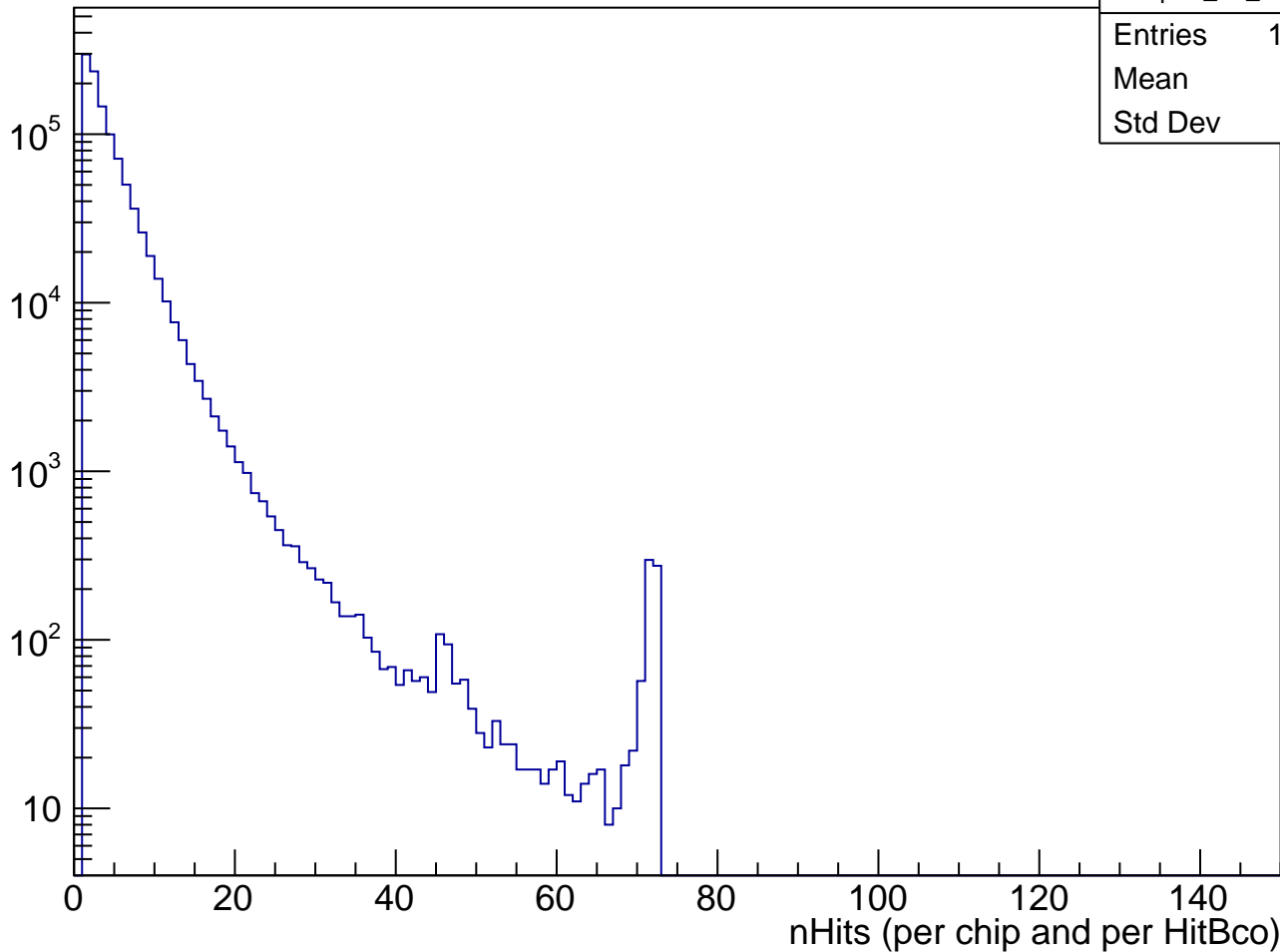
Mean 3.704

Std Dev 4.123

nHits (per chip and per HitBco)

nChipHit_F4_Fch7_Chip1

Entries

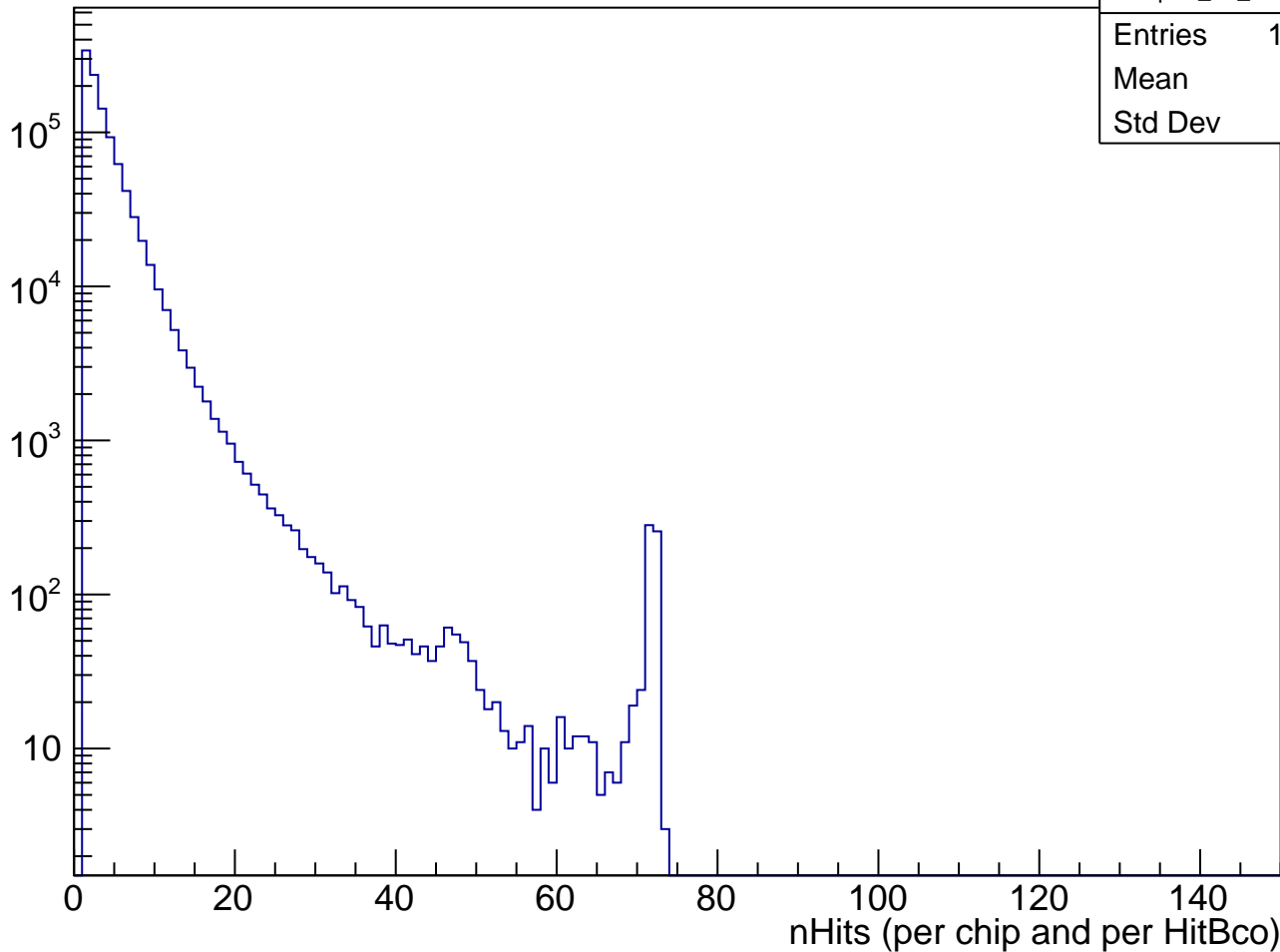


nChipHit_F4_Fch7_Chip1	
Entries	1044187
Mean	3.679
Std Dev	4.118

nHits (per chip and per HitBco)

nChipHit_F4_Fch7_Chip10

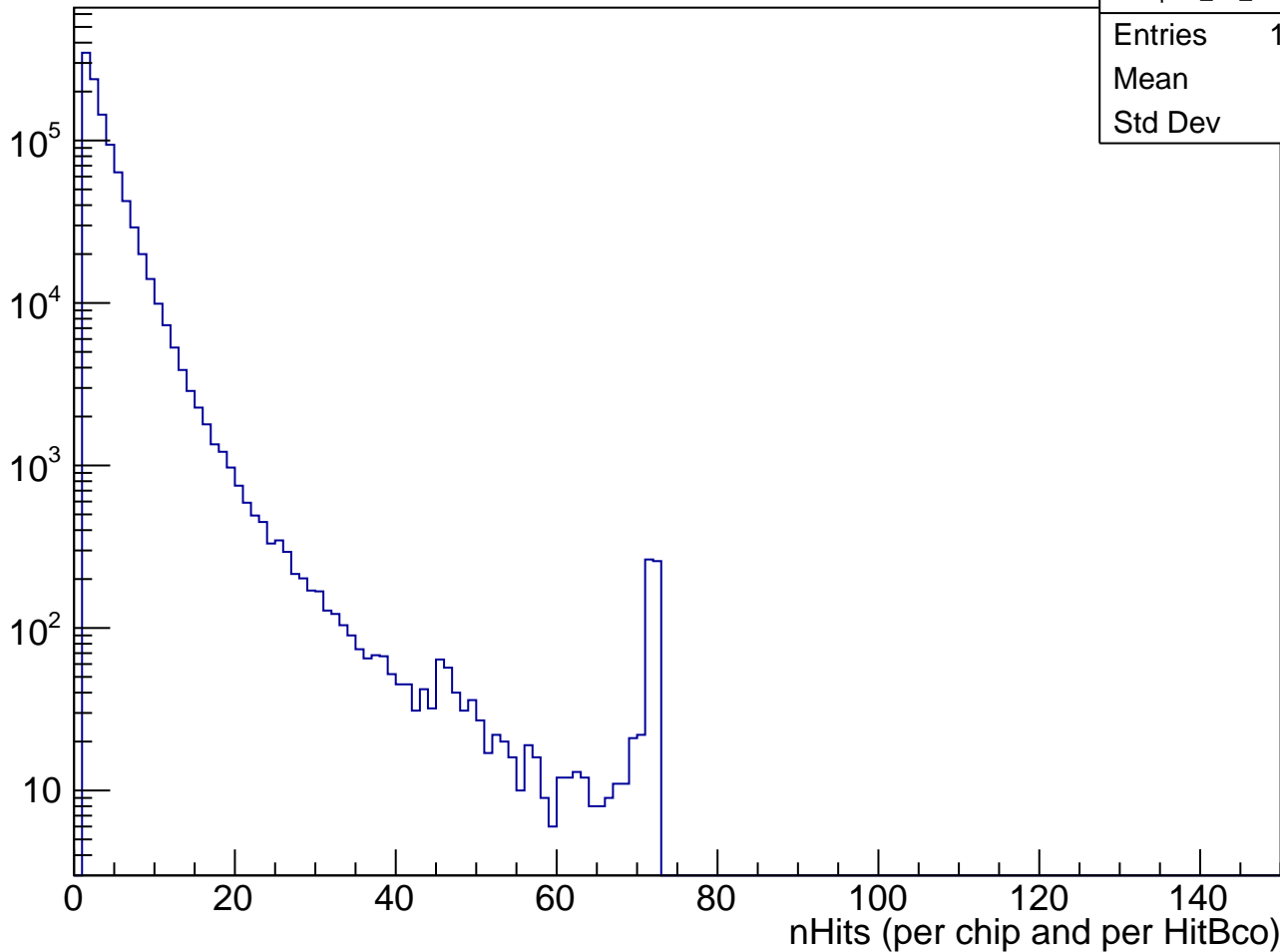
Entries



nChipHit_F4_Fch7_Chip10	
Entries	1020045
Mean	3.236
Std Dev	3.686

nChipHit_F4_Fch7_Chip11

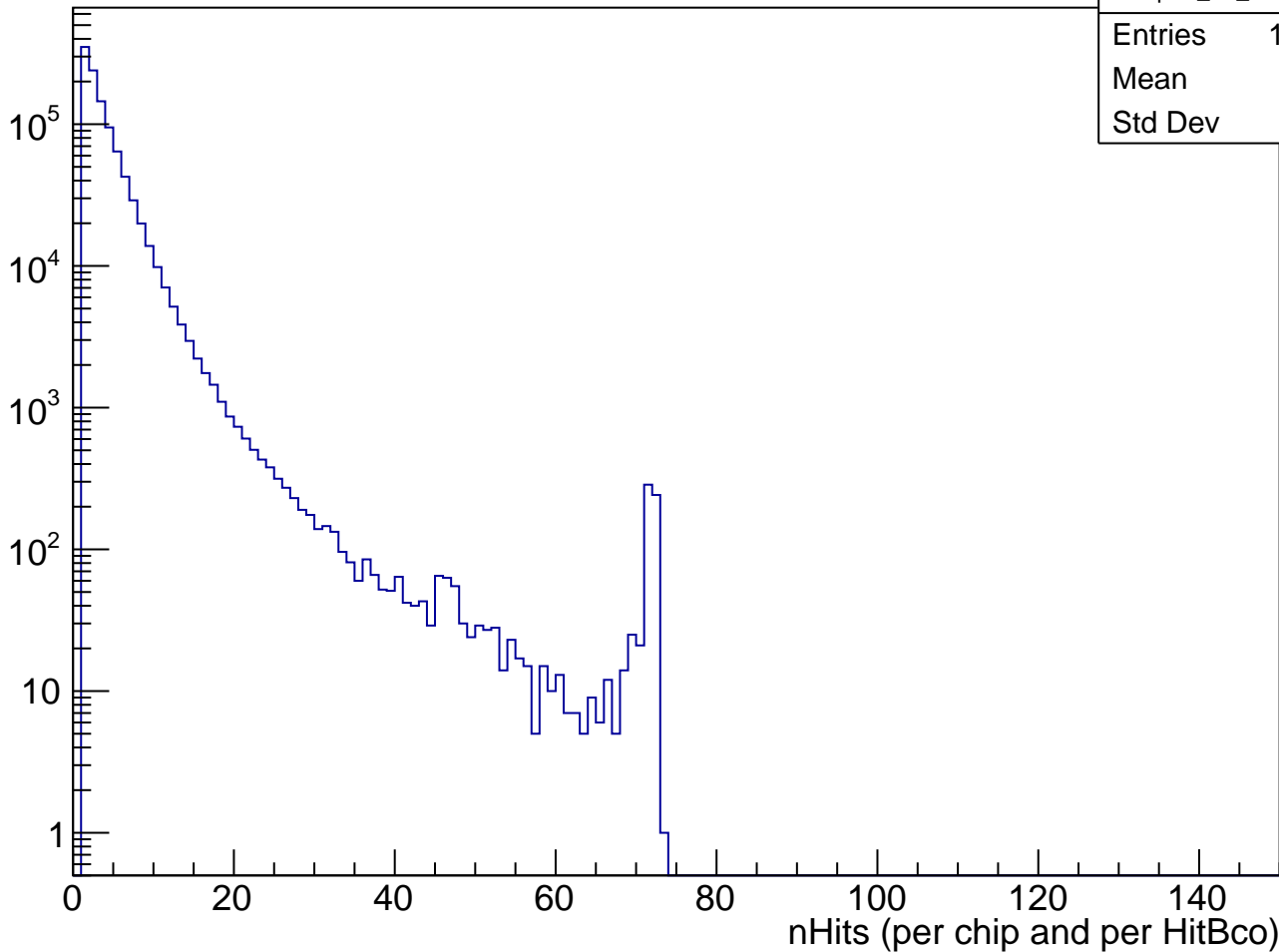
Entries



nChipHit_F4_Fch7_Chip11	
Entries	1035723
Mean	3.235
Std Dev	3.667

nChipHit_F4_Fch7_Chip12

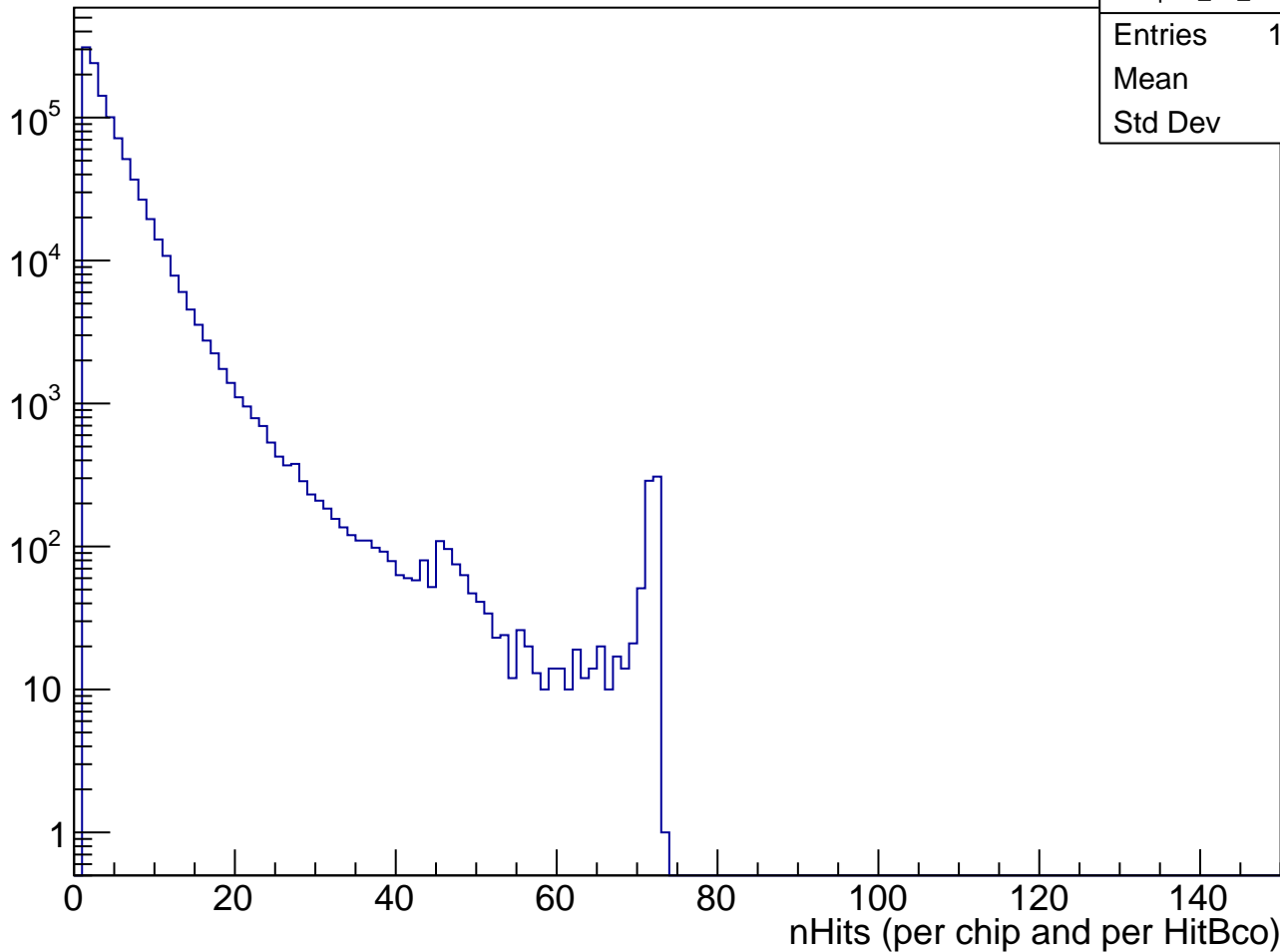
Entries



nChipHit_F4_Fch7_Chip12	
Entries	1042110
Mean	3.219
Std Dev	3.654

nChipHit_F4_Fch7_Chip13

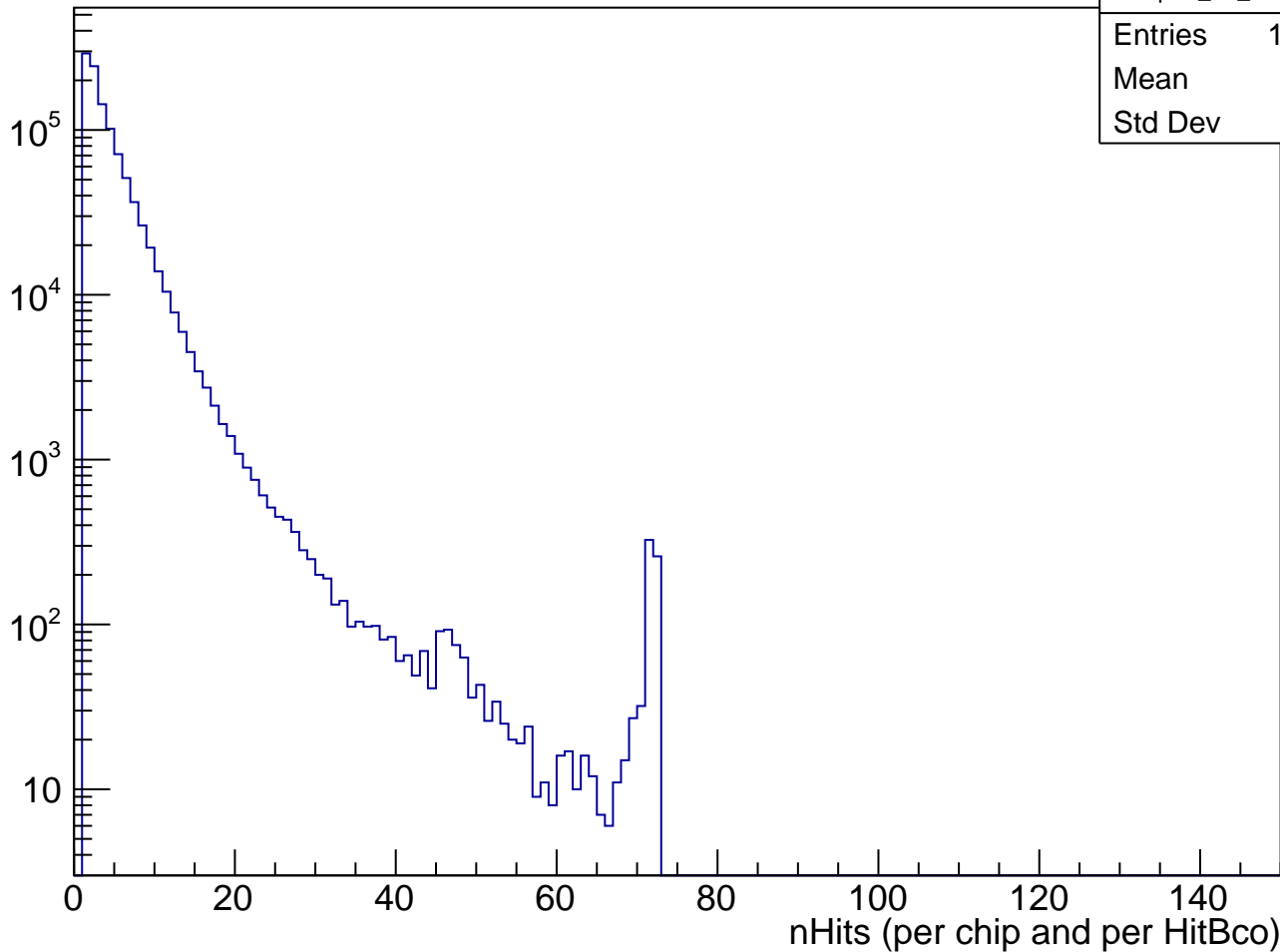
Entries



nChipHit_F4_Fch7_Chip13	
Entries	1062485
Mean	3.666
Std Dev	4.131

nChipHit_F4_Fch7_Chip14

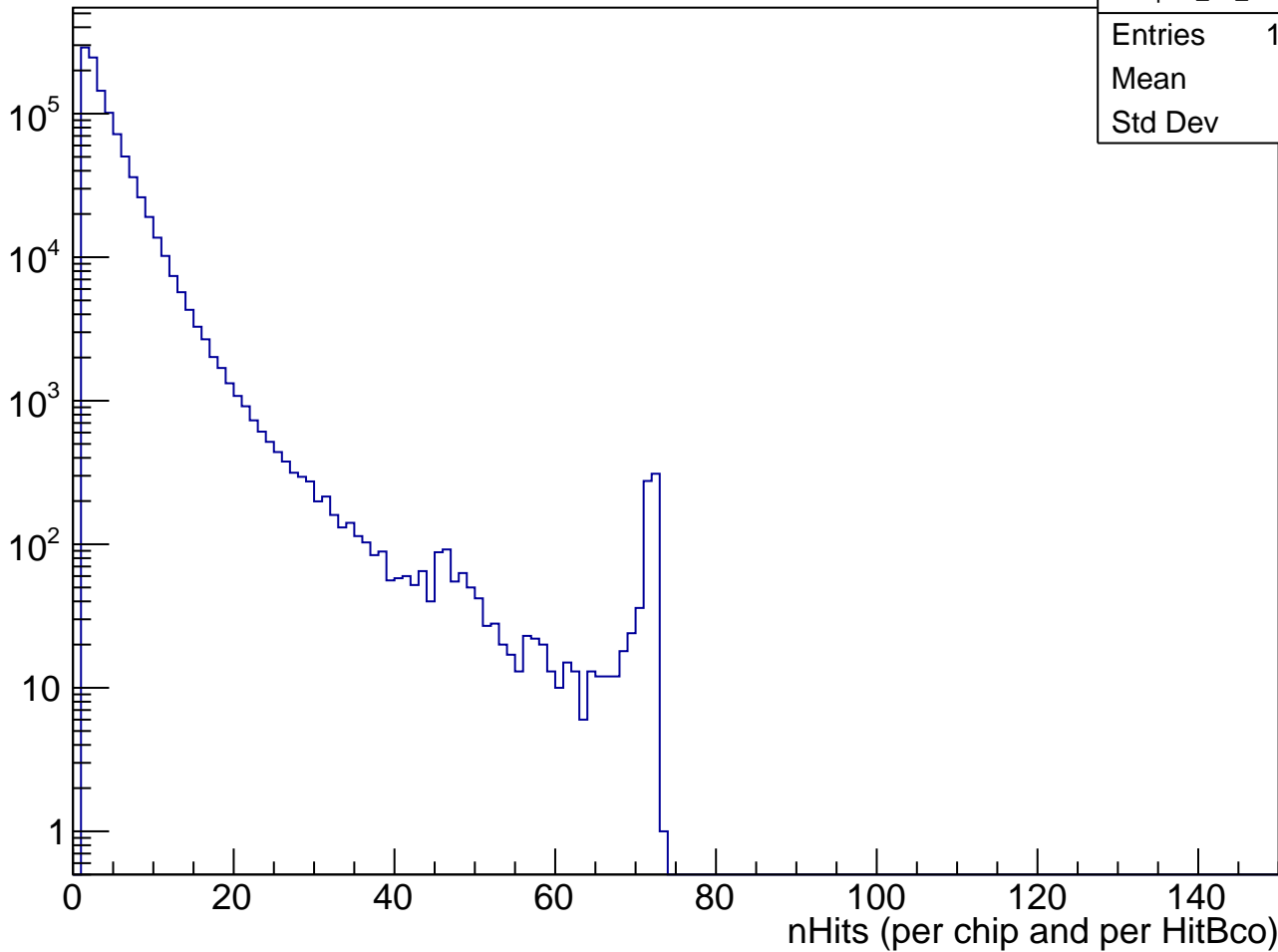
Entries



nChipHit_F4_Fch7_Chip14	
Entries	1047408
Mean	3.685
Std Dev	4.087

nChipHit_F4_Fch7_Chip15

Entries

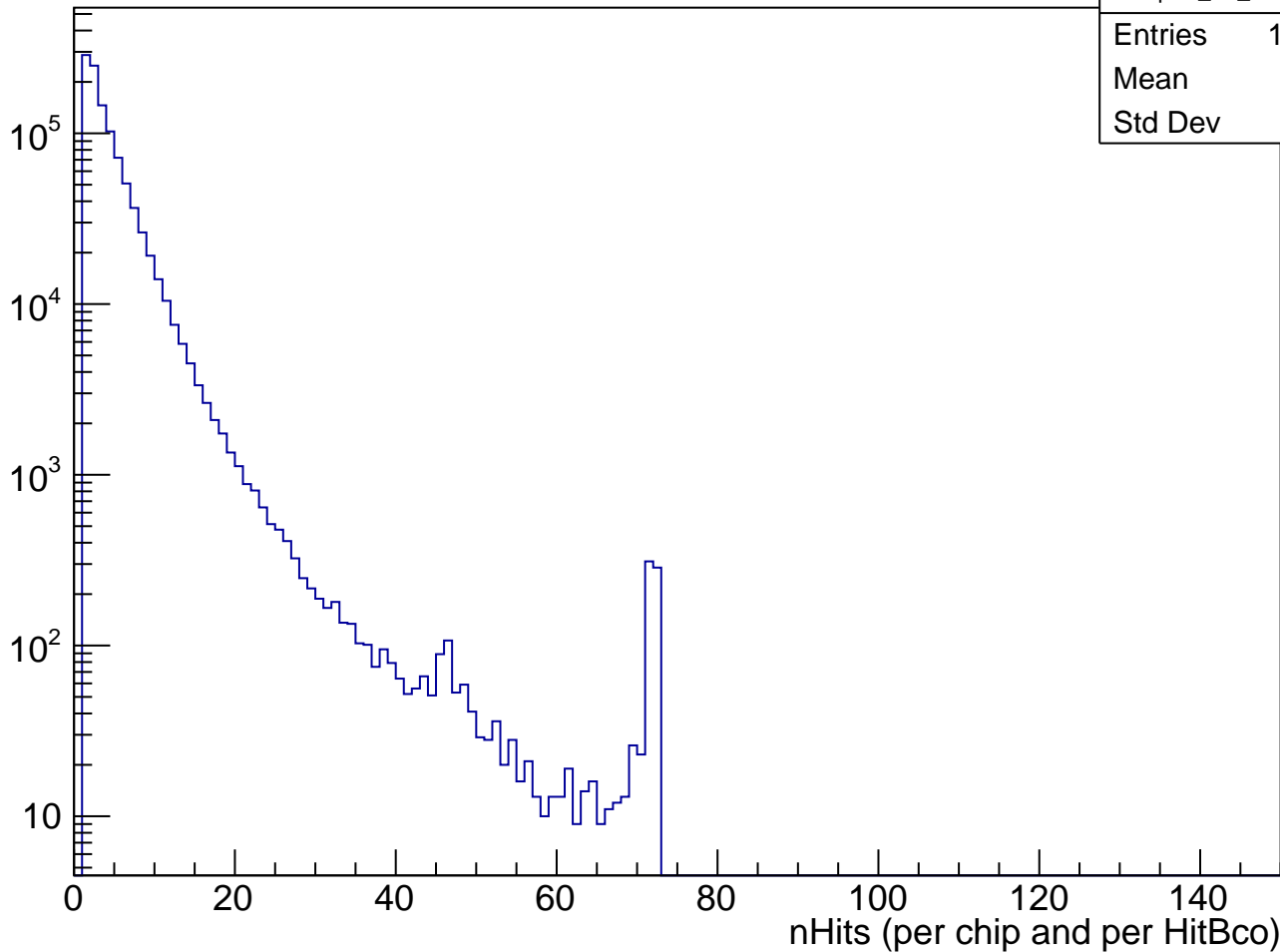


nChipHit_F4_Fch7_Chip15	
Entries	1045599
Mean	3.668
Std Dev	4.076

nHits (per chip and per HitBco)

nChipHit_F4_Fch7_Chip16

Entries

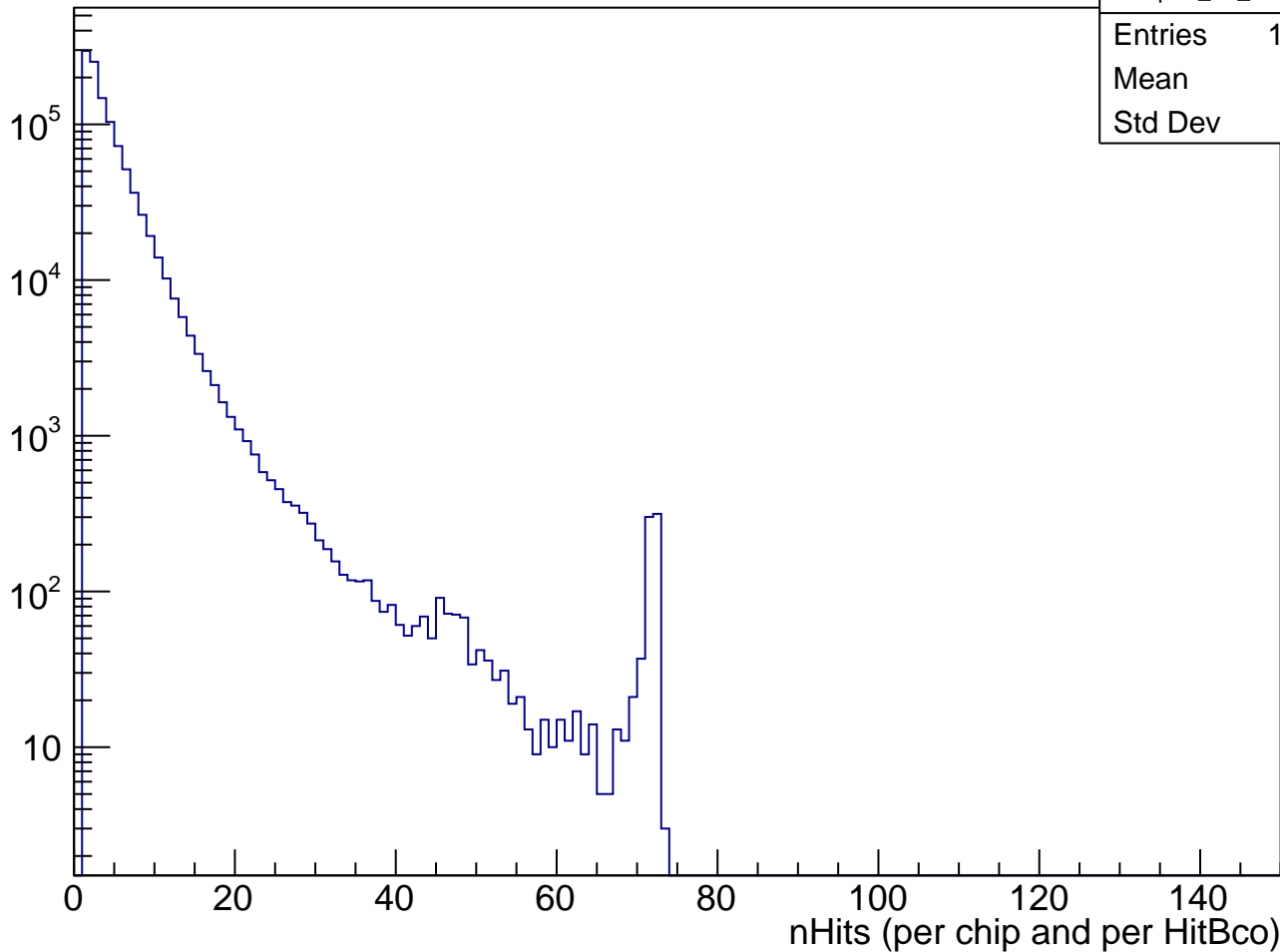


nChipHit_F4_Fch7_Chip16	
Entries	1051718
Mean	3.682
Std Dev	4.076

nHits (per chip and per HitBco)

nChipHit_F4_Fch7_Chip17

Entries

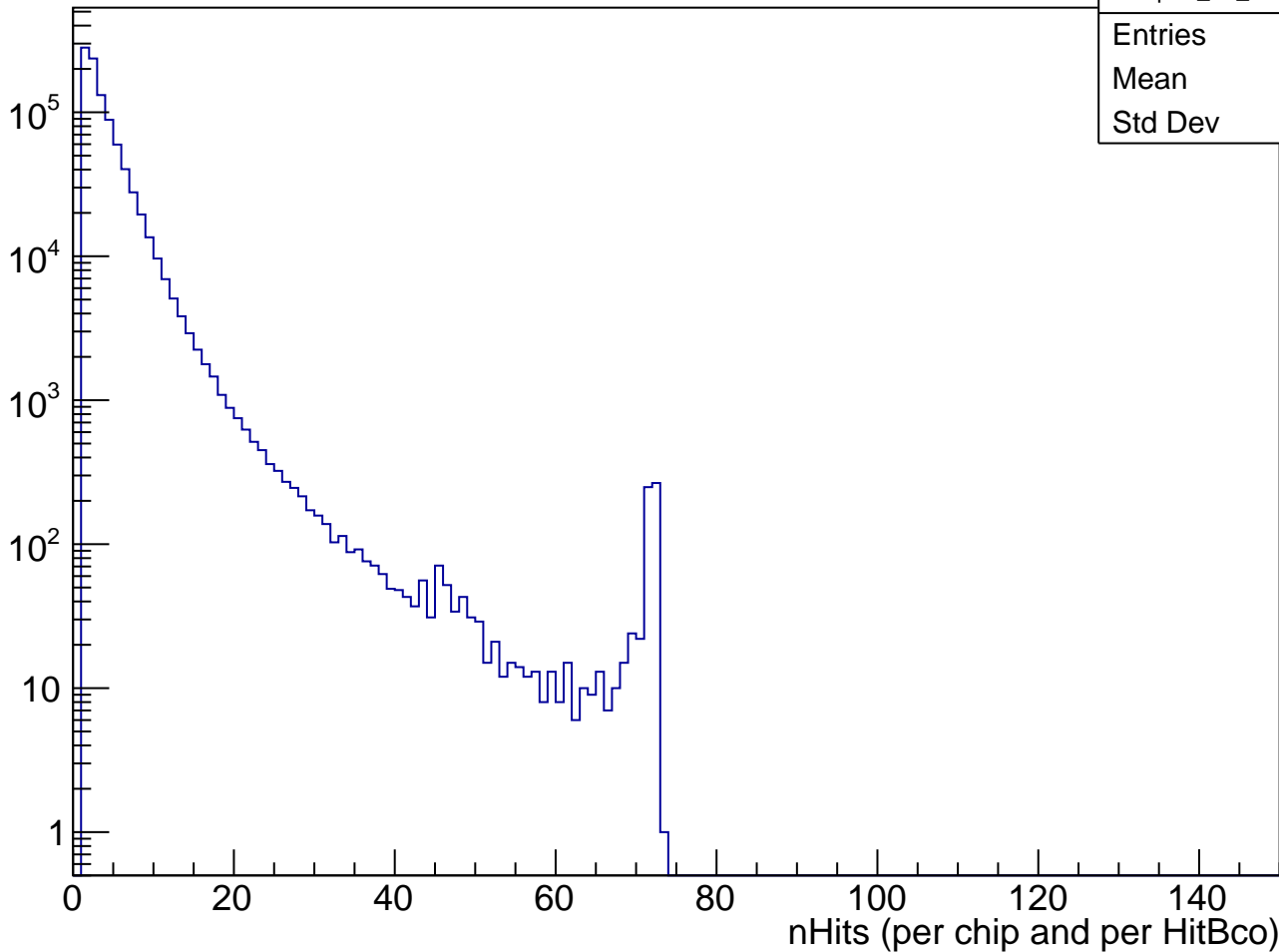


nChipHit_F4_Fch7_Chip17	
Entries	1067506
Mean	3.652
Std Dev	4.064

nHits (per chip and per HitBco)

nChipHit_F4_Fch7_Chip18

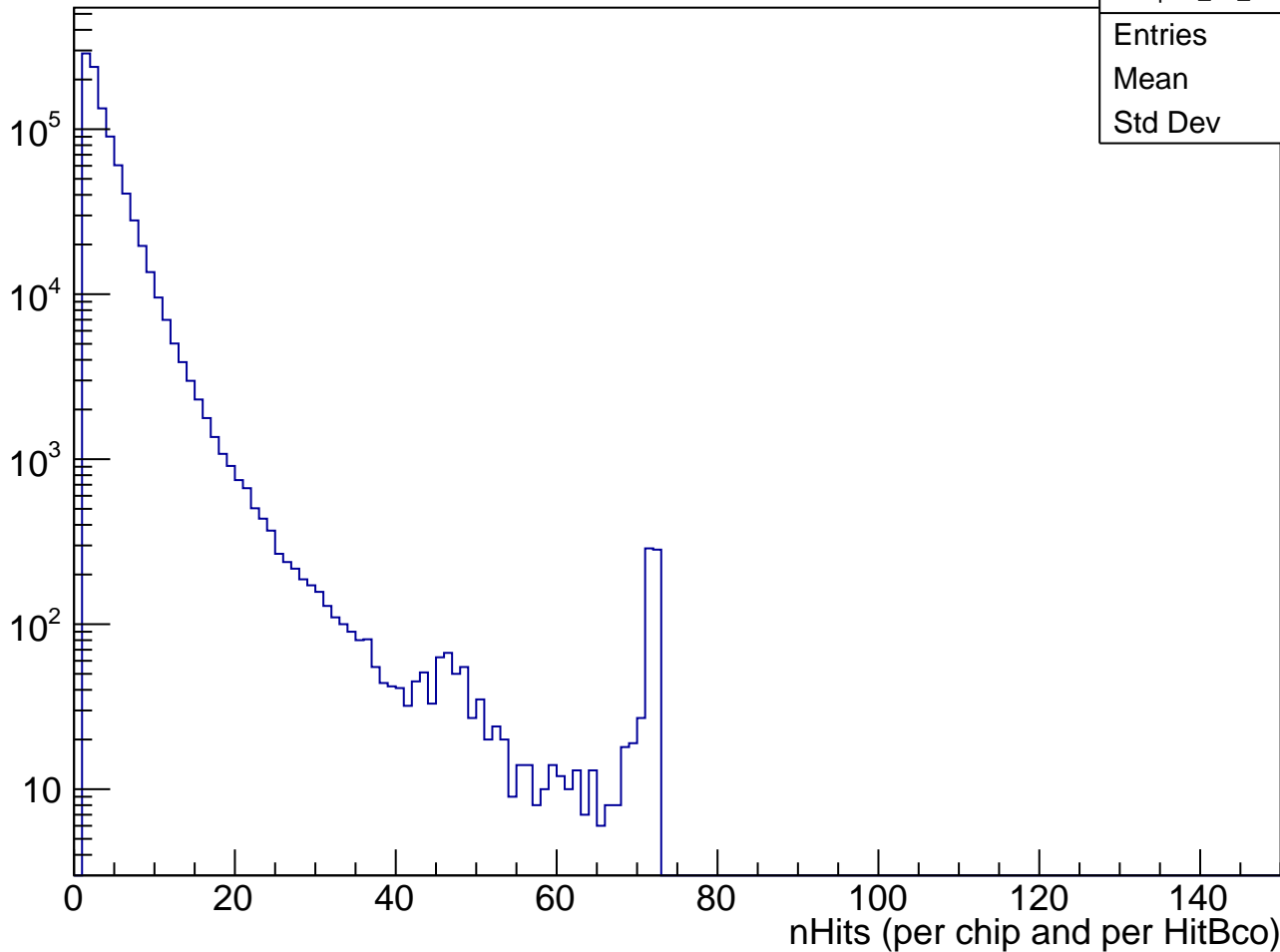
Entries



nChipHit_F4_Fch7_Chip18	
Entries	940766
Mean	3.36
Std Dev	3.782

nChipHit_F4_Fch7_Chip19

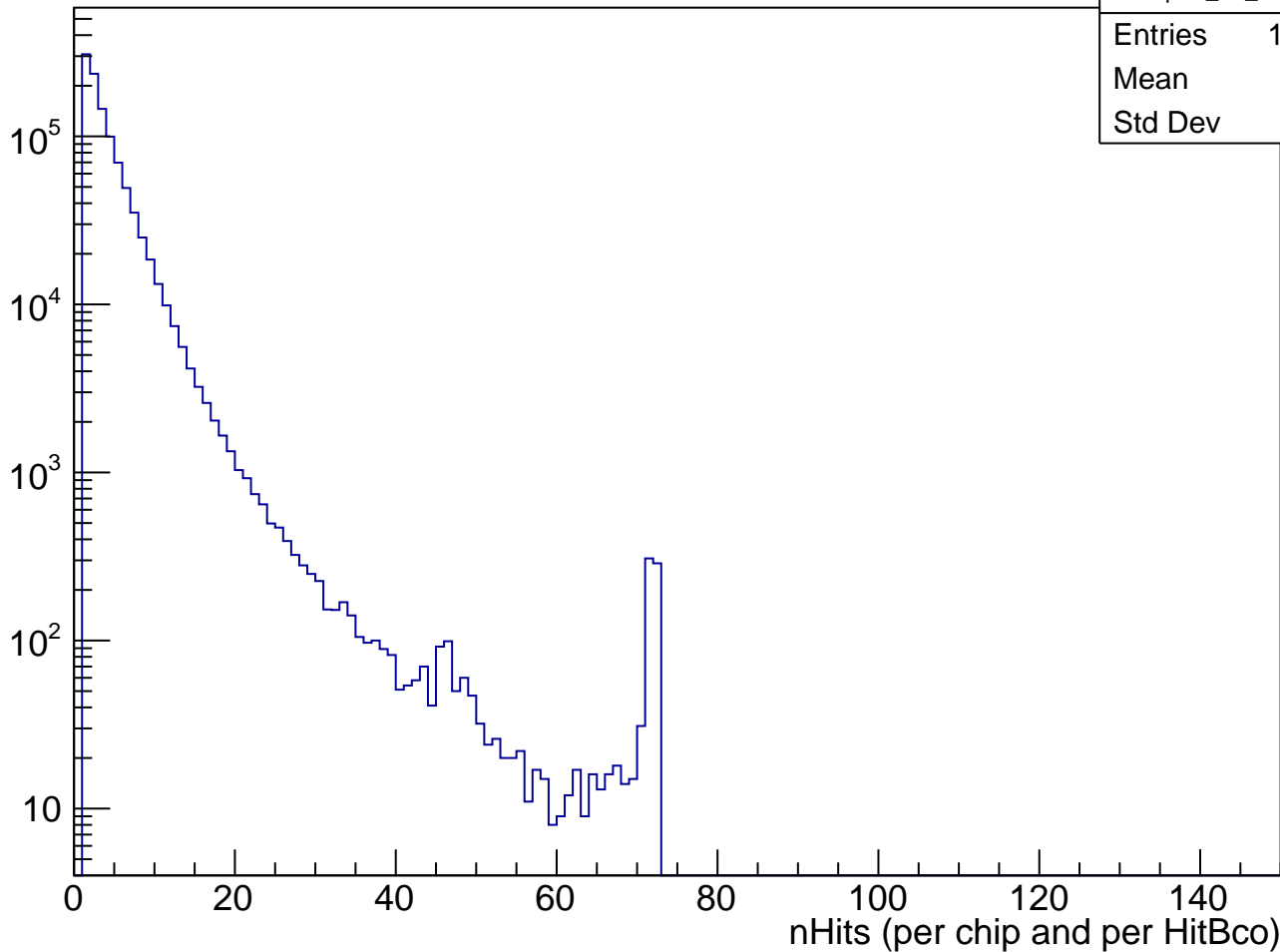
Entries



nChipHit_F4_Fch7_Chip19	
Entries	954467
Mean	3.346
Std Dev	3.79

nChipHit_F4_Fch7_Chip2

Entries

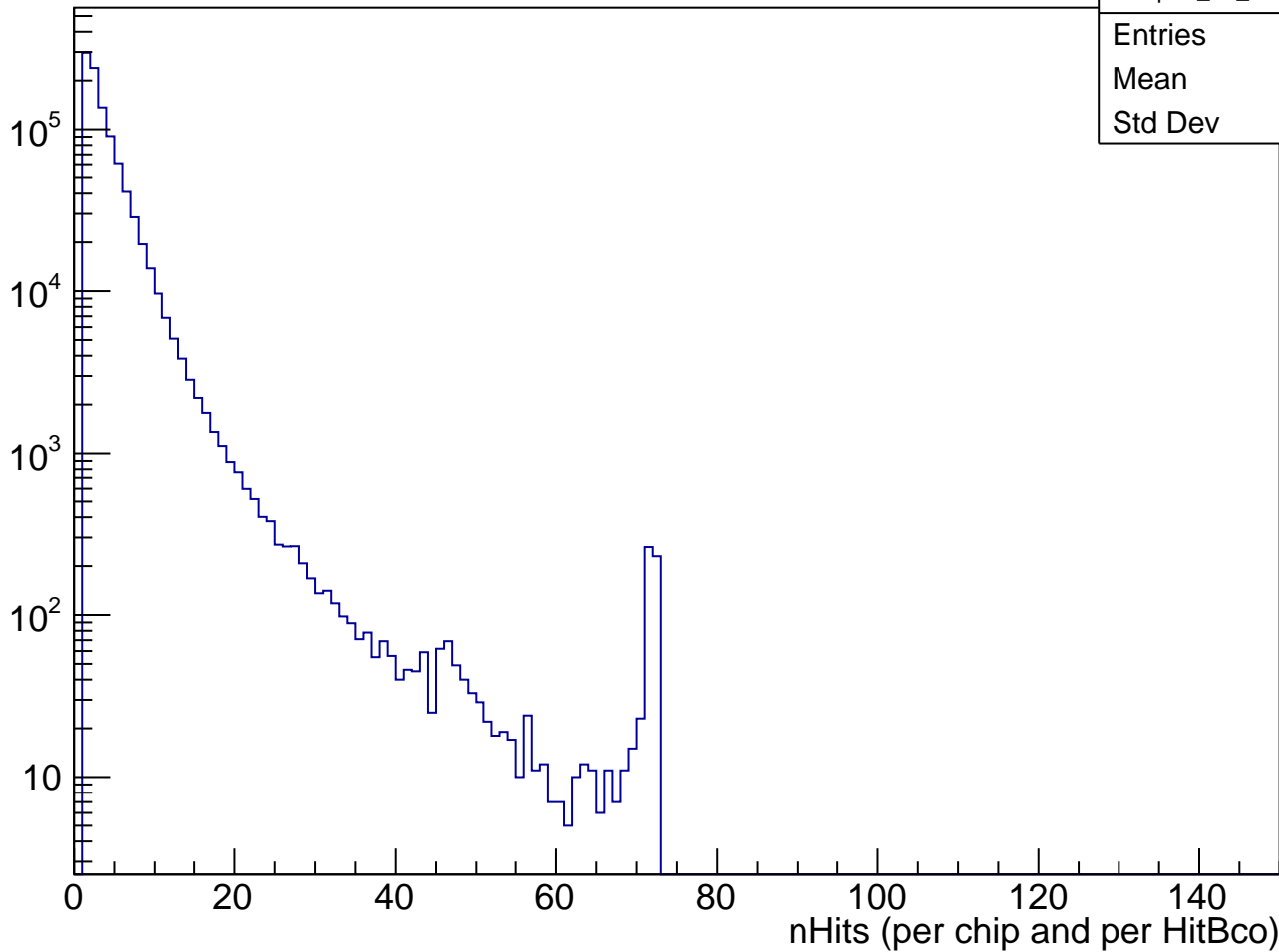


nChipHit_F4_Fch7_Chip2	
Entries	1046035
Mean	3.611
Std Dev	4.068

nHits (per chip and per HitBco)

nChipHit_F4_Fch7_Chip20

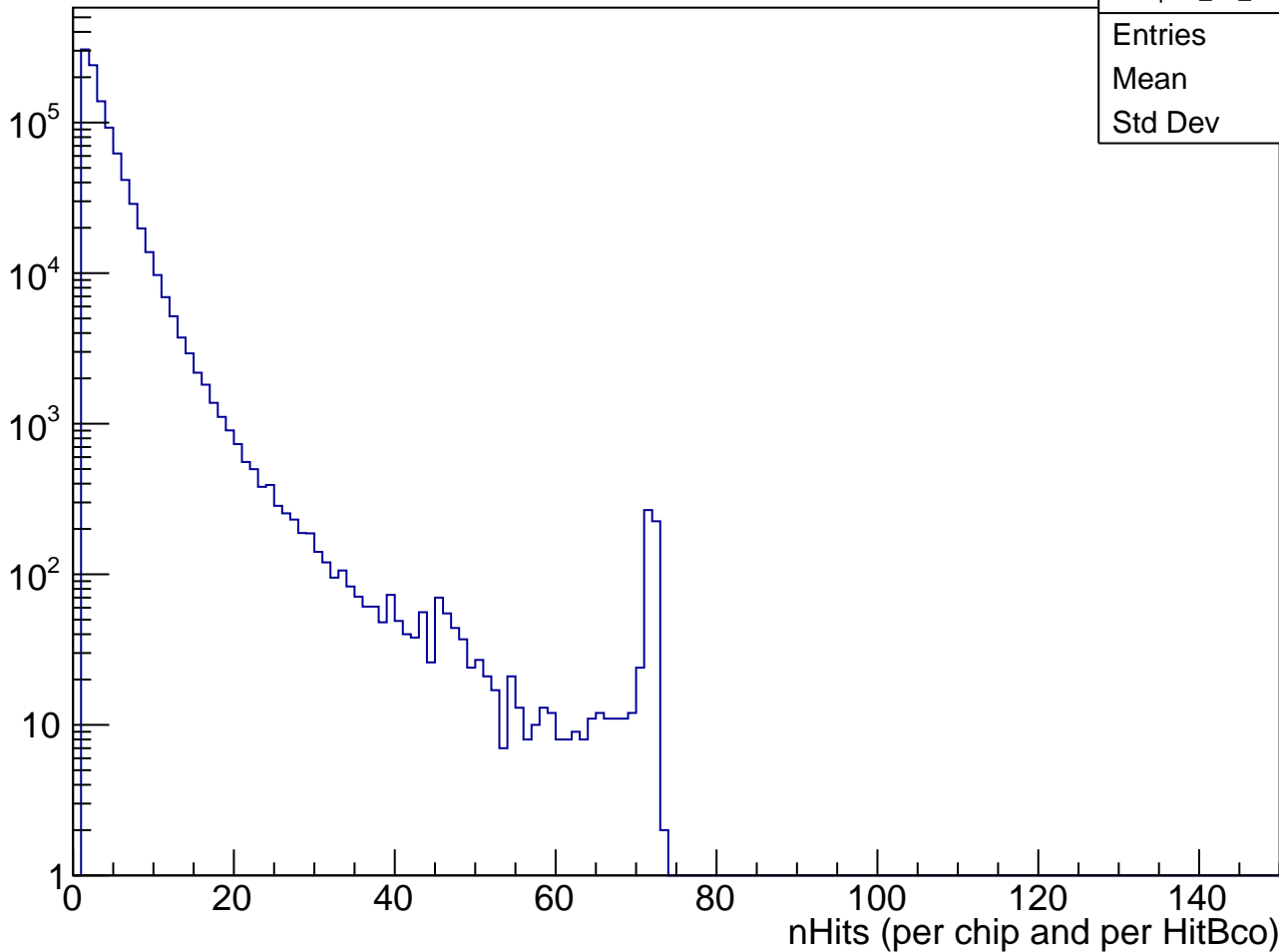
Entries



nChipHit_F4_Fch7_Chip20	
Entries	968588
Mean	3.319
Std Dev	3.714

nChipHit_F4_Fch7_Chip21

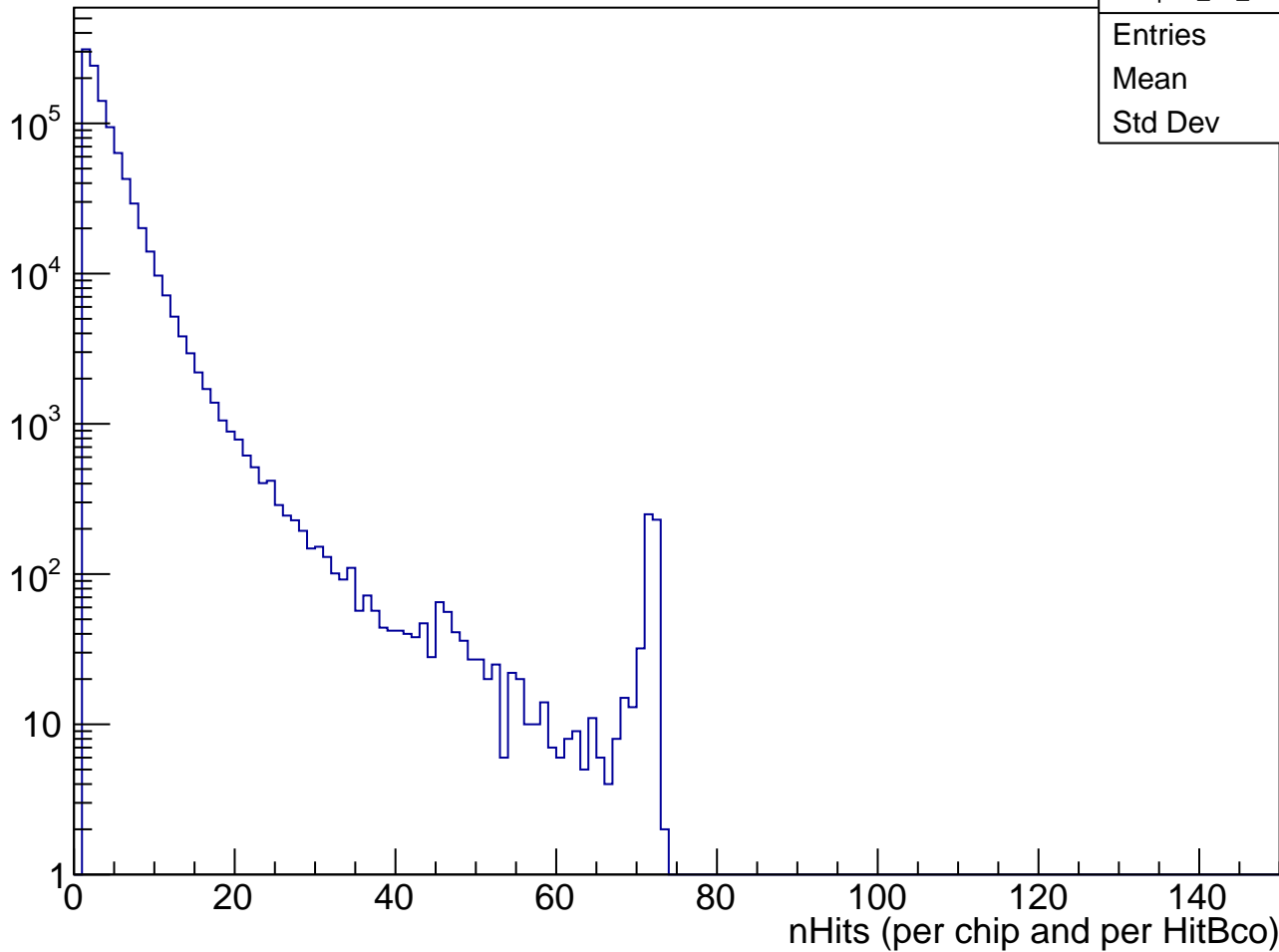
Entries



nChipHit_F4_Fch7_Chip21	
Entries	985199
Mean	3.3
Std Dev	3.673

nChipHit_F4_Fch7_Chip22

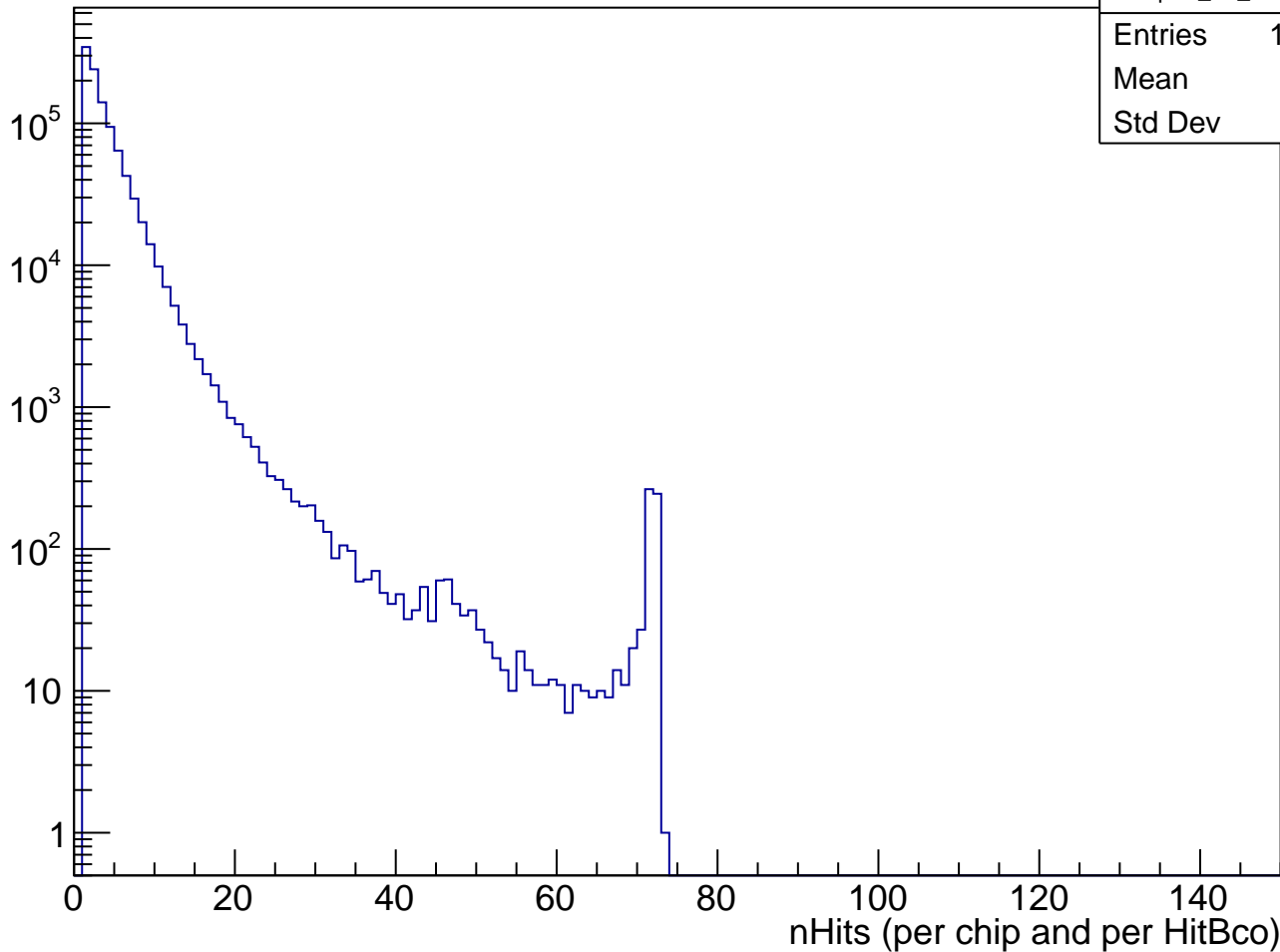
Entries



nChipHit_F4_Fch7_Chip22	
Entries	999968
Mean	3.296
Std Dev	3.645

nChipHit_F4_Fch7_Chip23

Entries

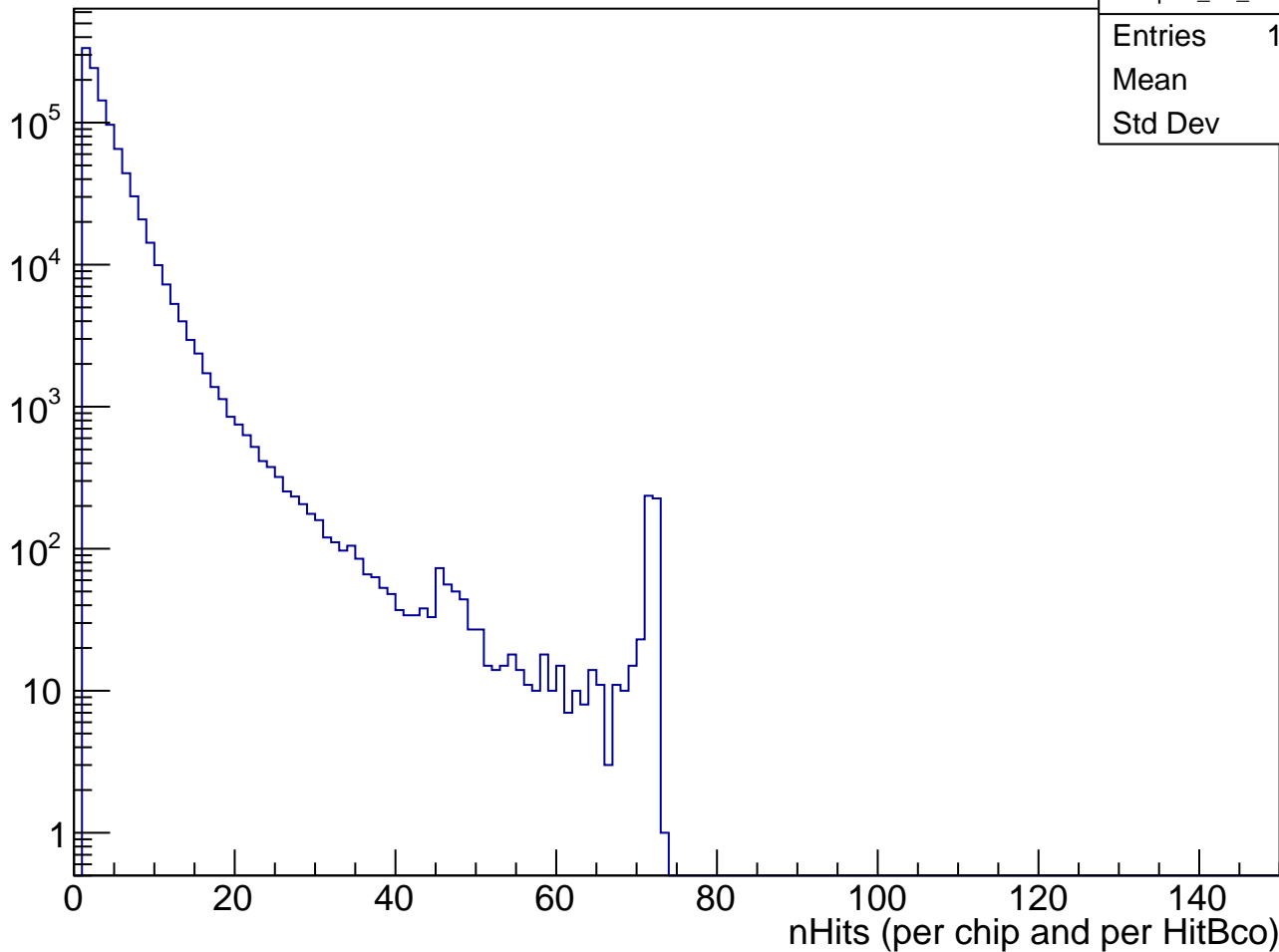


nChipHit_F4_Fch7_Chip23	
Entries	1032740
Mean	3.227
Std Dev	3.64

nHits (per chip and per HitBco)

nChipHit_F4_Fch7_Chip24

Entries

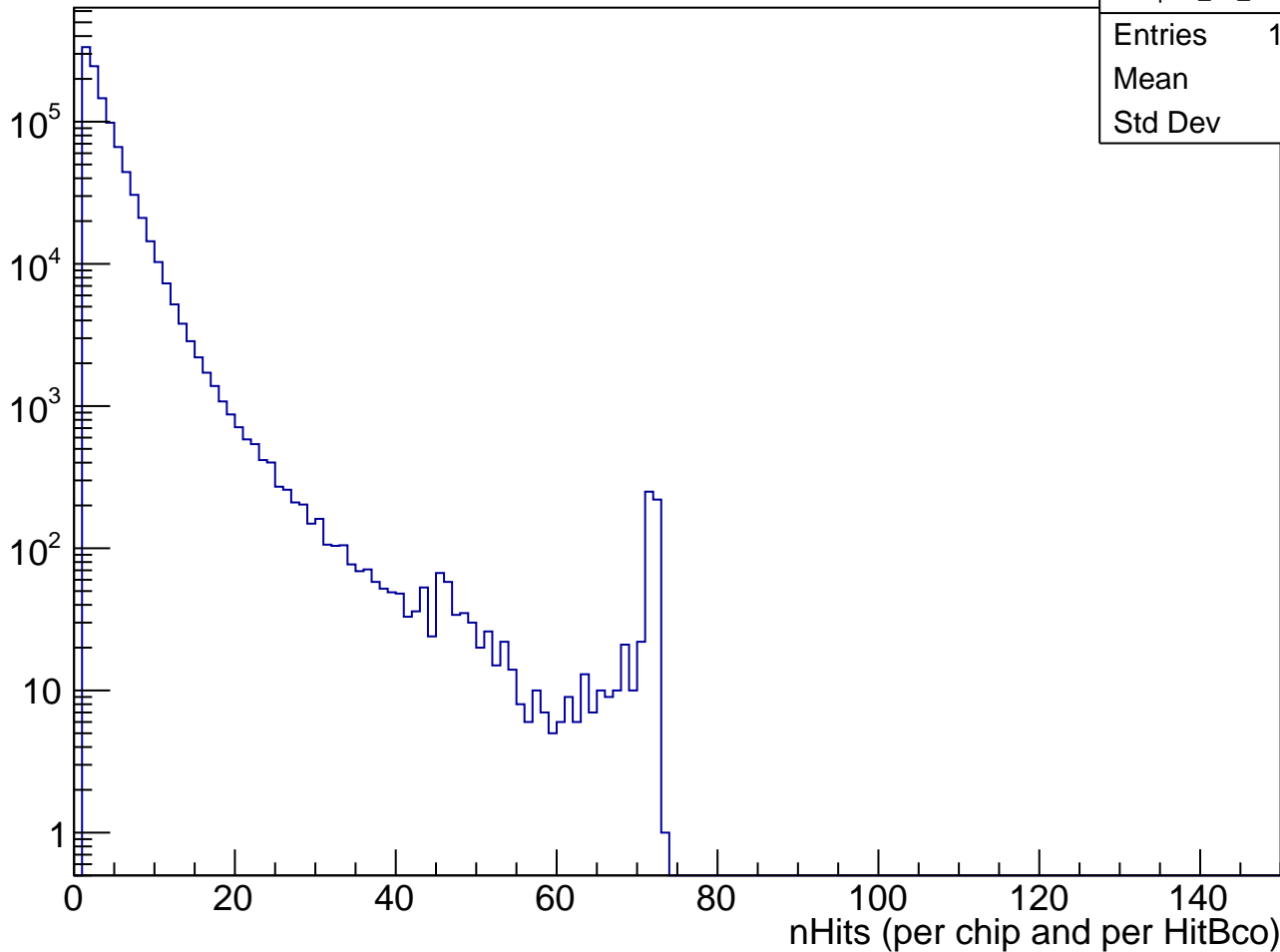


nChipHit_F4_Fch7_Chip24	
Entries	1035102
Mean	3.267
Std Dev	3.62

nHits (per chip and per HitBco)

nChipHit_F4_Fch7_Chip25

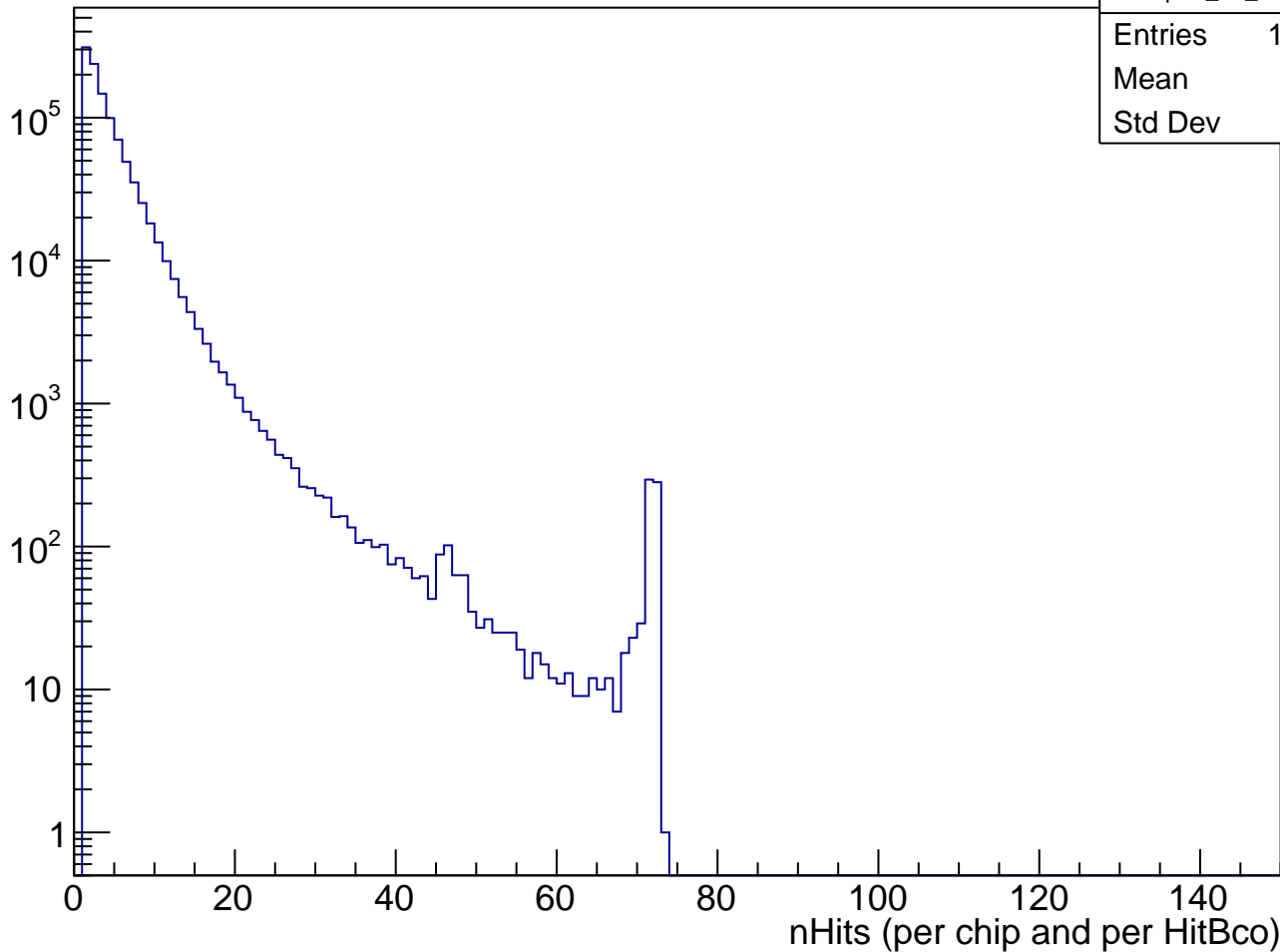
Entries



nChipHit_F4_Fch7_Chip25	
Entries	1043992
Mean	3.262
Std Dev	3.579

nChipHit_F4_Fch7_Chip3

Entries



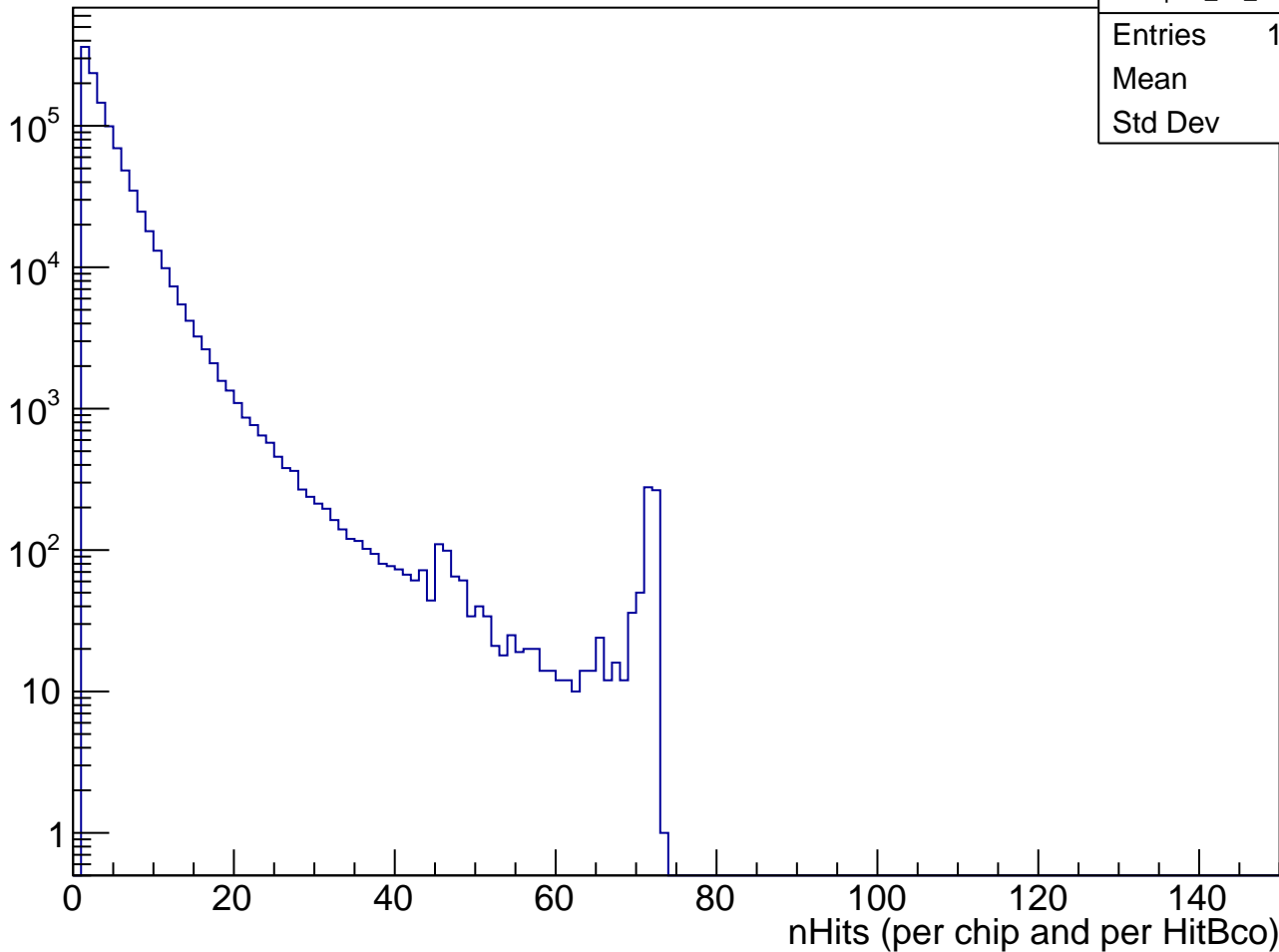
nChipHit_F4_Fch7_Chip3

Entries	1053352
Mean	3.609
Std Dev	4.069

nHits (per chip and per HitBco)

nChipHit_F4_Fch7_Chip4

Entries



nChipHit_F4_Fch7_Chip4

Entries 1097415

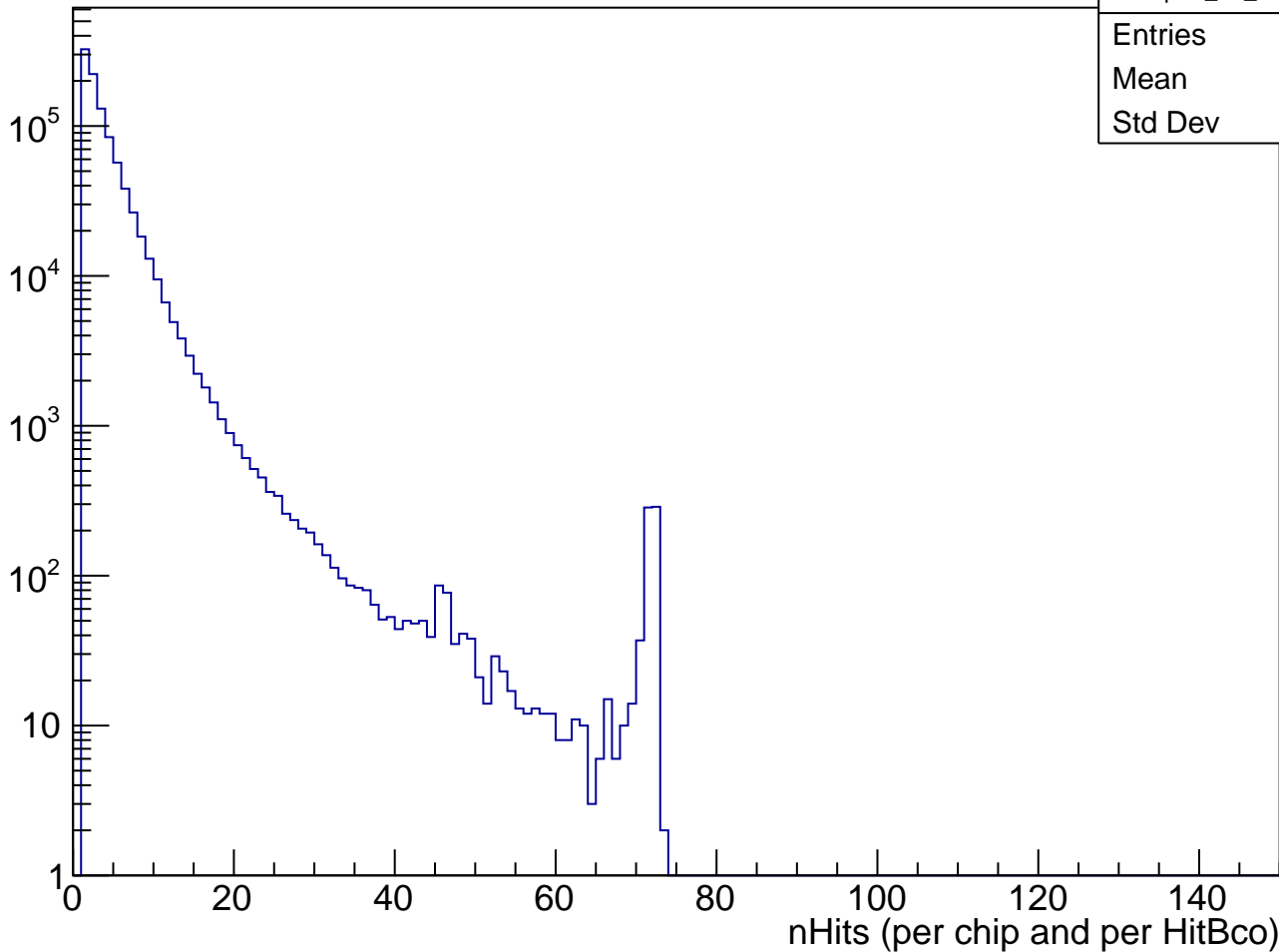
Mean 3.477

Std Dev 4.02

nHits (per chip and per HitBco)

nChipHit_F4_Fch7_Chip5

Entries

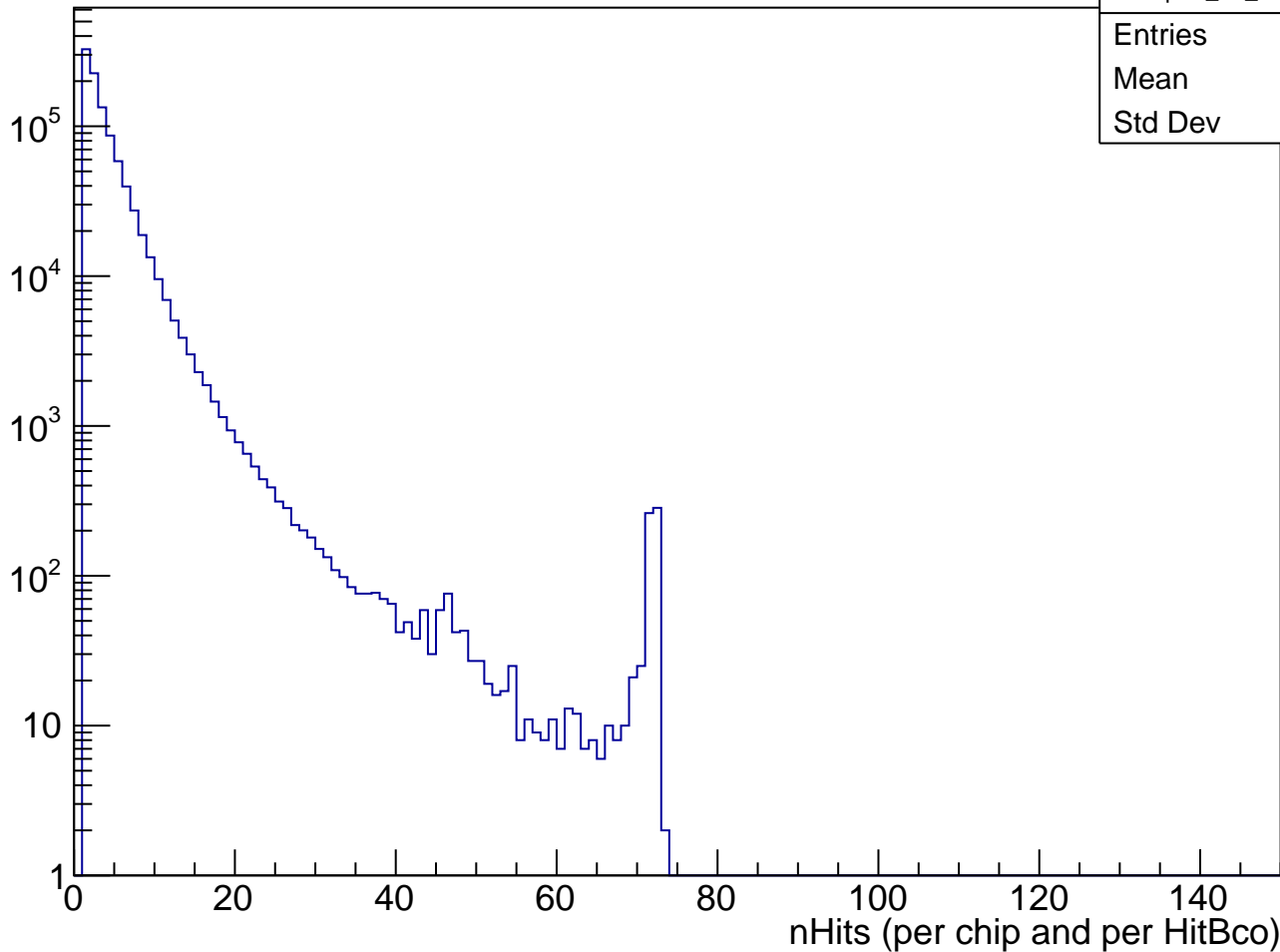


nChipHit_F4_Fch7_Chip5	
Entries	956865
Mean	3.246
Std Dev	3.812

nHits (per chip and per HitBco)

nChipHit_F4_Fch7_Chip6

Entries

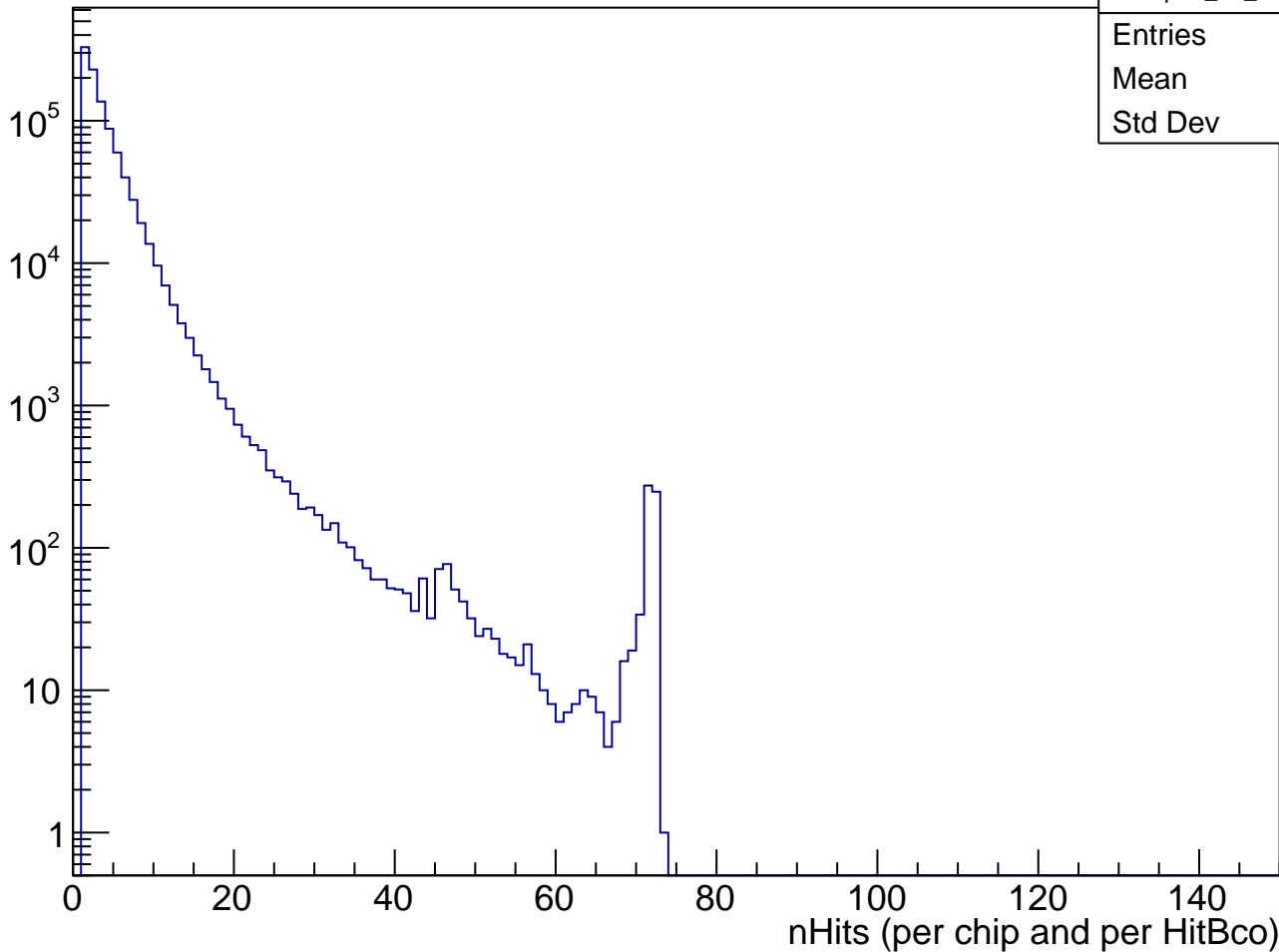


nChipHit_F4_Fch7_Chip6

Entries	972855
Mean	3.26
Std Dev	3.772

nChipHit_F4_Fch7_Chip7

Entries

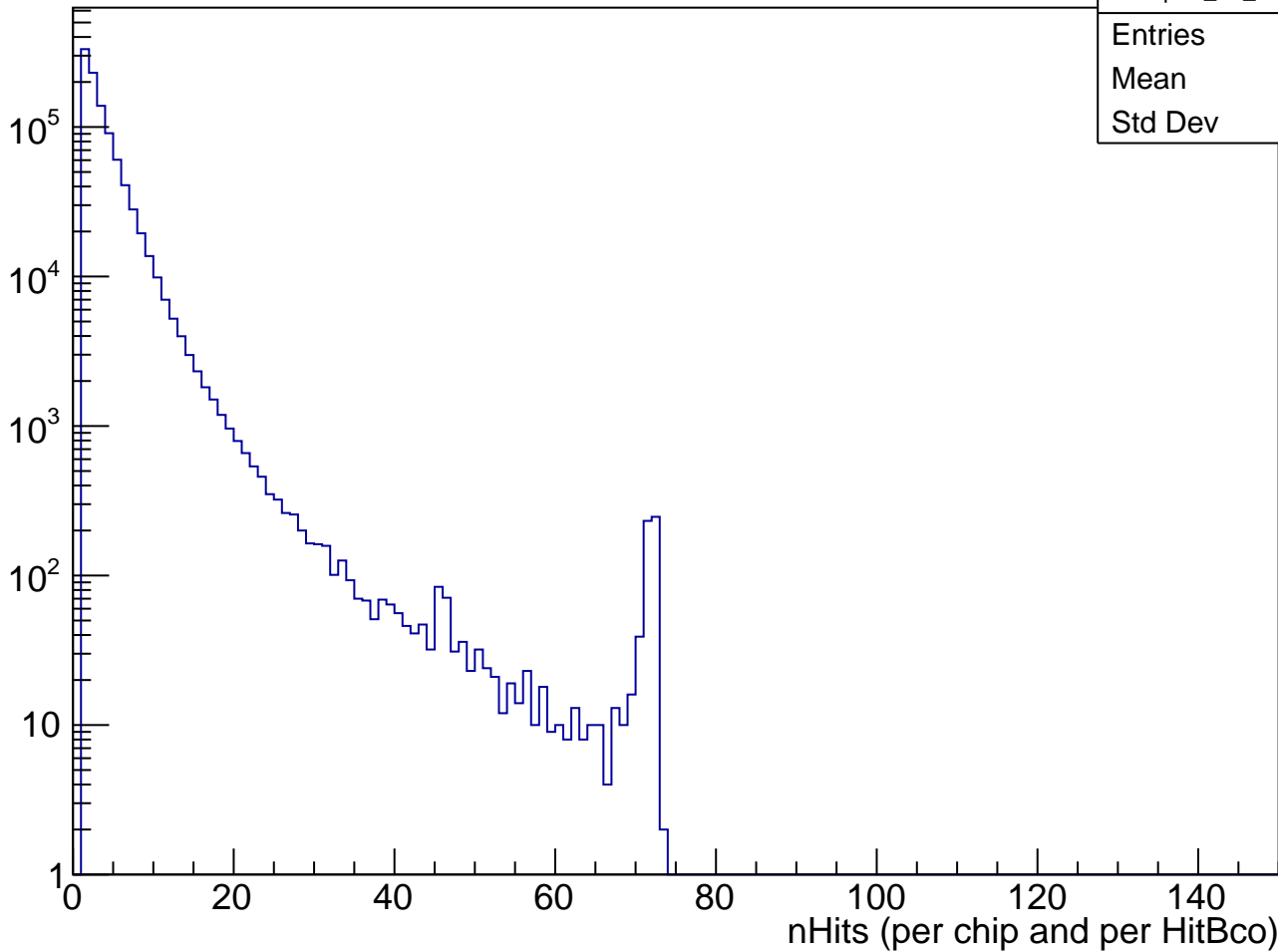


nChipHit_F4_Fch7_Chip7	
Entries	984914
Mean	3.259
Std Dev	3.756

nHits (per chip and per HitBco)

nChipHit_F4_Fch7_Chip8

Entries



nChipHit_F4_Fch7_Chip8

Entries 997487

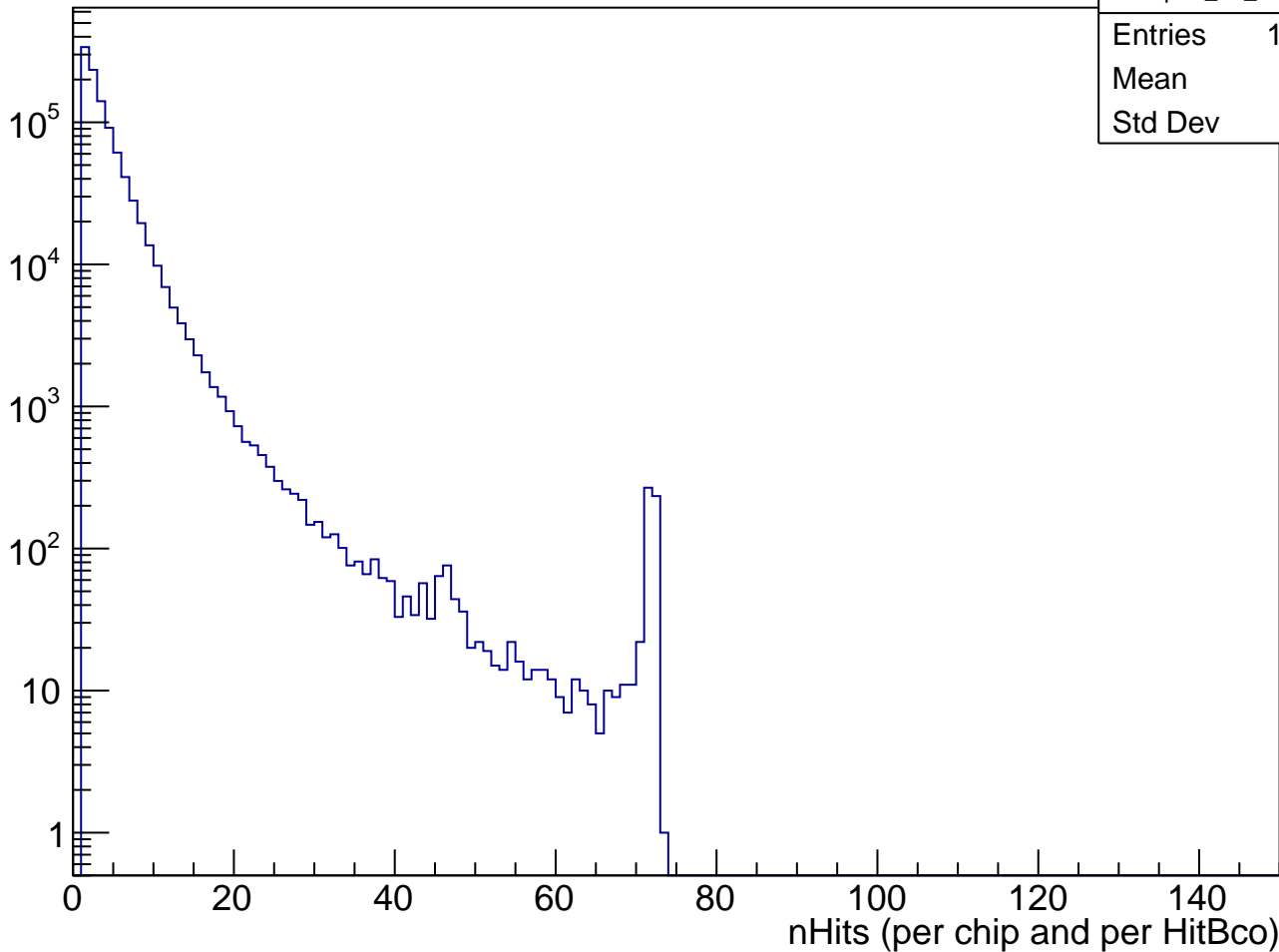
Mean 3.263

Std Dev 3.723

nHits (per chip and per HitBco)

nChipHit_F4_Fch7_Chip9

Entries



nChipHit_F4_Fch7_Chip9

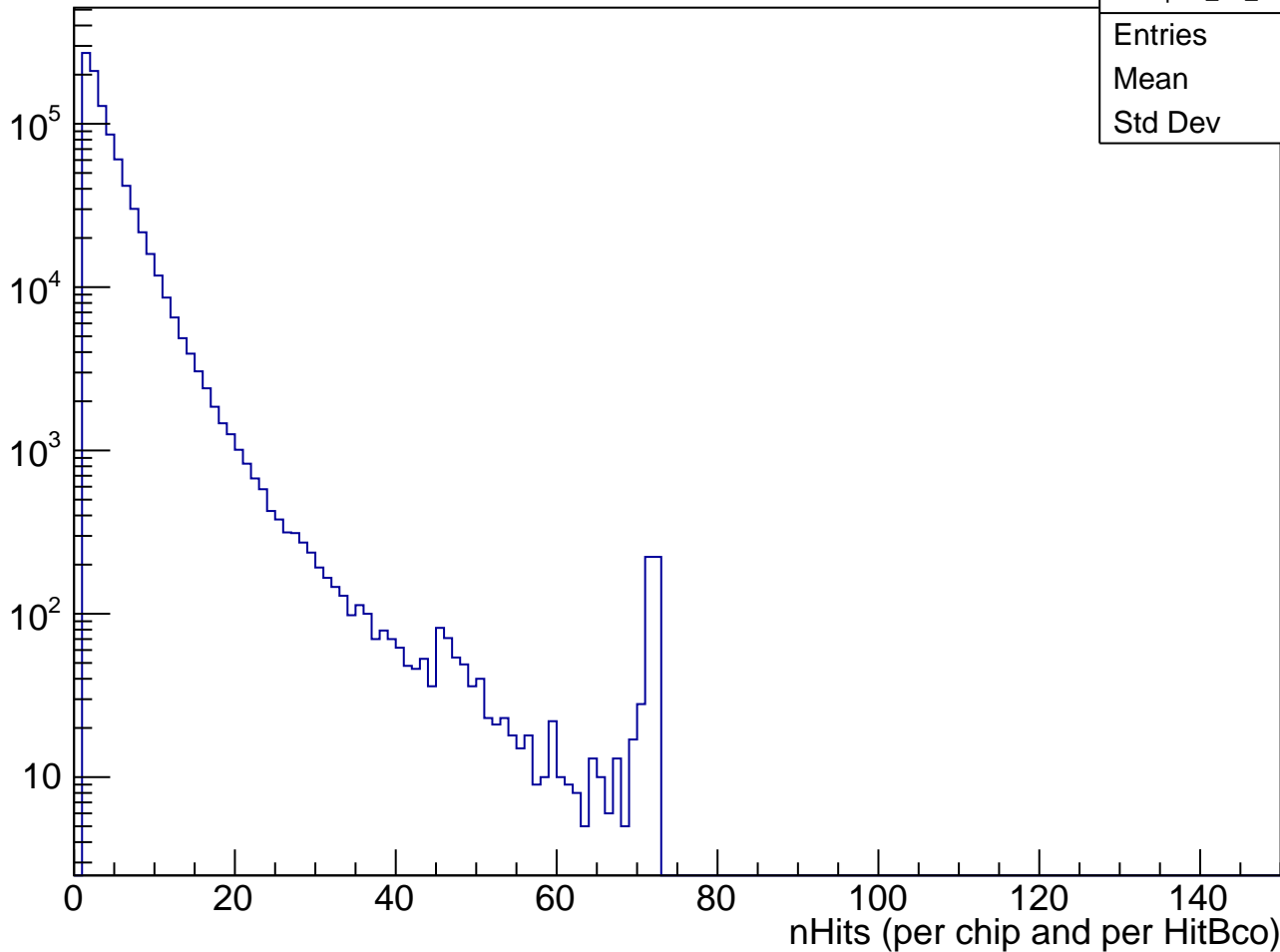
Entries 1010386

Mean 3.231

Std Dev 3.668

nChipHit_F4_Fch8_Chip0

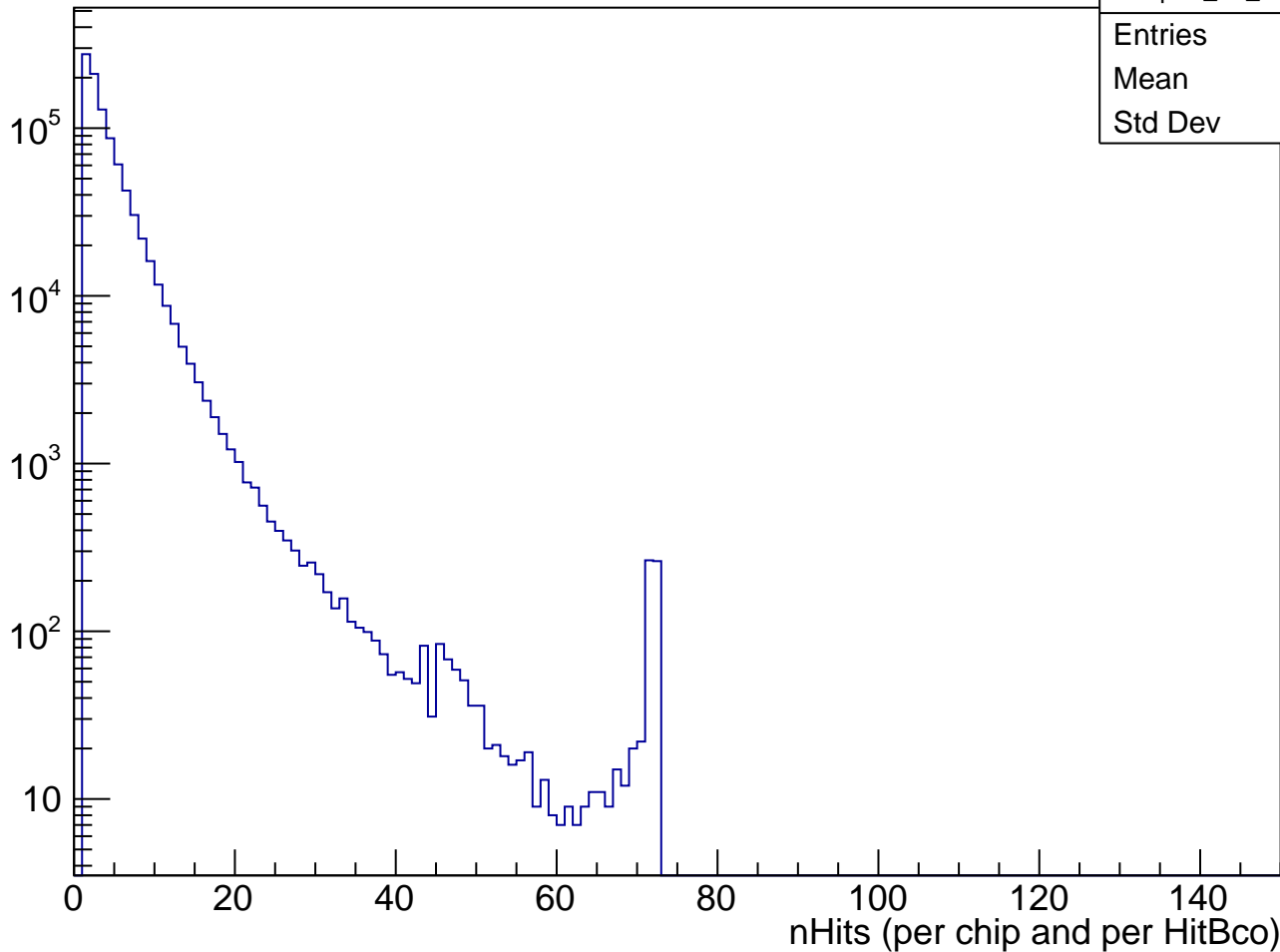
Entries



nChipHit_F4_Fch8_Chip0	
Entries	920018
Mean	3.597
Std Dev	4.032

nChipHit_F4_Fch8_Chip1

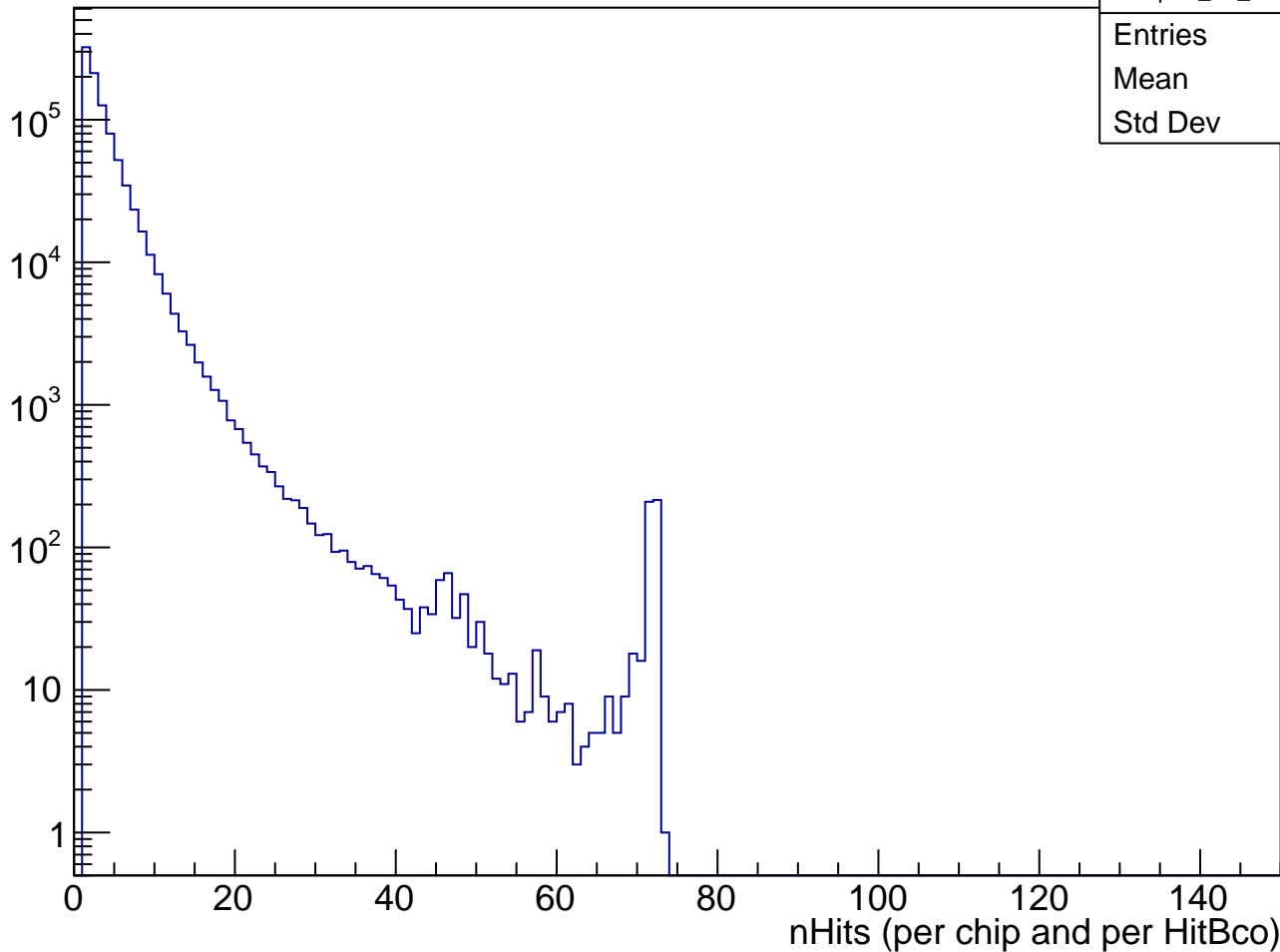
Entries



nChipHit_F4_Fch8_Chip1	
Entries	928407
Mean	3.602
Std Dev	4.085

nChipHit_F4_Fch8_Chip10

Entries

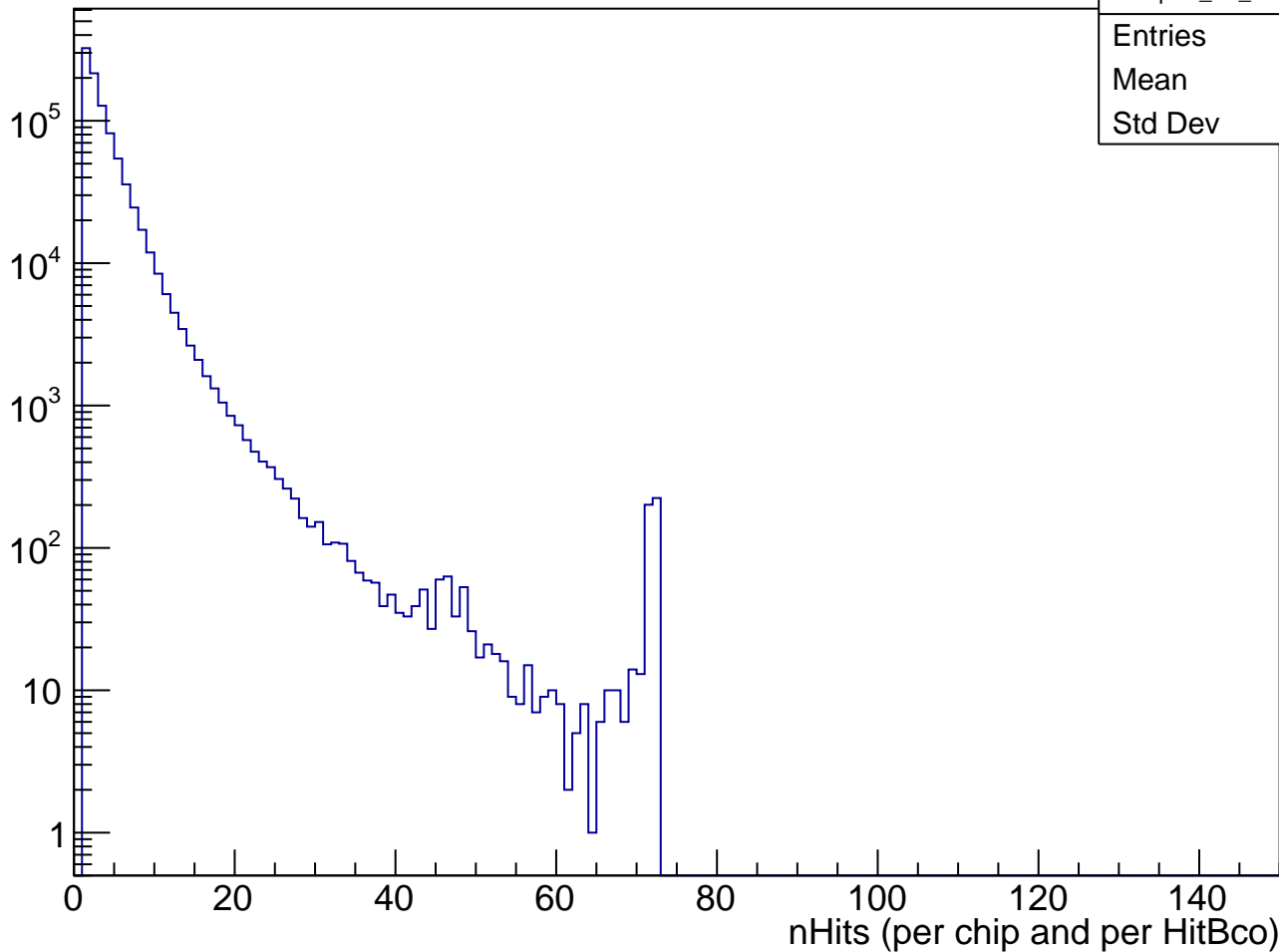


nChipHit_F4_Fch8_Chip10	
Entries	914960
Mean	3.136
Std Dev	3.607

nHits (per chip and per HitBco)

nChipHit_F4_Fch8_Chip11

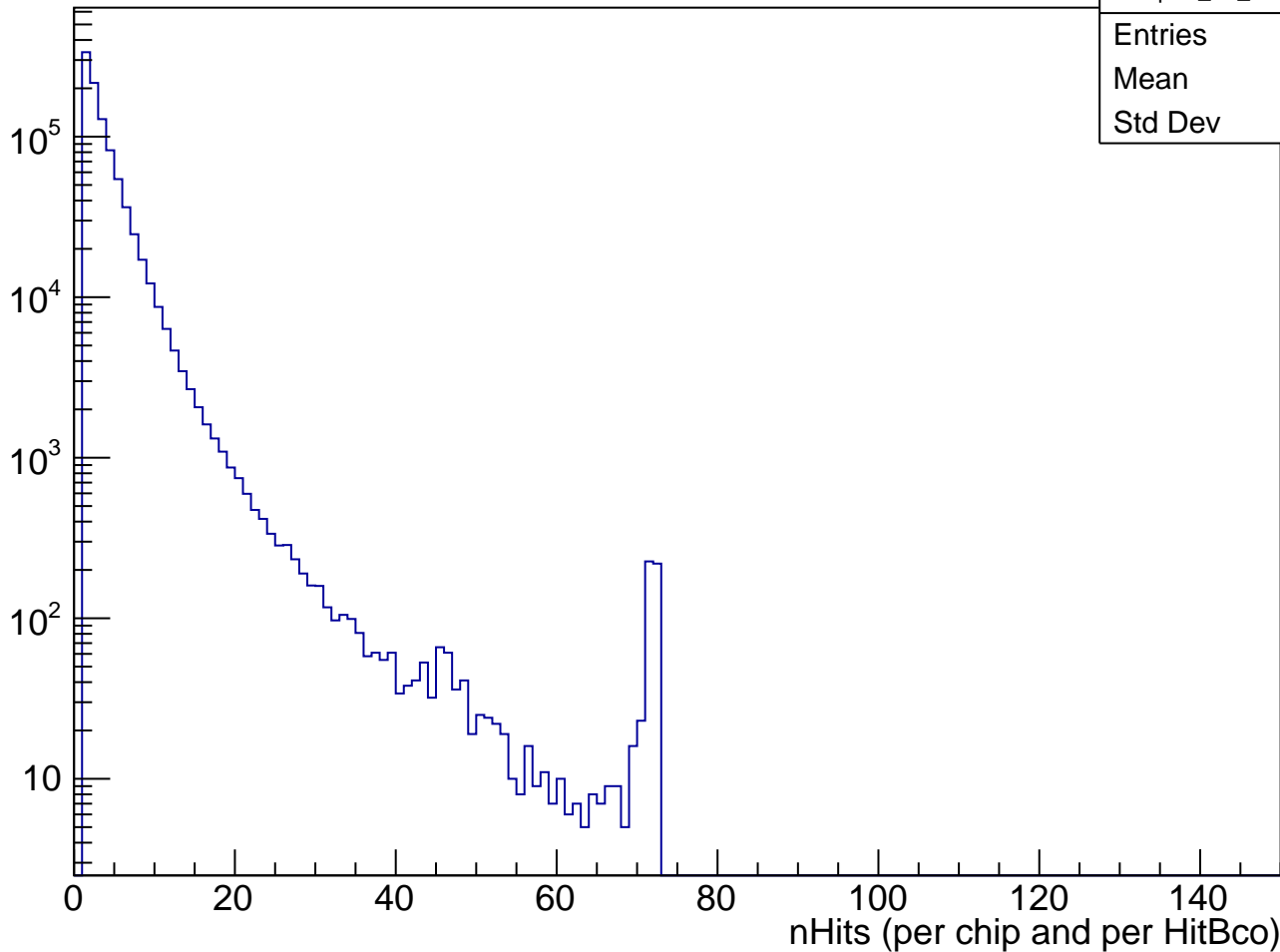
Entries



nChipHit_F4_Fch8_Chip11	
Entries	929487
Mean	3.163
Std Dev	3.612

nChipHit_F4_Fch8_Chip12

Entries

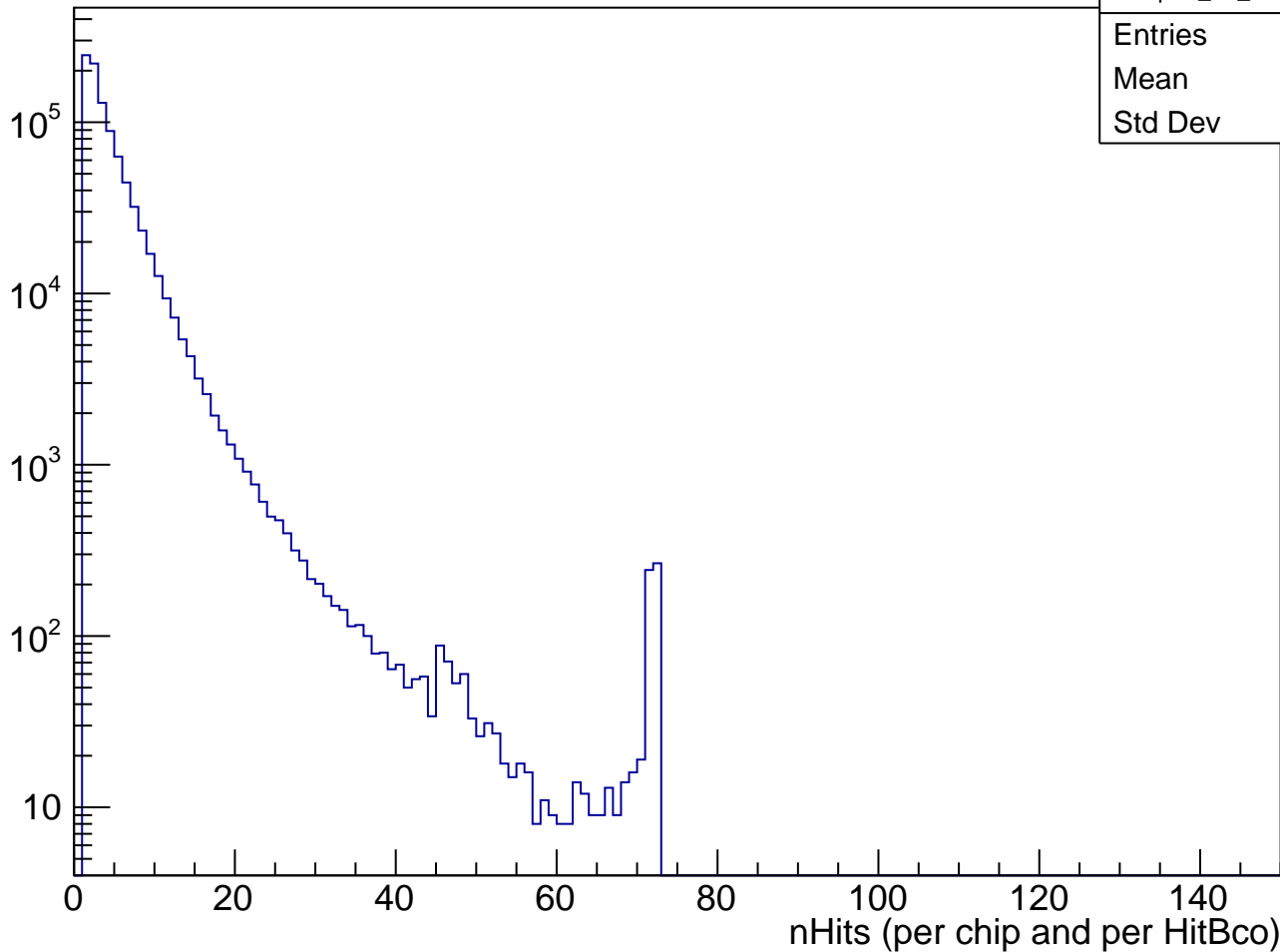


nChipHit_F4_Fch8_Chip12	
Entries	945877
Mean	3.155
Std Dev	3.65

nHits (per chip and per HitBco)

nChipHit_F4_Fch8_Chip13

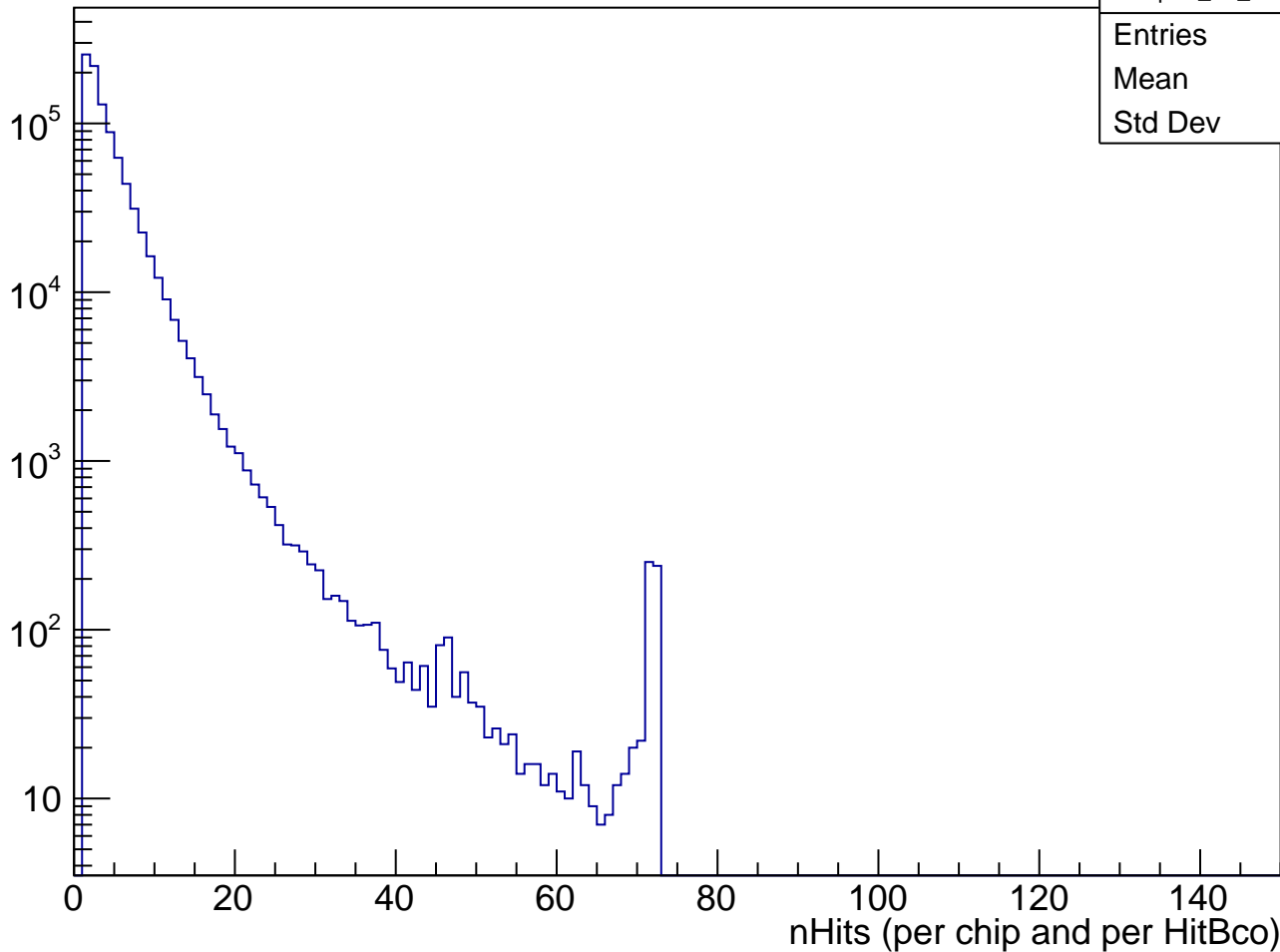
Entries



nChipHit_F4_Fch8_Chip13	
Entries	922807
Mean	3.737
Std Dev	4.133

nChipHit_F4_Fch8_Chip14

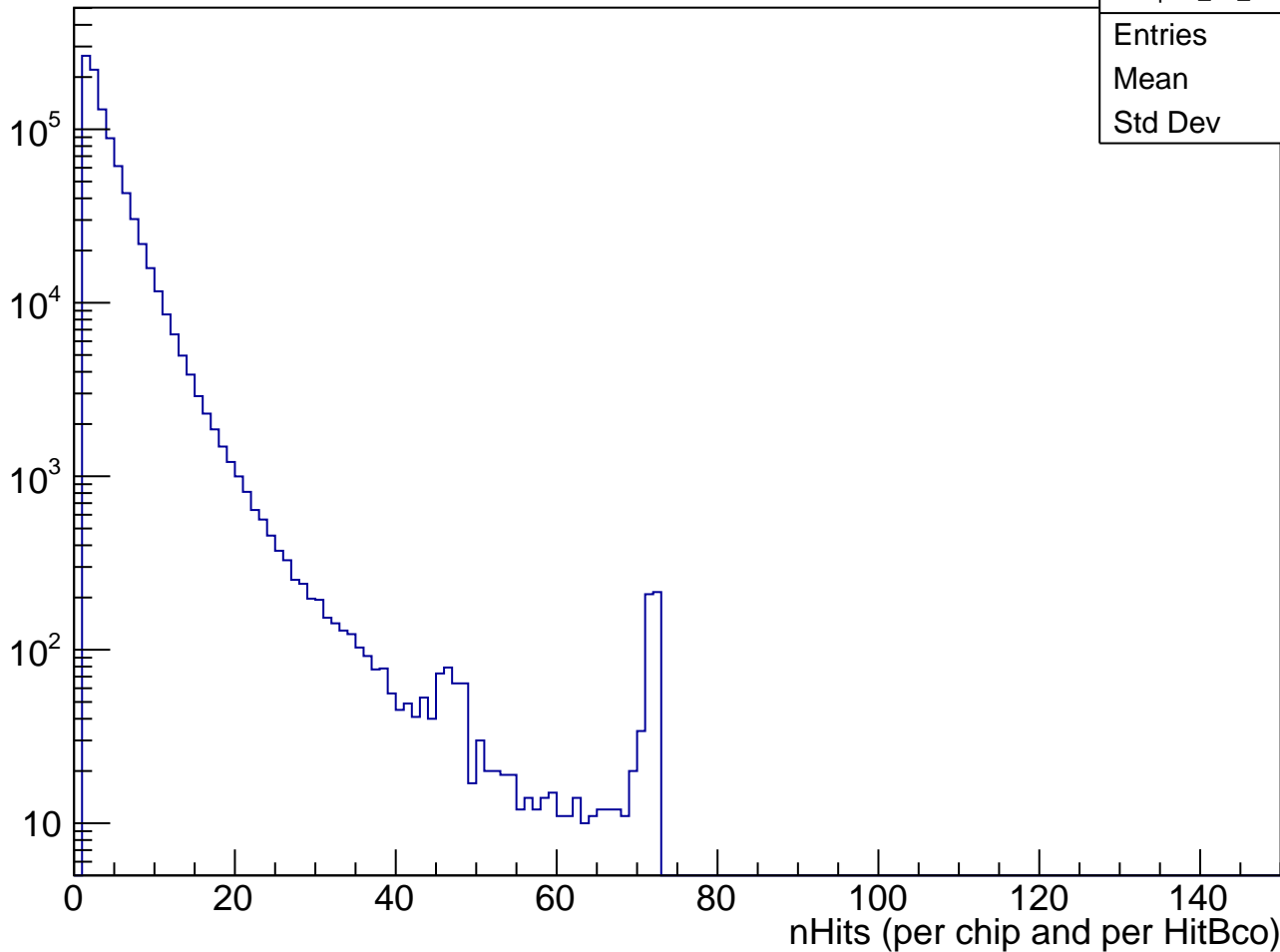
Entries



nChipHit_F4_Fch8_Chip14	
Entries	925675
Mean	3.679
Std Dev	4.107

nChipHit_F4_Fch8_Chip15

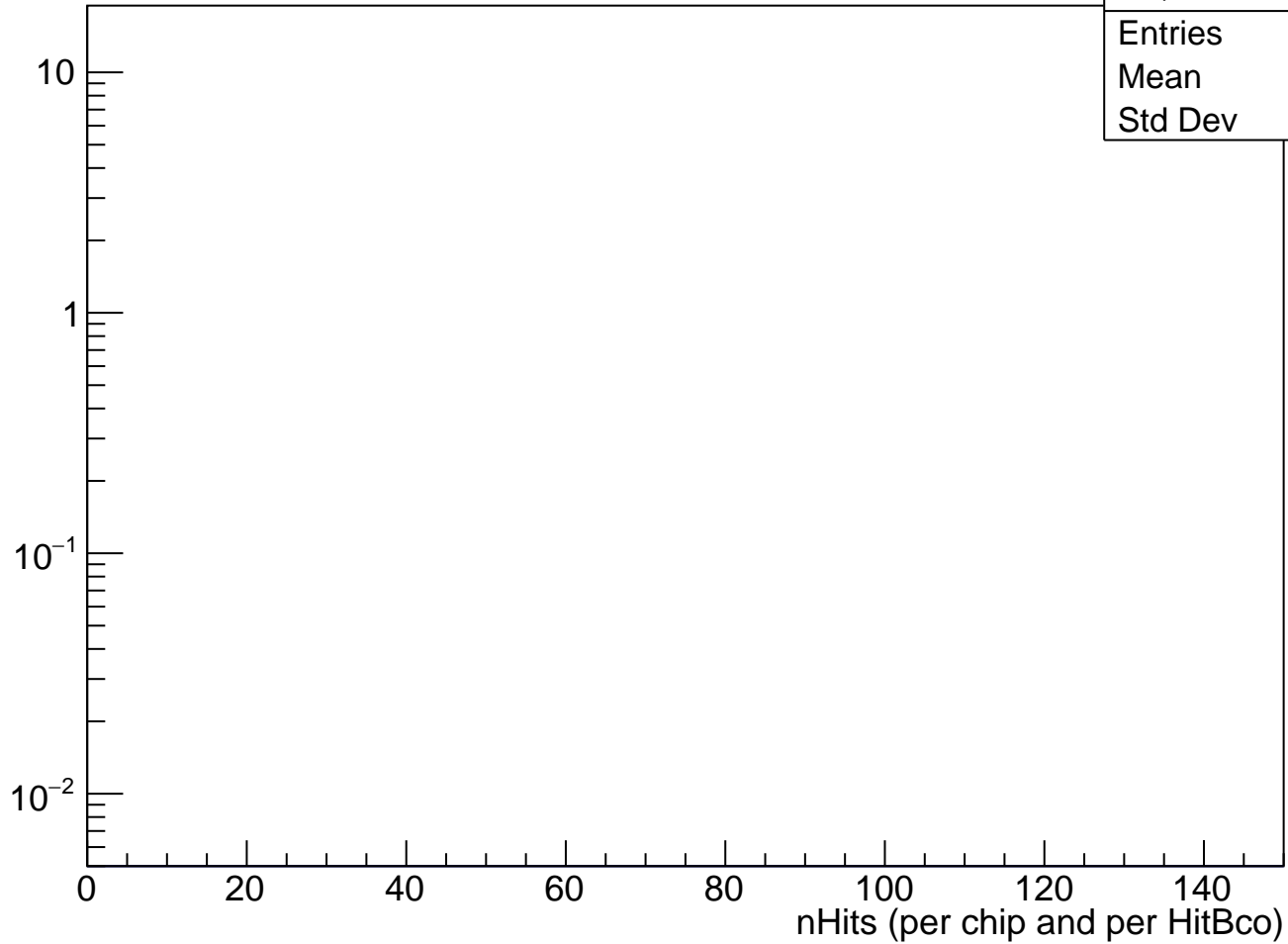
Entries



nChipHit_F4_Fch8_Chip15	
Entries	929709
Mean	3.592
Std Dev	3.984

nChipHit_F4_Fch8_Chip16

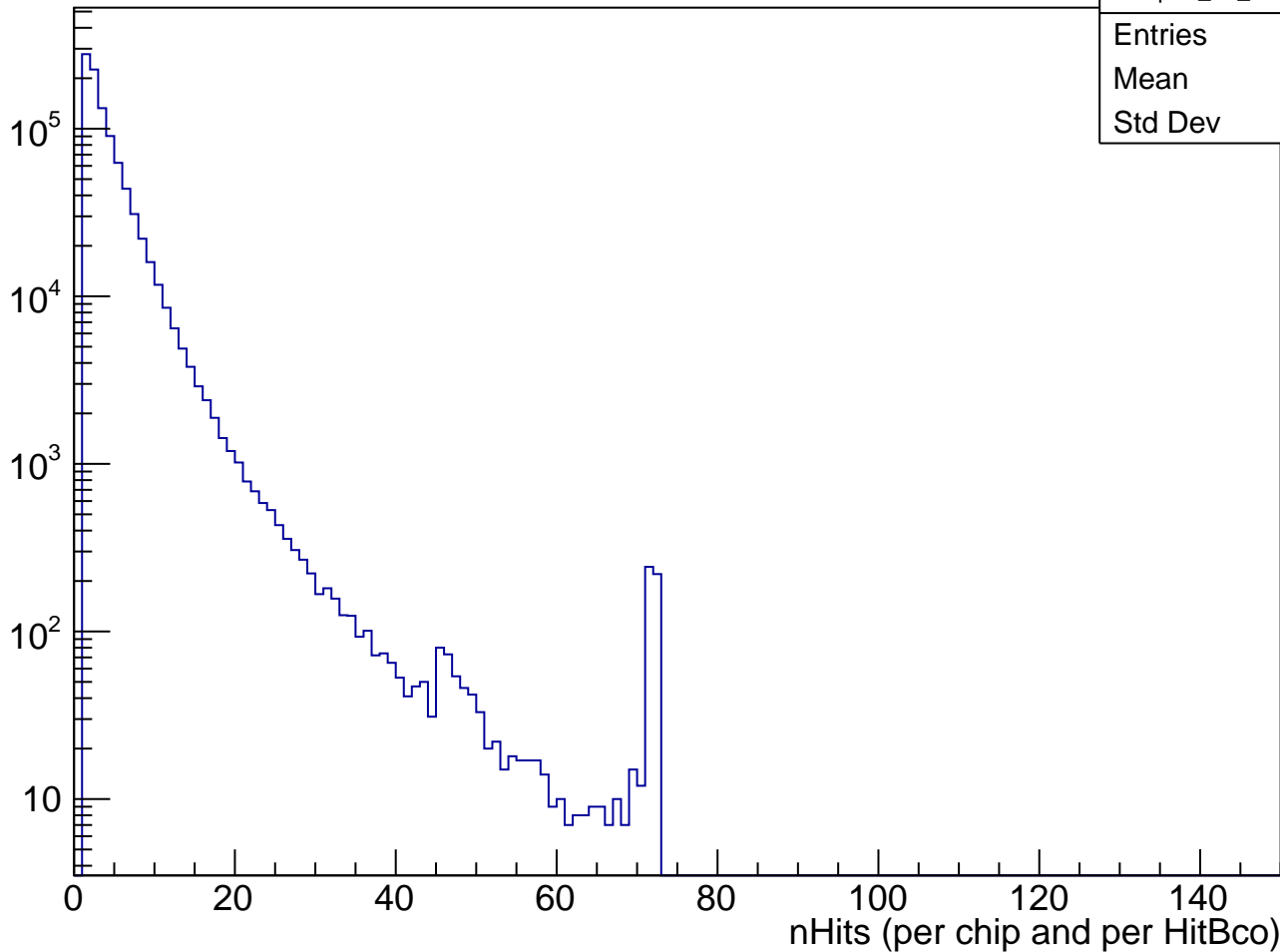
Entries



nChipHit_F4_Fch8_Chip16	
Entries	0
Mean	0
Std Dev	0

nChipHit_F4_Fch8_Chip17

Entries

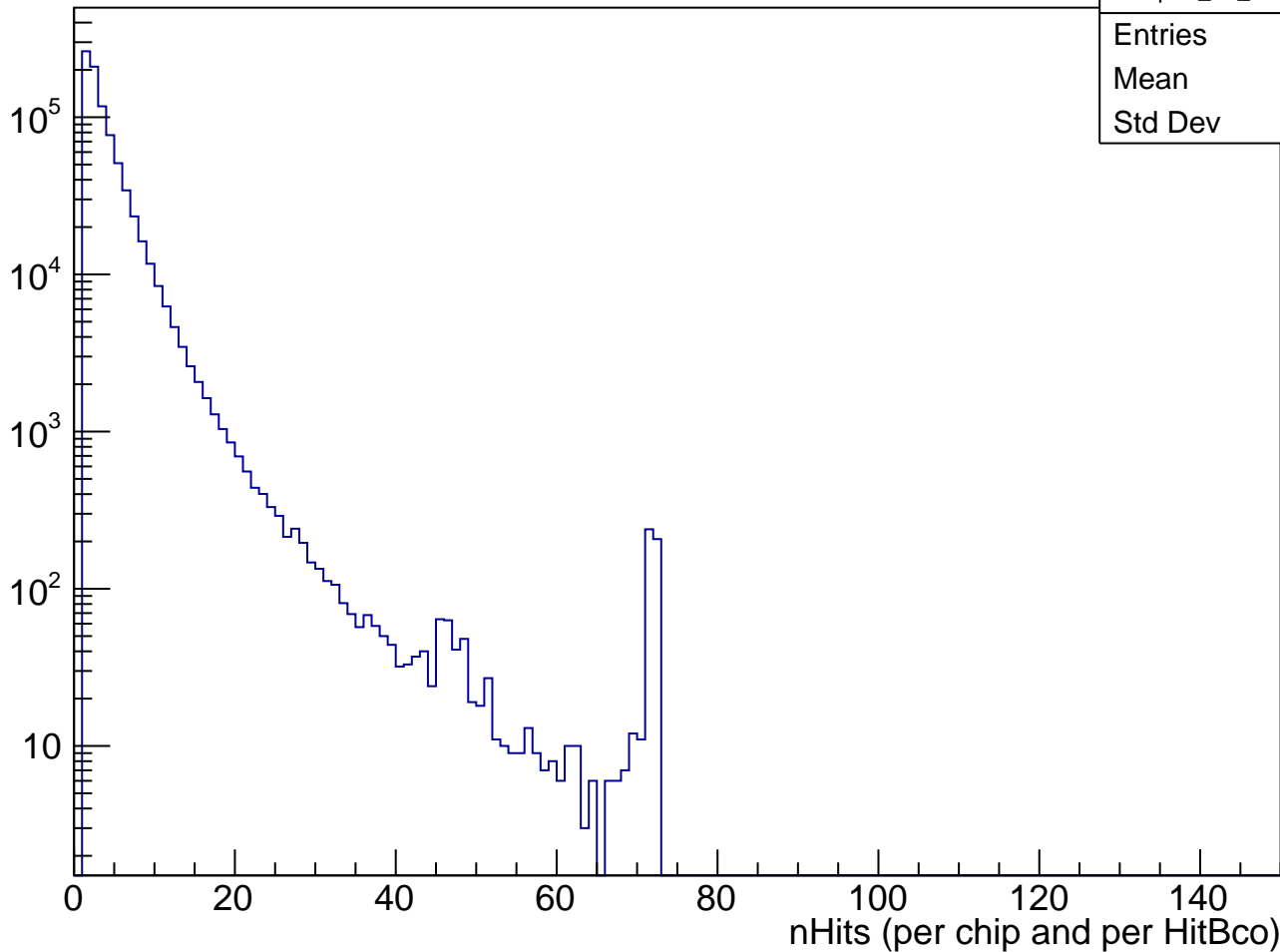


nChipHit_F4_Fch8_Chip17	
Entries	955095
Mean	3.56
Std Dev	3.961

nHits (per chip and per HitBco)

nChipHit_F4_Fch8_Chip18

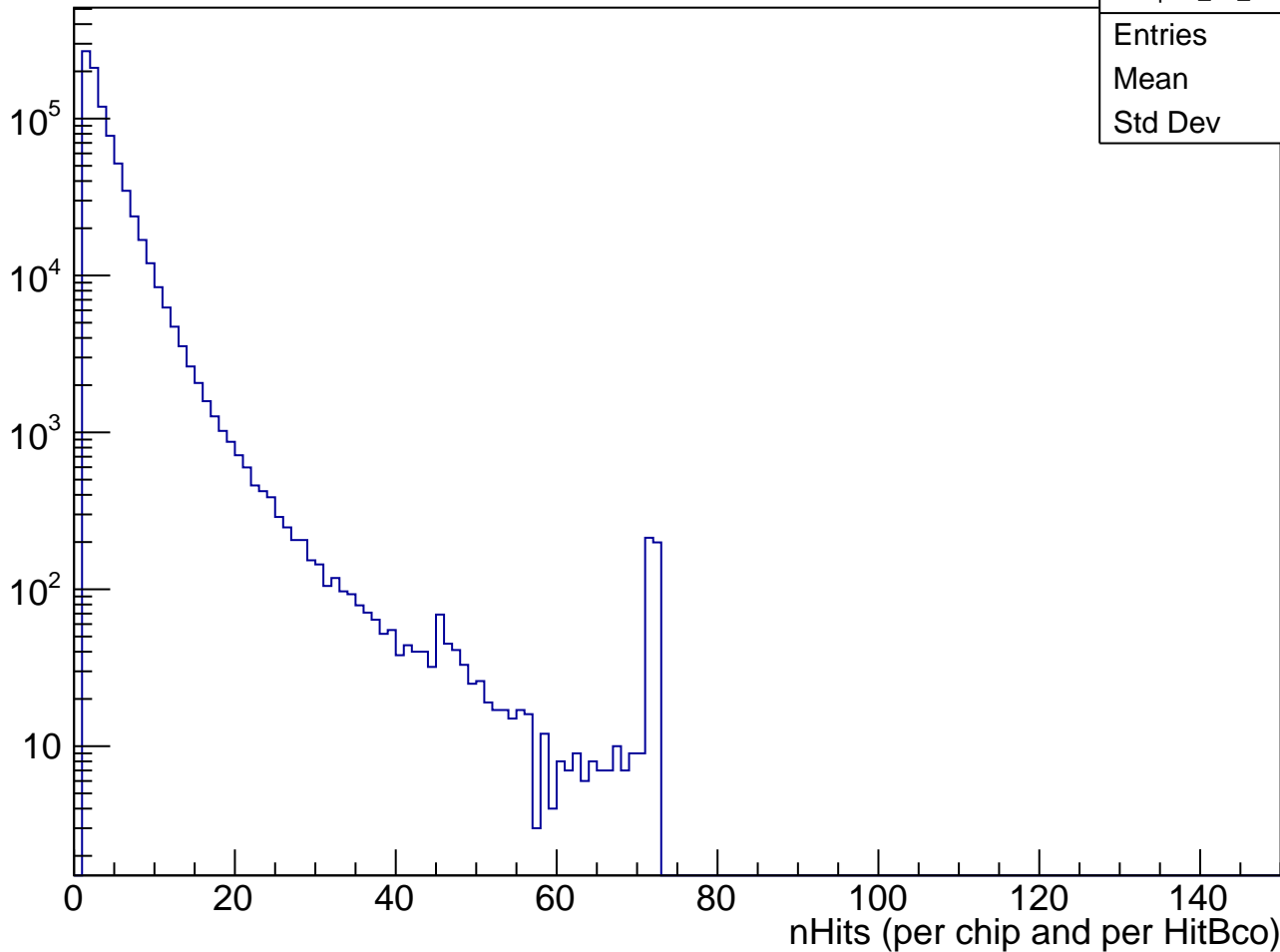
Entries



nChipHit_F4_Fch8_Chip18	
Entries	840730
Mean	3.299
Std Dev	3.729

nChipHit_F4_Fch8_Chip19

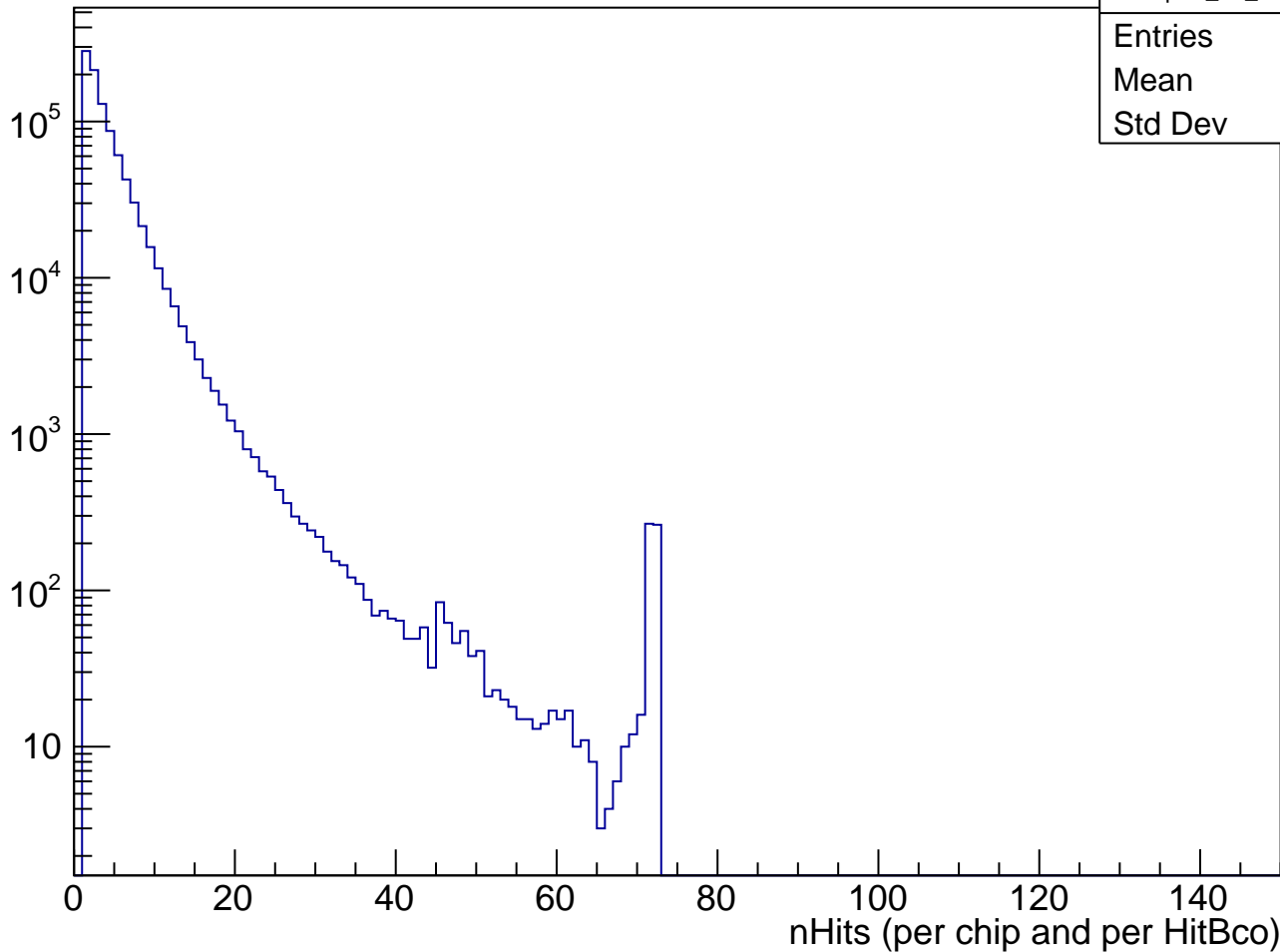
Entries



nChipHit_F4_Fch8_Chip19	
Entries	853318
Mean	3.299
Std Dev	3.73

nChipHit_F4_Fch8_Chip2

Entries



nChipHit_F4_Fch8_Chip2

Entries 936581

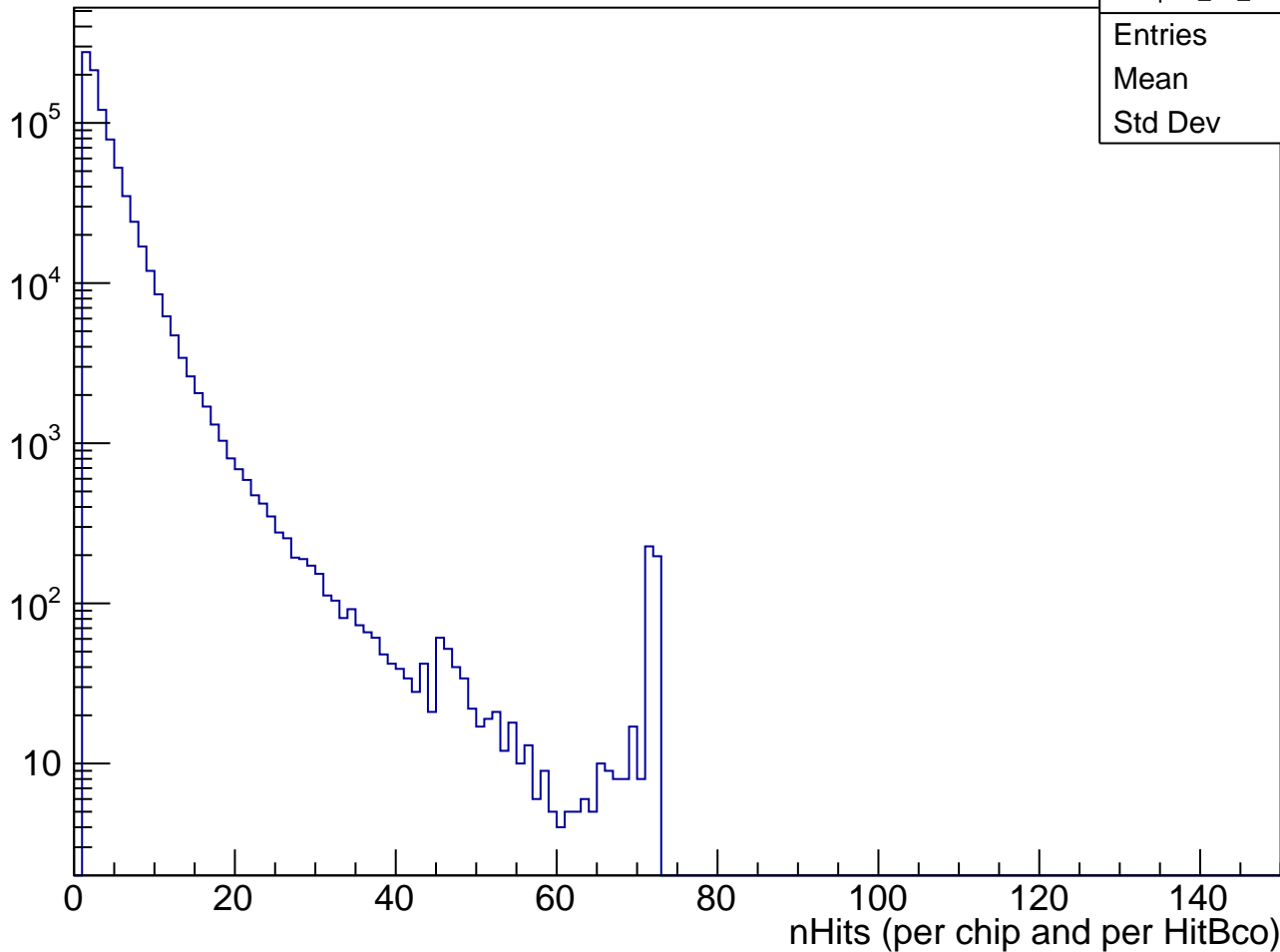
Mean 3.57

Std Dev 4.063

nHits (per chip and per HitBco)

nChipHit_F4_Fch8_Chip20

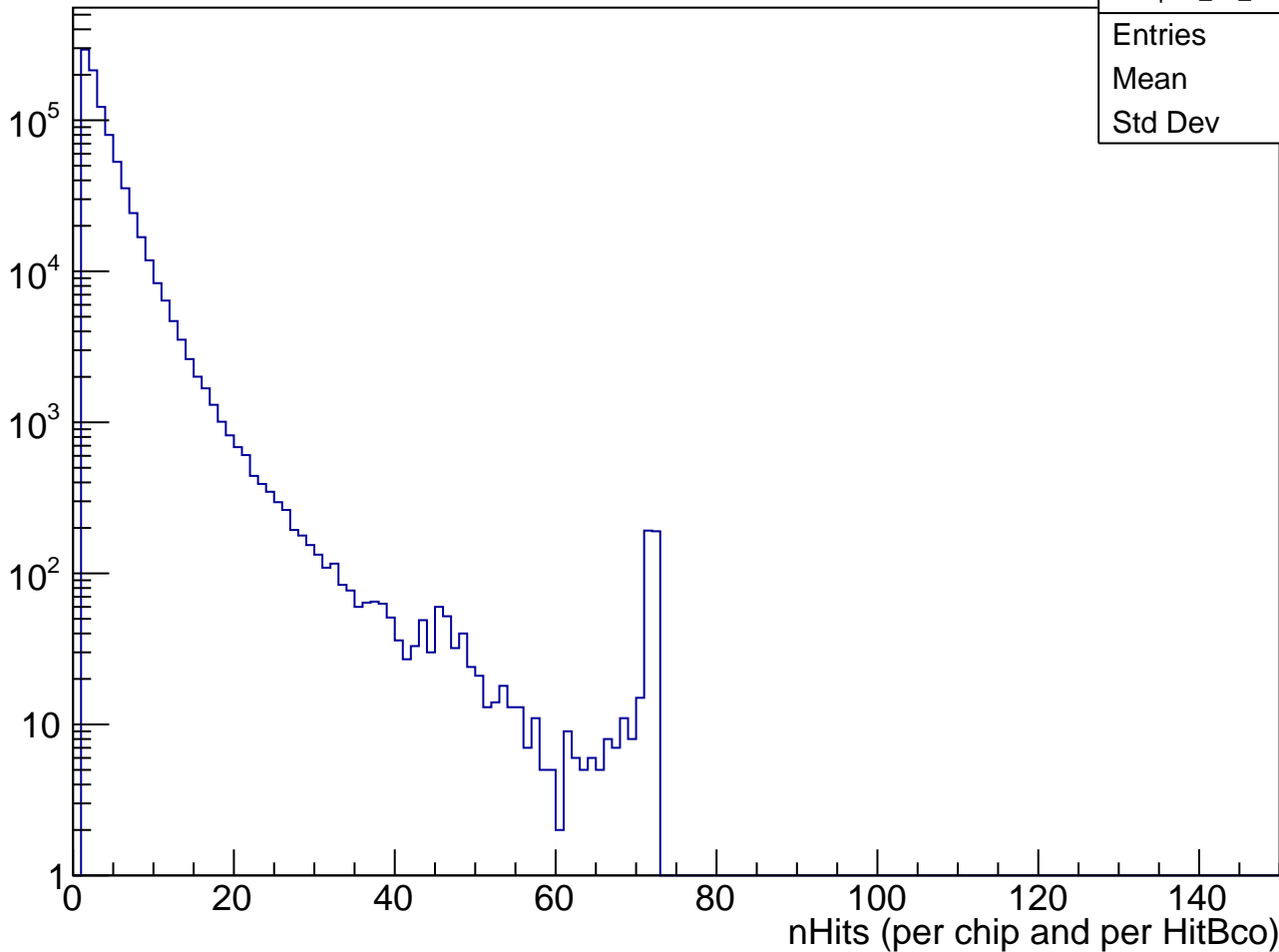
Entries



nChipHit_F4_Fch8_Chip20	
Entries	868077
Mean	3.272
Std Dev	3.688

nChipHit_F4_Fch8_Chip21

Entries

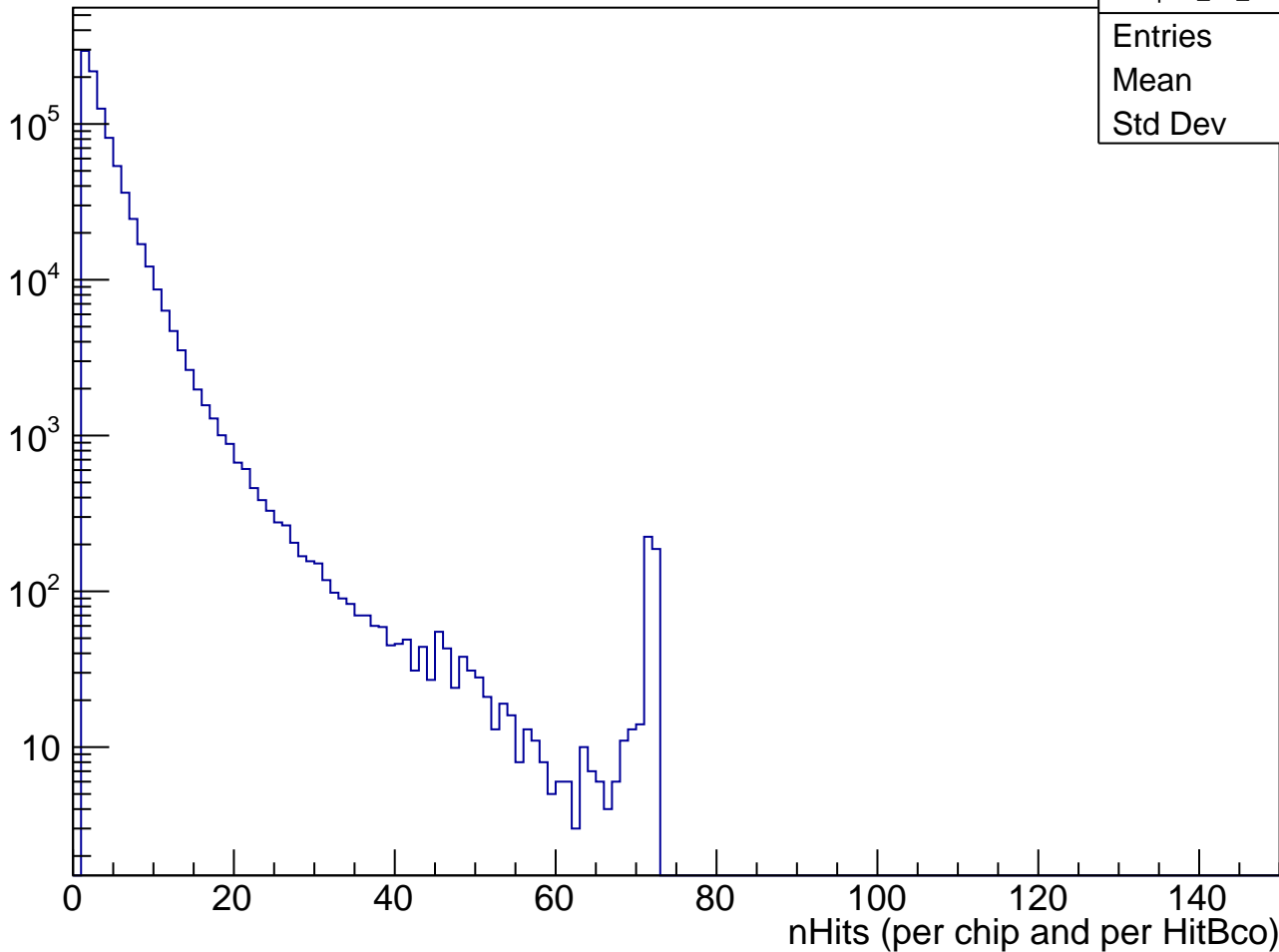


nChipHit_F4_Fch8_Chip21	
Entries	889464
Mean	3.227
Std Dev	3.621

nHits (per chip and per HitBco)

nChipHit_F4_Fch8_Chip22

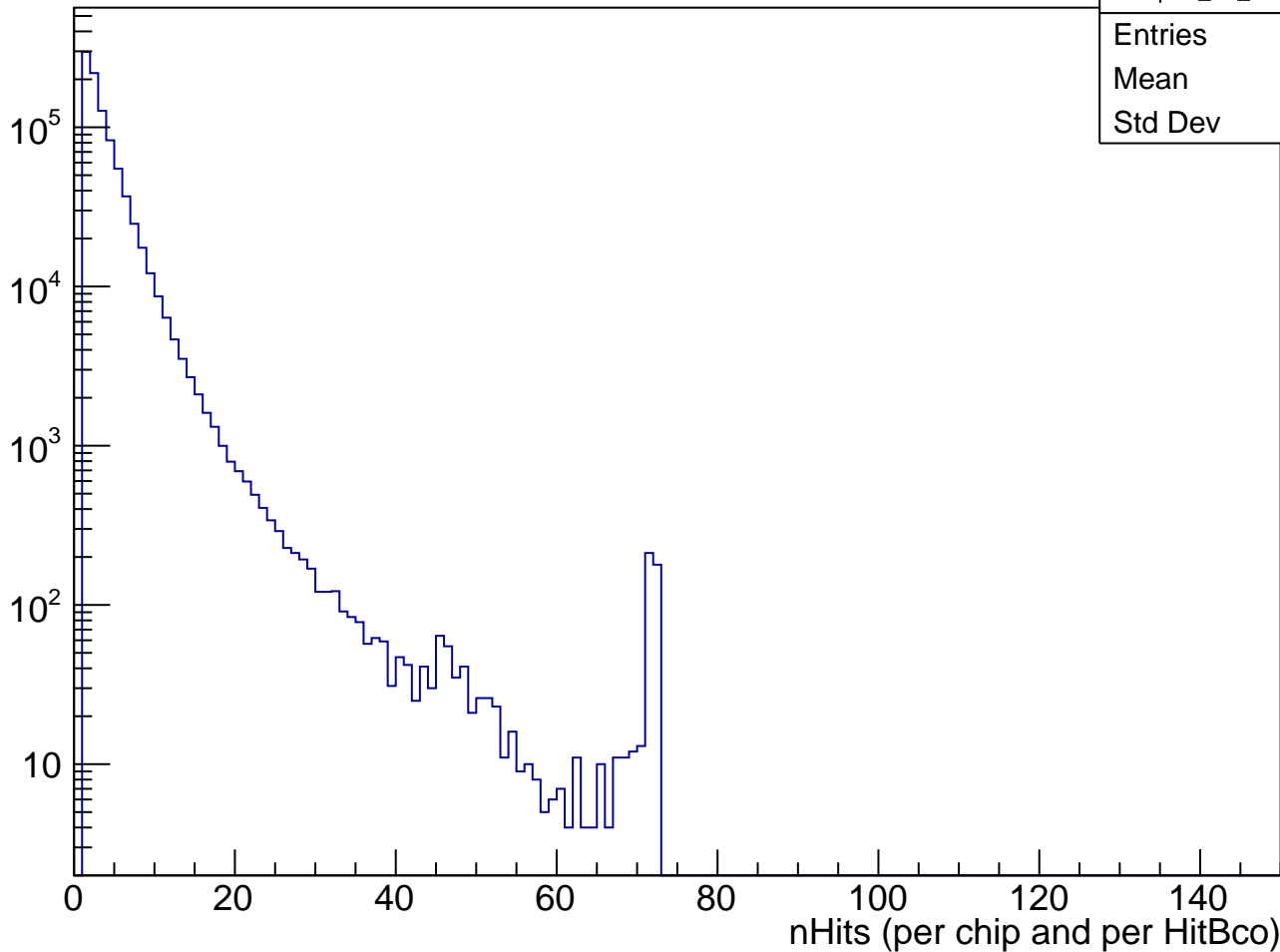
Entries



nChipHit_F4_Fch8_Chip22	
Entries	901001
Mean	3.232
Std Dev	3.635

nChipHit_F4_Fch8_Chip23

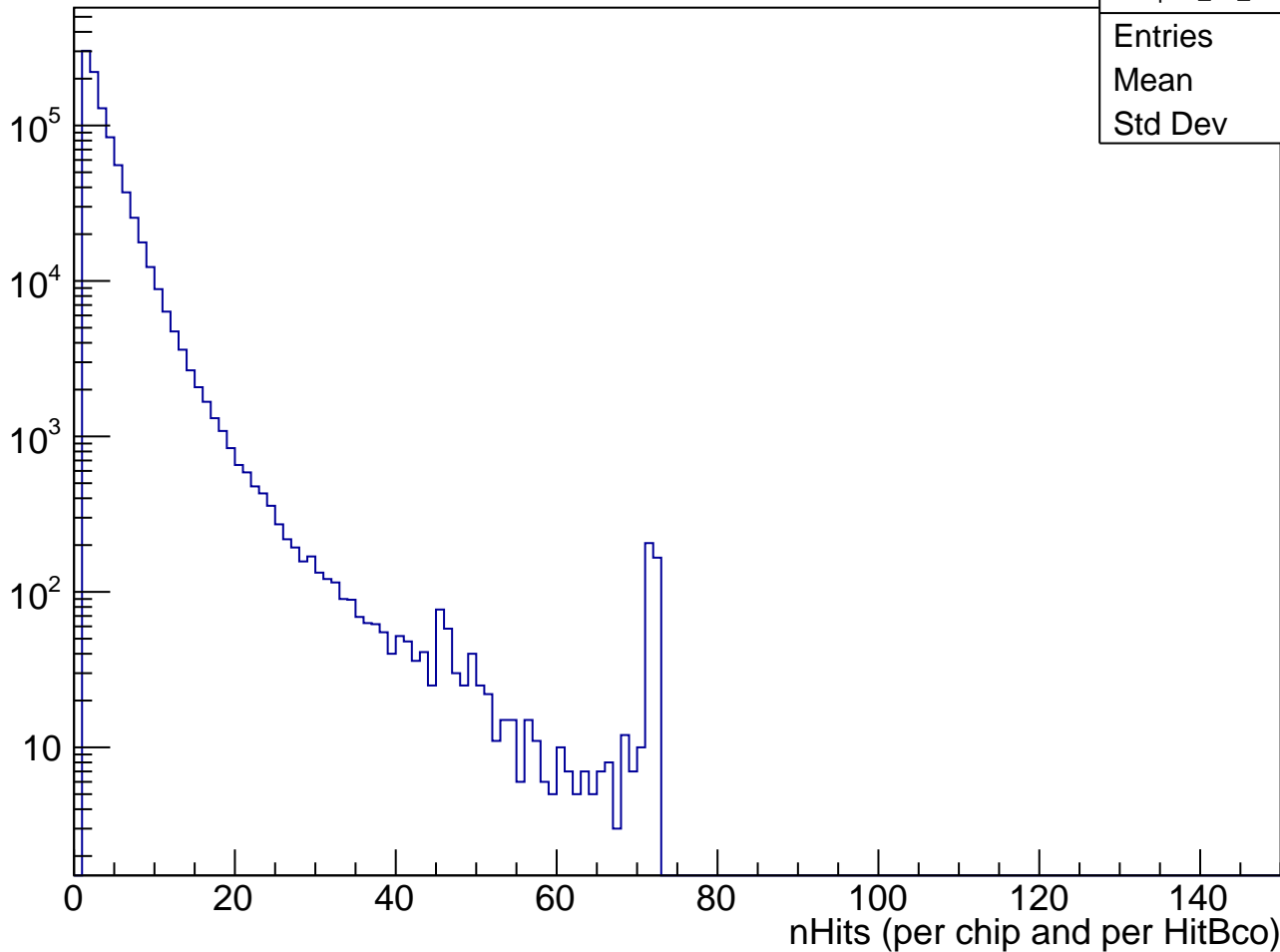
Entries



nChipHit_F4_Fch8_Chip23	
Entries	910757
Mean	3.234
Std Dev	3.618

nChipHit_F4_Fch8_Chip24

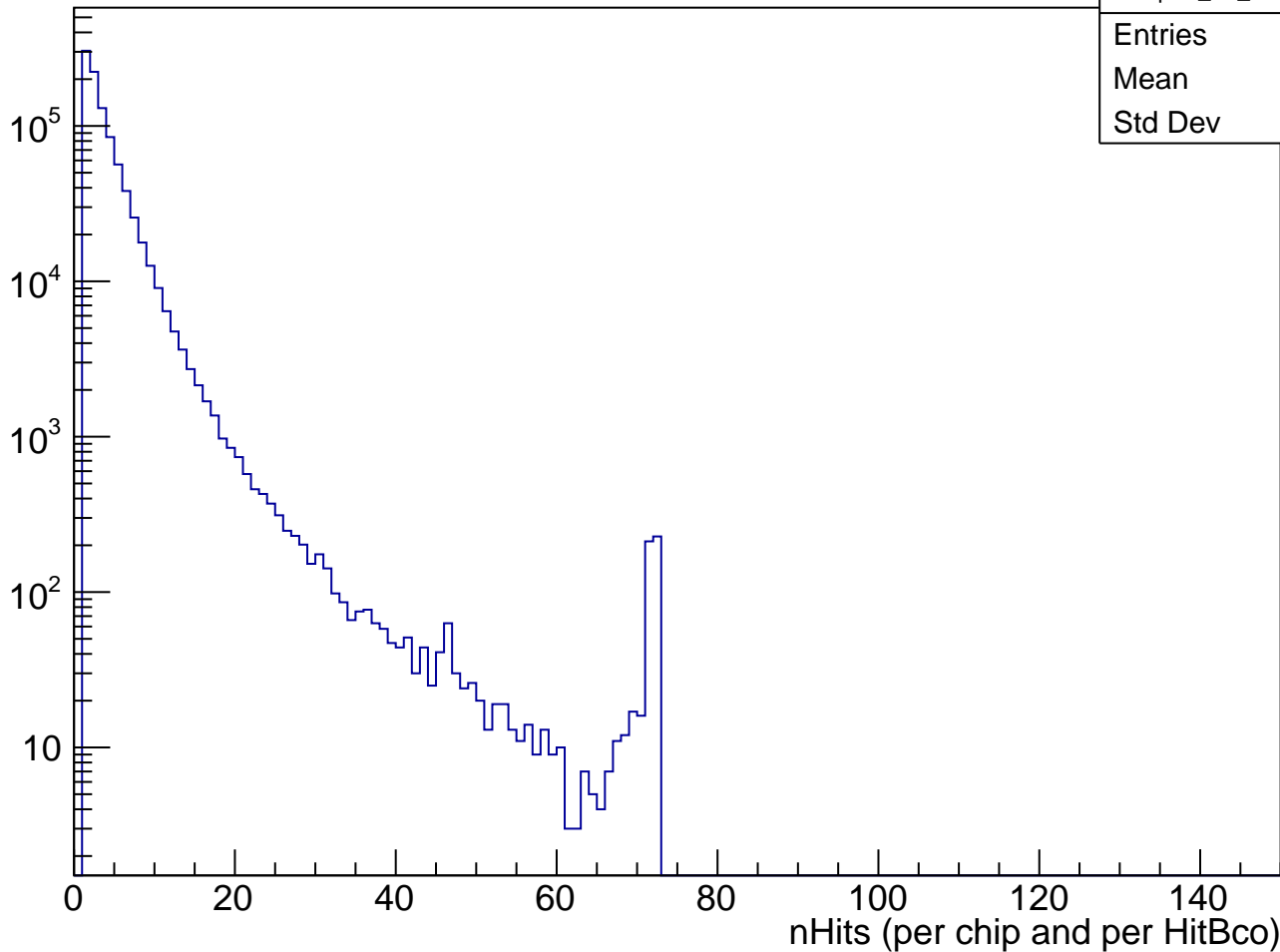
Entries



nChipHit_F4_Fch8_Chip24	
Entries	922831
Mean	3.23
Std Dev	3.589

nChipHit_F4_Fch8_Chip25

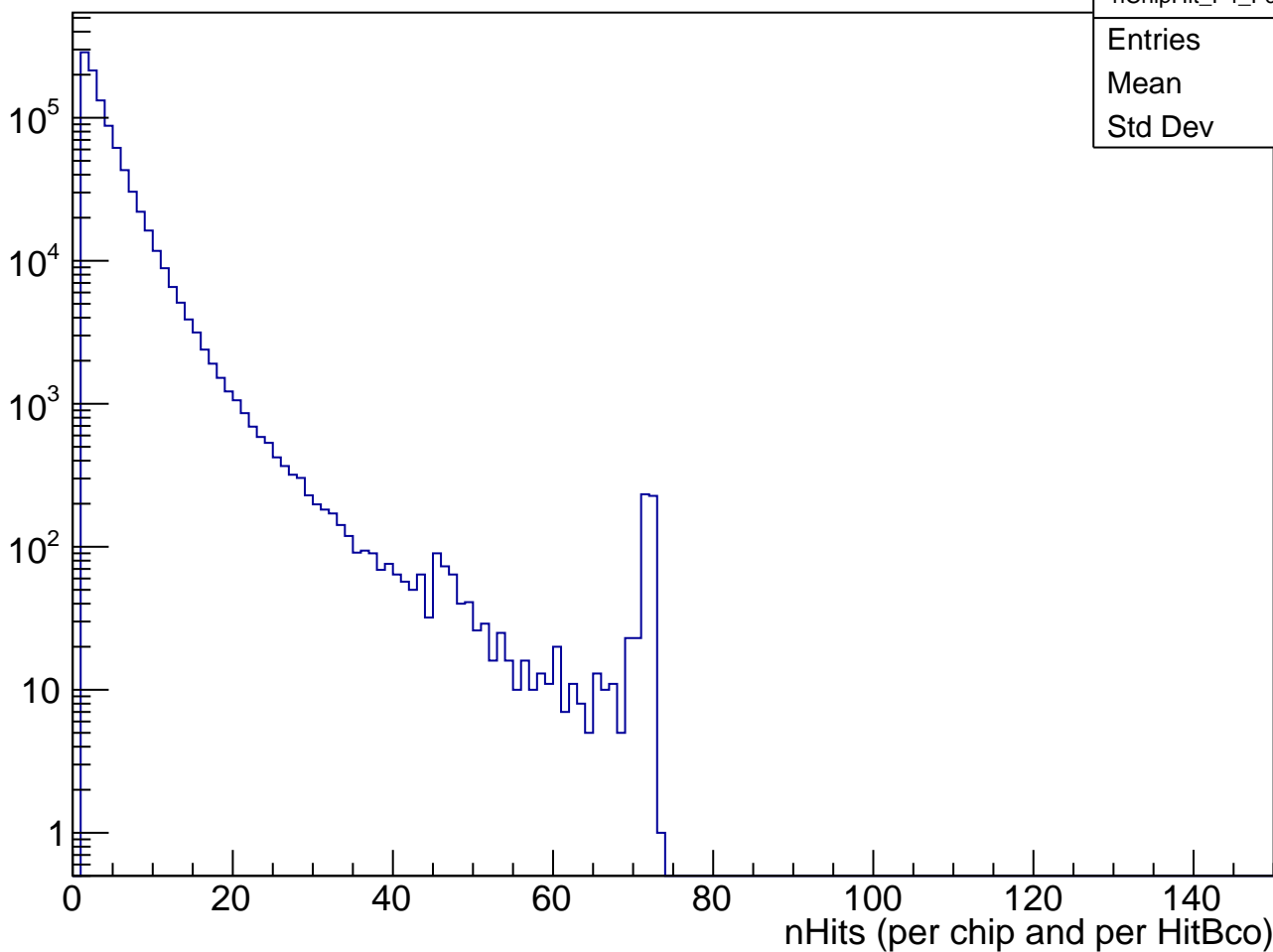
Entries



nChipHit_F4_Fch8_Chip25	
Entries	932712
Mean	3.243
Std Dev	3.647

nChipHit_F4_Fch8_Chip3

Entries



nChipHit_F4_Fch8_Chip3

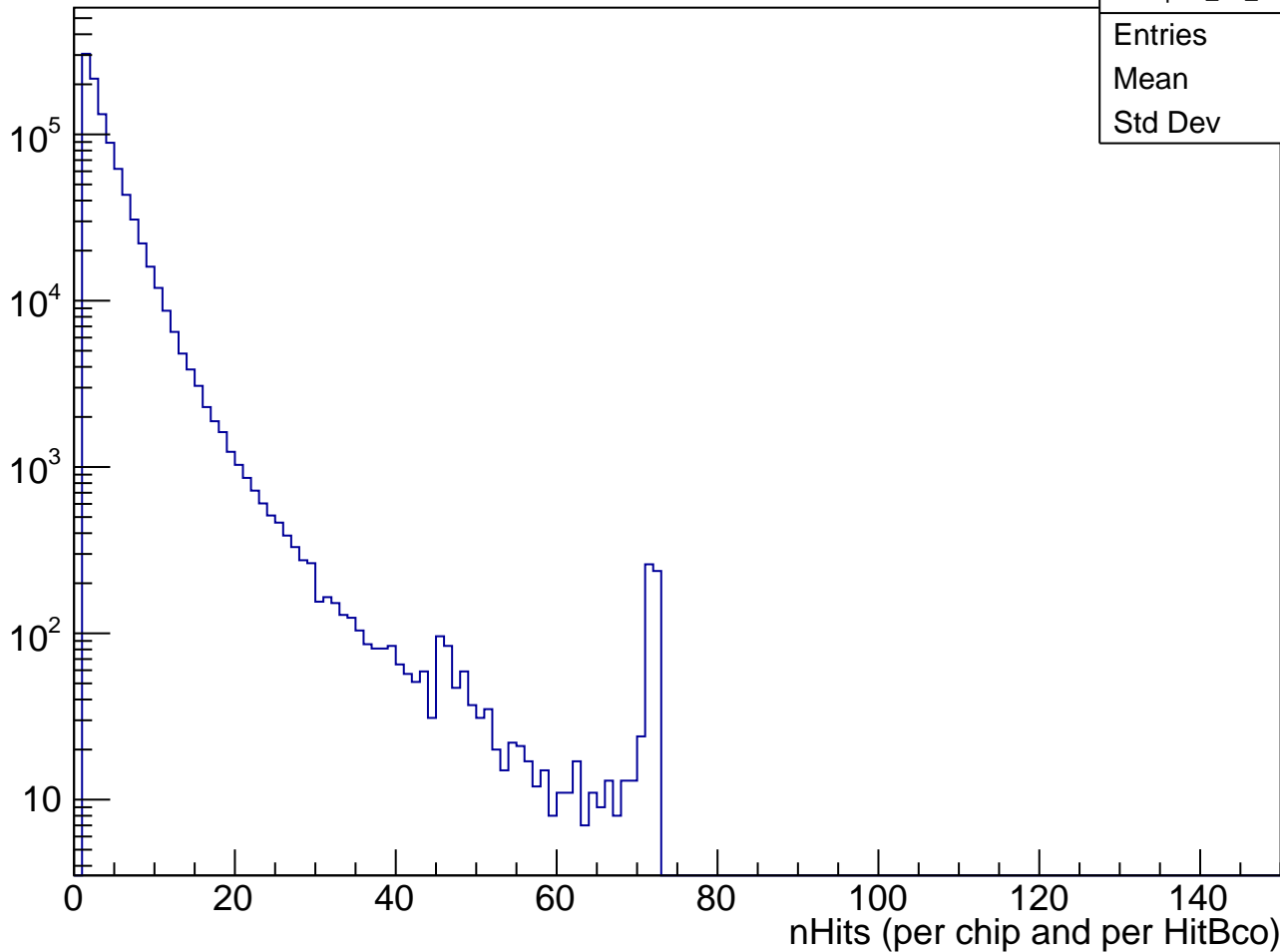
Entries 948965

Mean 3.572

Std Dev 4.034

nChipHit_F4_Fch8_Chip4

Entries



nChipHit_F4_Fch8_Chip4

Entries 970875

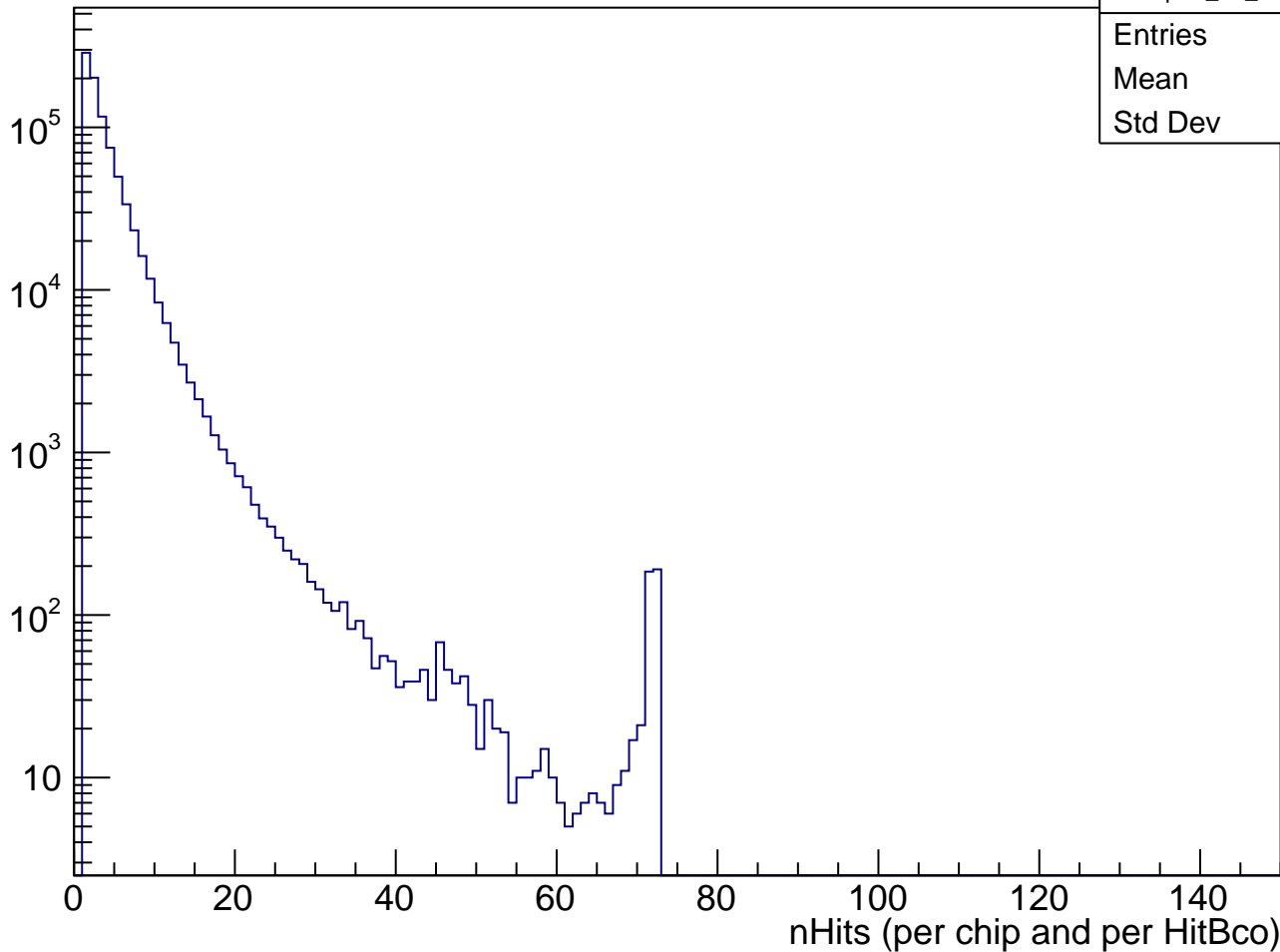
Mean 3.523

Std Dev 4.031

nHits (per chip and per HitBco)

nChipHit_F4_Fch8_Chip5

Entries



nChipHit_F4_Fch8_Chip5

Entries 853519

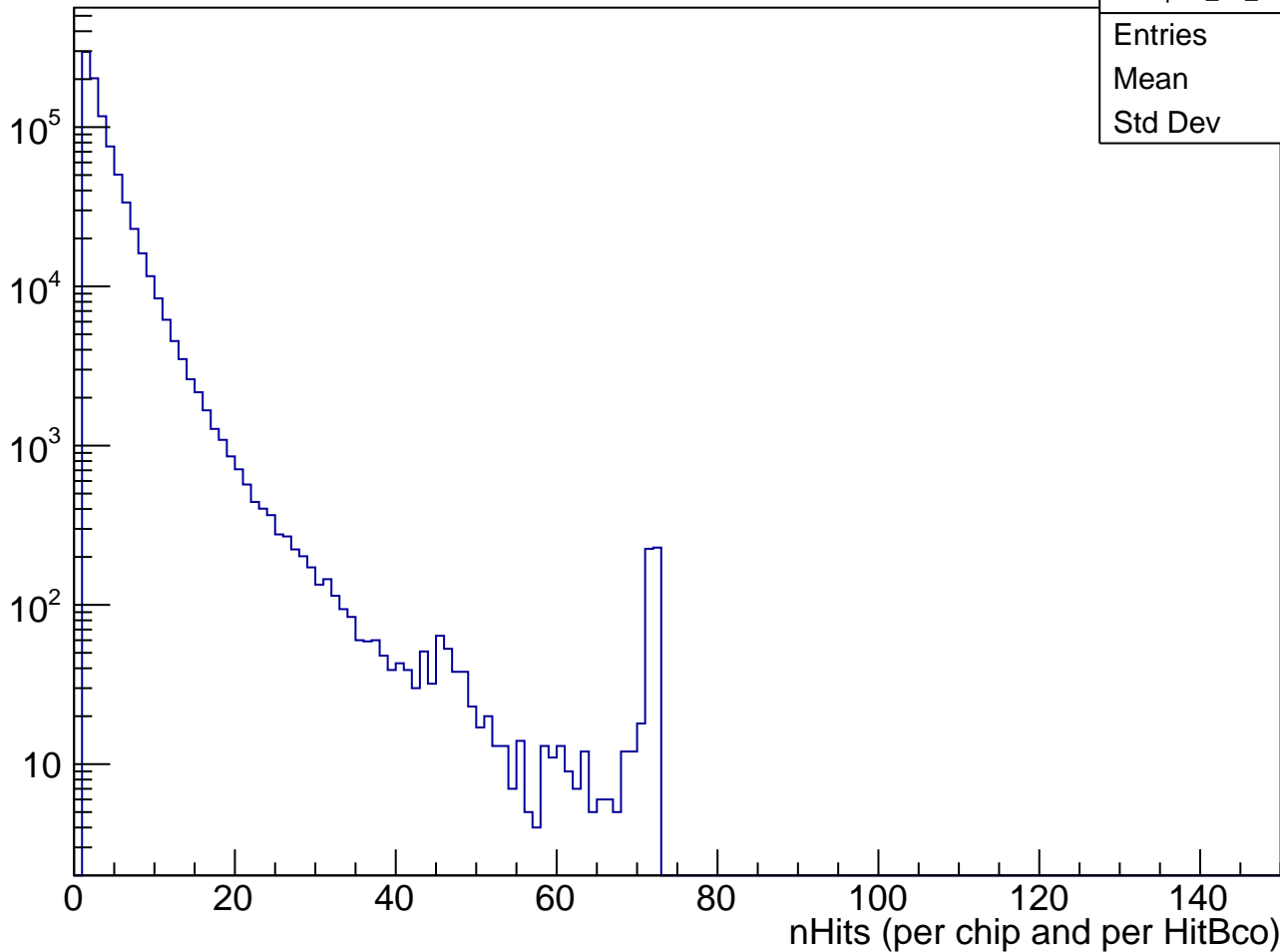
Mean 3.246

Std Dev 3.73

nHits (per chip and per HitBco)

nChipHit_F4_Fch8_Chip6

Entries



nChipHit_F4_Fch8_Chip6

Entries 864660

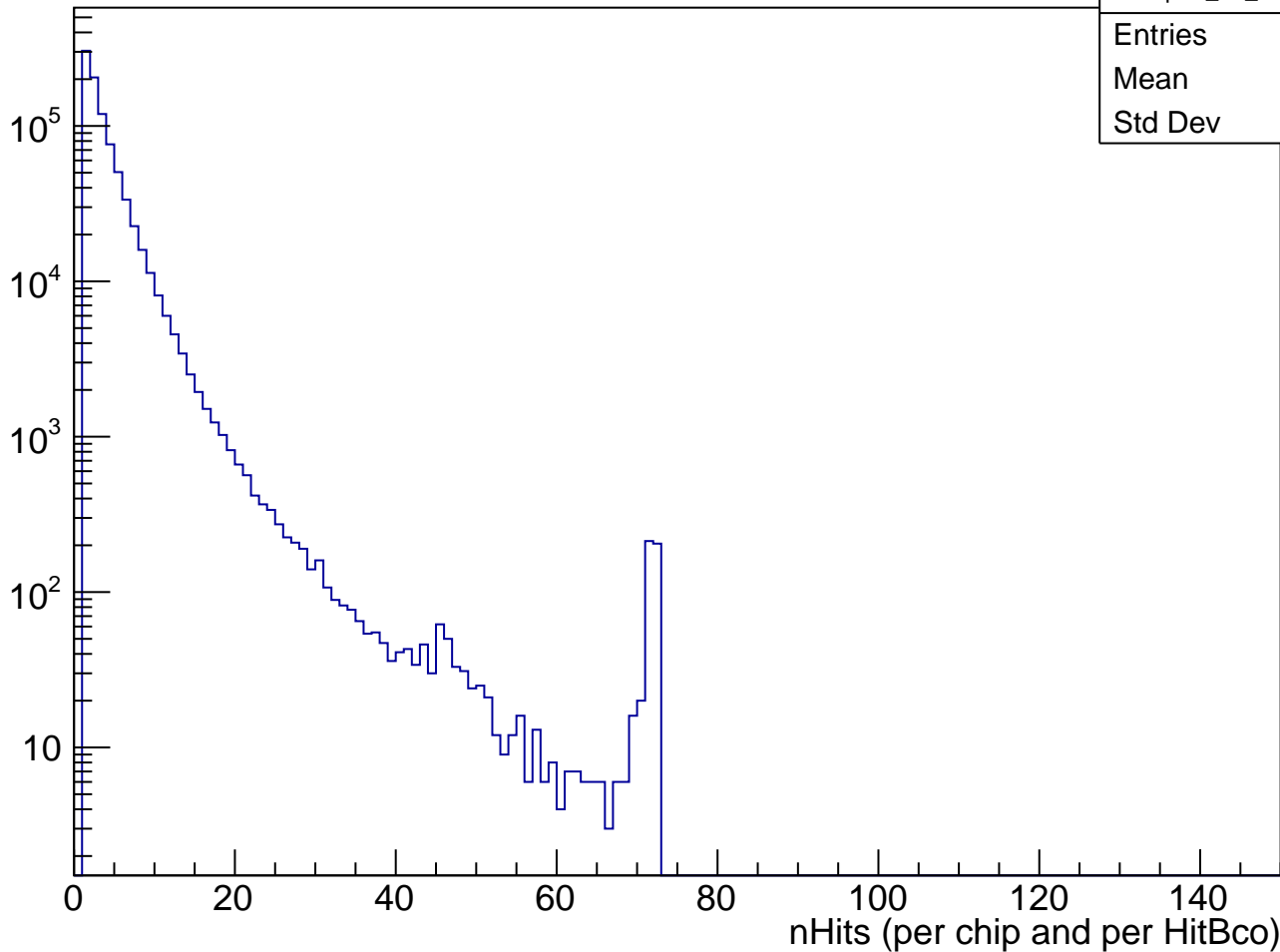
Mean 3.22

Std Dev 3.742

nHits (per chip and per HitBco)

nChipHit_F4_Fch8_Chip7

Entries

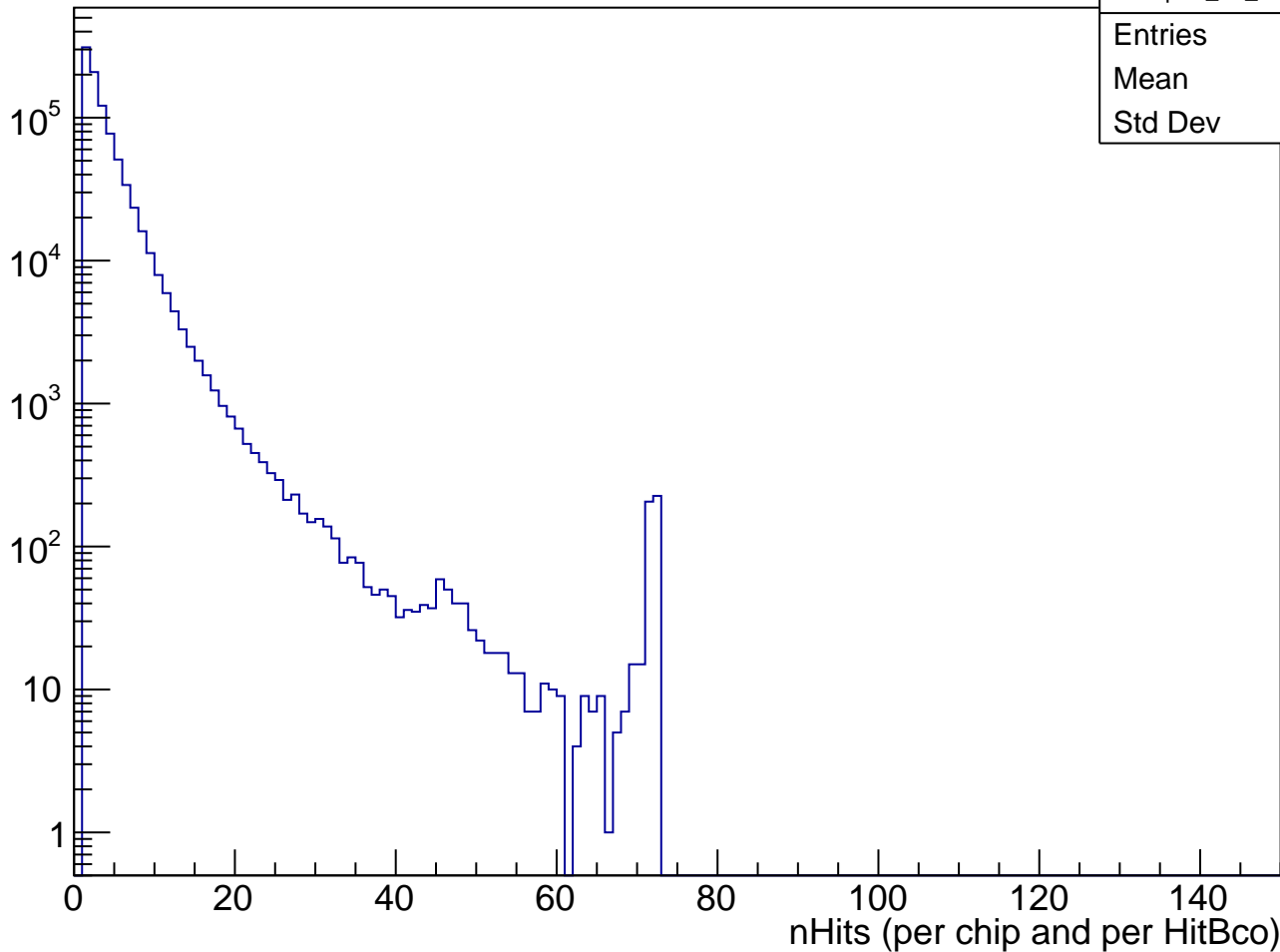


nChipHit_F4_Fch8_Chip7	
Entries	875748
Mean	3.167
Std Dev	3.639

nHits (per chip and per HitBco)

nChipHit_F4_Fch8_Chip8

Entries



nChipHit_F4_Fch8_Chip8

Entries 889167

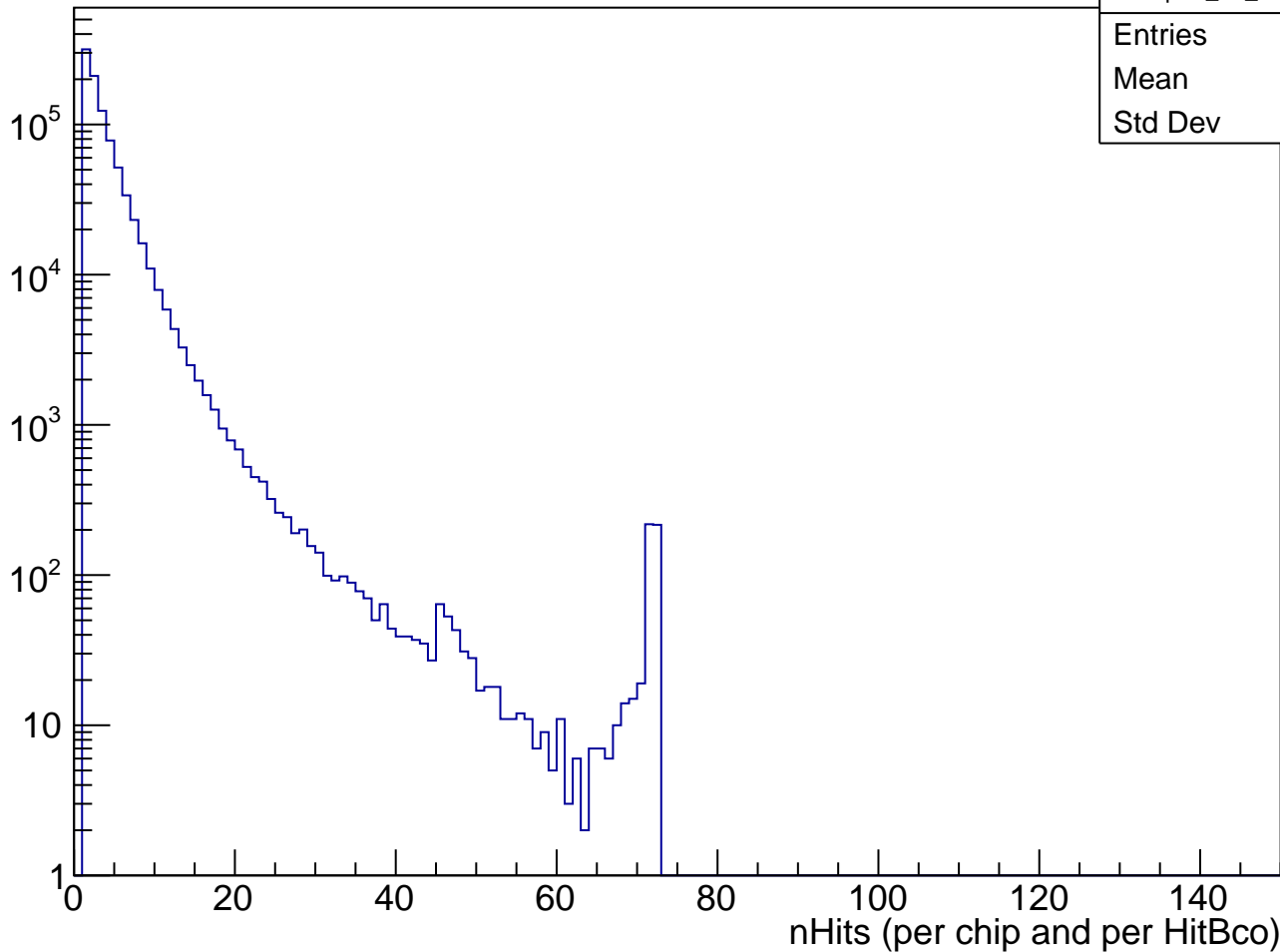
Mean 3.155

Std Dev 3.636

nHits (per chip and per HitBco)

nChipHit_F4_Fch8_Chip9

Entries

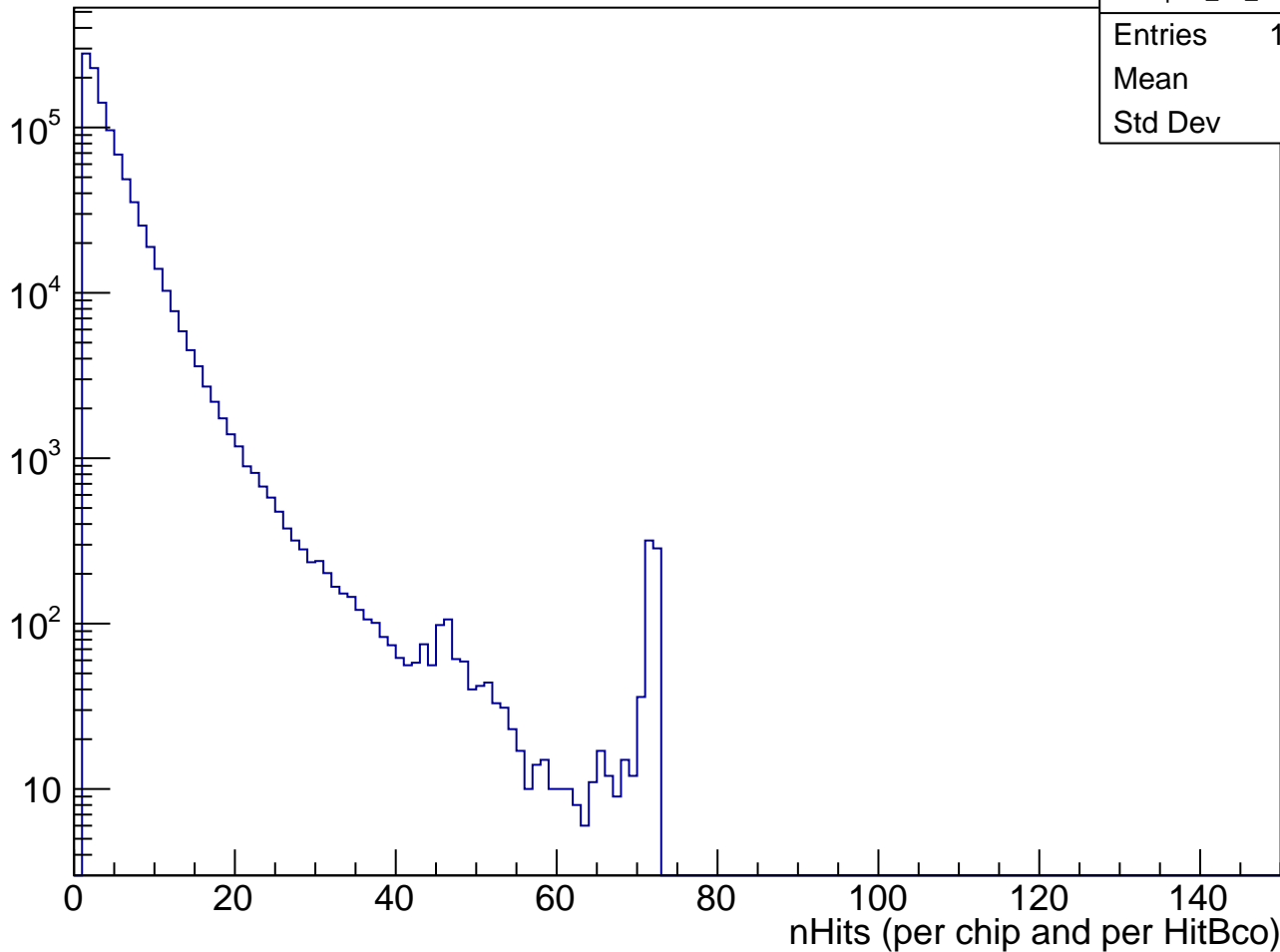


nChipHit_F4_Fch8_Chip9

Entries	900079
Mean	3.136
Std Dev	3.623

nChipHit_F4_Fch9_Chip0

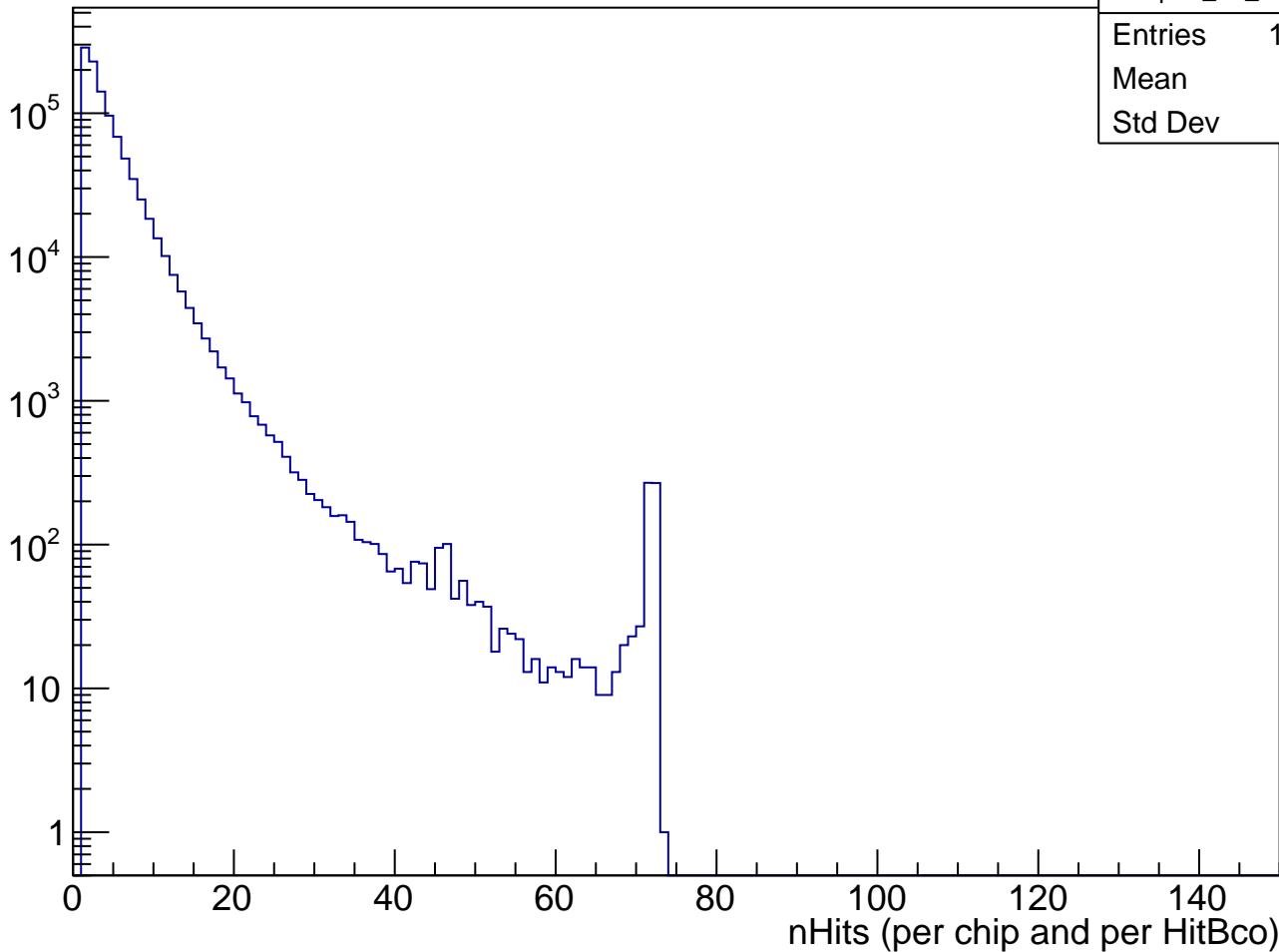
Entries



nChipHit_F4_Fch9_Chip0	
Entries	1005723
Mean	3.734
Std Dev	4.186

nChipHit_F4_Fch9_Chip1

Entries

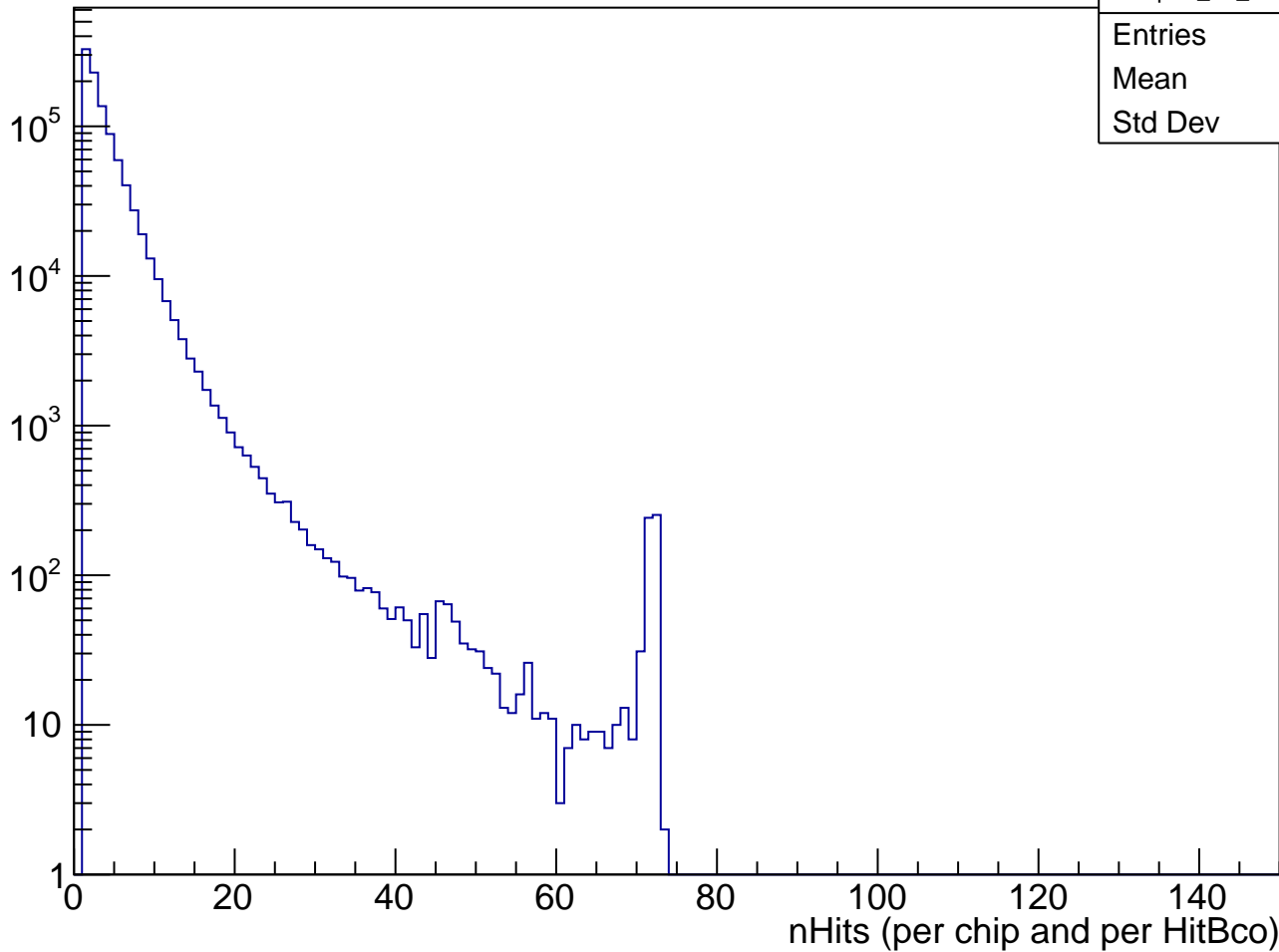


nChipHit_F4_Fch9_Chip1	
Entries	1009803
Mean	3.696
Std Dev	4.133

nHits (per chip and per HitBco)

nChipHit_F4_Fch9_Chip10

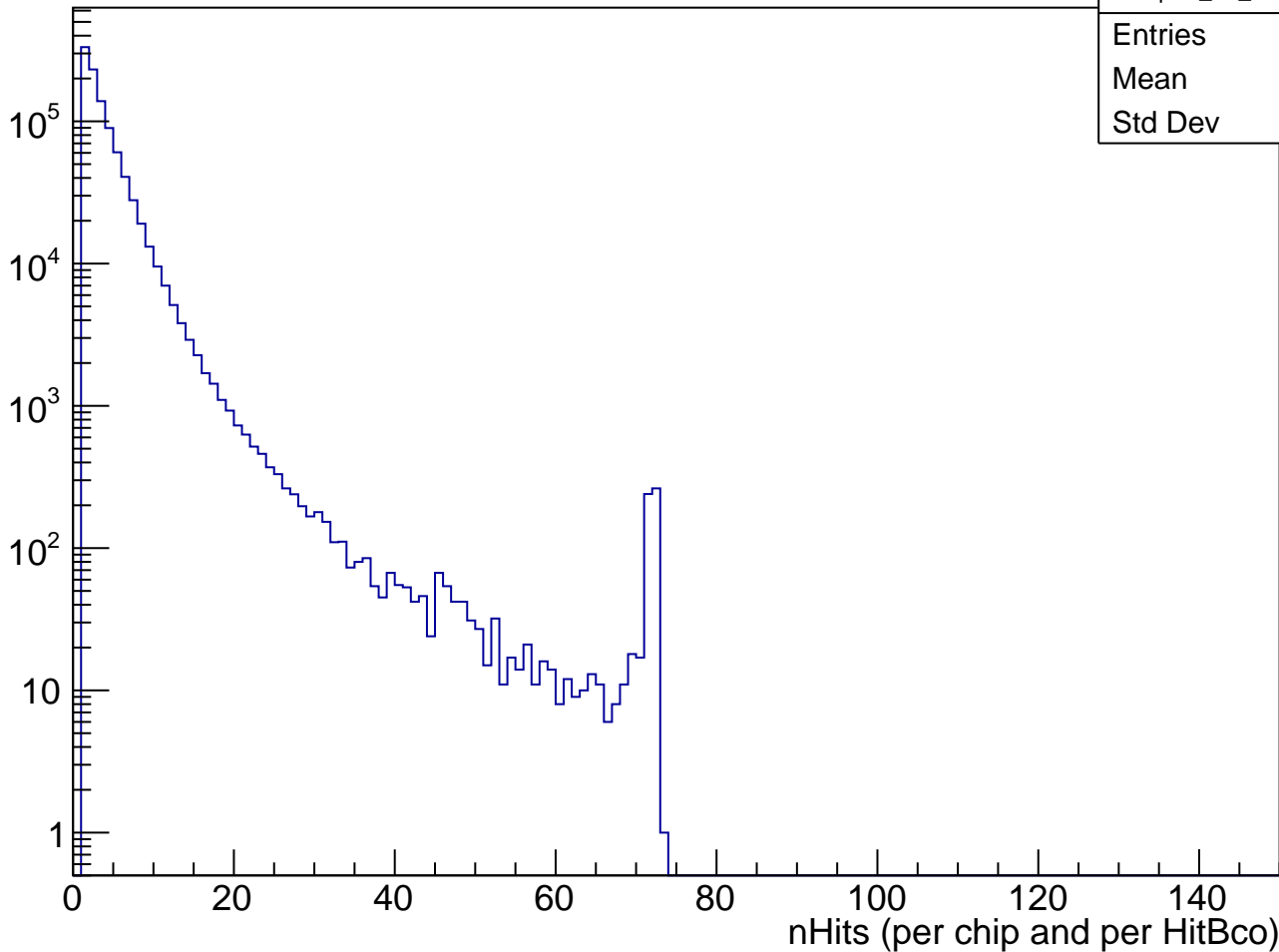
Entries



nChipHit_F4_Fch9_Chip10	
Entries	982392
Mean	3.245
Std Dev	3.711

nChipHit_F4_Fch9_Chip11

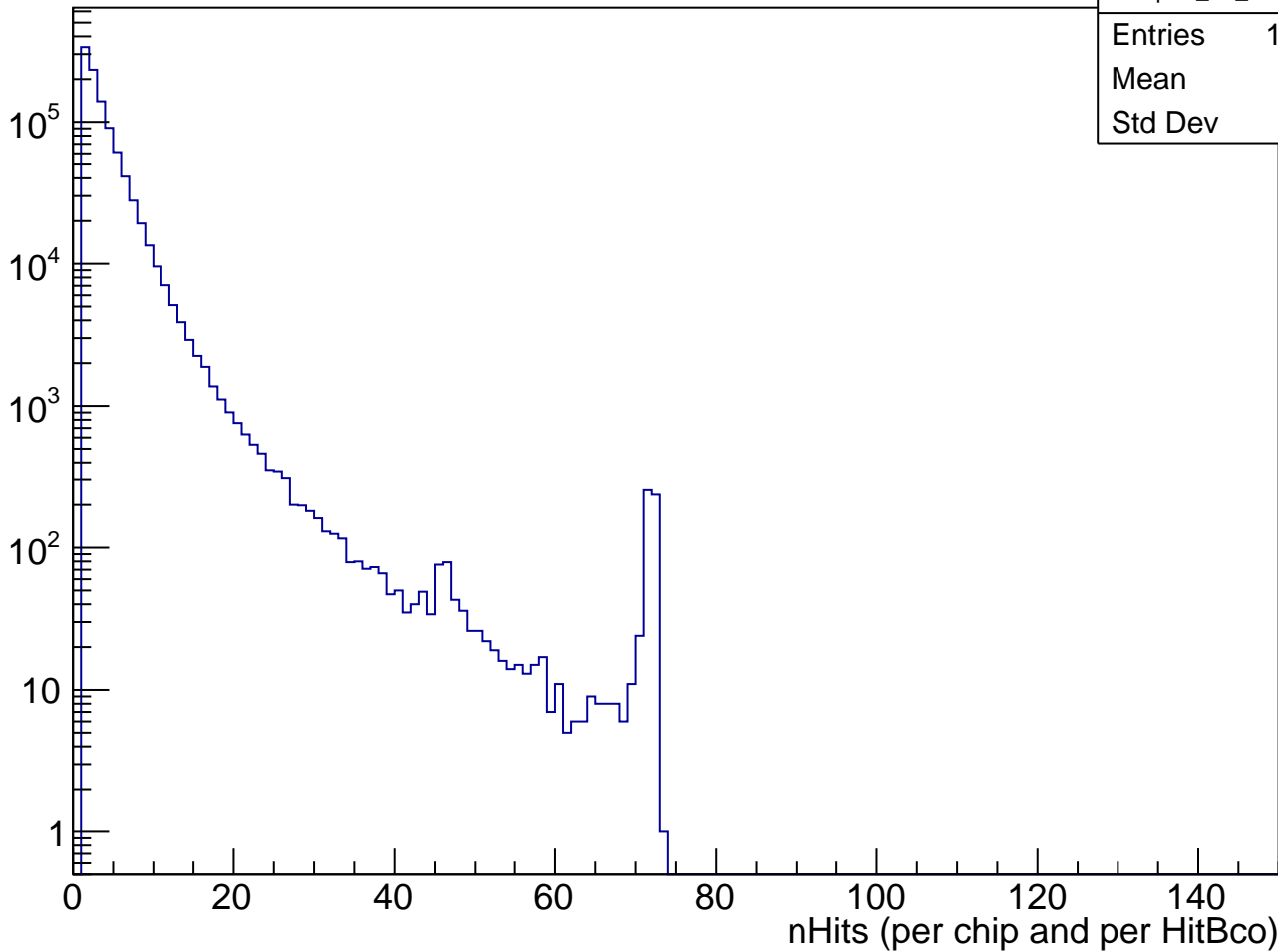
Entries



nChipHit_F4_Fch9_Chip11	
Entries	995532
Mean	3.241
Std Dev	3.702

nChipHit_F4_Fch9_Chip12

Entries

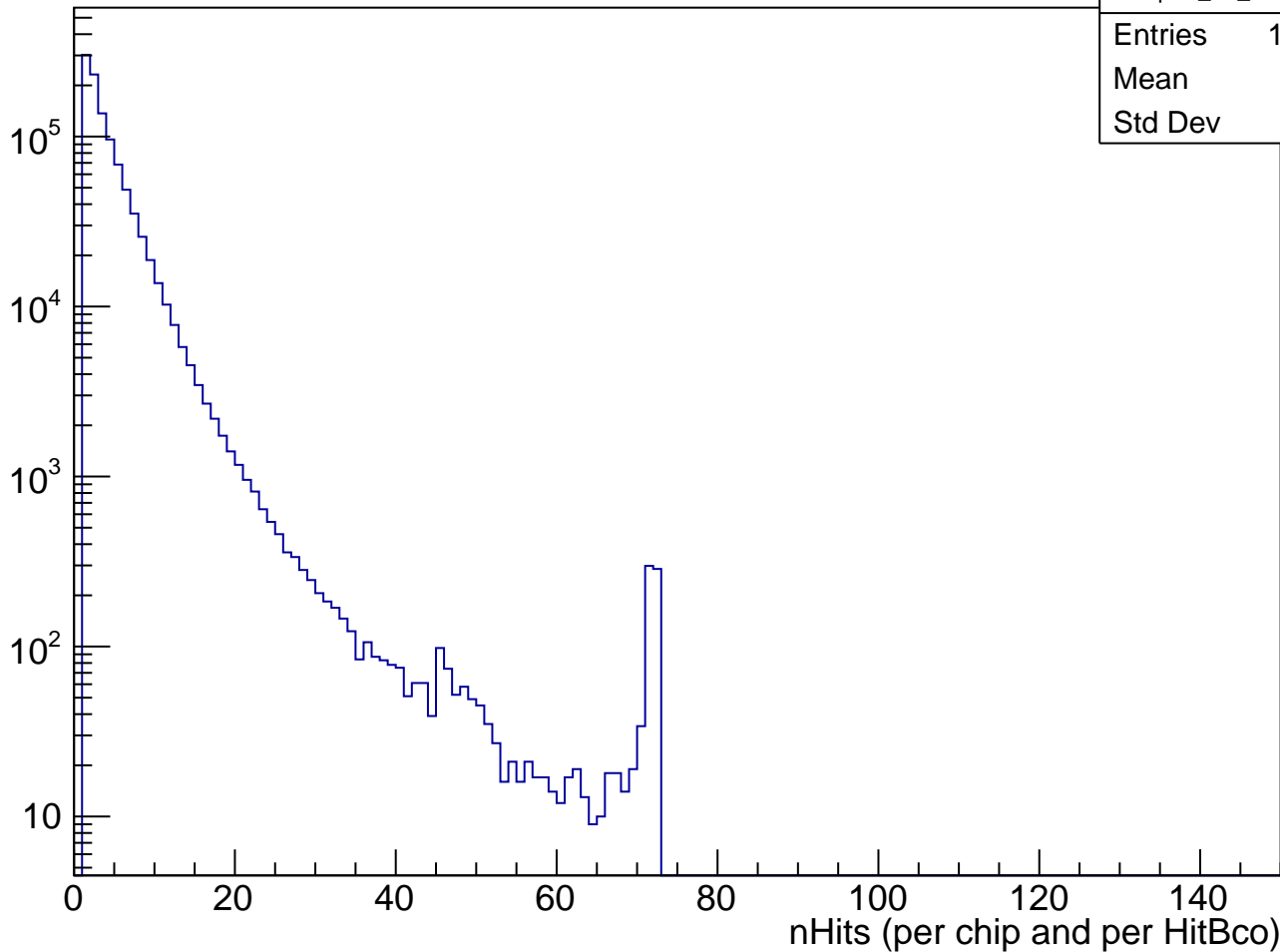


nChipHit_F4_Fch9_Chip12	
Entries	1004302
Mean	3.238
Std Dev	3.678

nHits (per chip and per HitBco)

nChipHit_F4_Fch9_Chip13

Entries

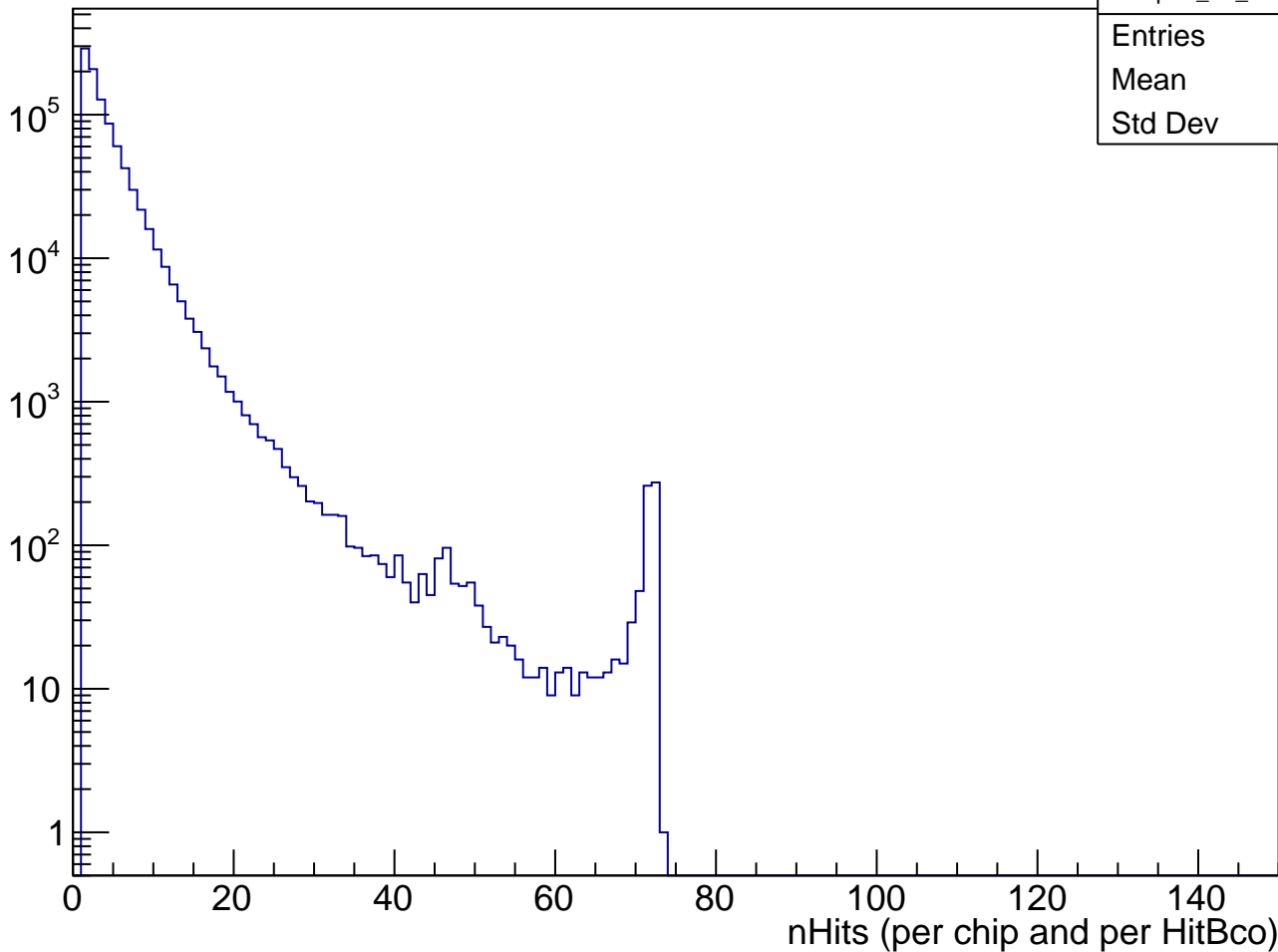


nChipHit_F4_Fch9_Chip13	
Entries	1026325
Mean	3.662
Std Dev	4.139

nHits (per chip and per HitBco)

nChipHit_F4_Fch9_Chip14

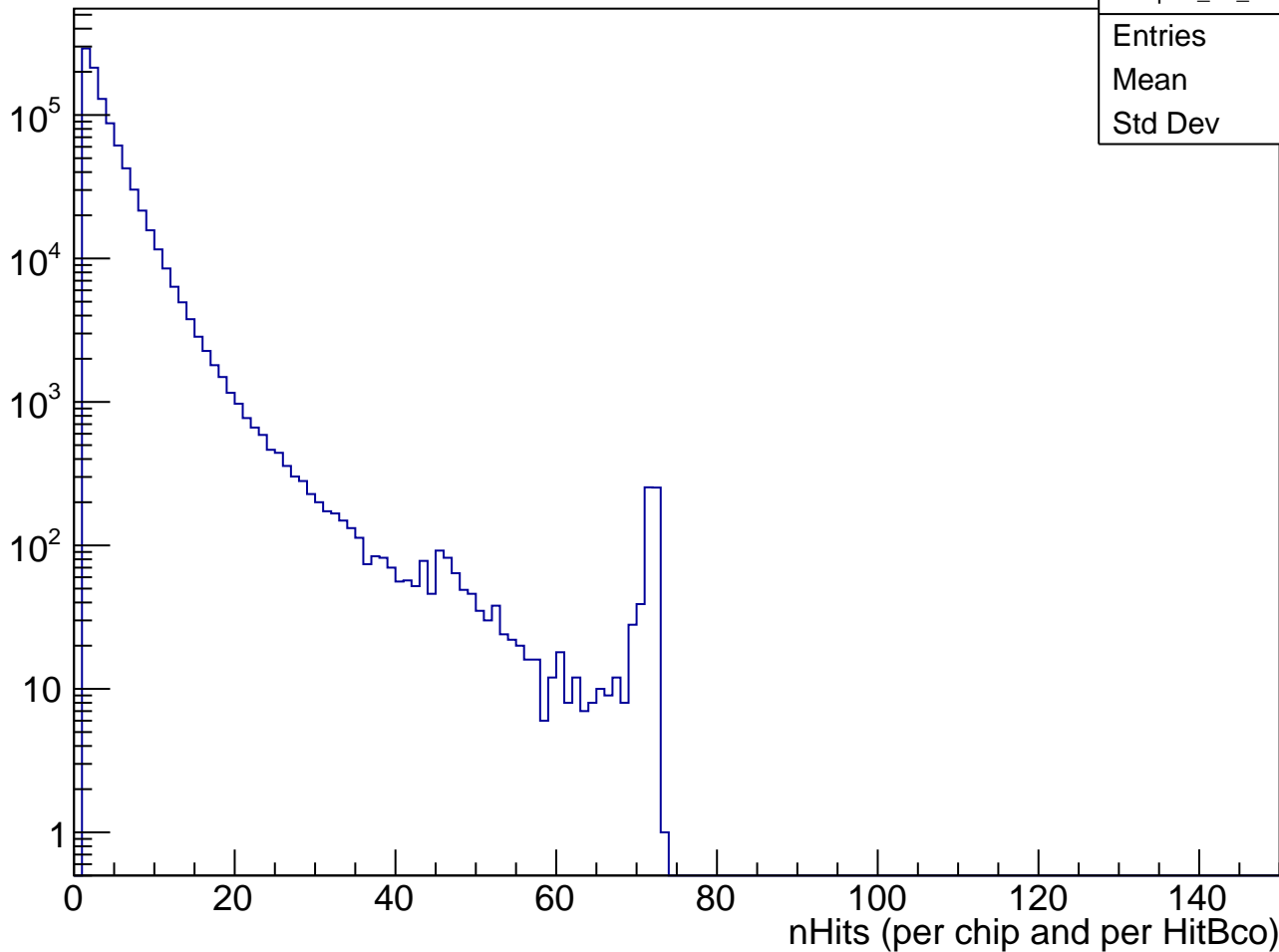
Entries



nChipHit_F4_Fch9_Chip14	
Entries	934987
Mean	3.569
Std Dev	4.126

nChipHit_F4_Fch9_Chip15

Entries

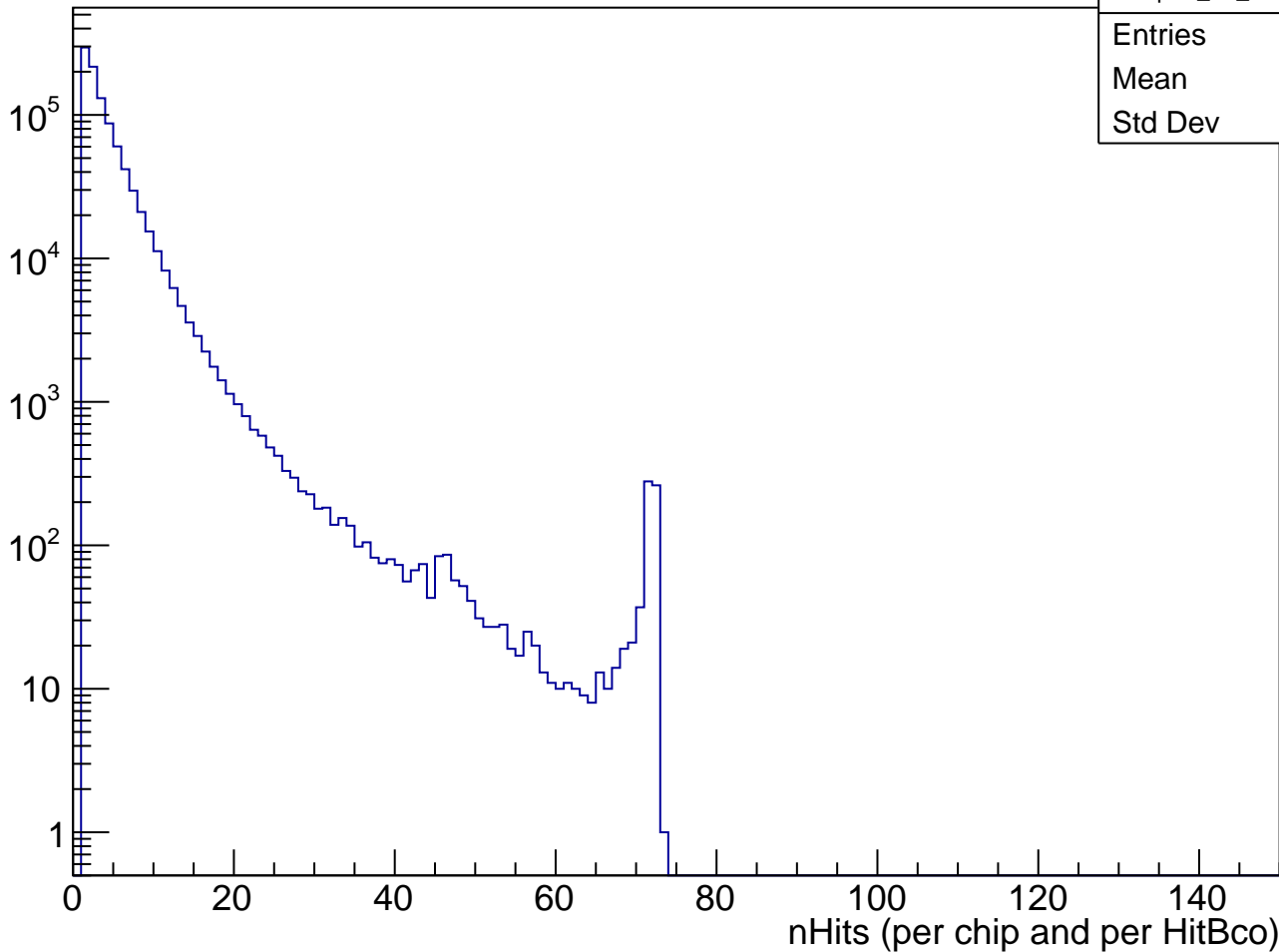


nChipHit_F4_Fch9_Chip15	
Entries	945626
Mean	3.544
Std Dev	4.074

nHits (per chip and per HitBco)

nChipHit_F4_Fch9_Chip16

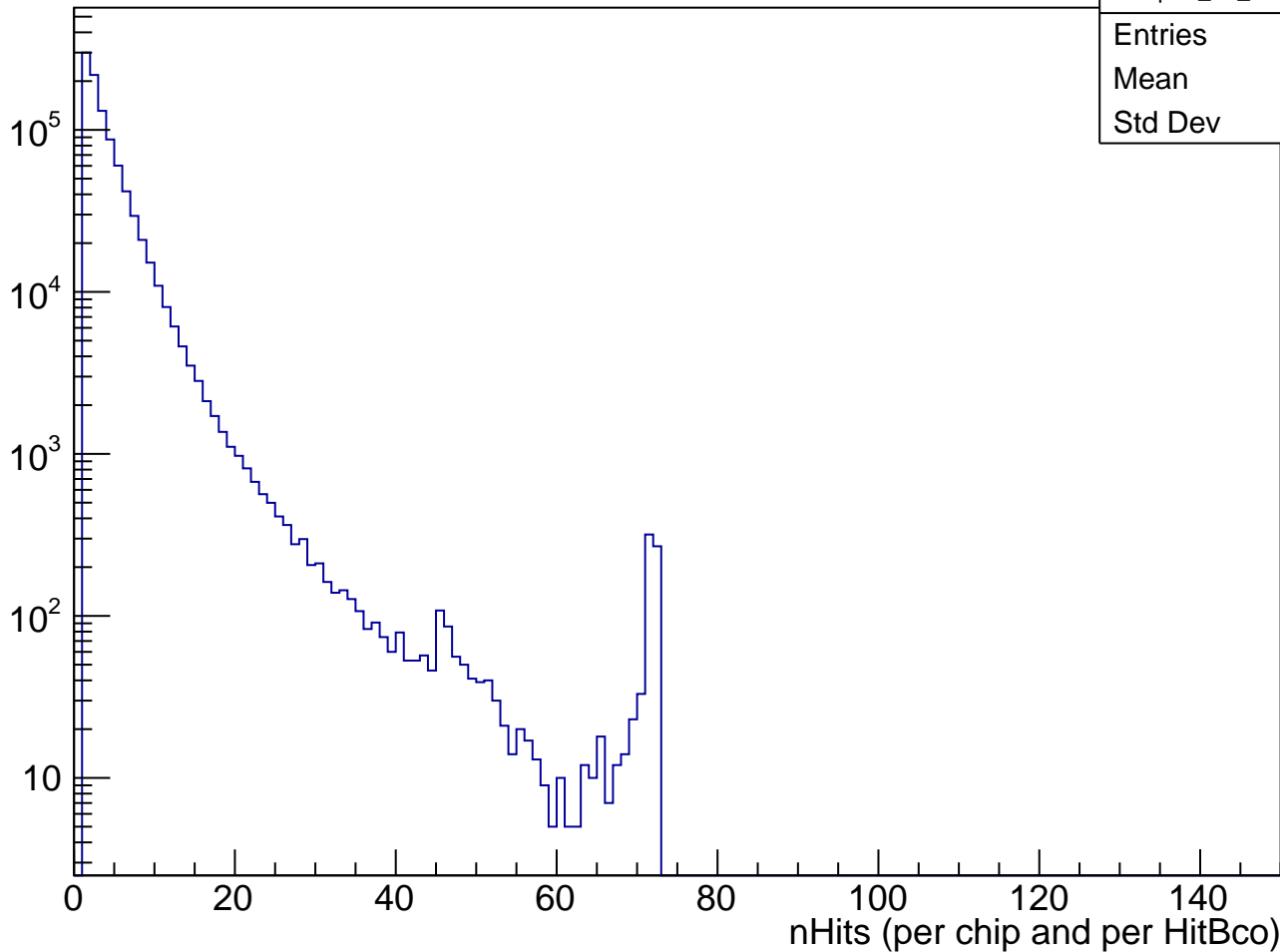
Entries



nChipHit_F4_Fch9_Chip16	
Entries	949534
Mean	3.505
Std Dev	4.068

nChipHit_F4_Fch9_Chip17

Entries

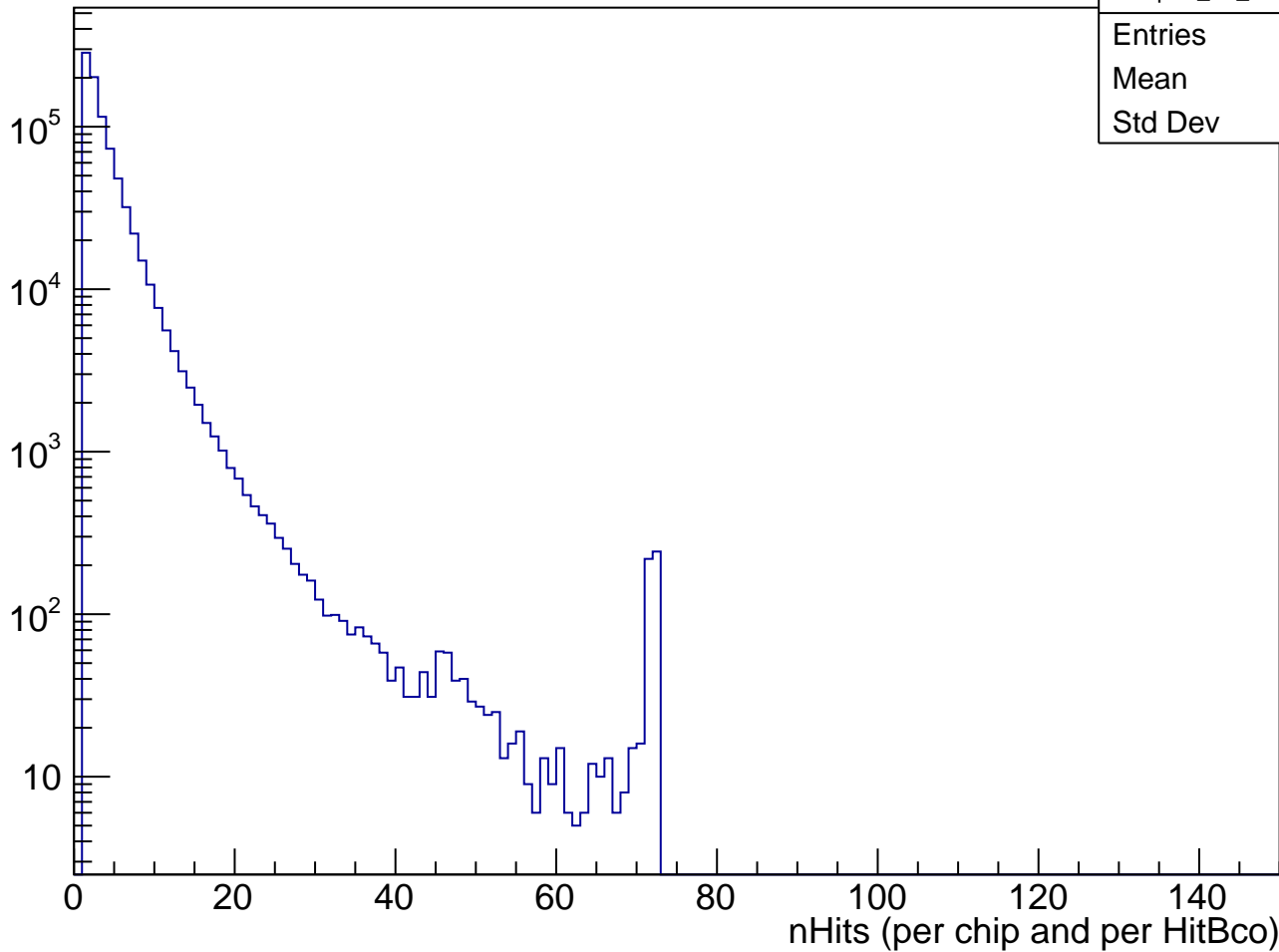


nChipHit_F4_Fch9_Chip17	
Entries	954274
Mean	3.479
Std Dev	4.063

nHits (per chip and per HitBco)

nChipHit_F4_Fch9_Chip18

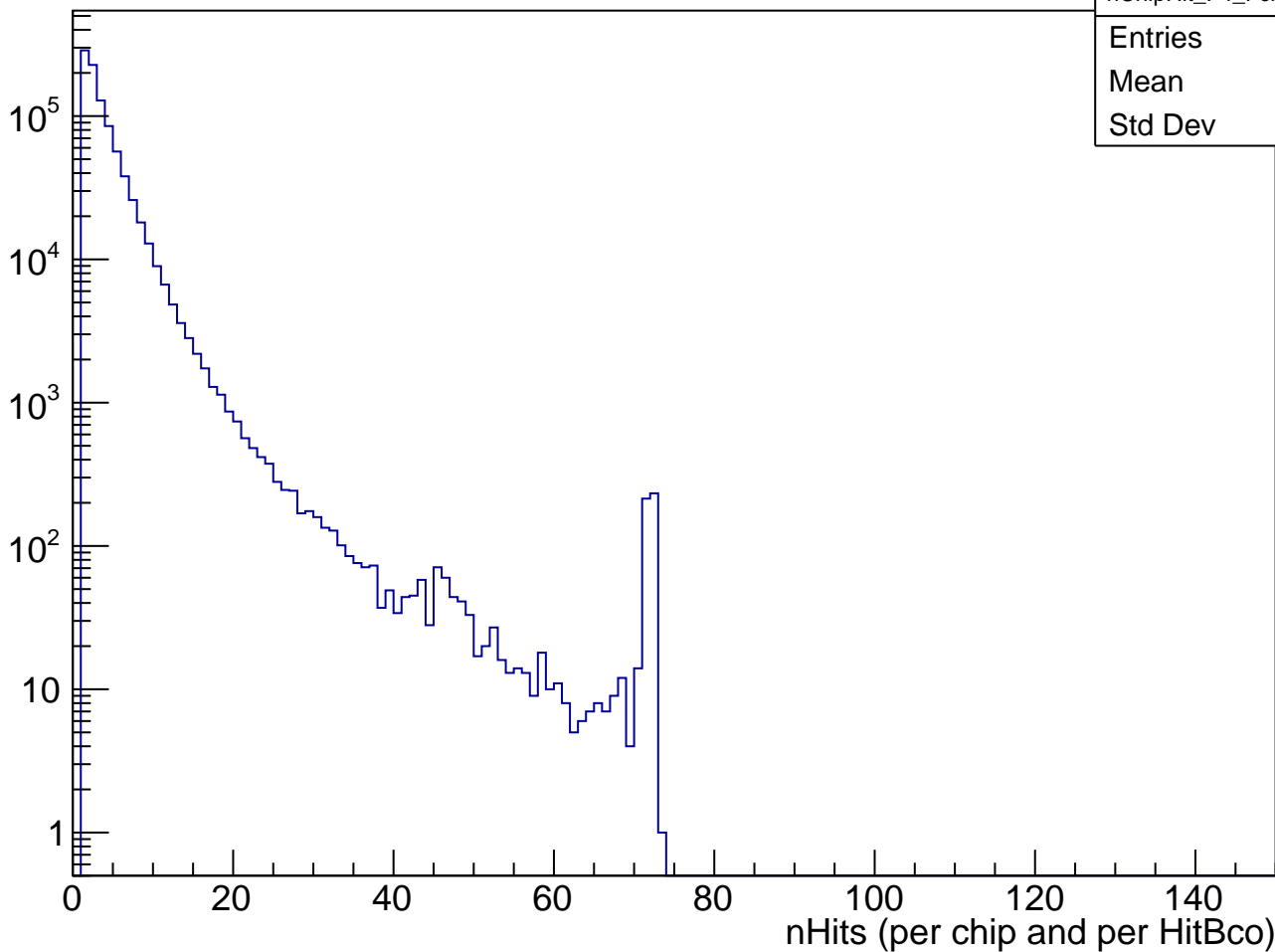
Entries



nChipHit_F4_Fch9_Chip18	
Entries	838363
Mean	3.194
Std Dev	3.757

nChipHit_F4_Fch9_Chip19

Entries

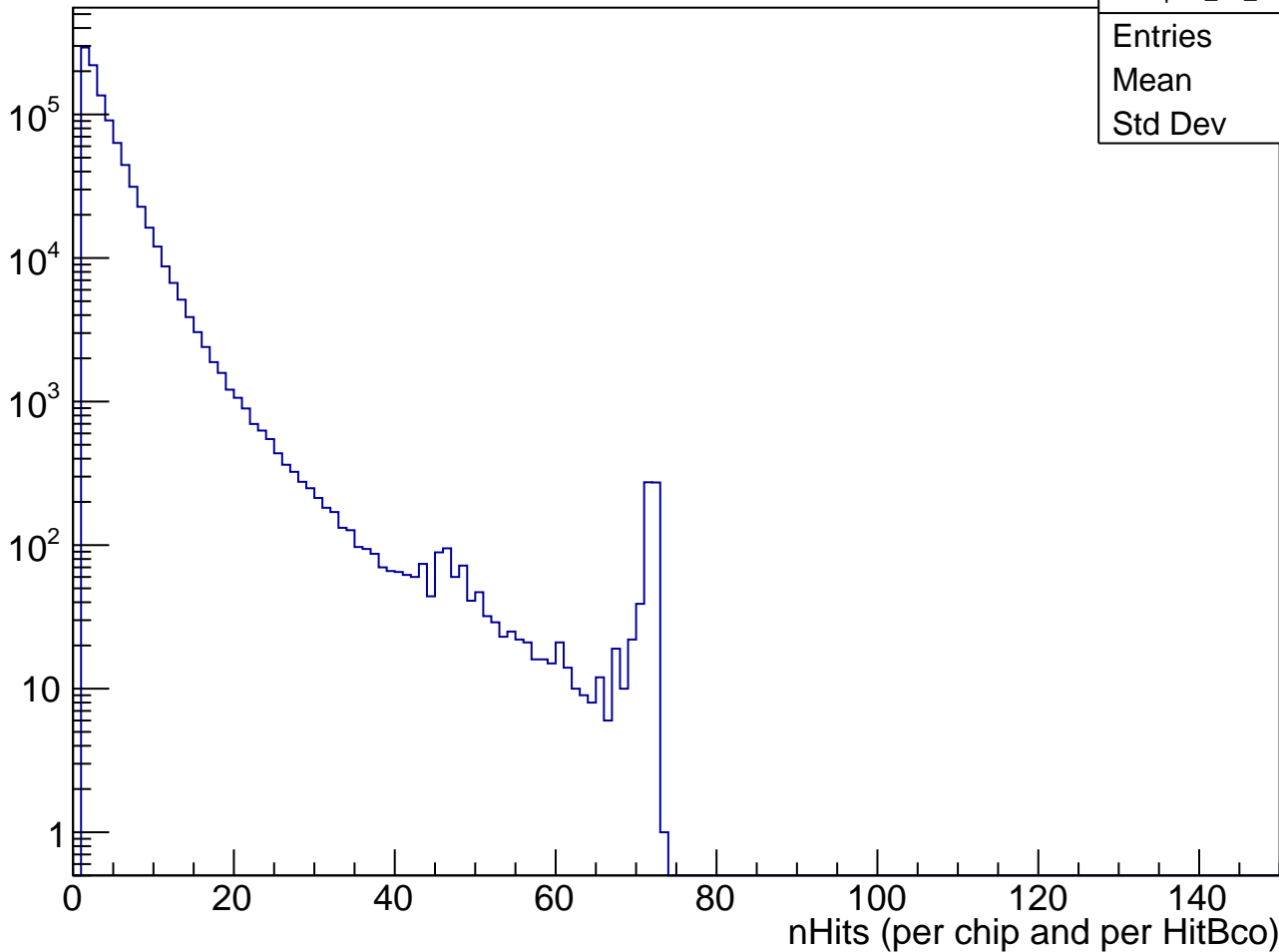


nChipHit_F4_Fch9_Chip19	
Entries	919846
Mean	3.296
Std Dev	3.704

nHits (per chip and per HitBco)

nChipHit_F4_Fch9_Chip2

Entries



nChipHit_F4_Fch9_Chip2

Entries 972511

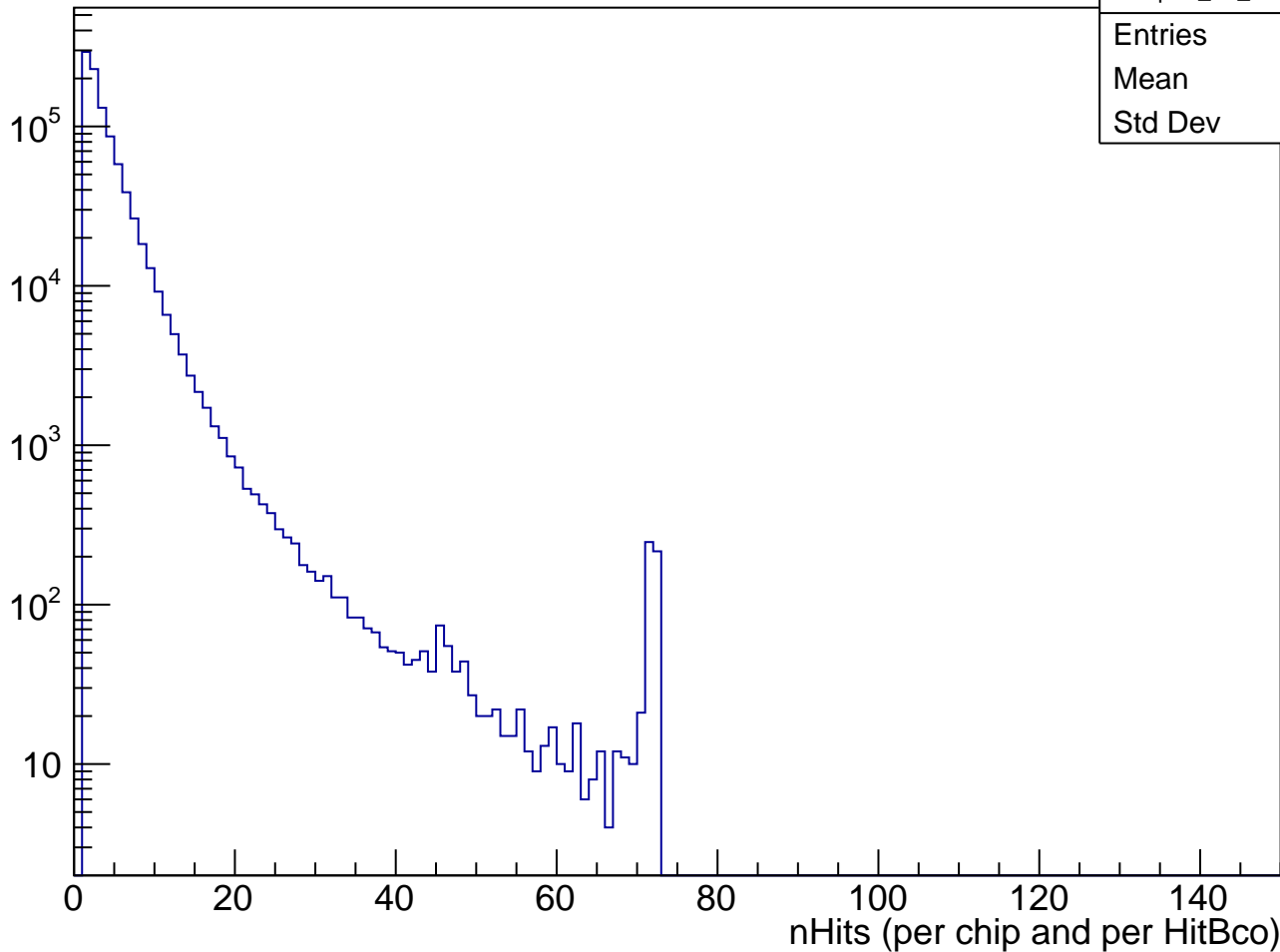
Mean 3.58

Std Dev 4.114

nHits (per chip and per HitBco)

nChipHit_F4_Fch9_Chip20

Entries

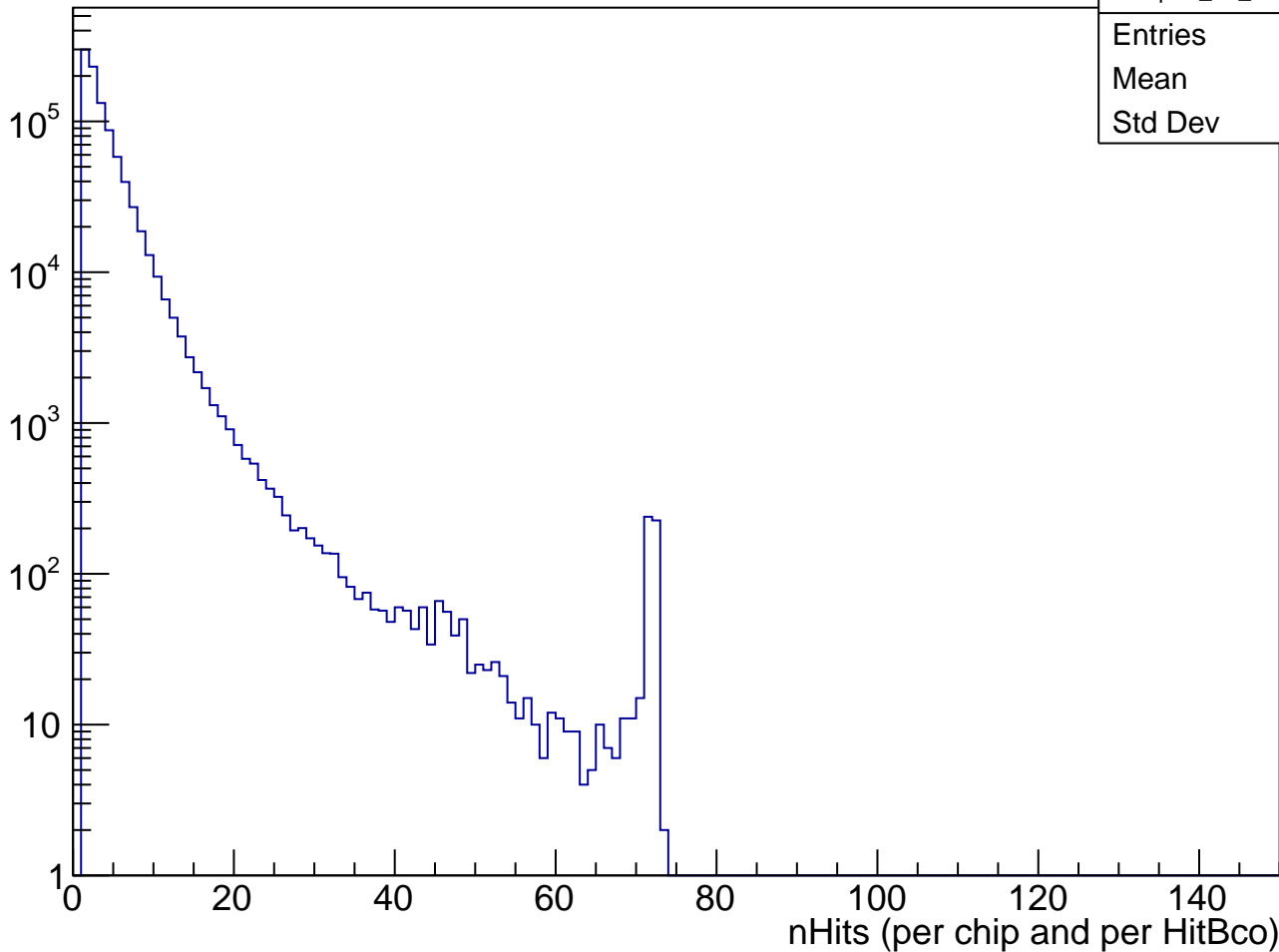


nChipHit_F4_Fch9_Chip20	
Entries	934695
Mean	3.292
Std Dev	3.717

nHits (per chip and per HitBco)

nChipHit_F4_Fch9_Chip21

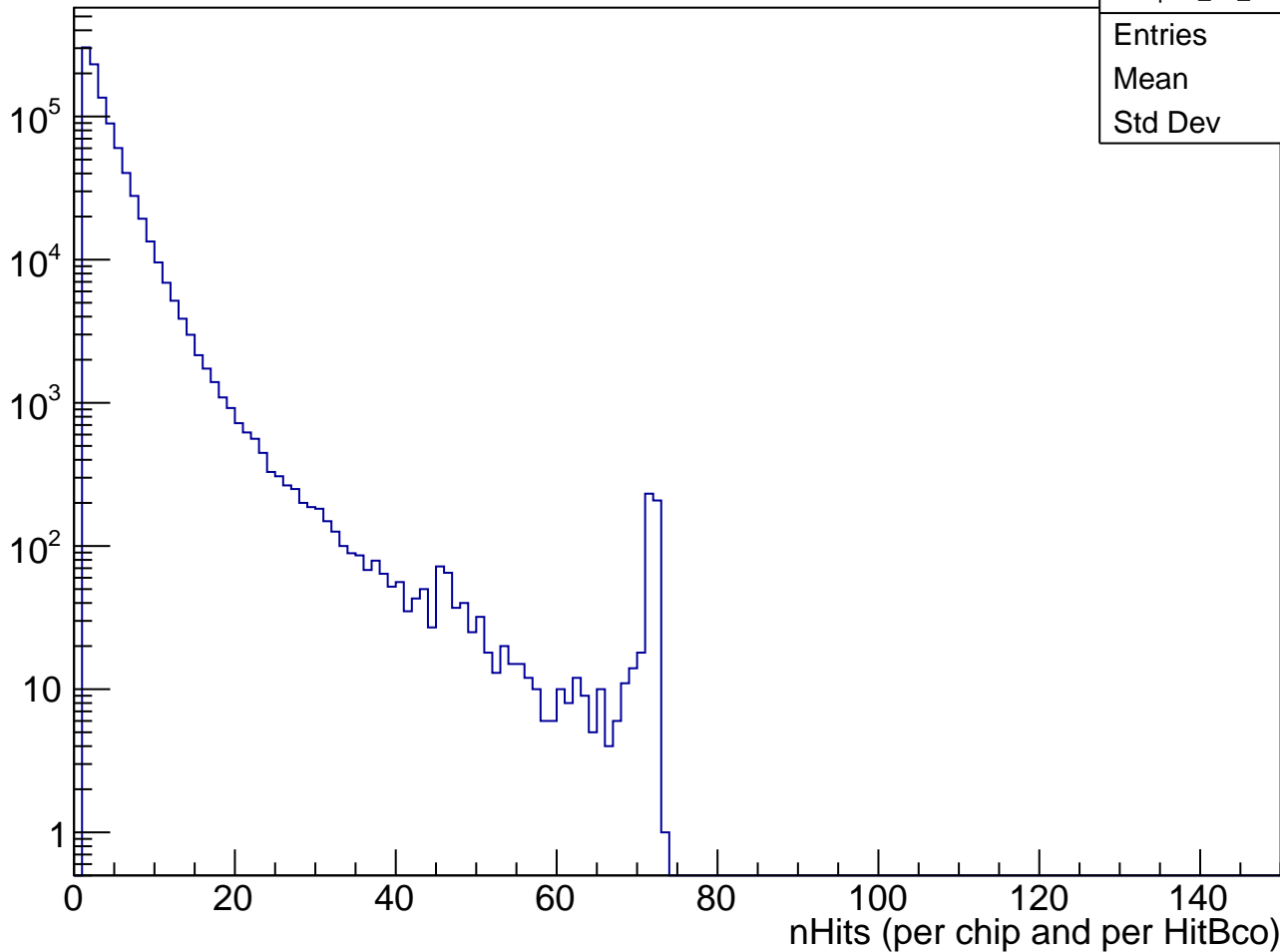
Entries



nChipHit_F4_Fch9_Chip21	
Entries	946946
Mean	3.285
Std Dev	3.698

nChipHit_F4_Fch9_Chip22

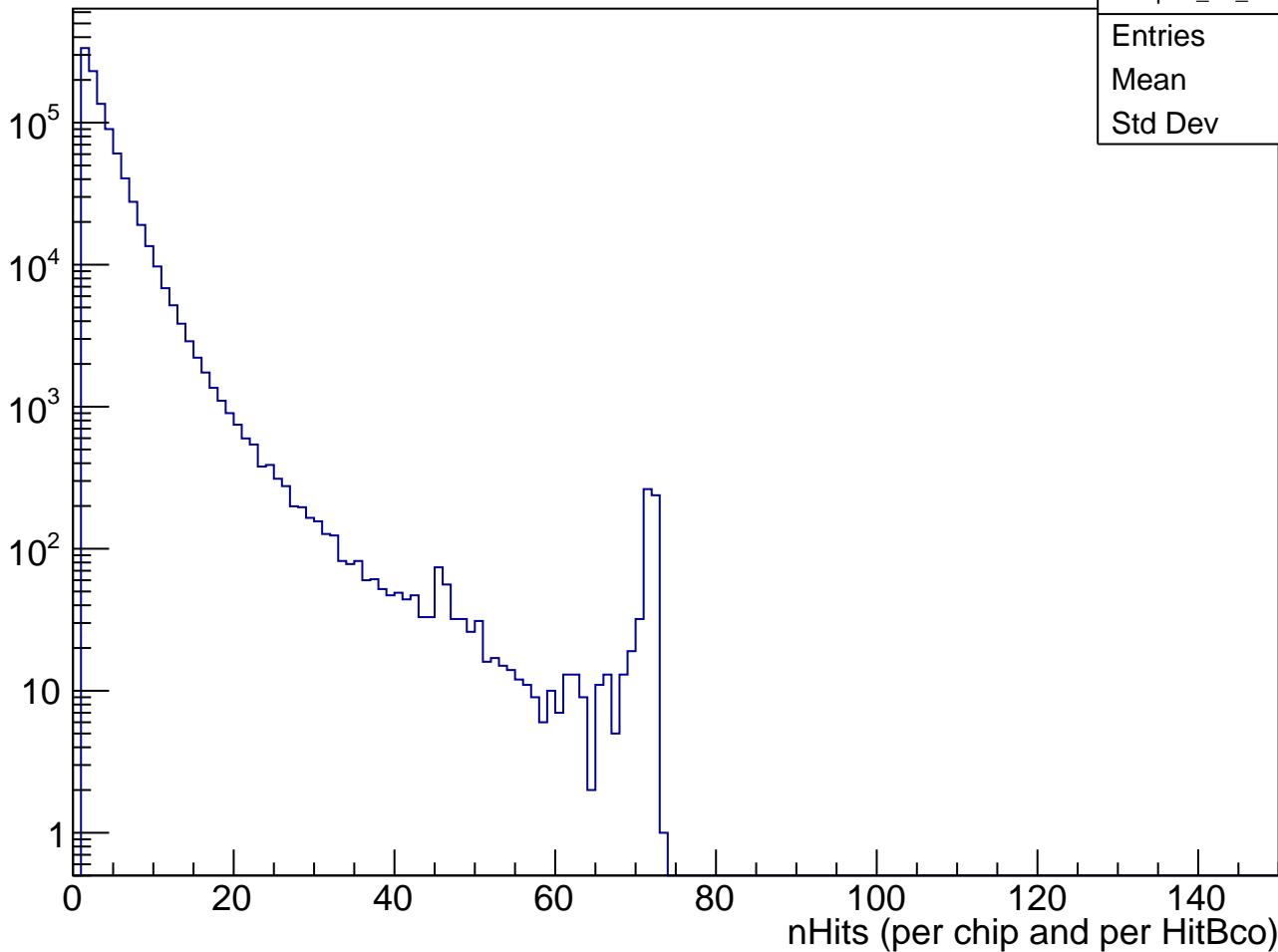
Entries



nChipHit_F4_Fch9_Chip22	
Entries	963100
Mean	3.302
Std Dev	3.685

nChipHit_F4_Fch9_Chip23

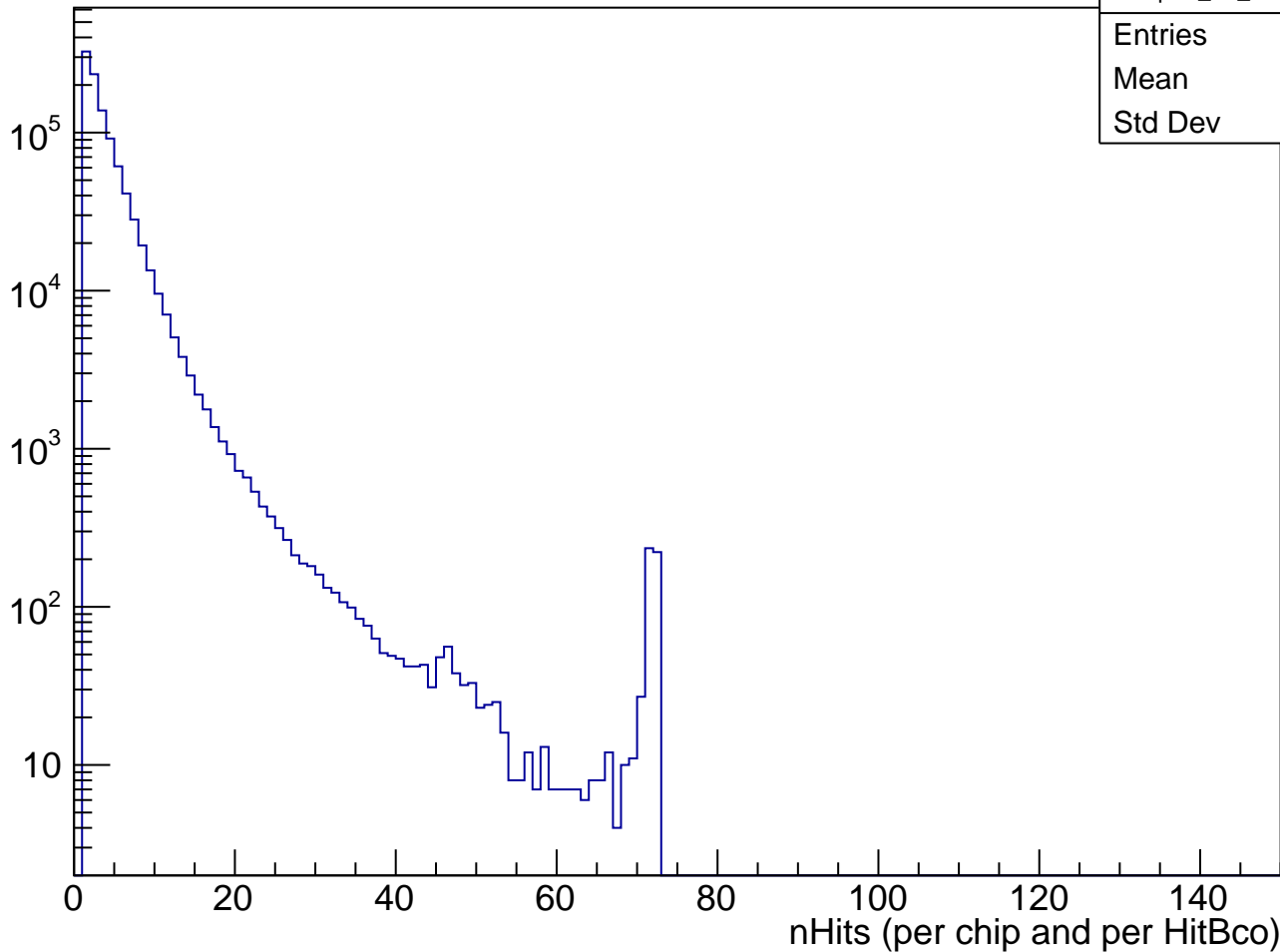
Entries



nChipHit_F4_Fch9_Chip23	
Entries	994985
Mean	3.23
Std Dev	3.672

nChipHit_F4_Fch9_Chip24

Entries

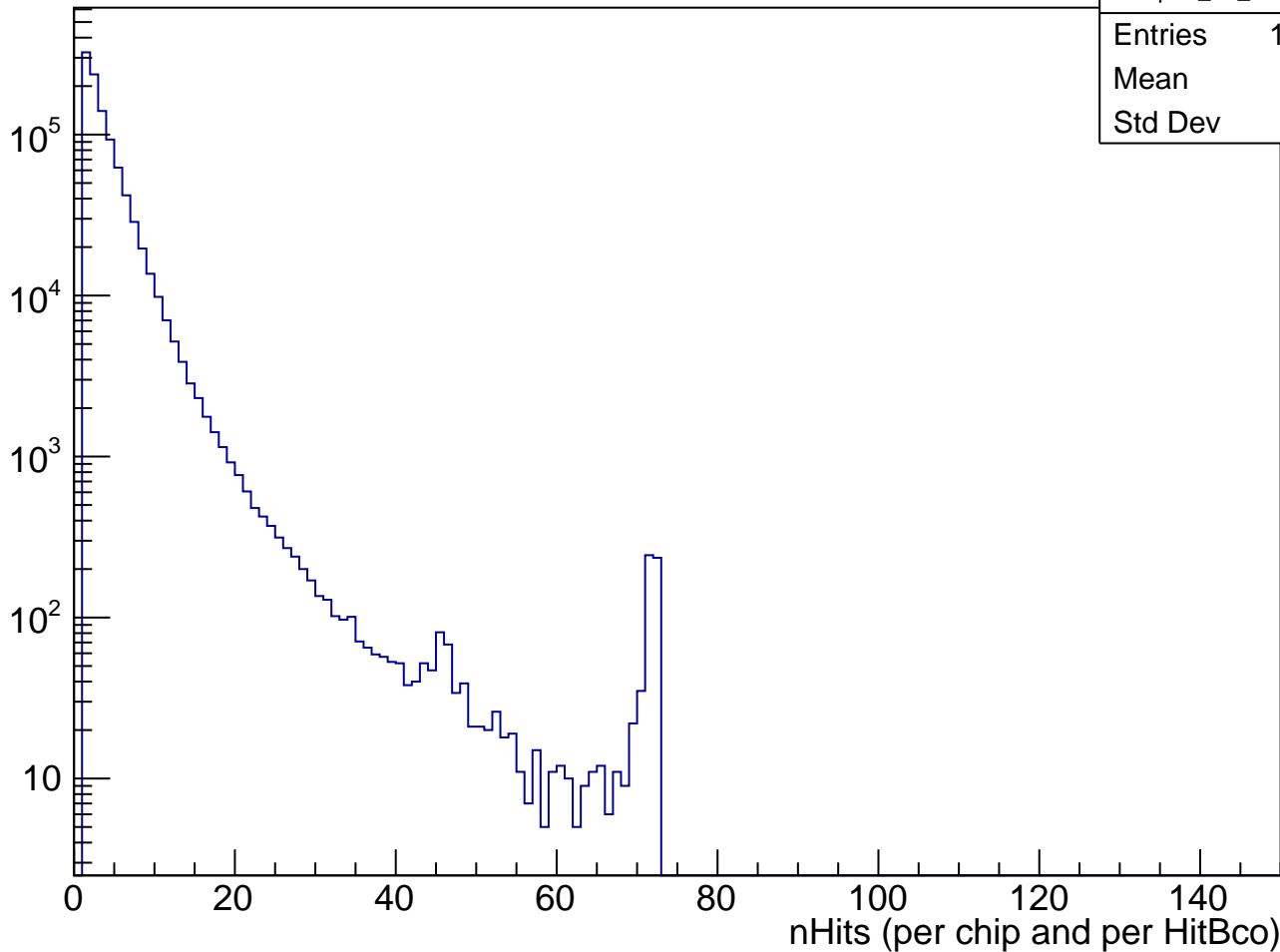


nChipHit_F4_Fch9_Chip24	
Entries	994627
Mean	3.252
Std Dev	3.638

nHits (per chip and per HitBco)

nChipHit_F4_Fch9_Chip25

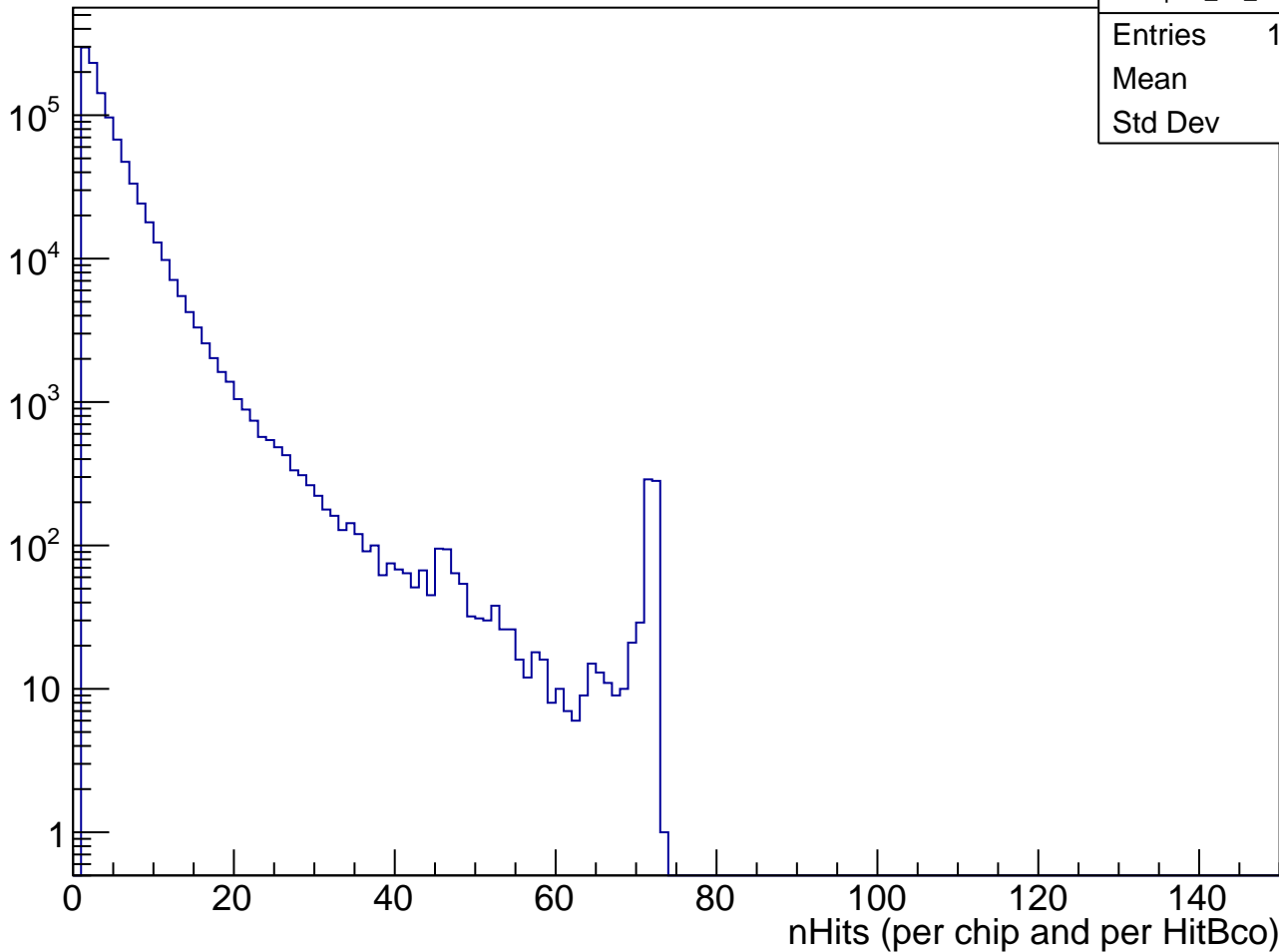
Entries



nChipHit_F4_Fch9_Chip25	
Entries	1002510
Mean	3.27
Std Dev	3.675

nChipHit_F4_Fch9_Chip3

Entries



nChipHit_F4_Fch9_Chip3

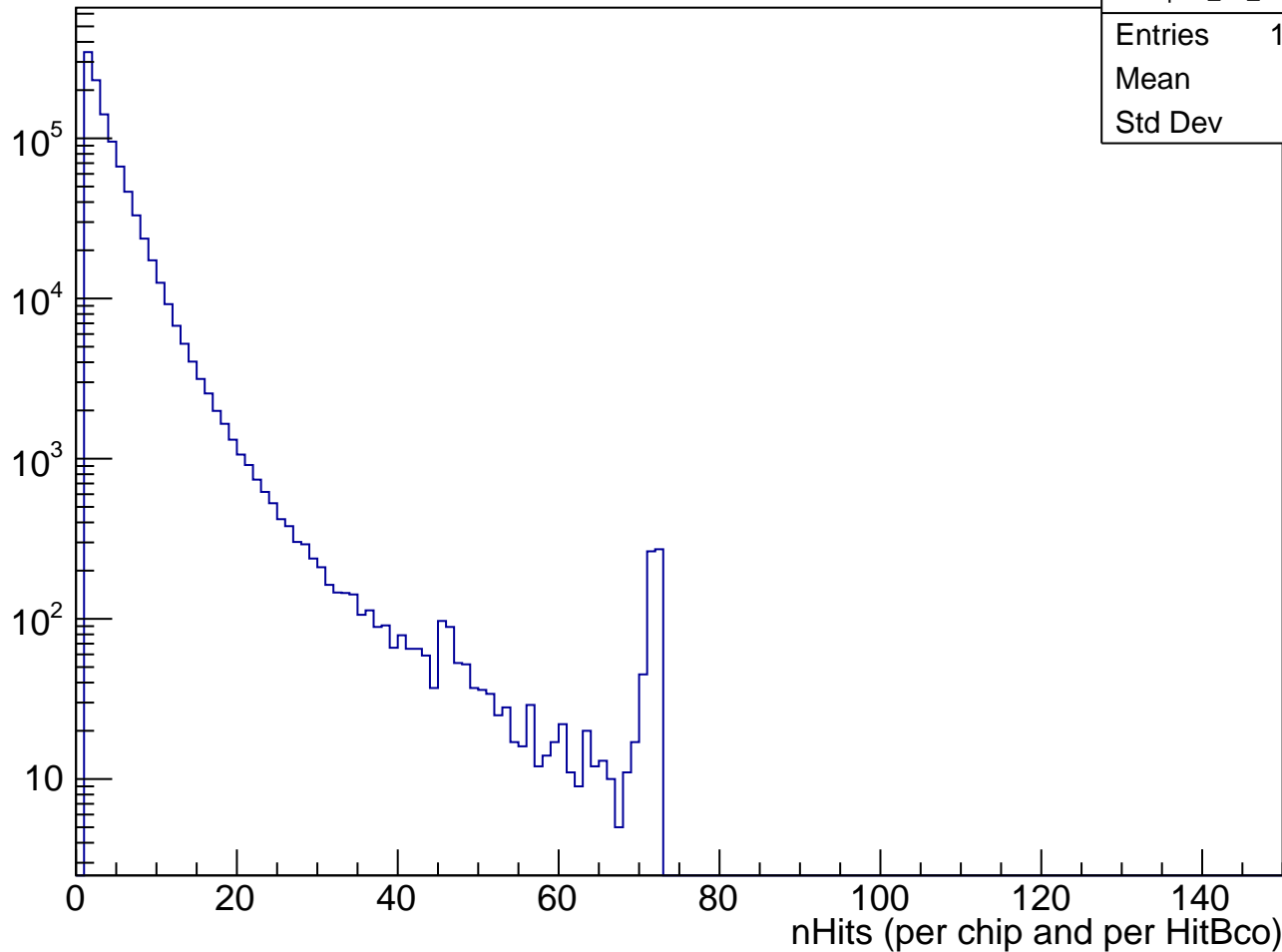
Entries 1016684

Mean 3.62

Std Dev 4.086

nChipHit_F4_Fch9_Chip4

Entries



nChipHit_F4_Fch9_Chip4

Entries 1056429

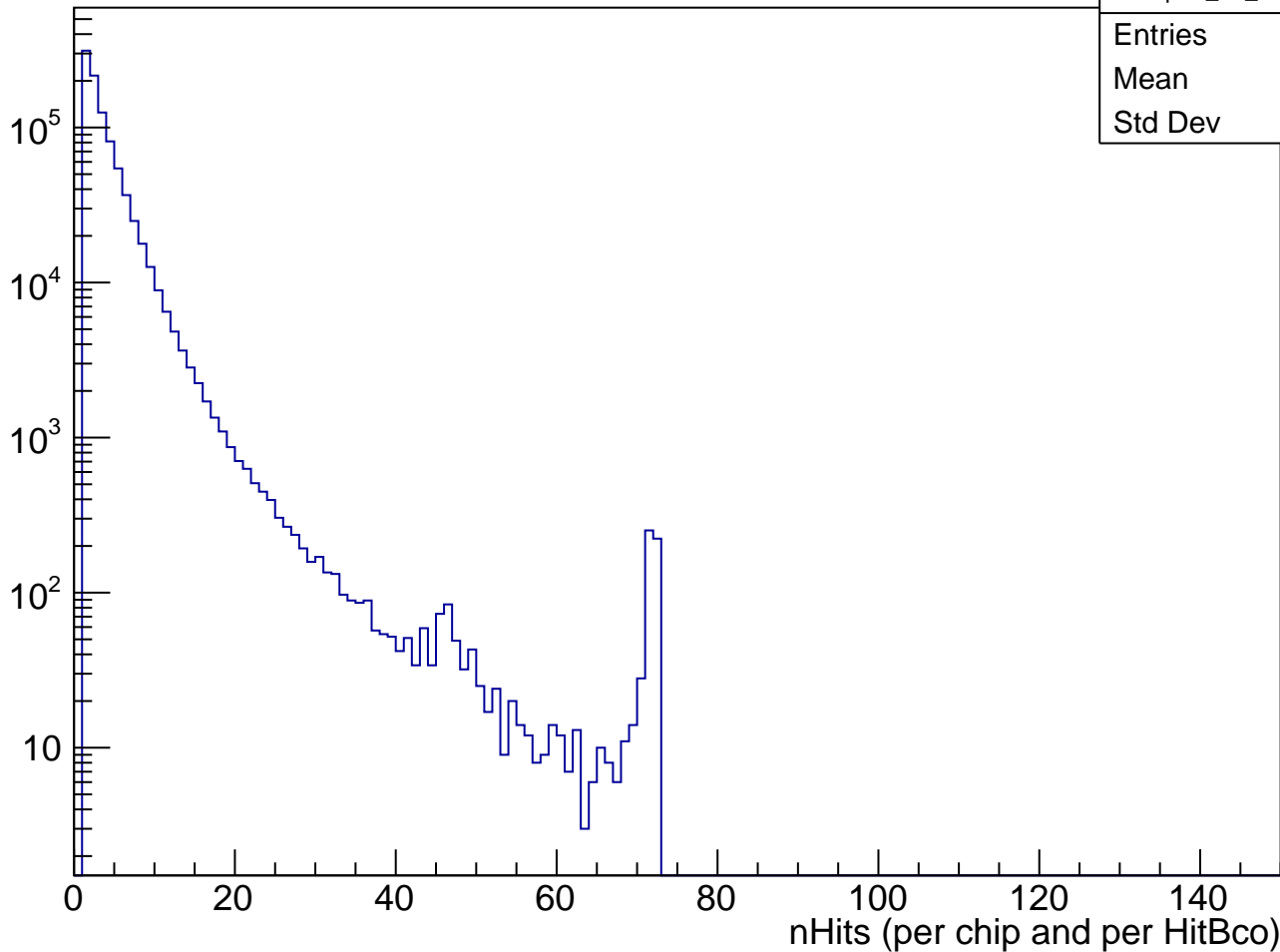
Mean 3.472

Std Dev 4.01

nHits (per chip and per HitBco)

nChipHit_F4_Fch9_Chip5

Entries



nChipHit_F4_Fch9_Chip5

Entries 921756

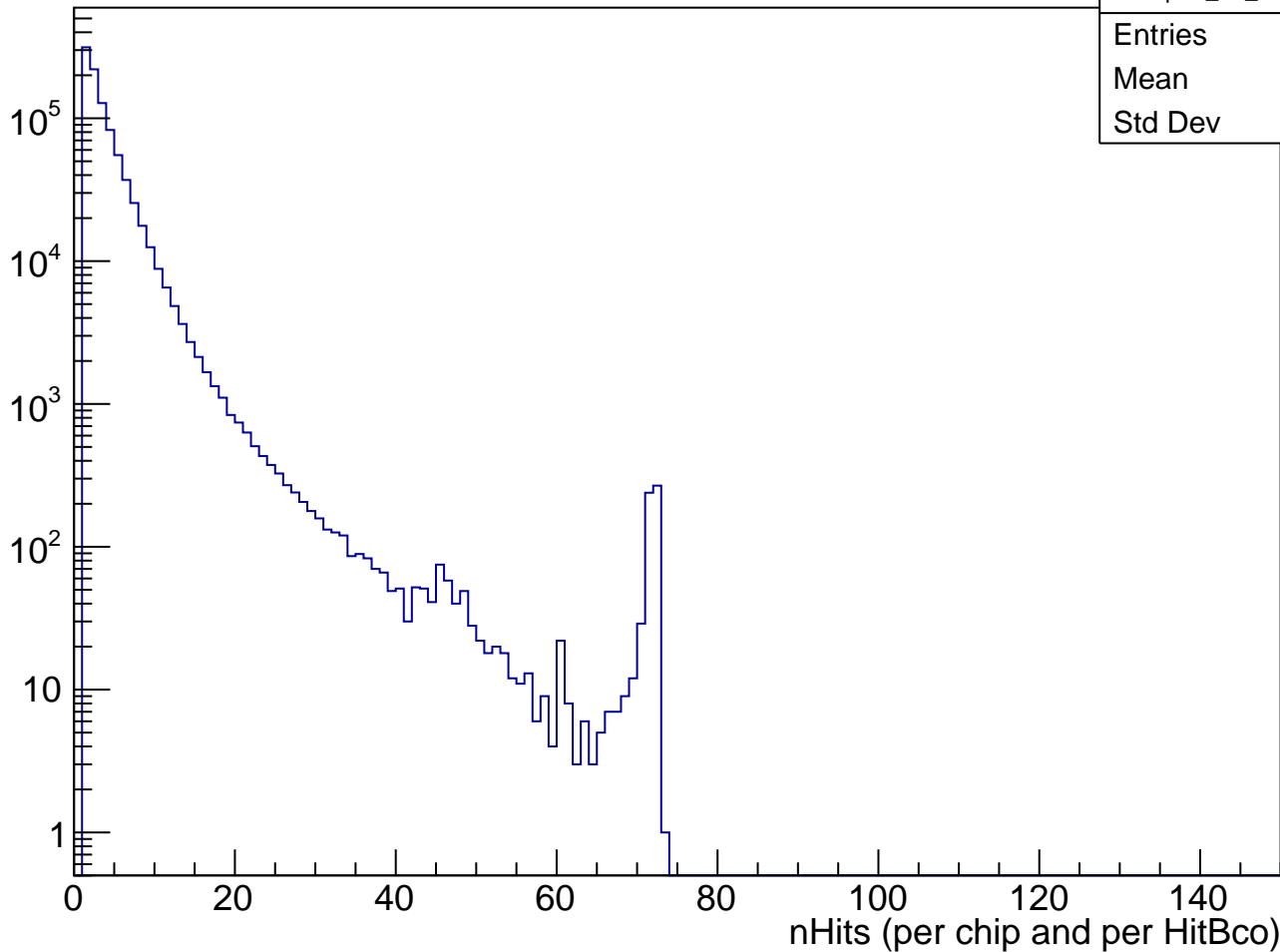
Mean 3.24

Std Dev 3.768

nHits (per chip and per HitBco)

nChipHit_F4_Fch9_Chip6

Entries



nChipHit_F4_Fch9_Chip6

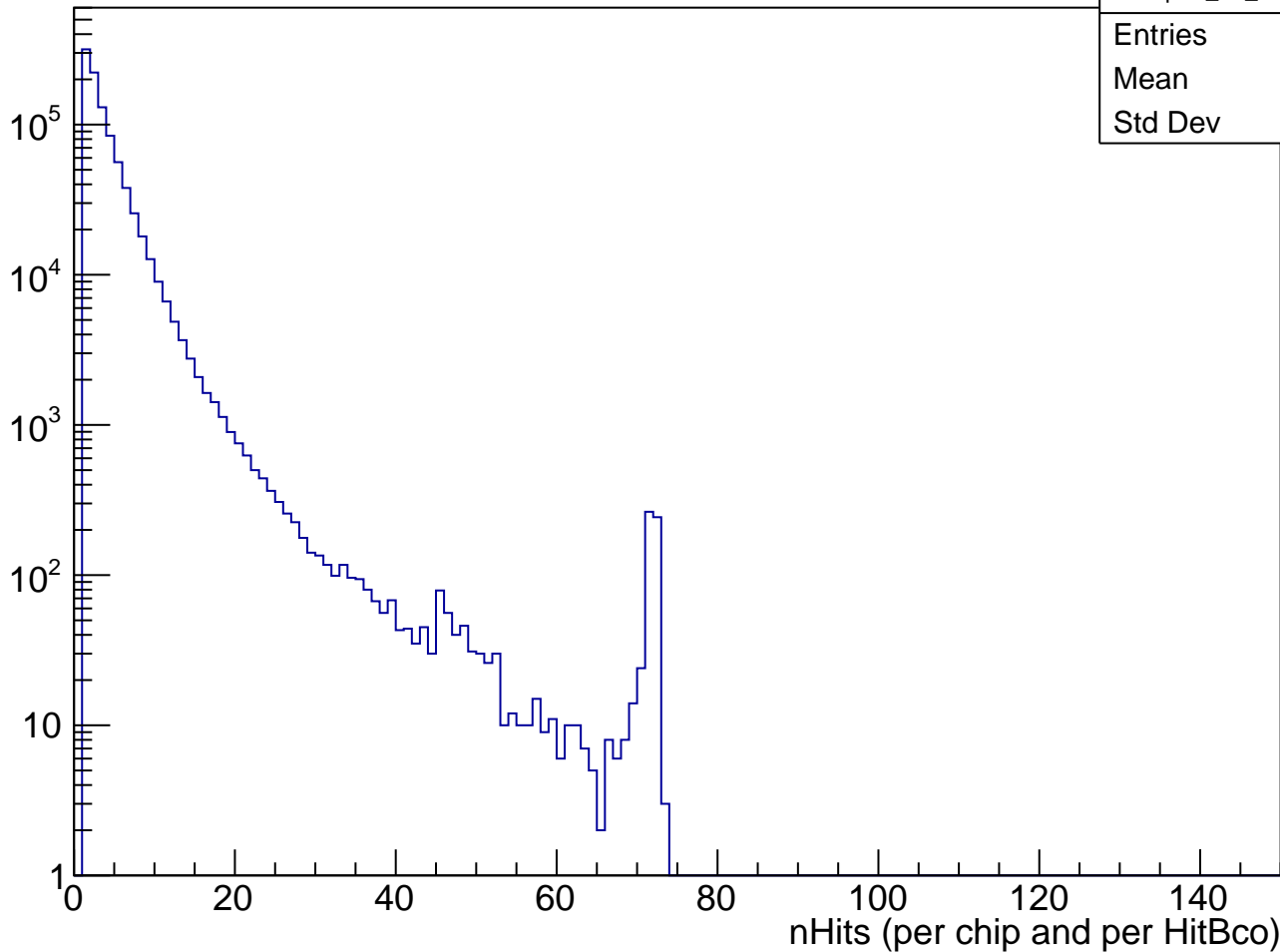
Entries 932435

Mean 3.234

Std Dev 3.754

nChipHit_F4_Fch9_Chip7

Entries



nChipHit_F4_Fch9_Chip7

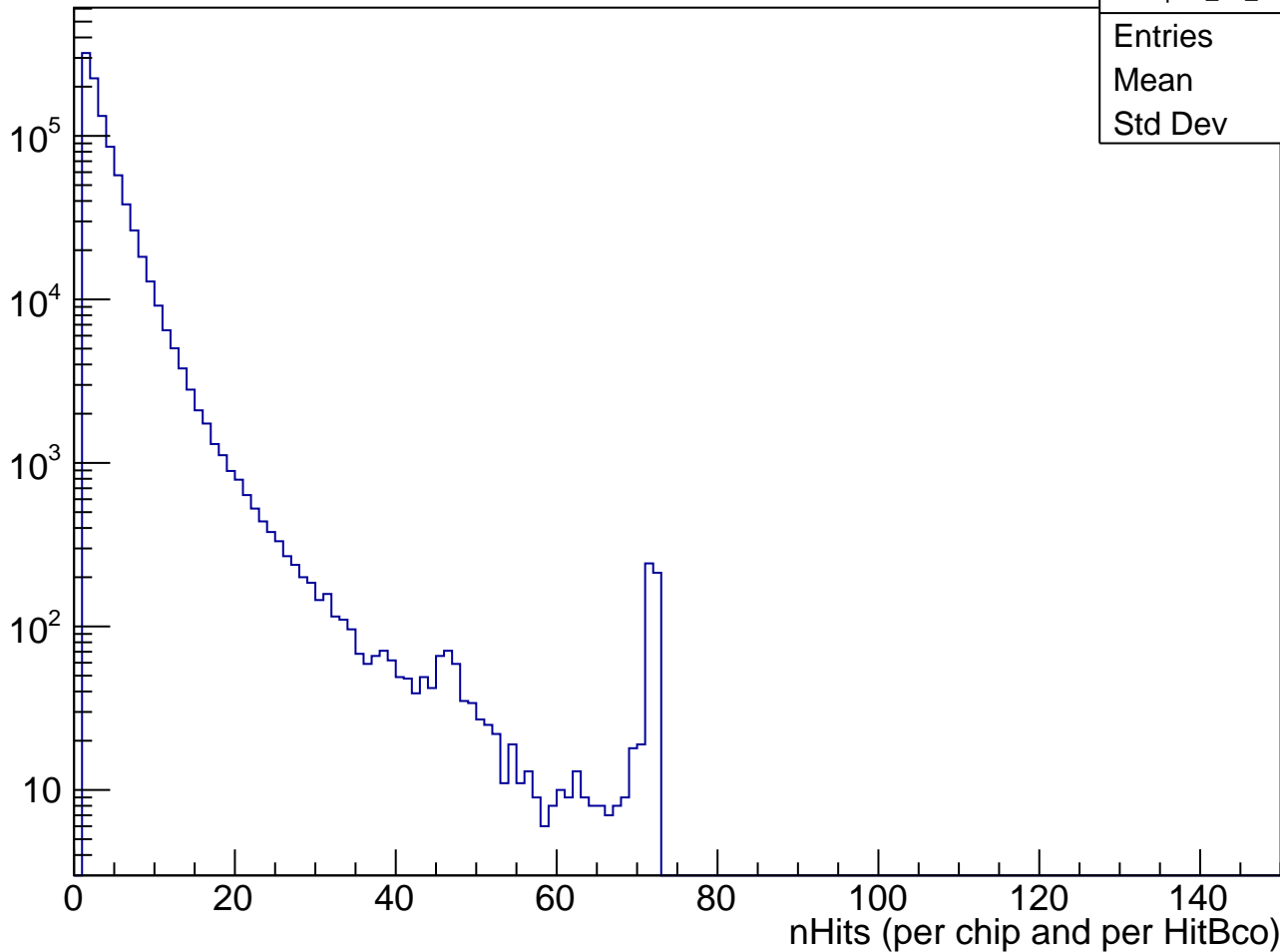
Entries 944336

Mean 3.233

Std Dev 3.729

nChipHit_F4_Fch9_Chip8

Entries



nChipHit_F4_Fch9_Chip8

Entries 958141

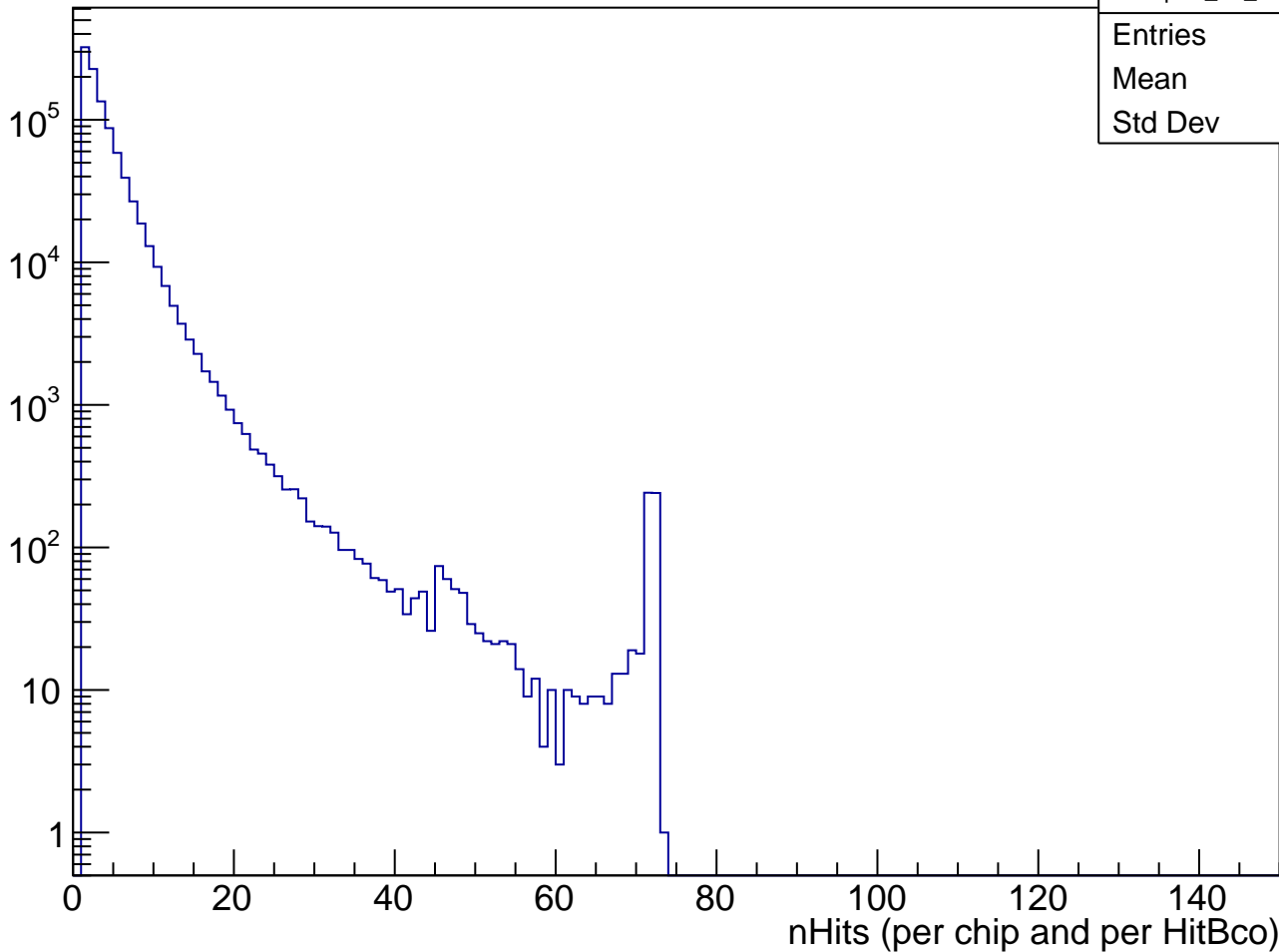
Mean 3.235

Std Dev 3.706

nHits (per chip and per HitBco)

nChipHit_F4_Fch9_Chip9

Entries



nChipHit_F4_Fch9_Chip9

Entries 969372

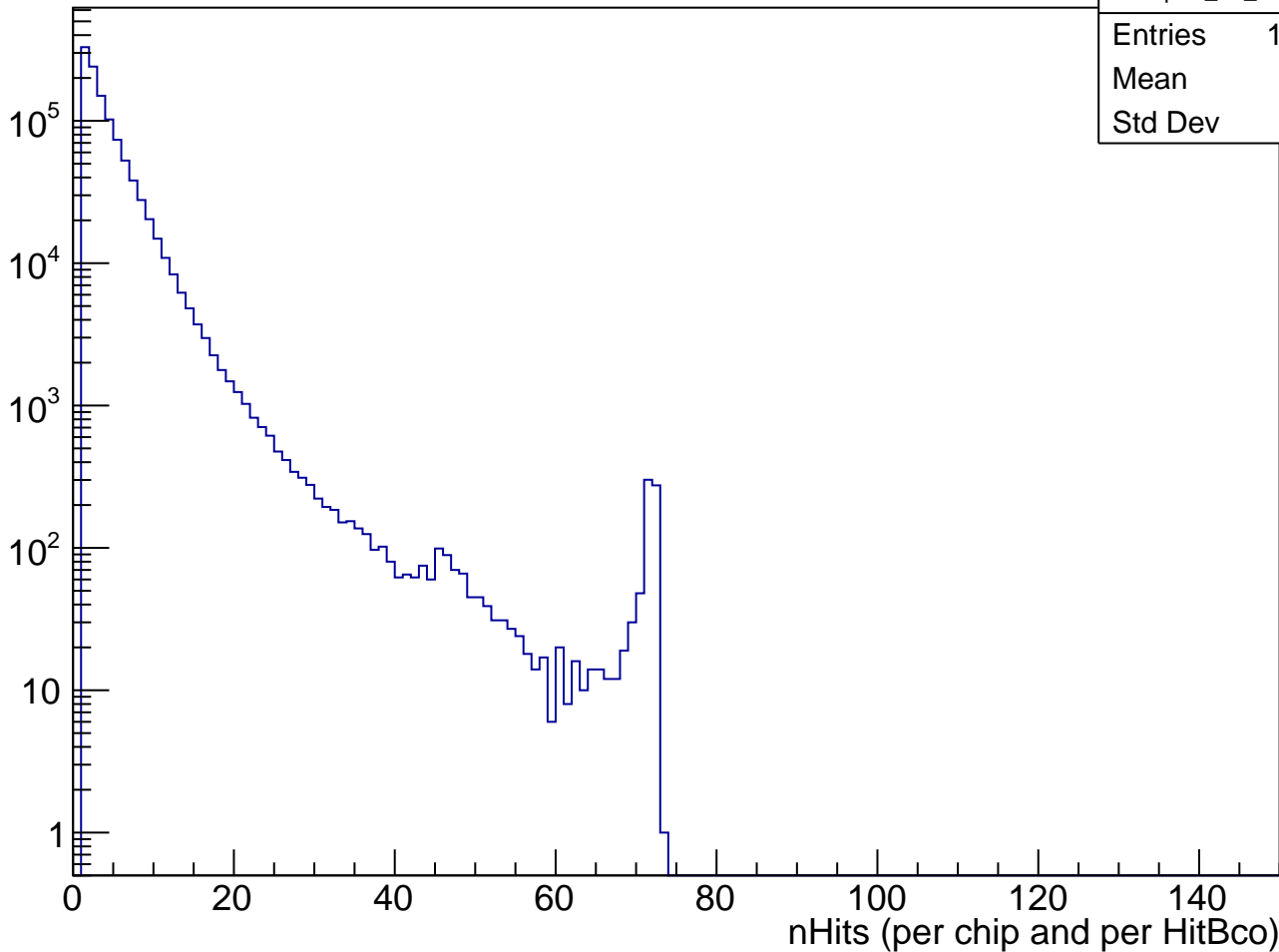
Mean 3.246

Std Dev 3.713

nHits (per chip and per HitBco)

nChipHit_F5_Fch0_Chip0

Entries

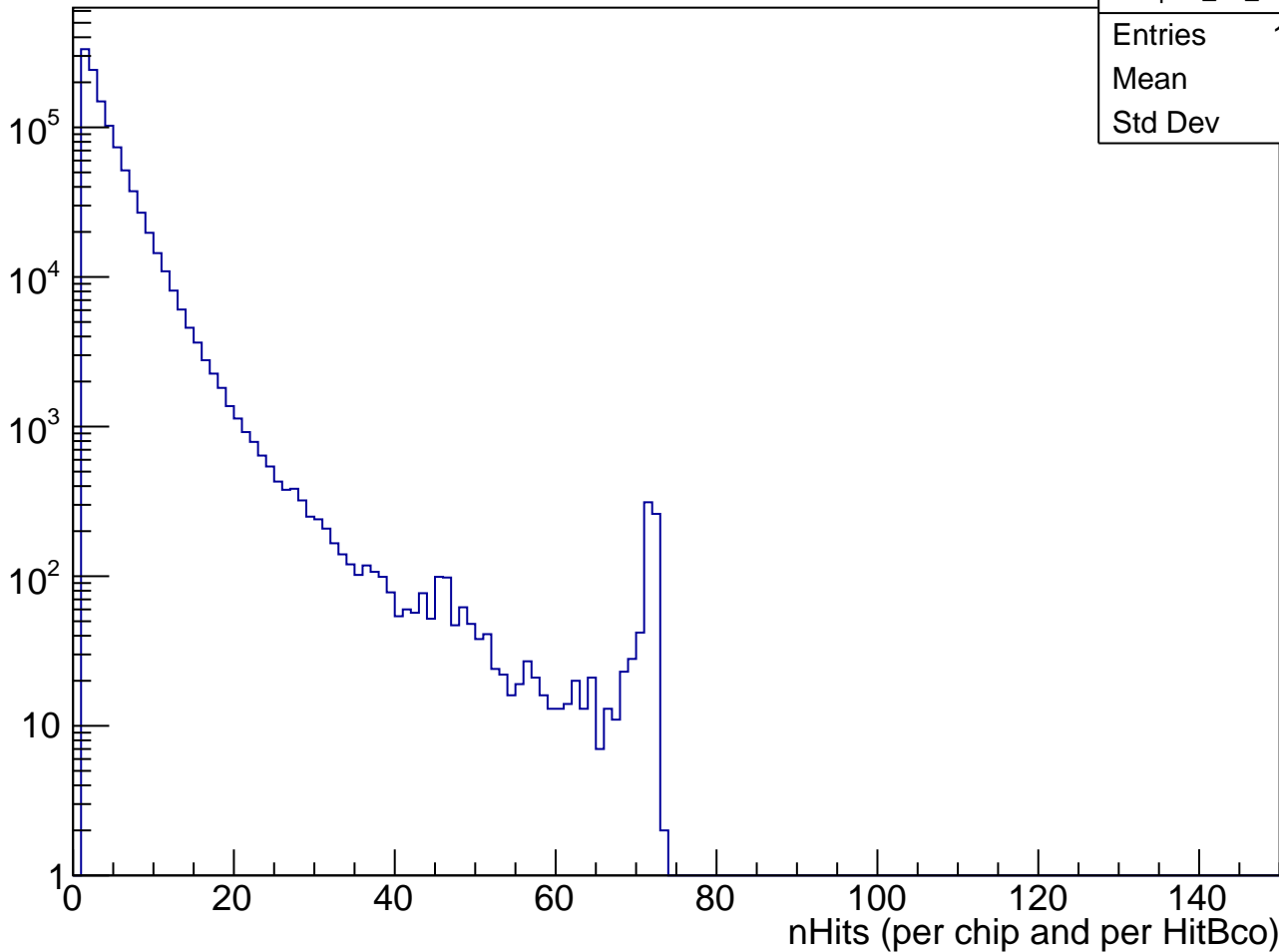


nChipHit_F5_Fch0_Chip0

Entries	1101156
Mean	3.665
Std Dev	4.136

nChipHit_F5_Fch0_Chip1

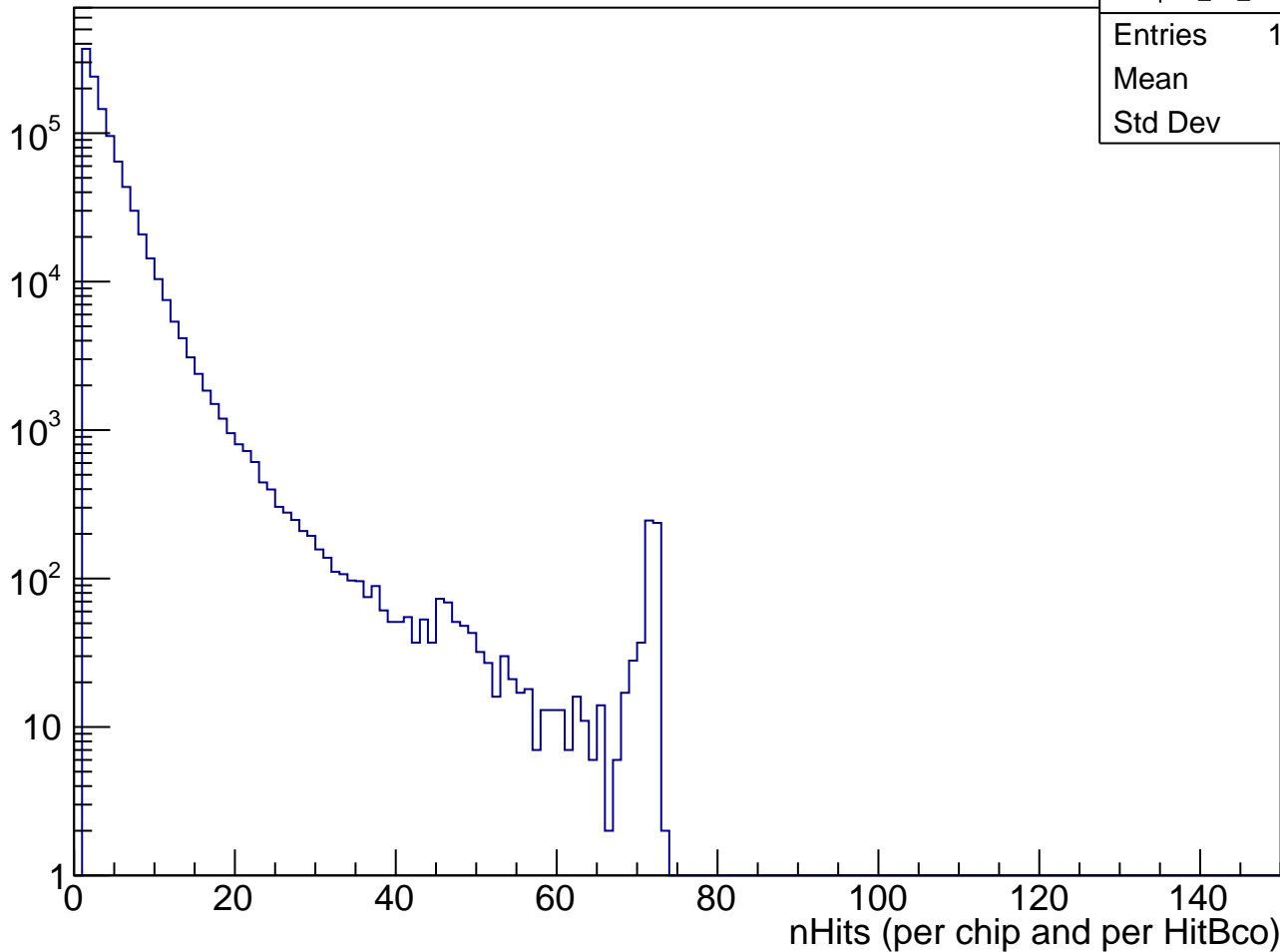
Entries



nChipHit_F5_Fch0_Chip1	
Entries	1100531
Mean	3.62
Std Dev	4.085

nChipHit_F5_Fch0_Chip10

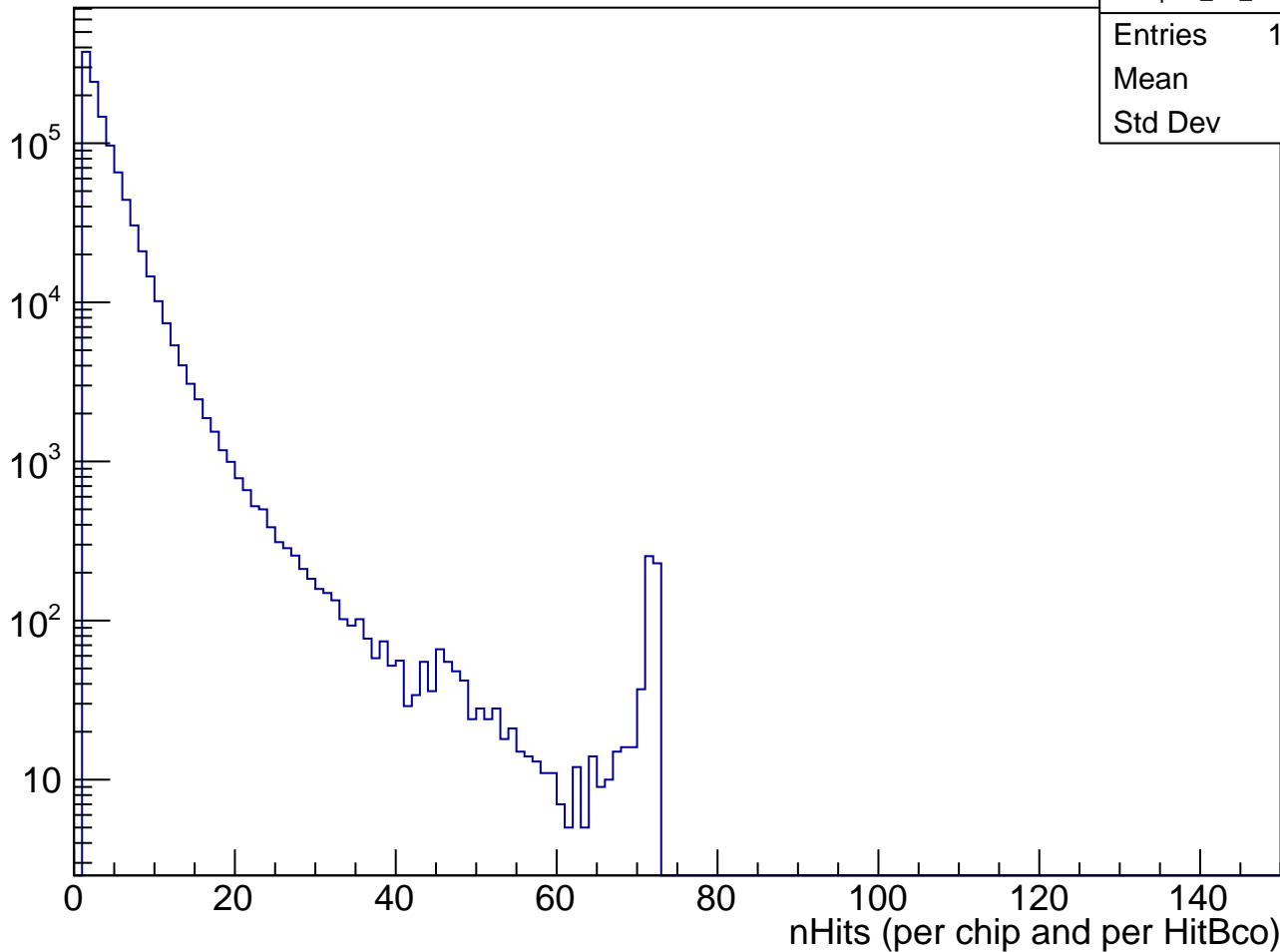
Entries



nChipHit_F5_Fch0_Chip10	
Entries	1069108
Mean	3.222
Std Dev	3.692

nChipHit_F5_Fch0_Chip11

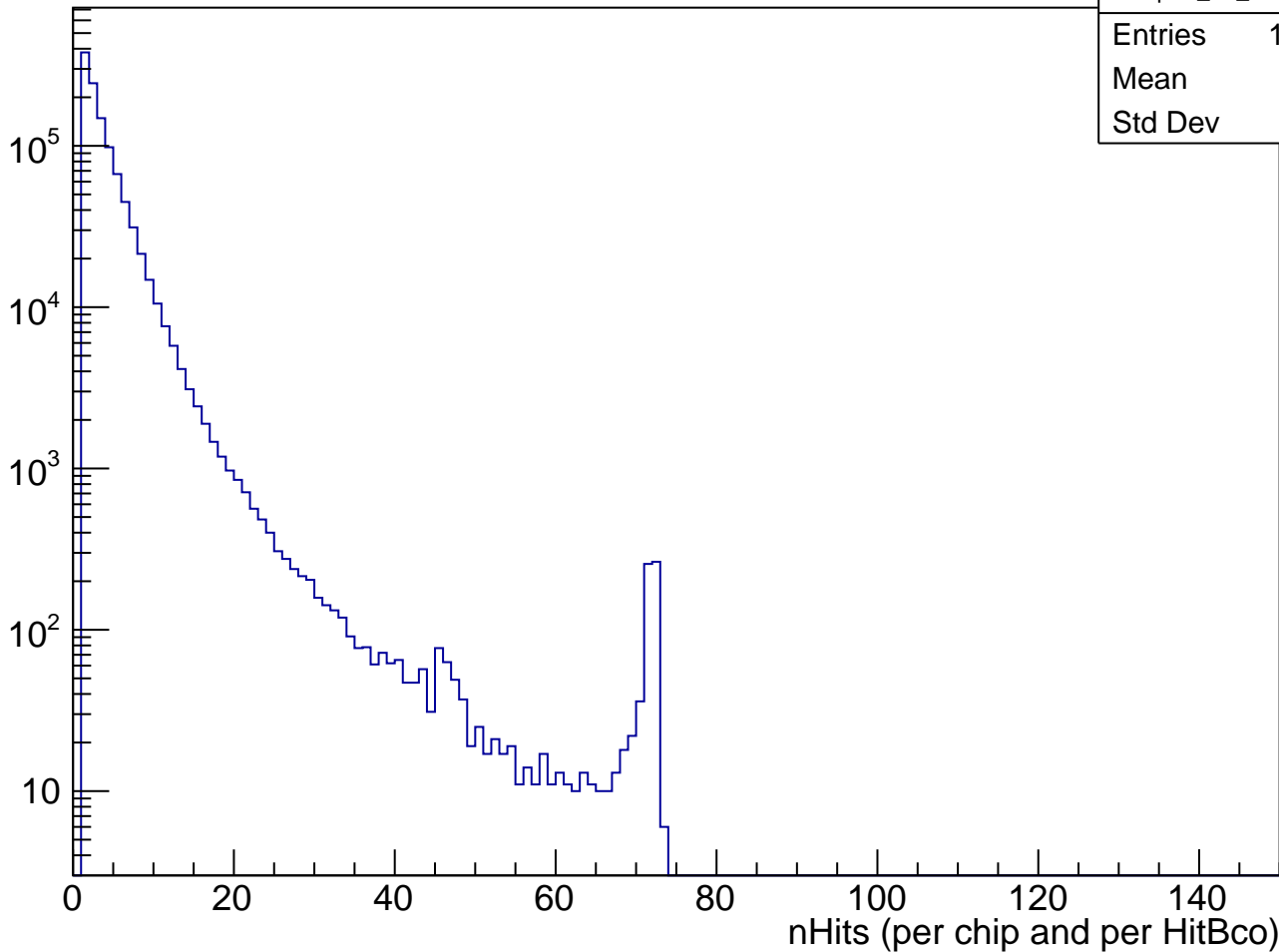
Entries



nChipHit_F5_Fch0_Chip11	
Entries	1081992
Mean	3.208
Std Dev	3.647

nChipHit_F5_Fch0_Chip12

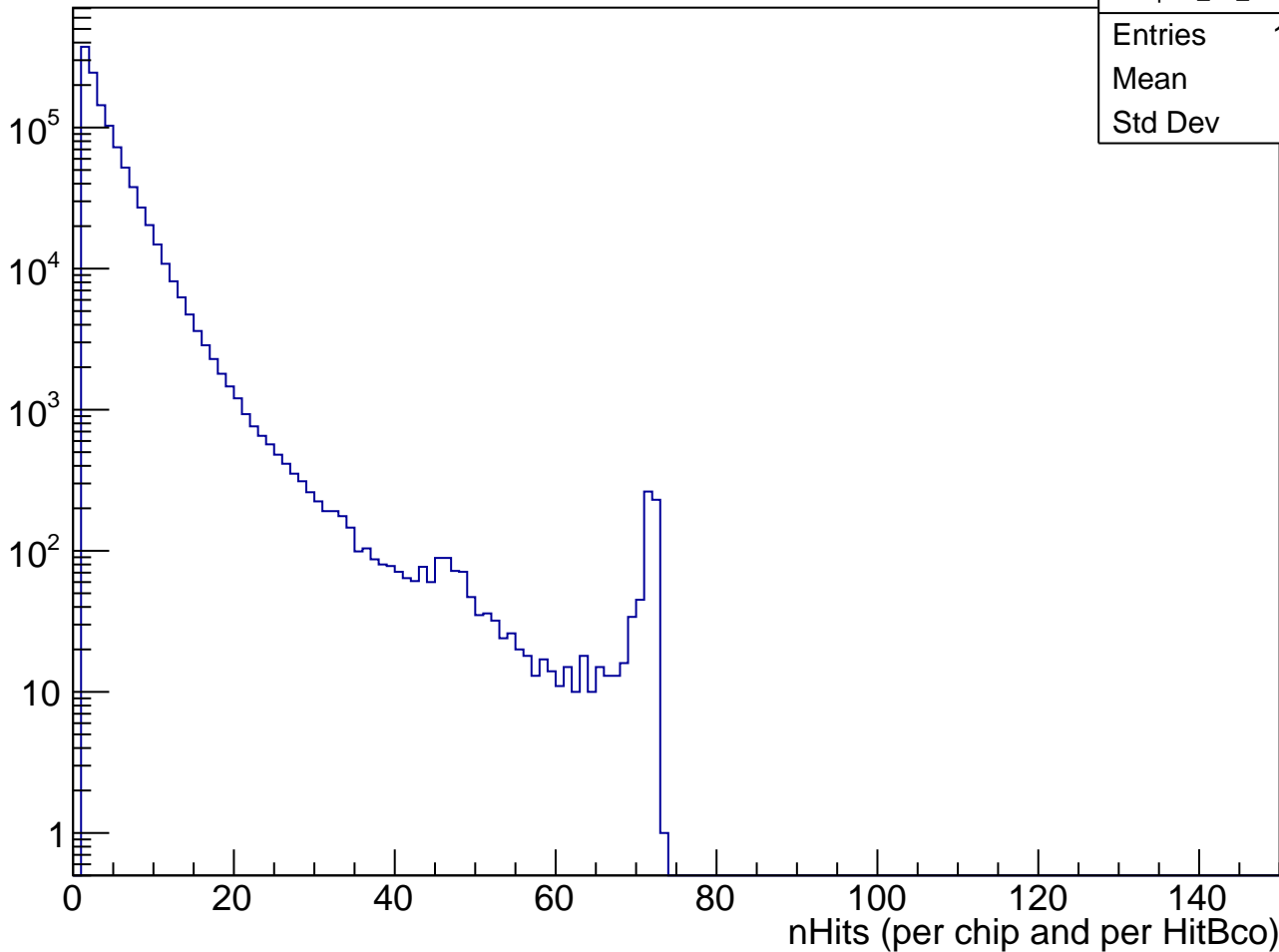
Entries



nChipHit_F5_Fch0_Chip12	
Entries	1095310
Mean	3.223
Std Dev	3.683

nChipHit_F5_Fch0_Chip13

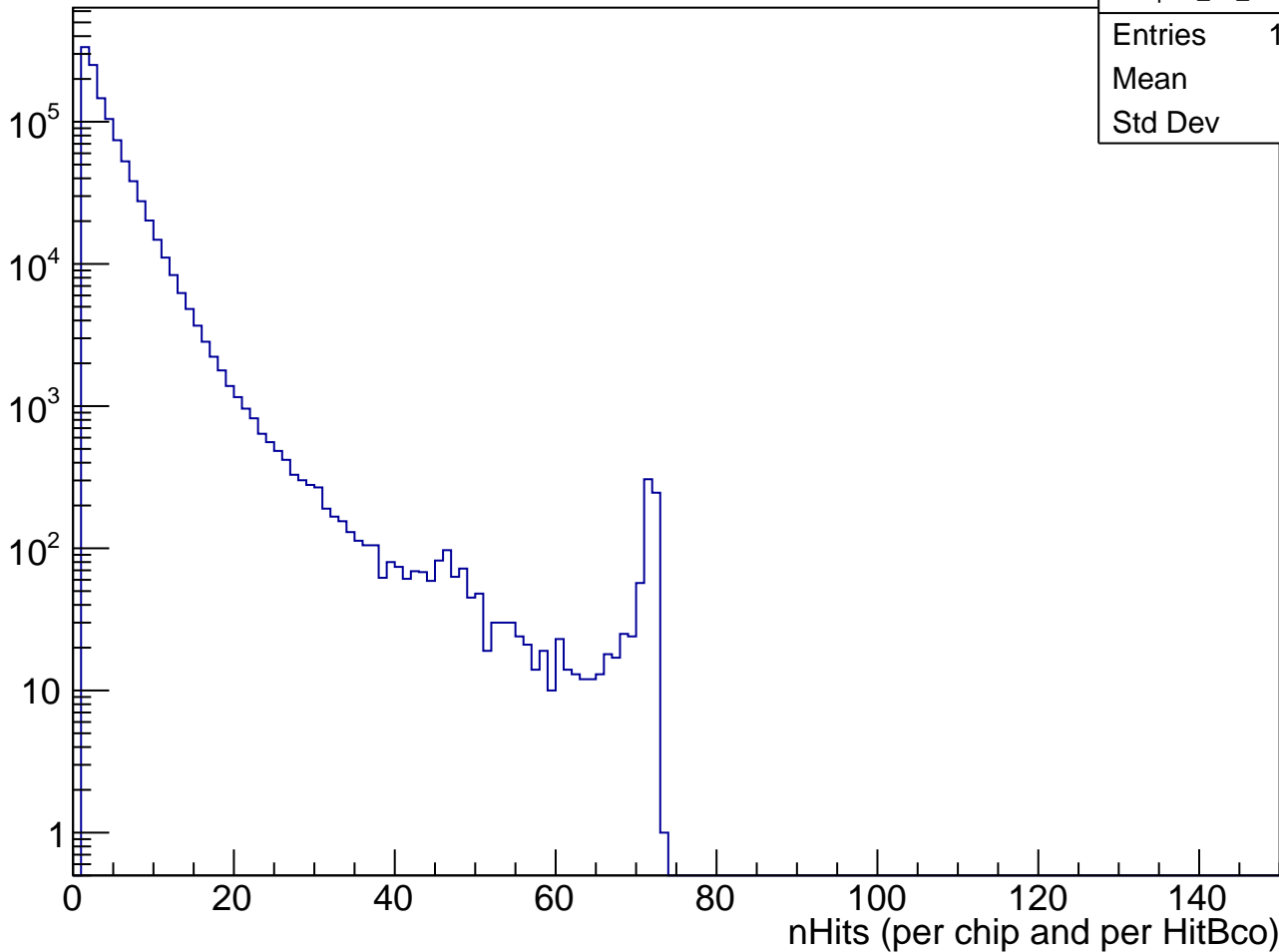
Entries



nChipHit_F5_Fch0_Chip13	
Entries	1140741
Mean	3.536
Std Dev	4.024

nChipHit_F5_Fch0_Chip14

Entries

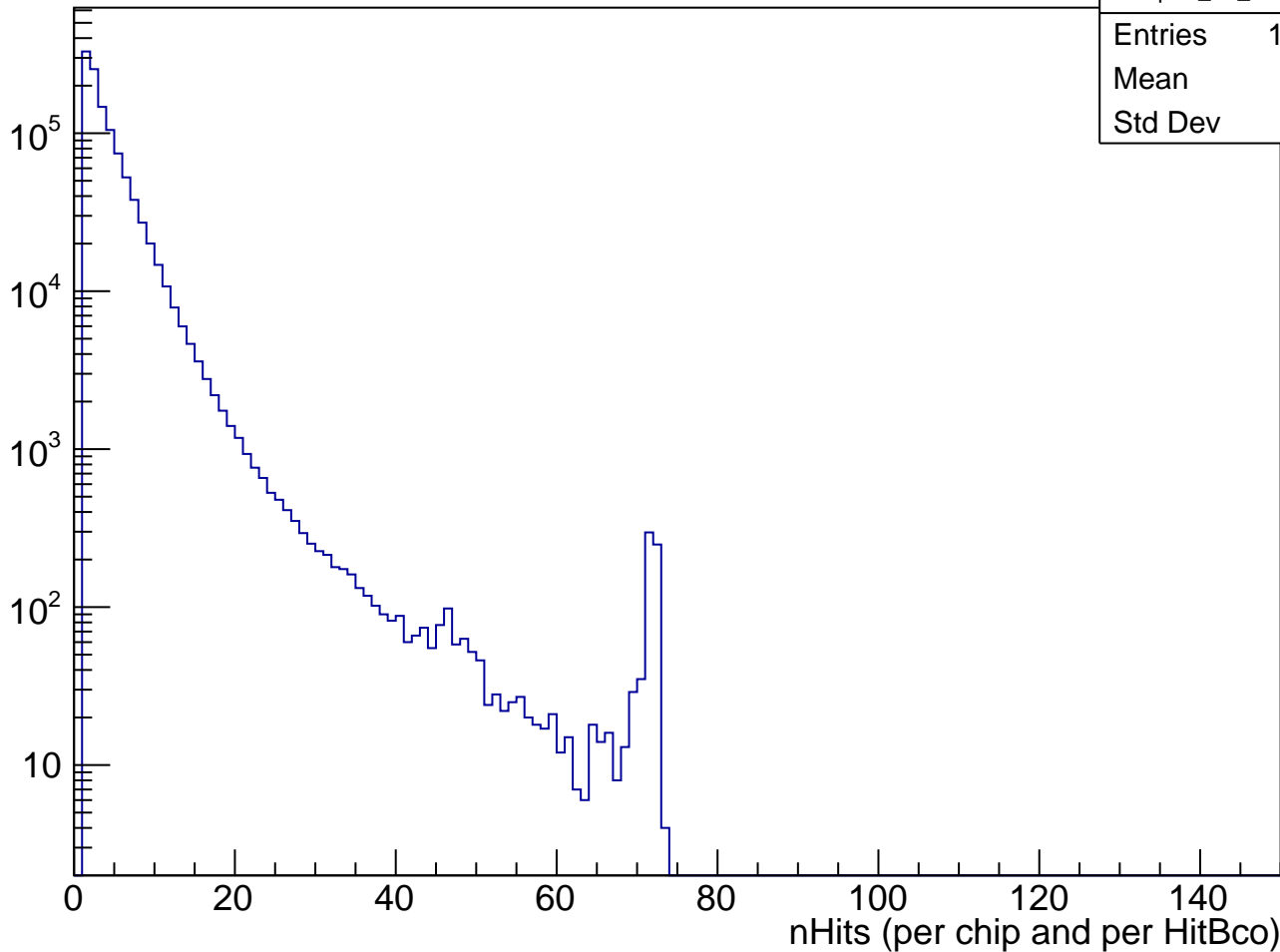


nChipHit_F5_Fch0_Chip14	
Entries	1115900
Mean	3.628
Std Dev	4.087

nHits (per chip and per HitBco)

nChipHit_F5_Fch0_Chip15

Entries

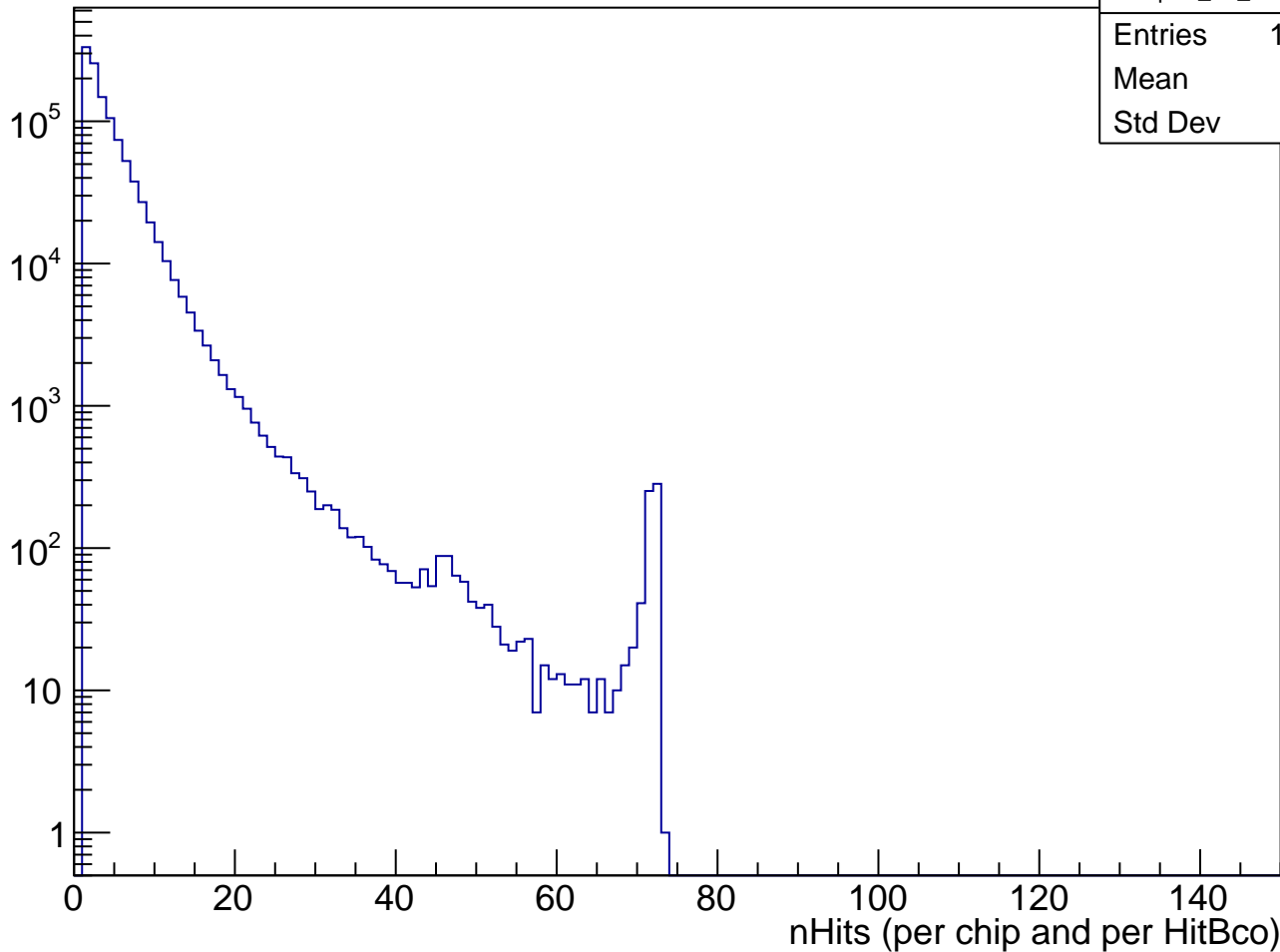


nChipHit_F5_Fch0_Chip15	
Entries	1112056
Mean	3.618
Std Dev	4.058

nHits (per chip and per HitBco)

nChipHit_F5_Fch0_Chip16

Entries

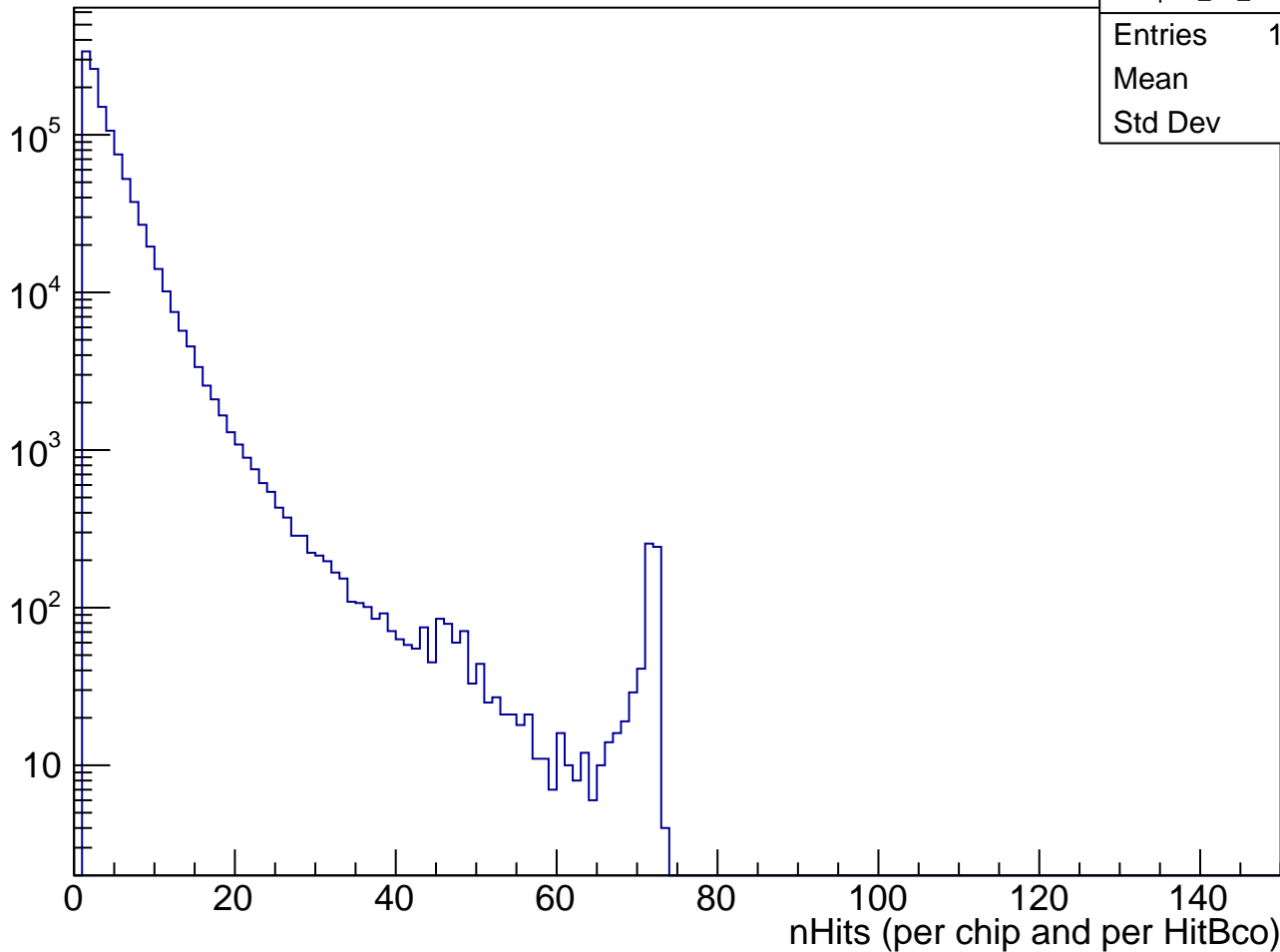


nChipHit_F5_Fch0_Chip16	
Entries	1114100
Mean	3.577
Std Dev	3.979

nHits (per chip and per HitBco)

nChipHit_F5_Fch0_Chip17

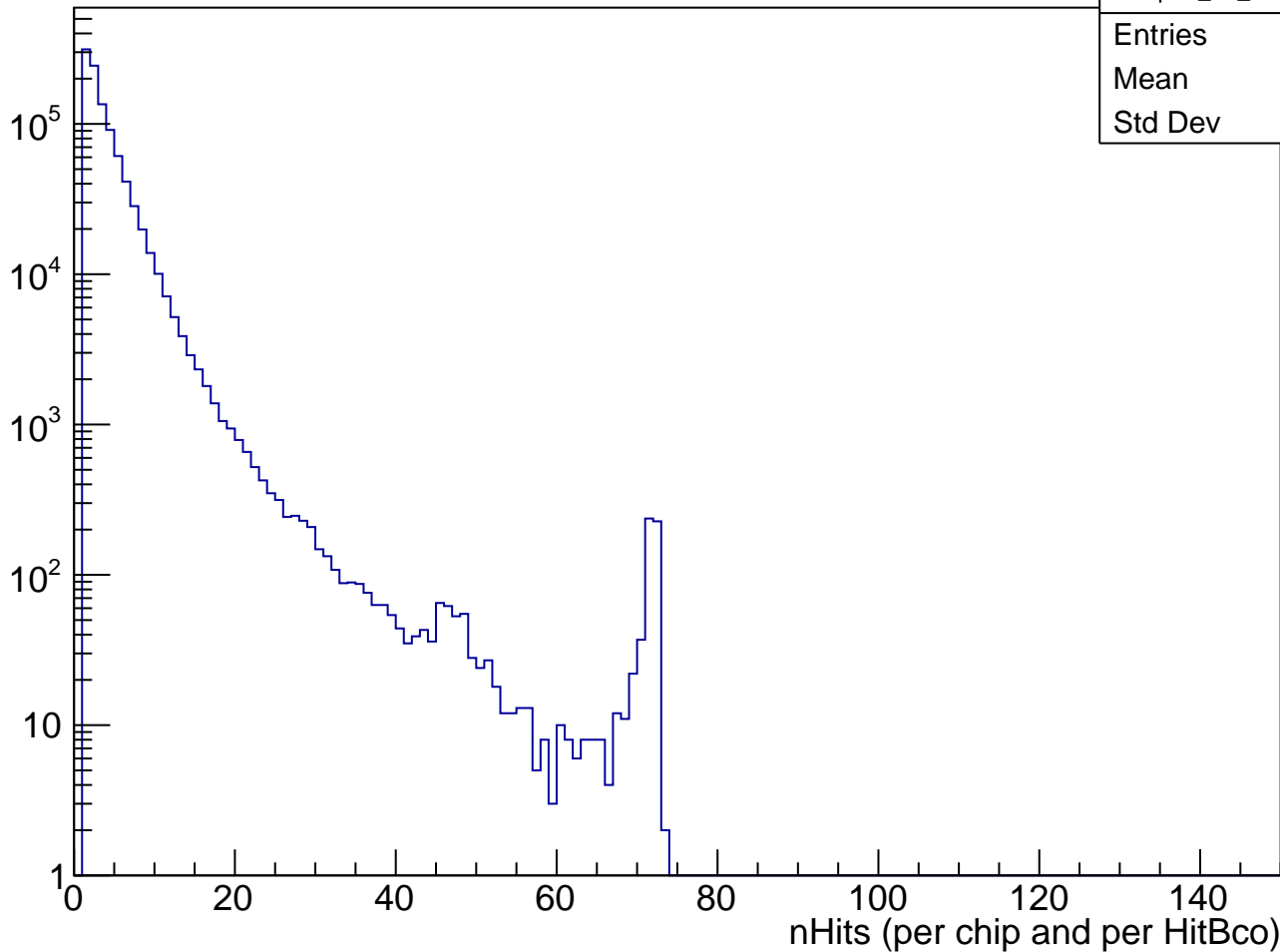
Entries



nChipHit_F5_Fch0_Chip17	
Entries	1129296
Mean	3.542
Std Dev	3.925

nChipHit_F5_Fch0_Chip18

Entries

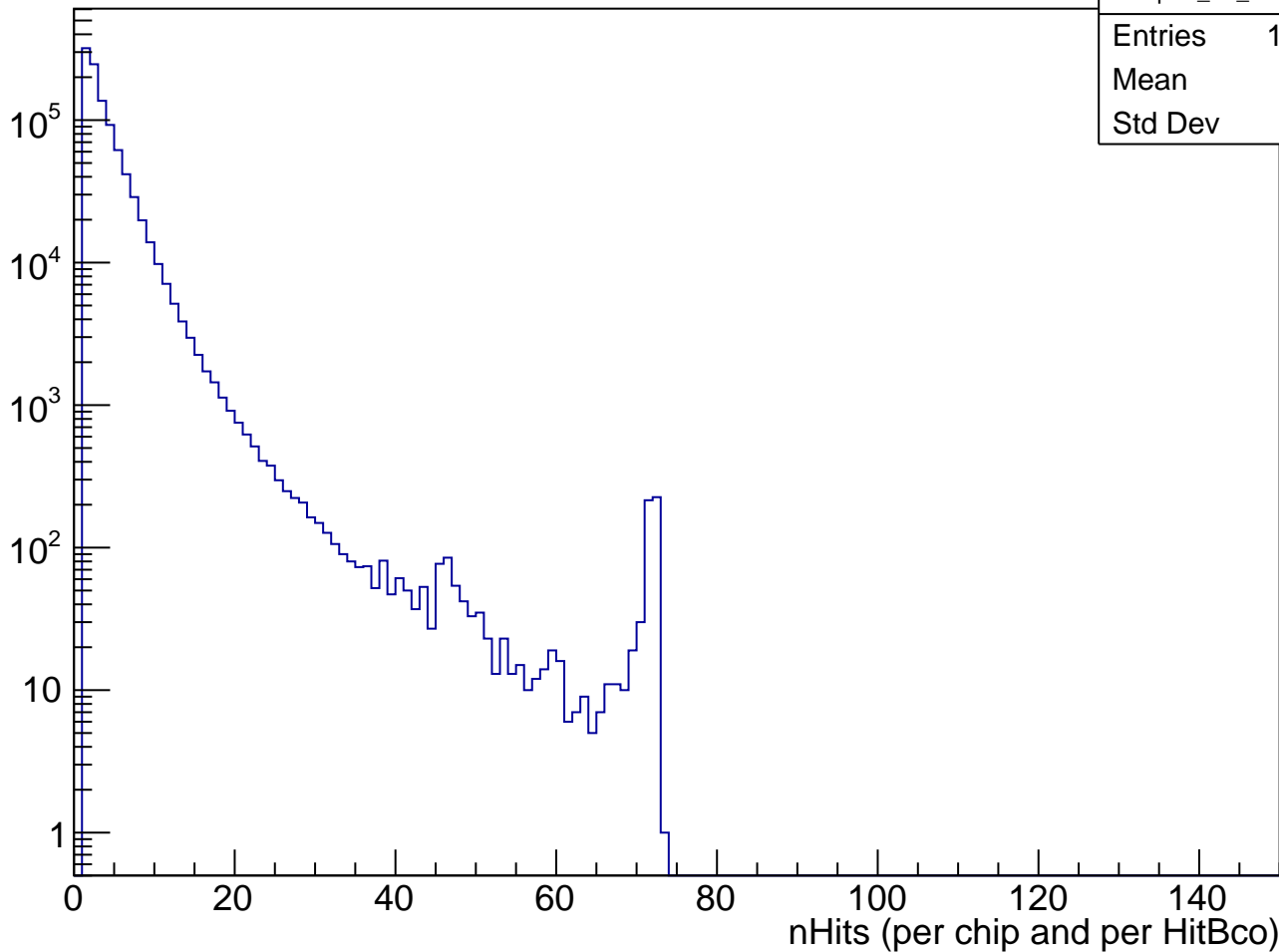


nChipHit_F5_Fch0_Chip18	
Entries	991076
Mean	3.286
Std Dev	3.685

nHits (per chip and per HitBco)

nChipHit_F5_Fch0_Chip19

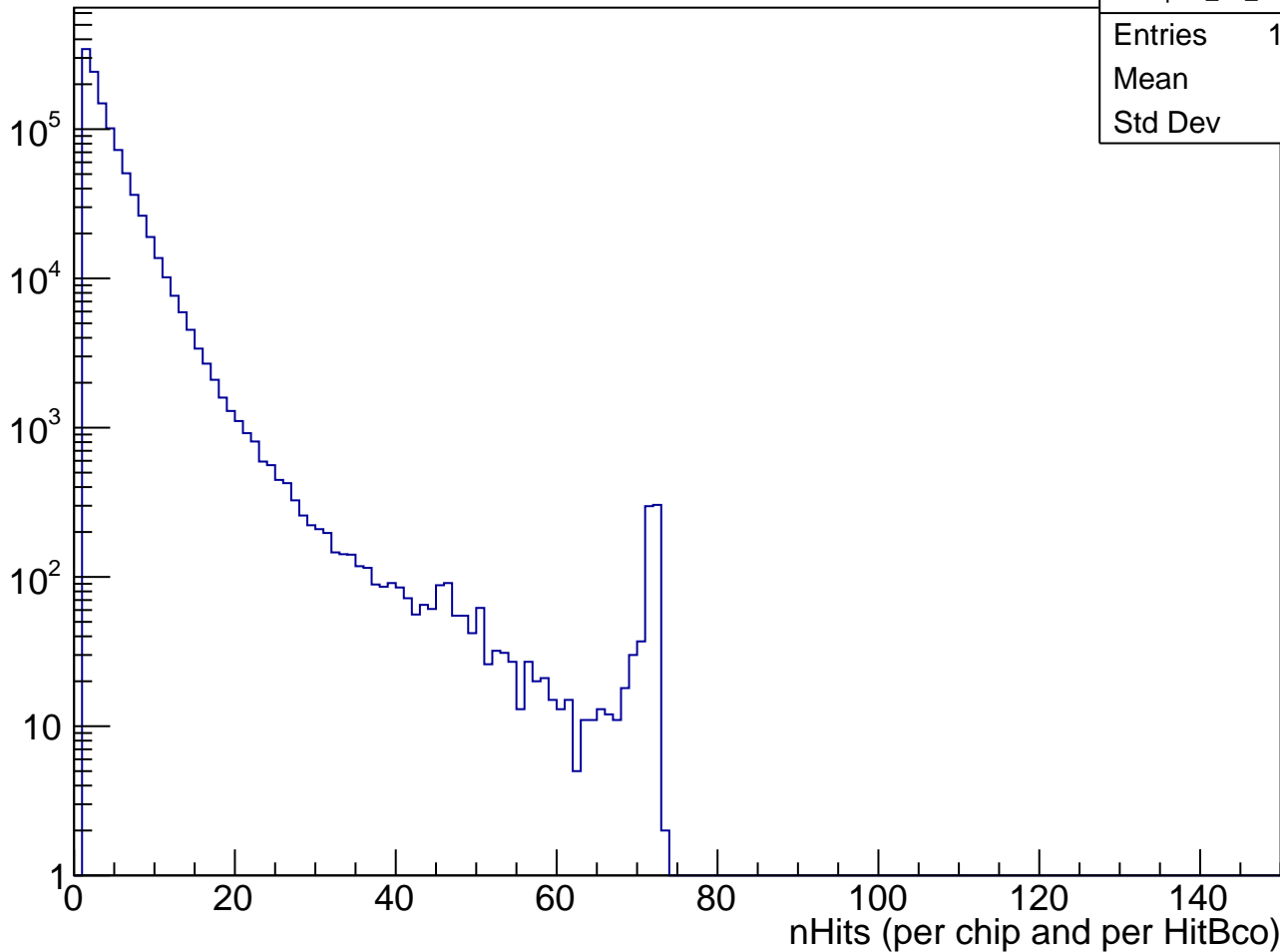
Entries



nChipHit_F5_Fch0_Chip19	
Entries	1003496
Mean	3.268
Std Dev	3.663

nChipHit_F5_Fch0_Chip2

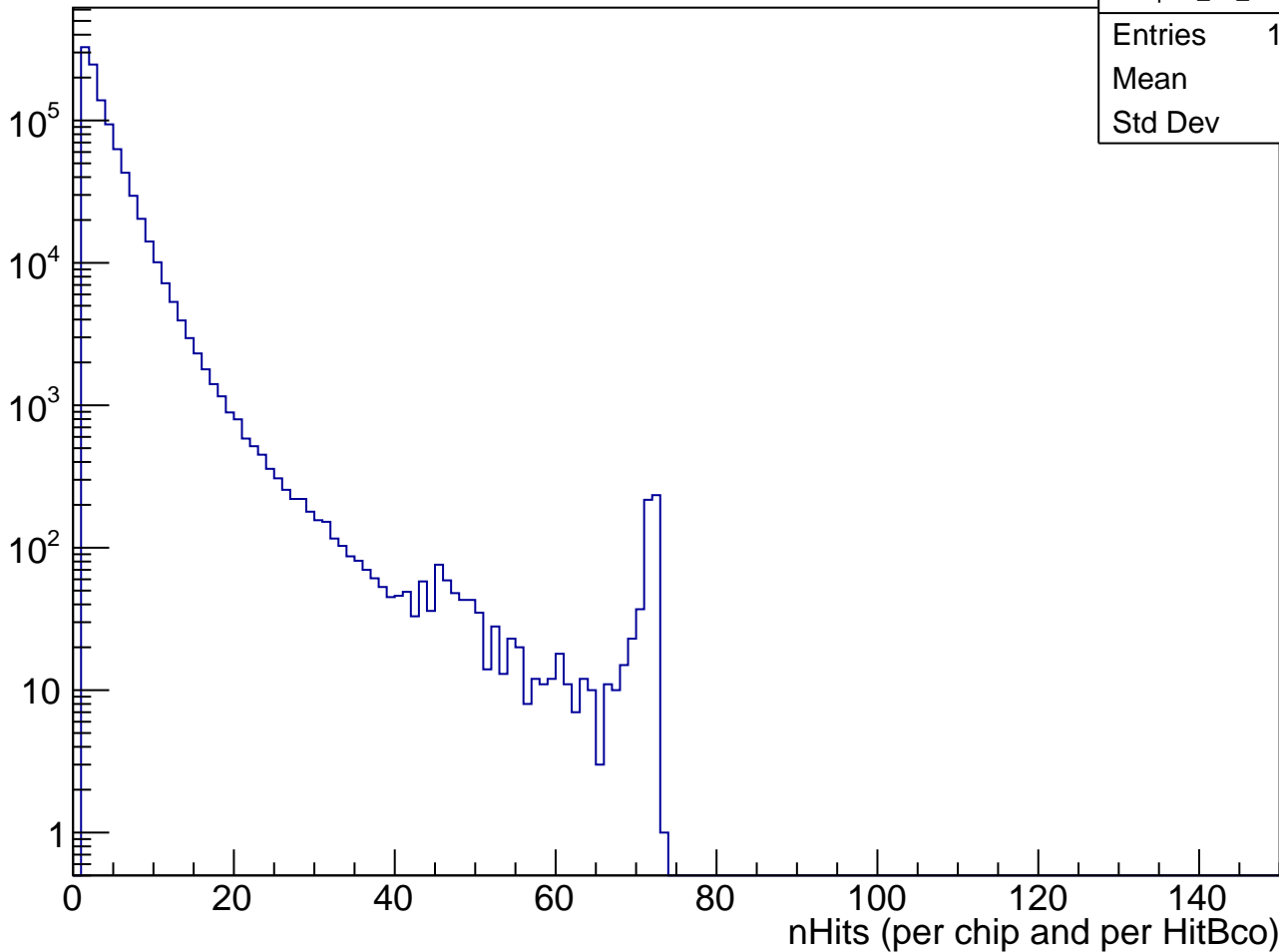
Entries



nChipHit_F5_Fch0_Chip2	
Entries	1103057
Mean	3.555
Std Dev	4.057

nChipHit_F5_Fch0_Chip20

Entries

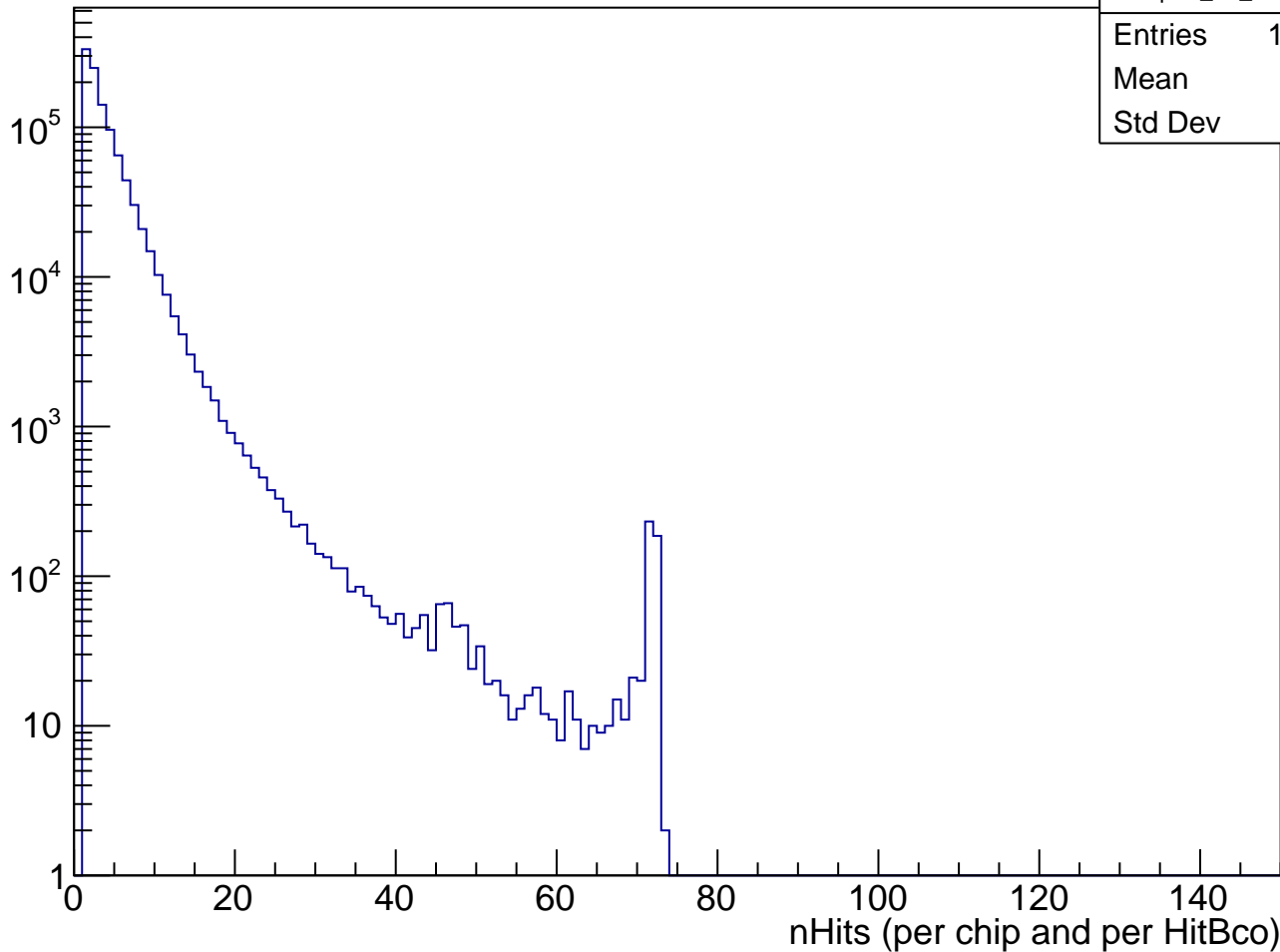


nChipHit_F5_Fch0_Chip20	
Entries	1019367
Mean	3.274
Std Dev	3.675

nHits (per chip and per HitBco)

nChipHit_F5_Fch0_Chip21

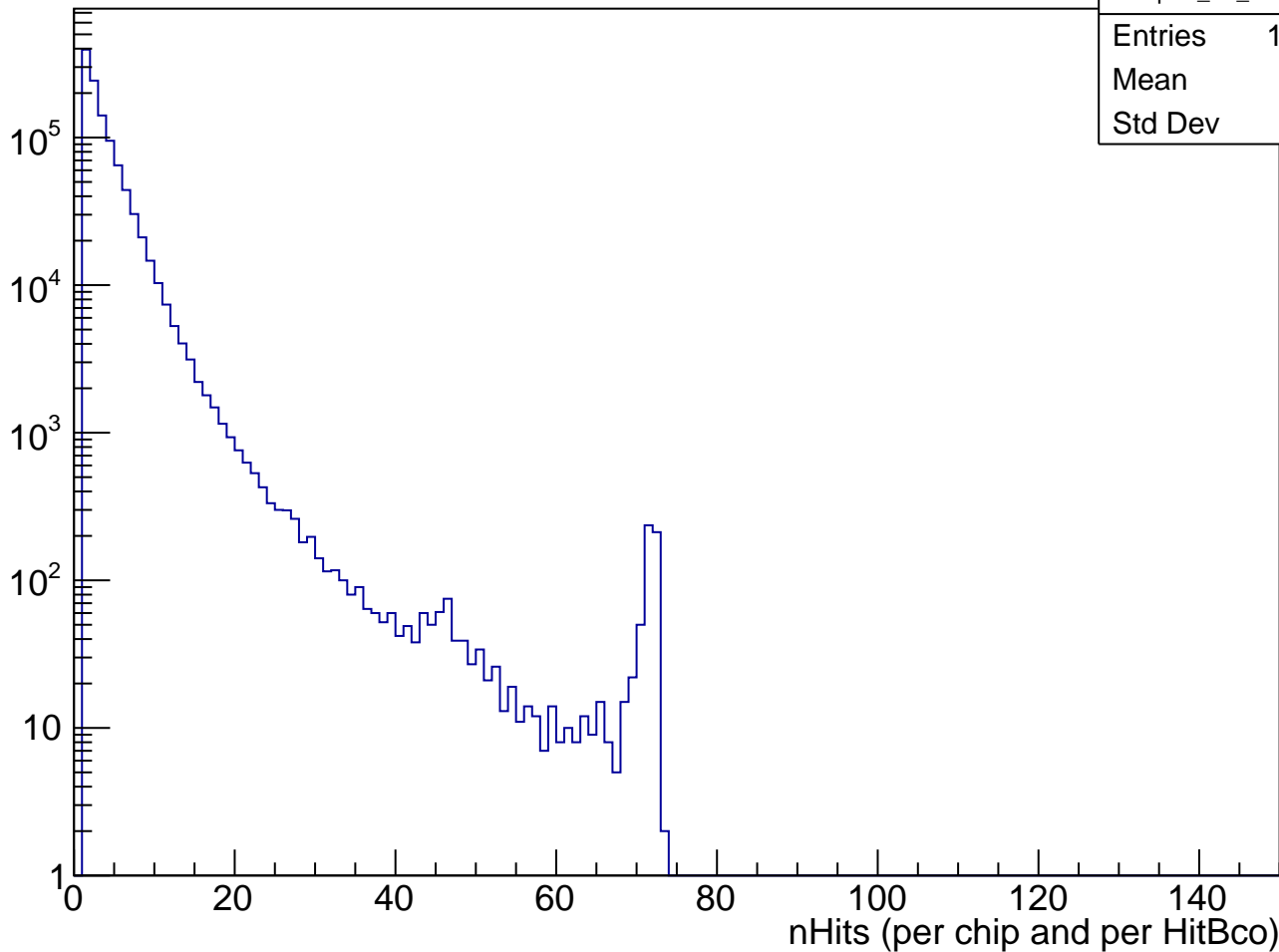
Entries



nChipHit_F5_Fch0_Chip21	
Entries	1038964
Mean	3.282
Std Dev	3.629

nChipHit_F5_Fch0_Chip22

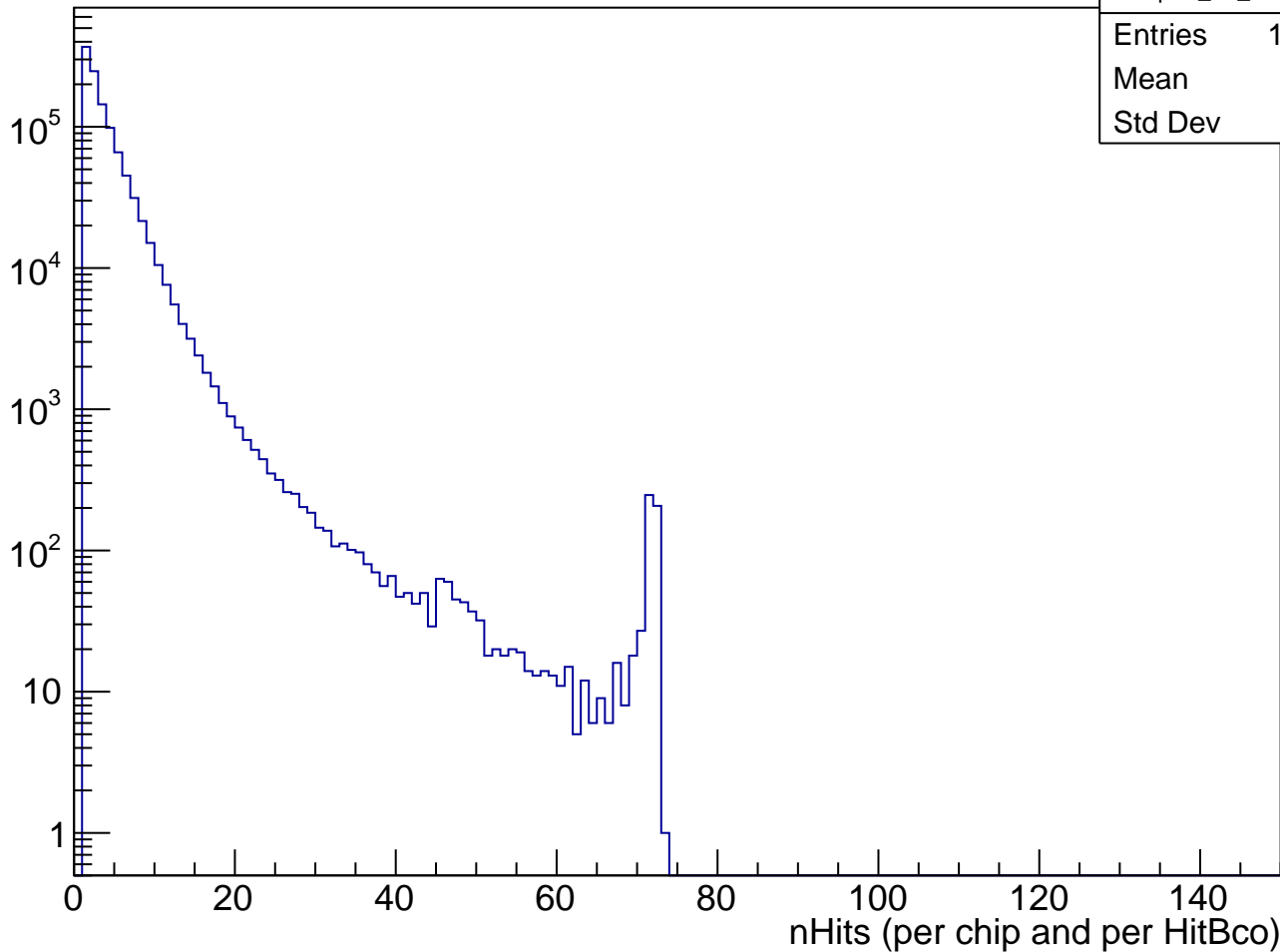
Entries



nChipHit_F5_Fch0_Chip22	
Entries	1091849
Mean	3.158
Std Dev	3.603

nChipHit_F5_Fch0_Chip23

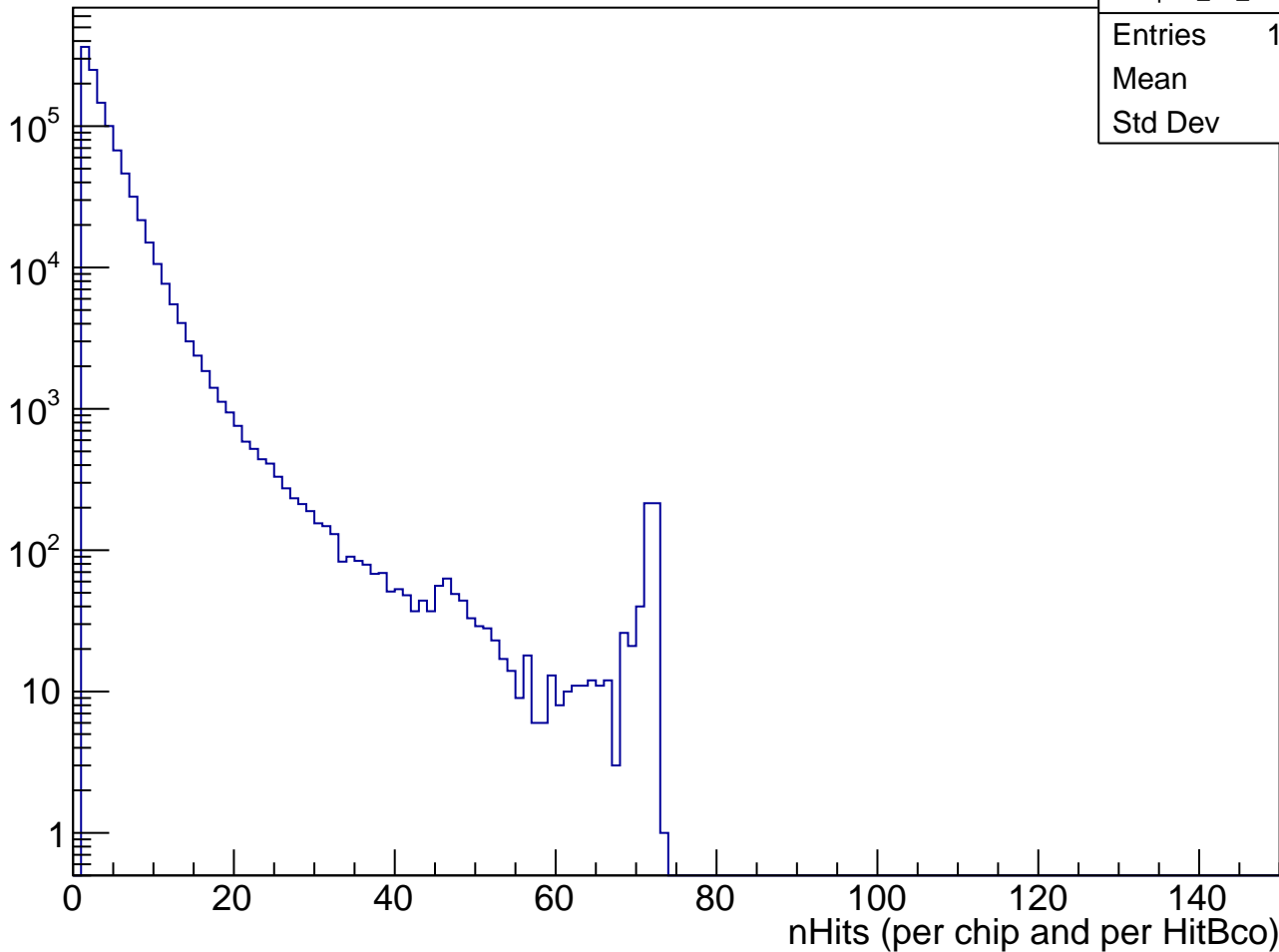
Entries



nChipHit_F5_Fch0_Chip23	
Entries	1082109
Mean	3.225
Std Dev	3.614

nChipHit_F5_Fch0_Chip24

Entries

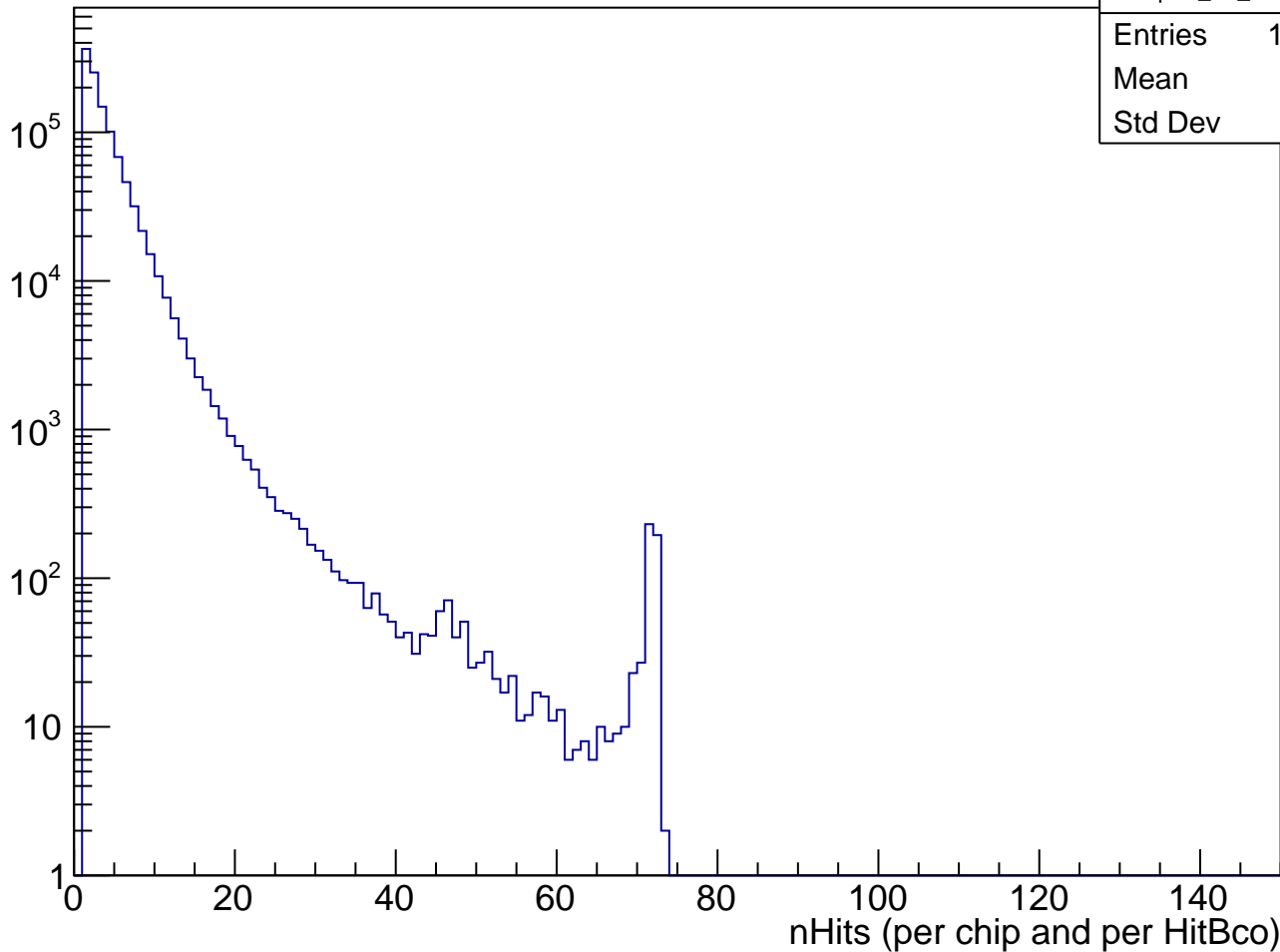


nChipHit_F5_Fch0_Chip24	
Entries	1086954
Mean	3.24
Std Dev	3.604

nHits (per chip and per HitBco)

nChipHit_F5_Fch0_Chip25

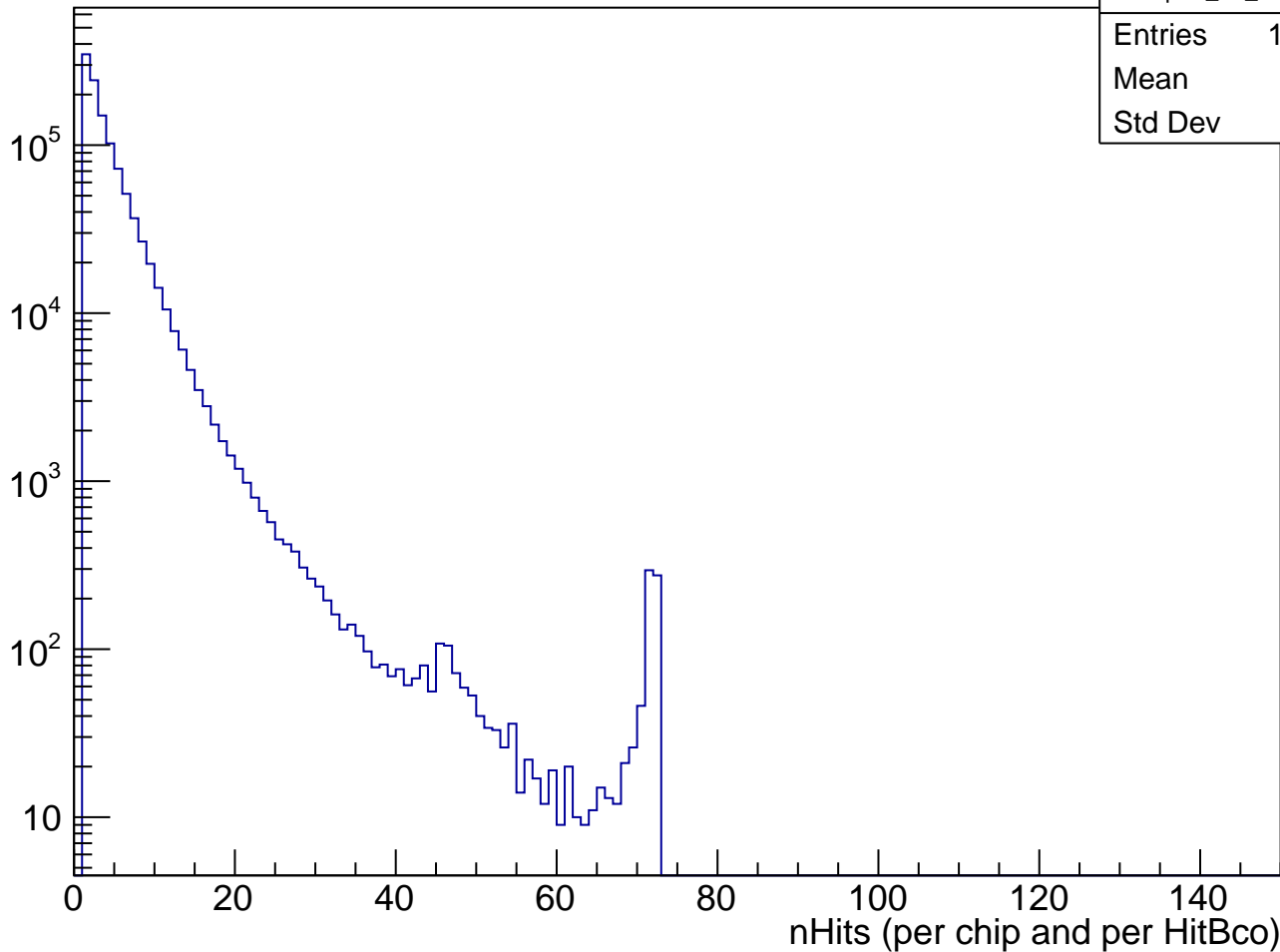
Entries



nChipHit_F5_Fch0_Chip25	
Entries	1094612
Mean	3.237
Std Dev	3.571

nChipHit_F5_Fch0_Chip3

Entries



nChipHit_F5_Fch0_Chip3

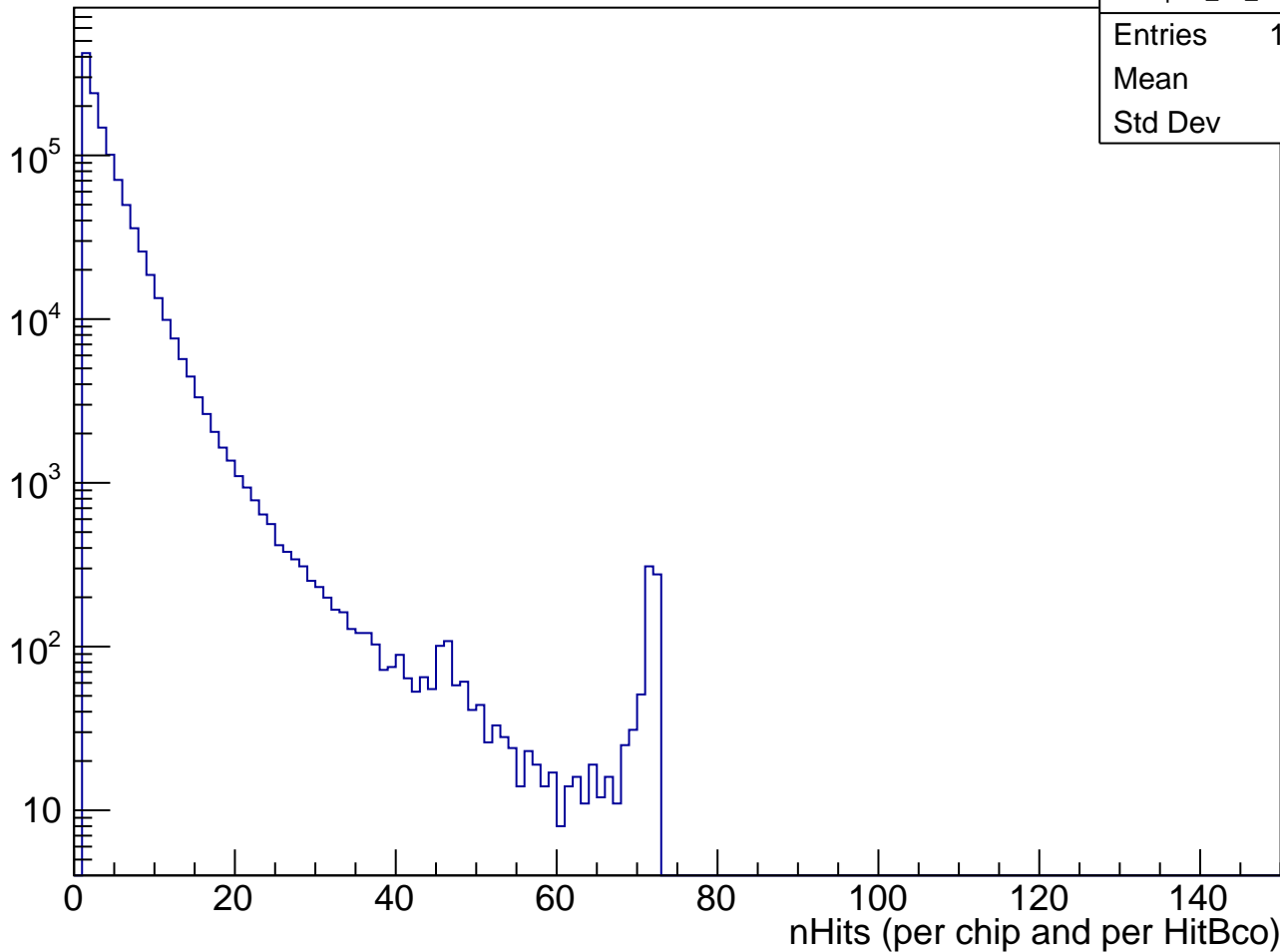
Entries 1113545

Mean 3.574

Std Dev 4.073

nChipHit_F5_Fch0_Chip4

Entries

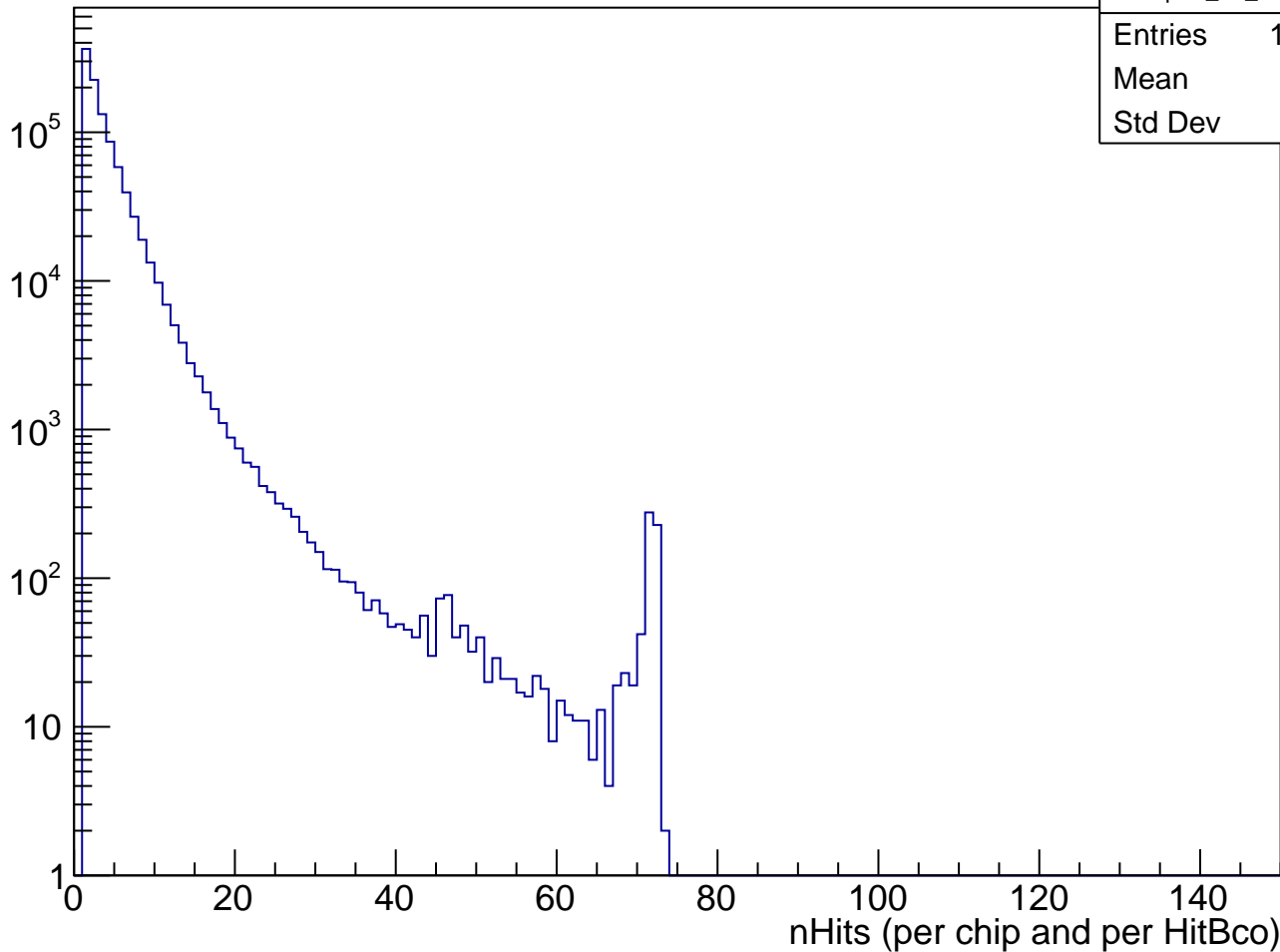


nChipHit_F5_Fch0_Chip4	
Entries	1171780
Mean	3.378
Std Dev	3.987

nHits (per chip and per HitBco)

nChipHit_F5_Fch0_Chip5

Entries

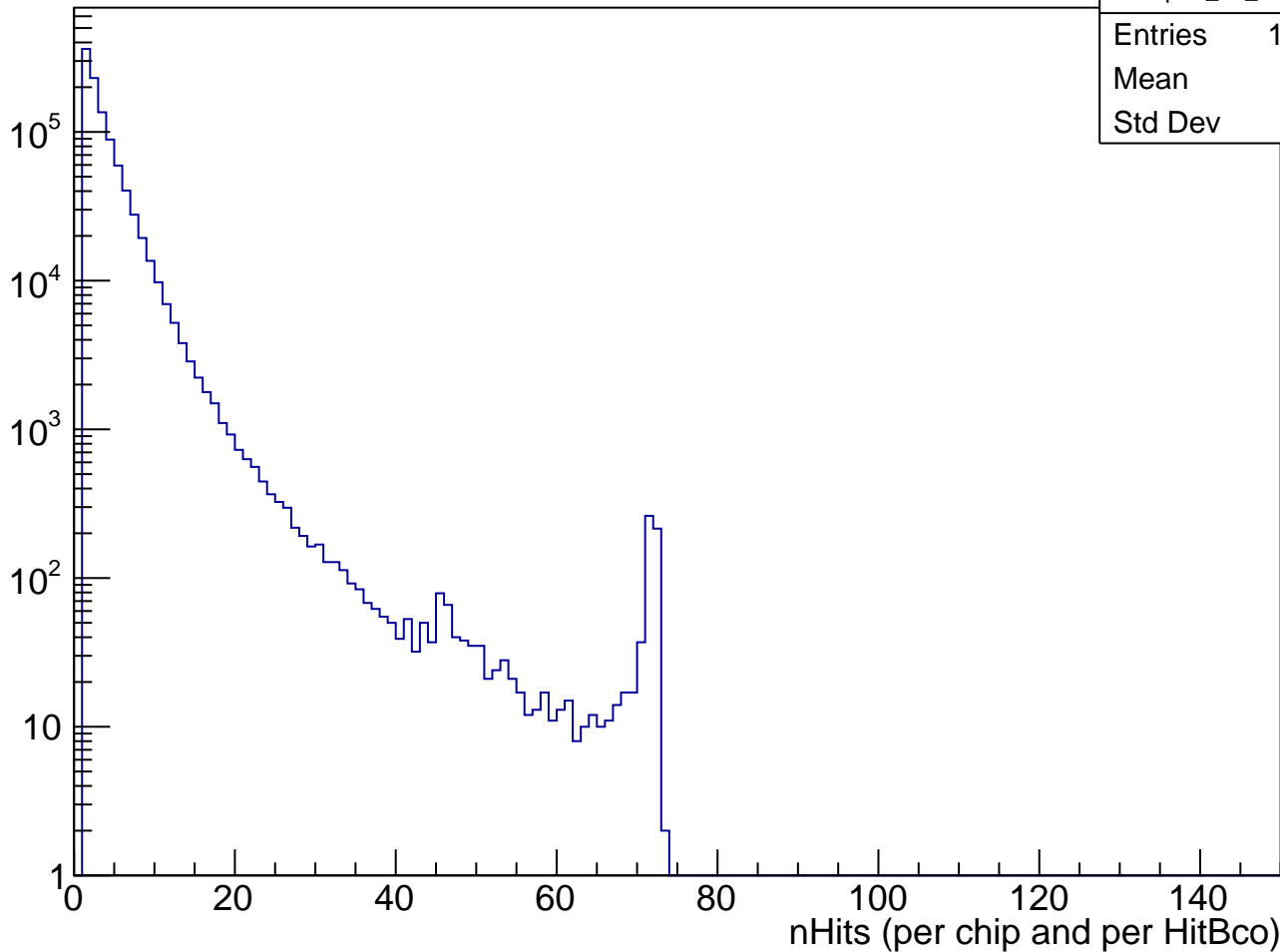


nChipHit_F5_Fch0_Chip5	
Entries	1006940
Mean	3.172
Std Dev	3.74

nHits (per chip and per HitBco)

nChipHit_F5_Fch0_Chip6

Entries



nChipHit_F5_Fch0_Chip6

Entries 1019579

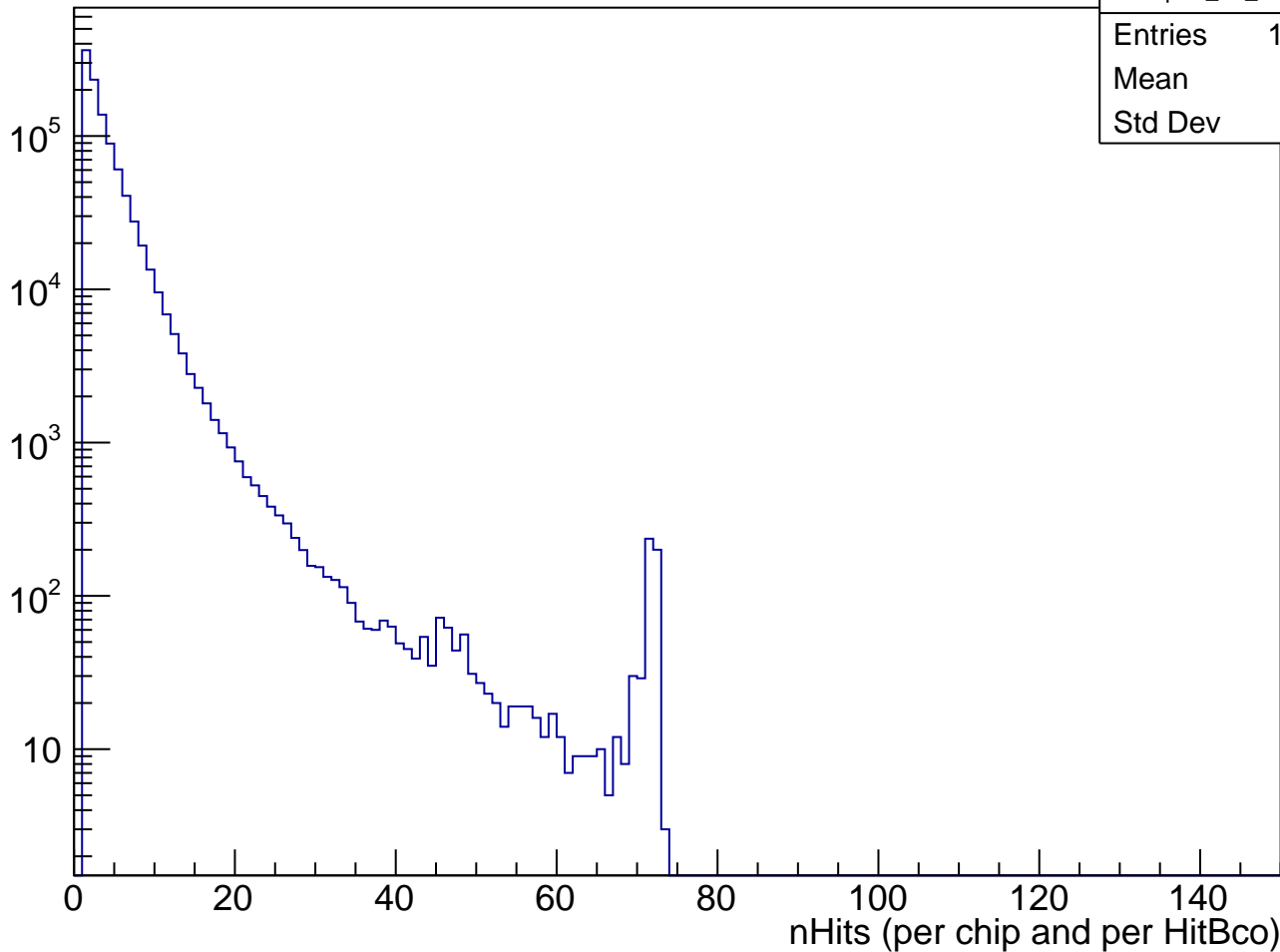
Mean 3.184

Std Dev 3.699

nHits (per chip and per HitBco)

nChipHit_F5_Fch0_Chip7

Entries

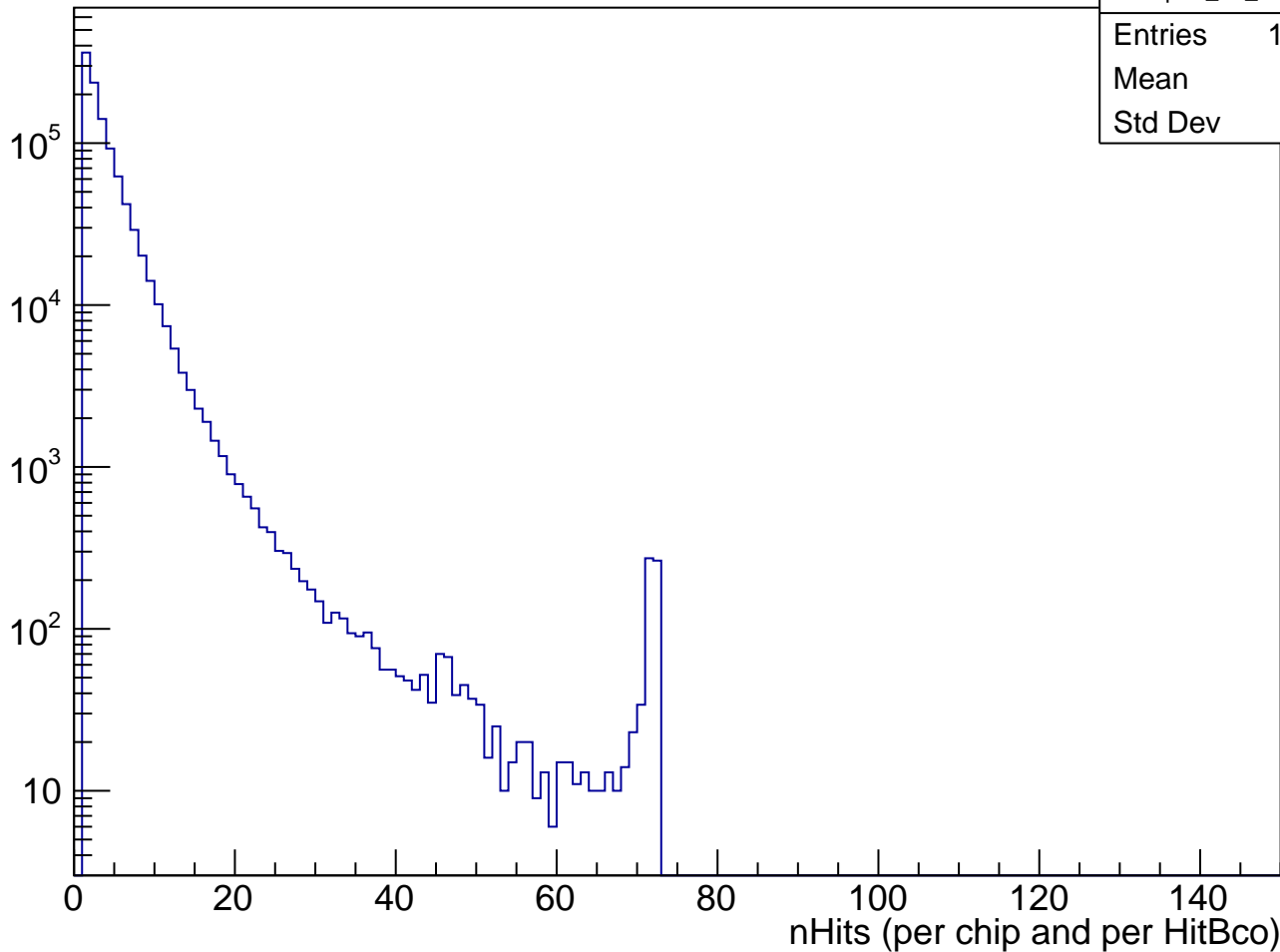


nChipHit_F5_Fch0_Chip7	
Entries	1026834
Mean	3.173
Std Dev	3.649

nHits (per chip and per HitBco)

nChipHit_F5_Fch0_Chip8

Entries



nChipHit_F5_Fch0_Chip8

Entries 1043908

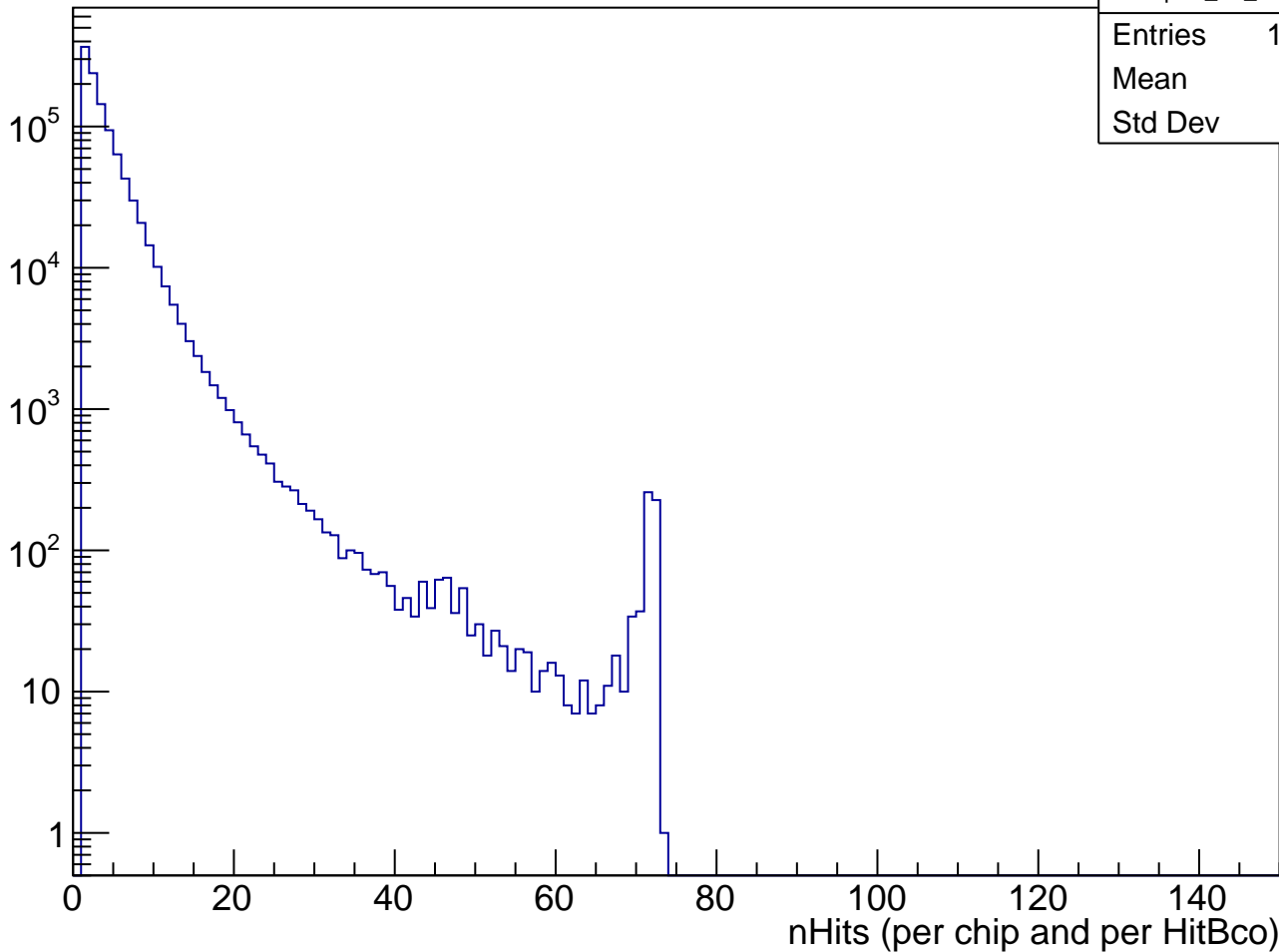
Mean 3.215

Std Dev 3.72

nHits (per chip and per HitBco)

nChipHit_F5_Fch0_Chip9

Entries



nChipHit_F5_Fch0_Chip9

Entries 1058805

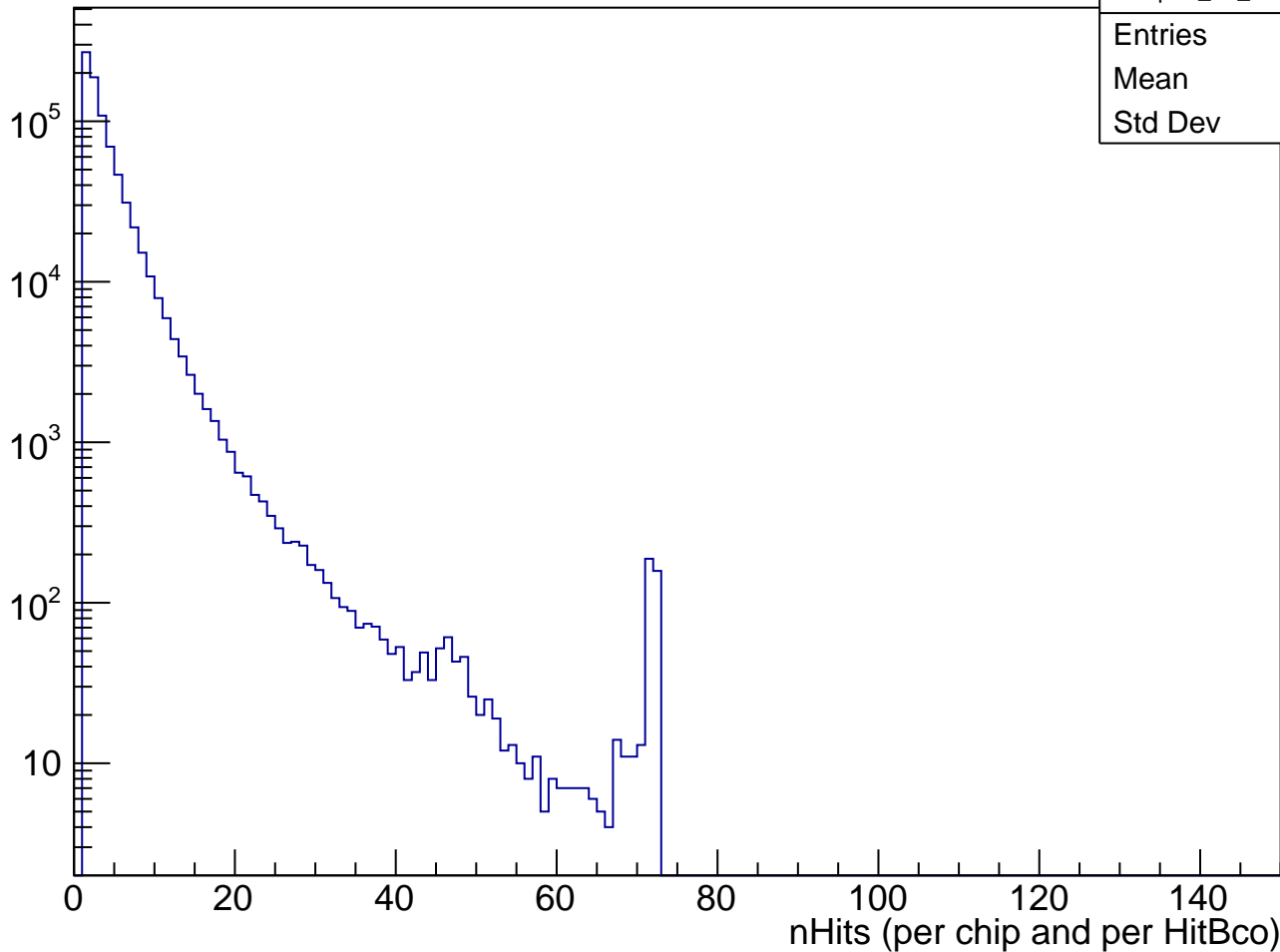
Mean 3.222

Std Dev 3.691

nHits (per chip and per HitBco)

nChipHit_F5_Fch10_Chip0

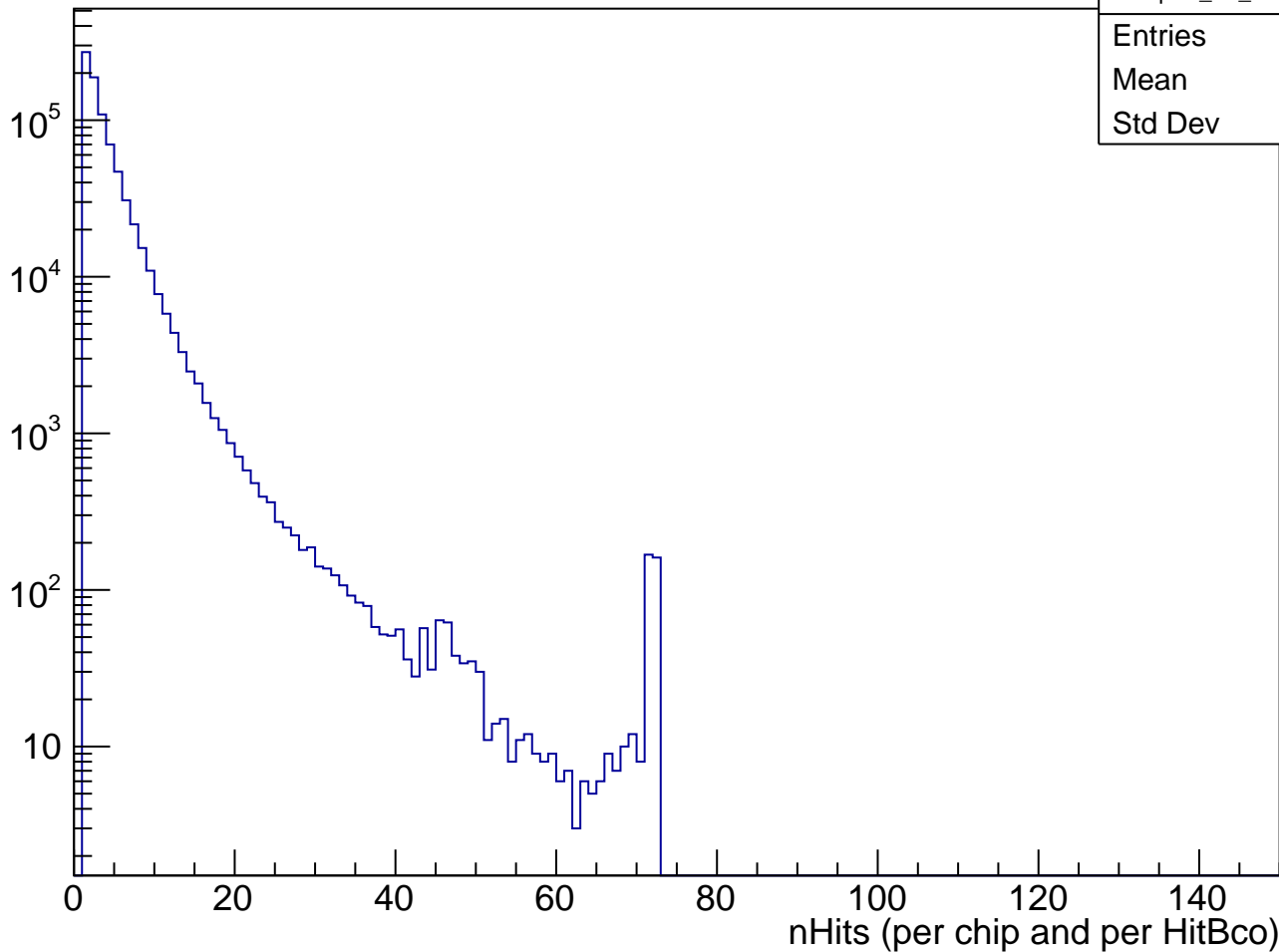
Entries



nChipHit_F5_Fch10_Chip0	
Entries	797465
Mean	3.266
Std Dev	3.779

nChipHit_F5_Fch10_Chip1

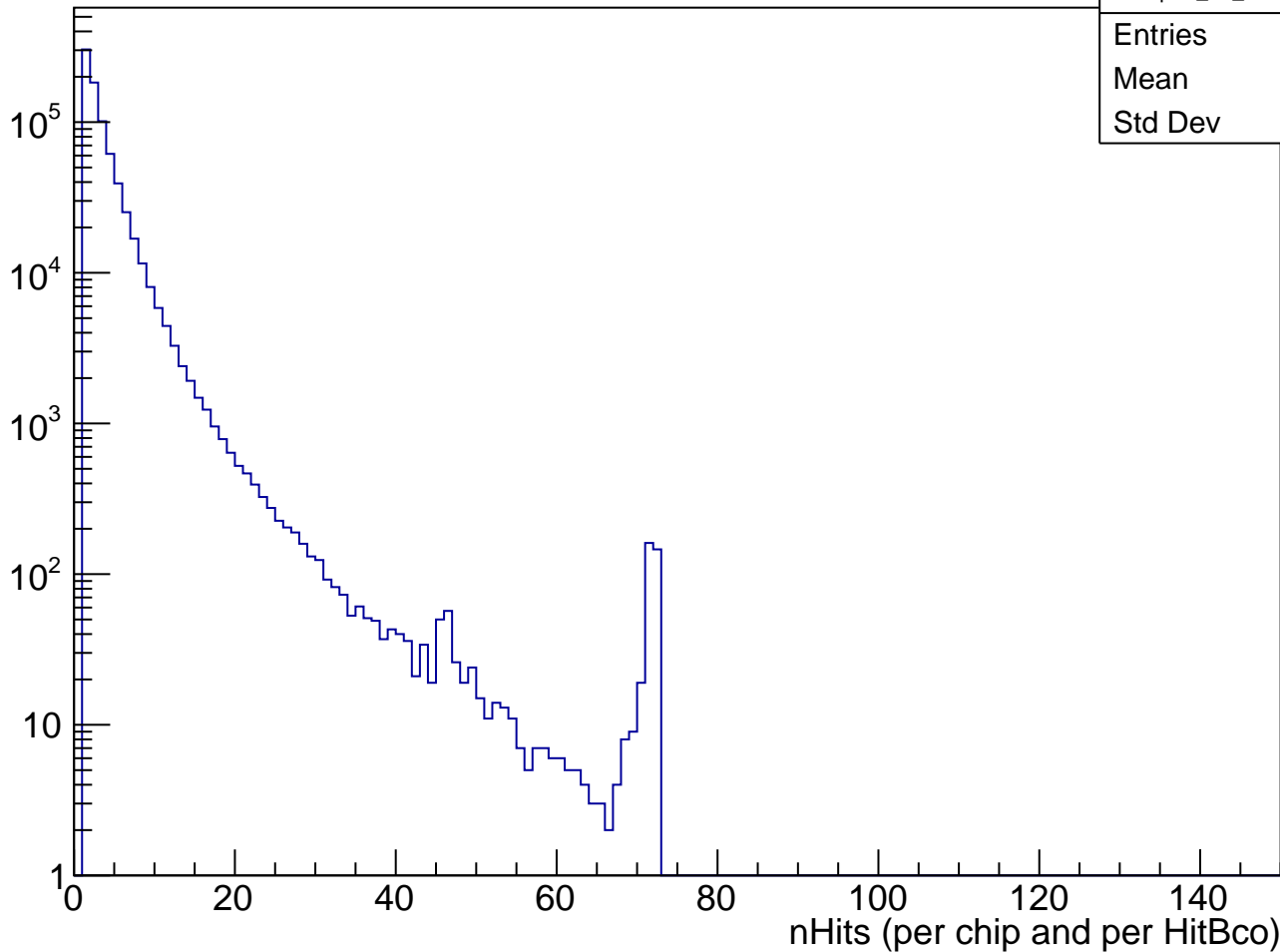
Entries



nChipHit_F5_Fch10_Chip1	
Entries	800357
Mean	3.249
Std Dev	3.742

nChipHit_F5_Fch10_Chip10

Entries



nChipHit_F5_Fch10_Chip10

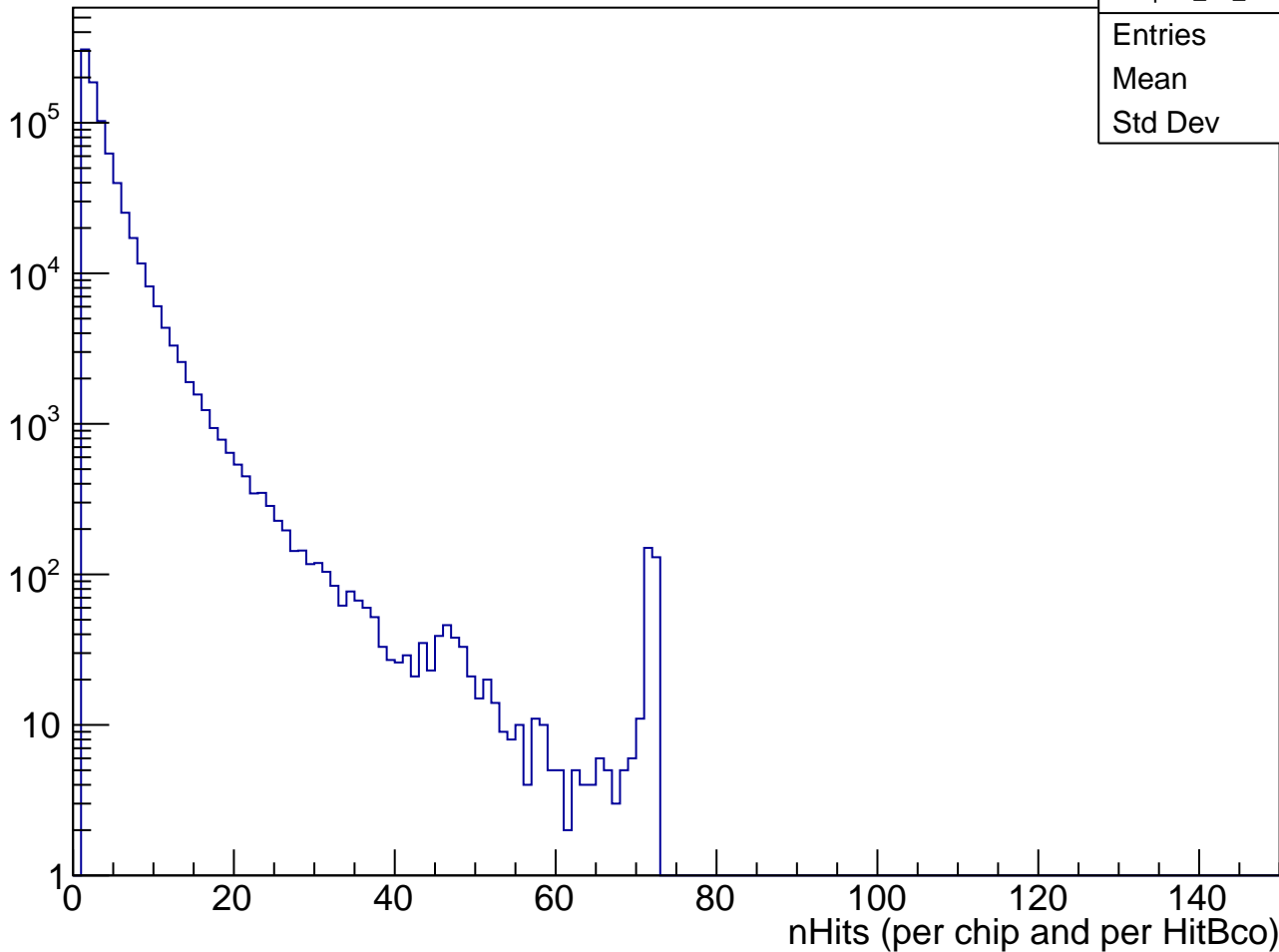
Entries 777815

Mean 2.925

Std Dev 3.457

nChipHit_F5_Fch10_Chip11

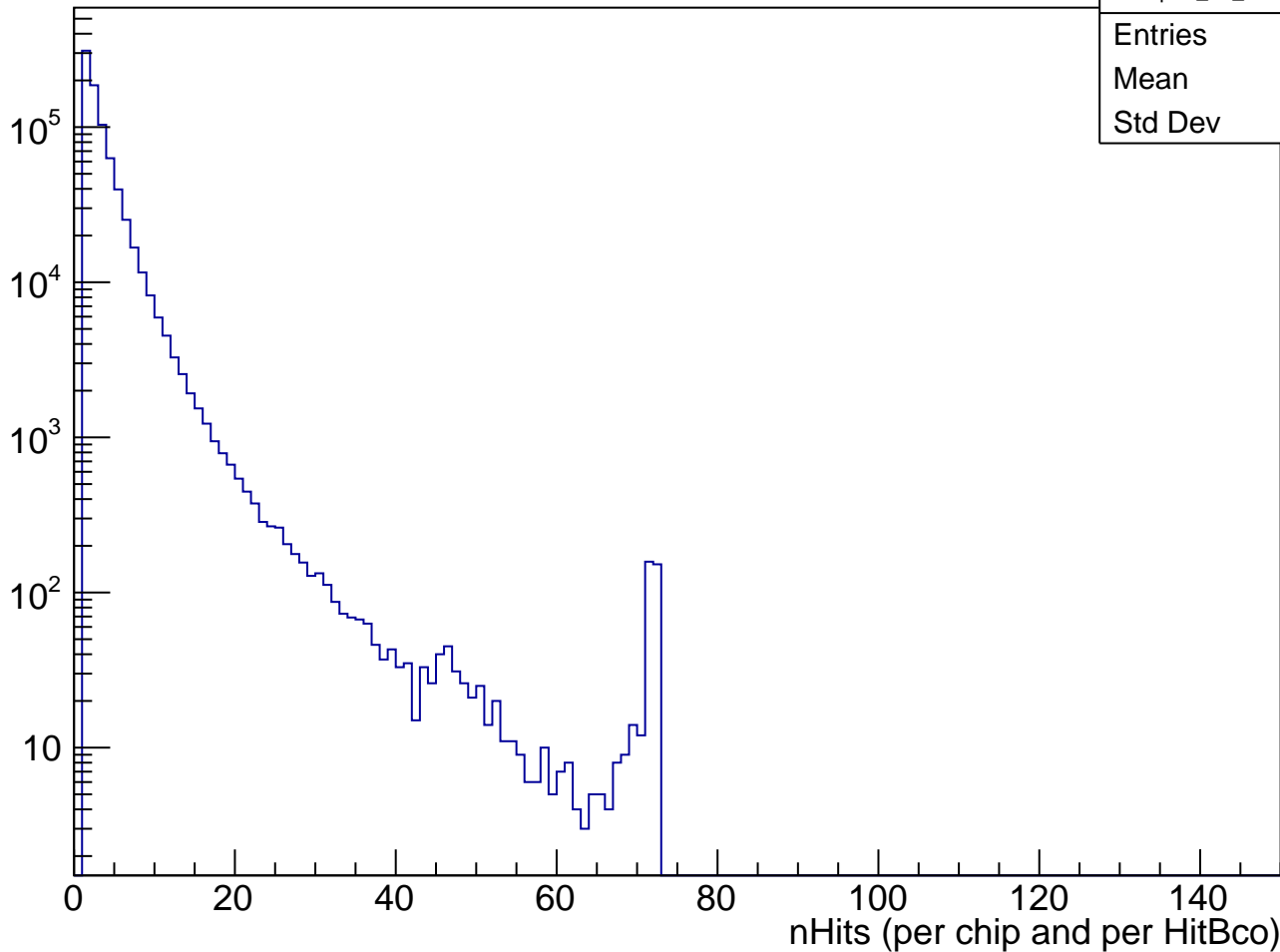
Entries



nChipHit_F5_Fch10_Chip11	
Entries	787434
Mean	2.918
Std Dev	3.408

nChipHit_F5_Fch10_Chip12

Entries



nChipHit_F5_Fch10_Chip12

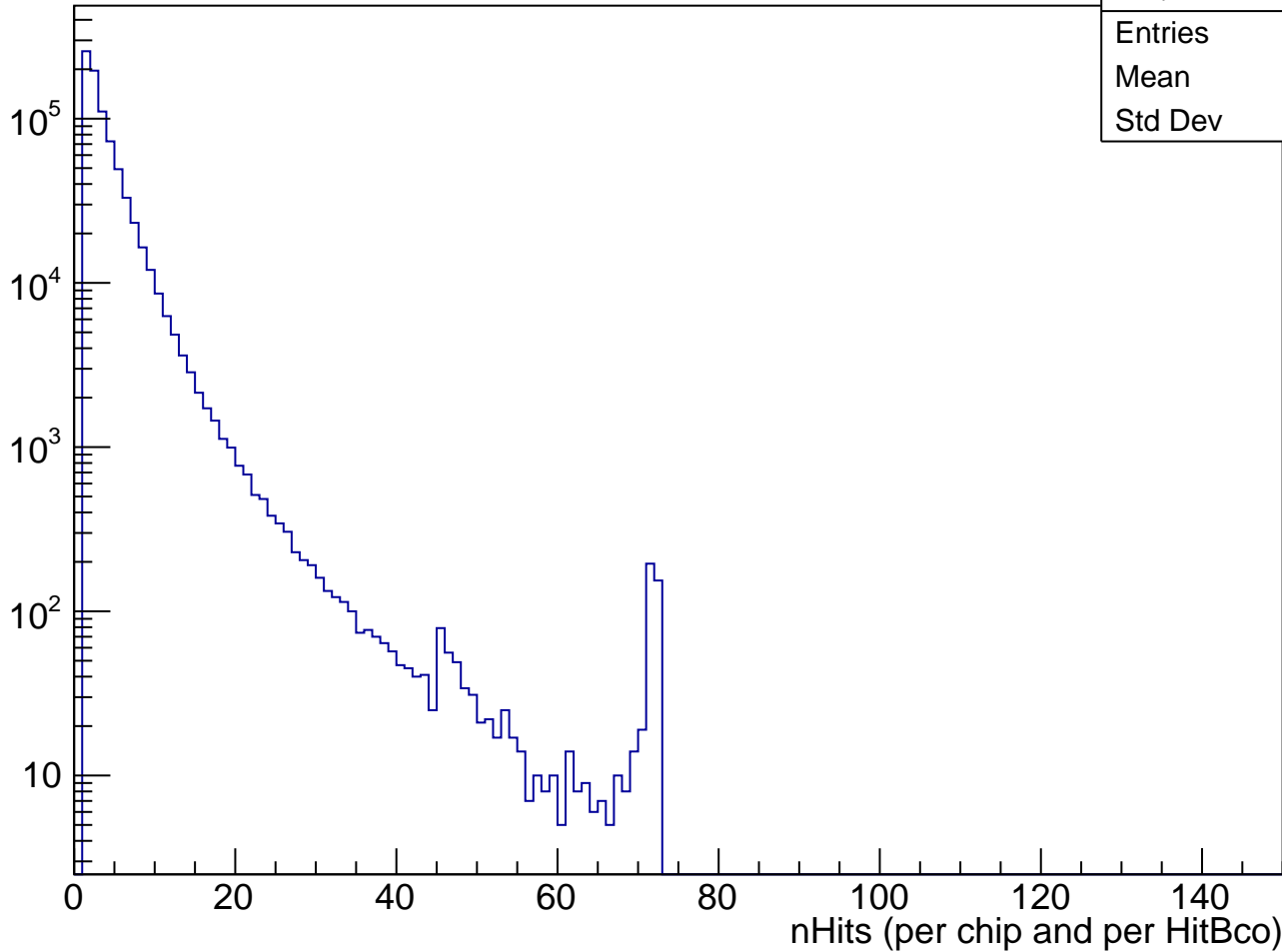
Entries 792777

Mean 2.915

Std Dev 3.459

nChipHit_F5_Fch10_Chip13

Entries



nChipHit_F5_Fch10_Chip13

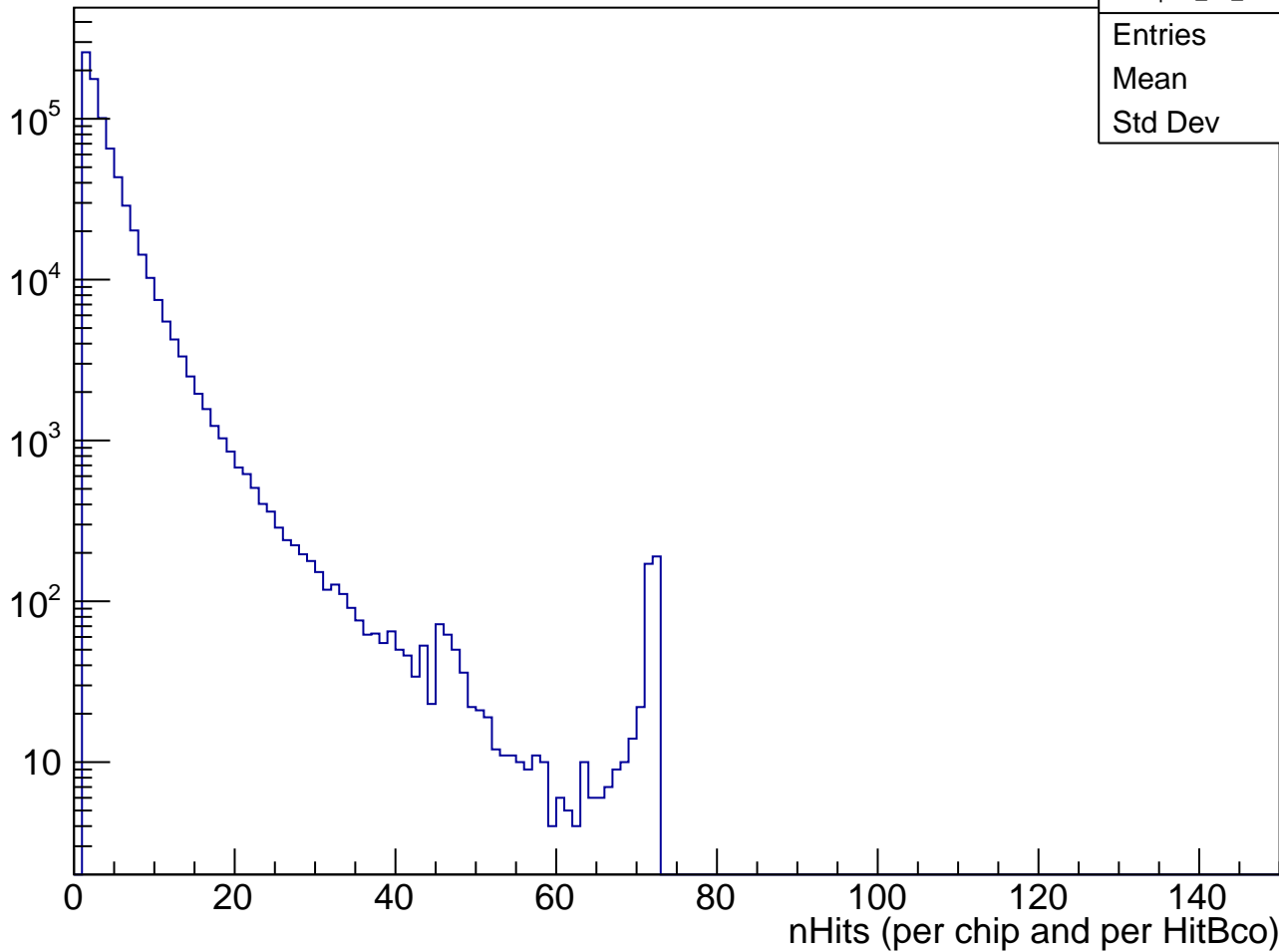
Entries 810648

Mean 3.368

Std Dev 3.853

nChipHit_F5_Fch10_Chip14

Entries



nChipHit_F5_Fch10_Chip14

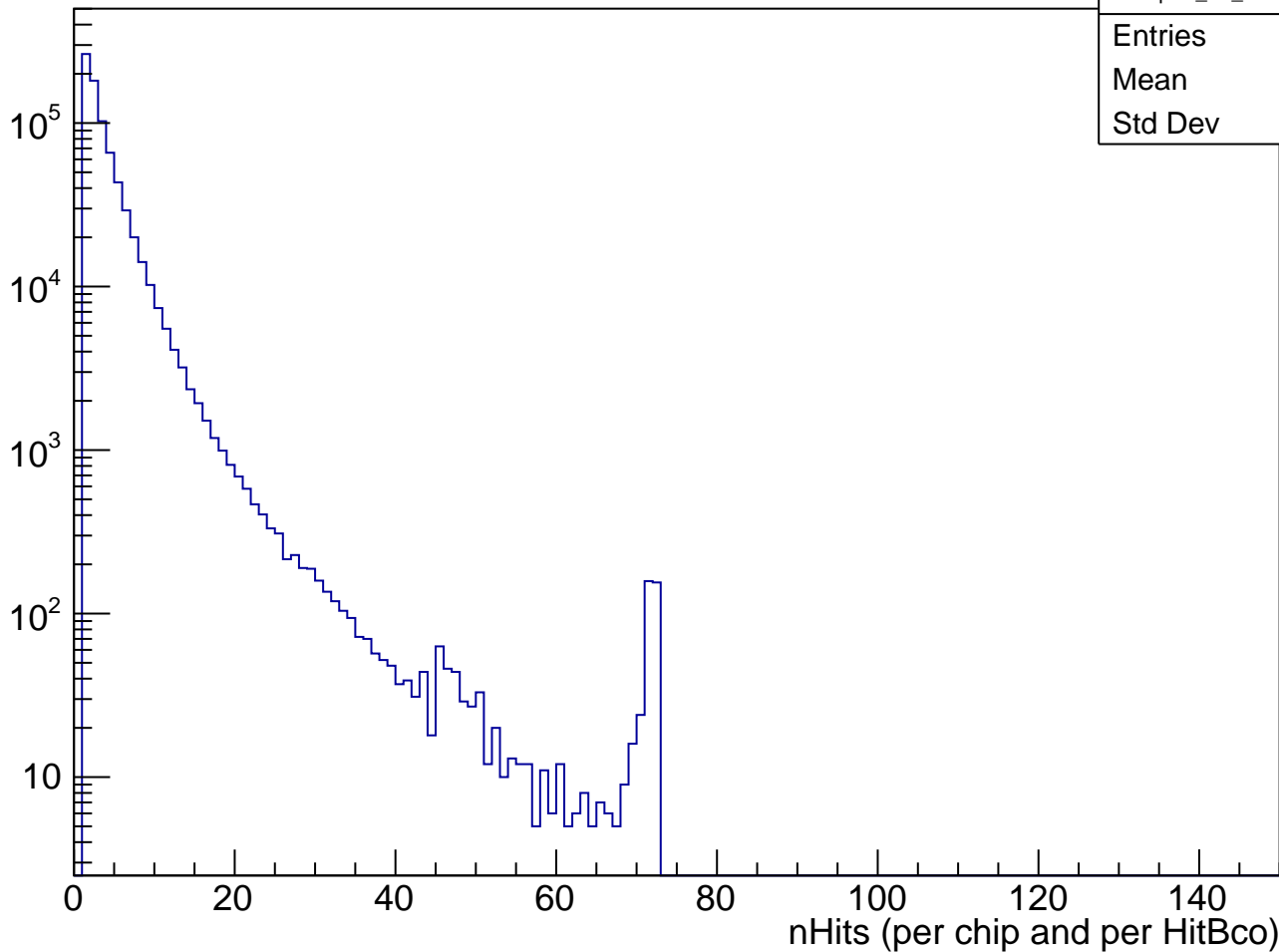
Entries 754542

Mean 3.266

Std Dev 3.854

nChipHit_F5_Fch10_Chip15

Entries



nChipHit_F5_Fch10_Chip15

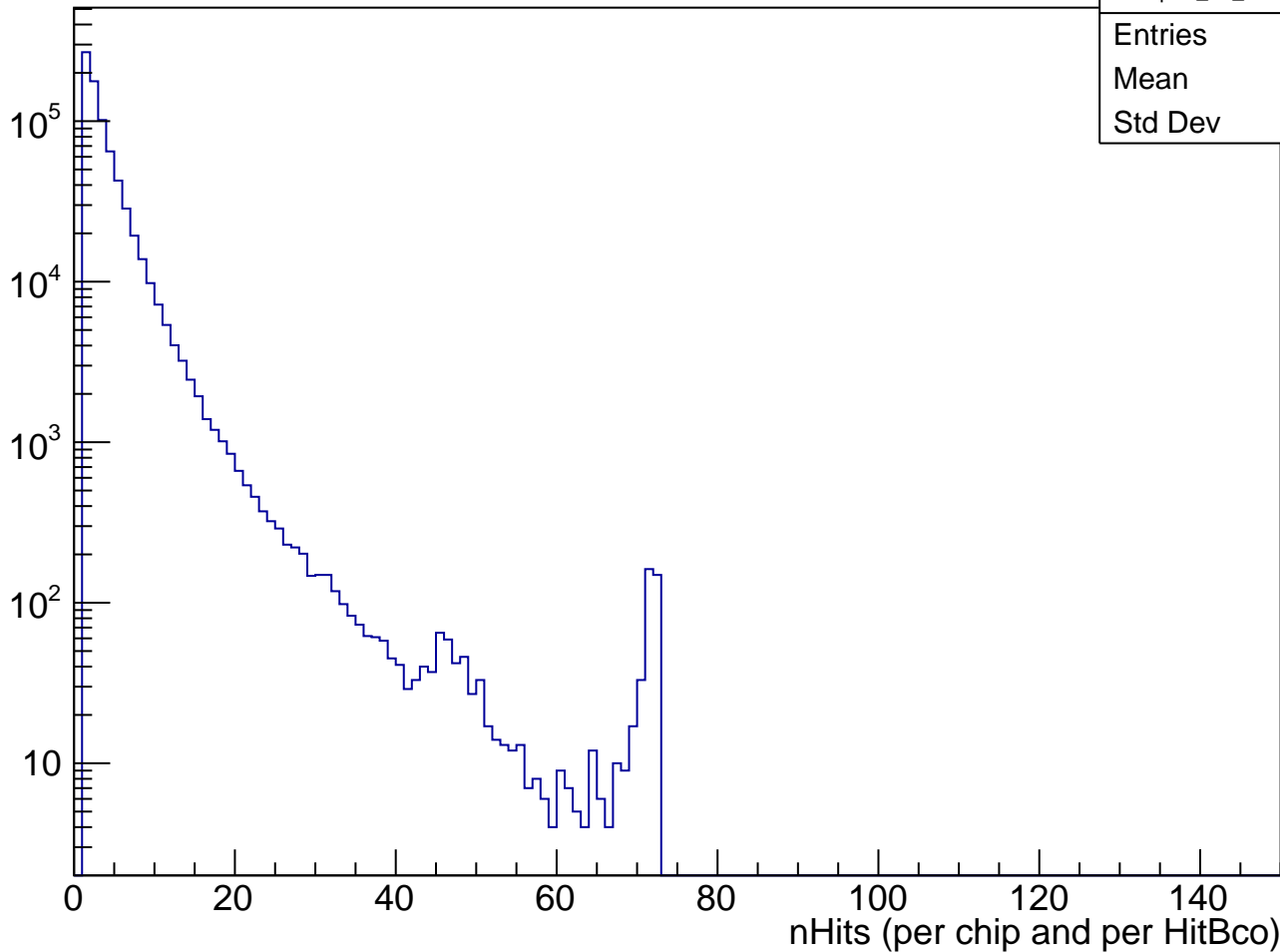
Entries 765814

Mean 3.223

Std Dev 3.759

nChipHit_F5_Fch10_Chip16

Entries



nChipHit_F5_Fch10_Chip16

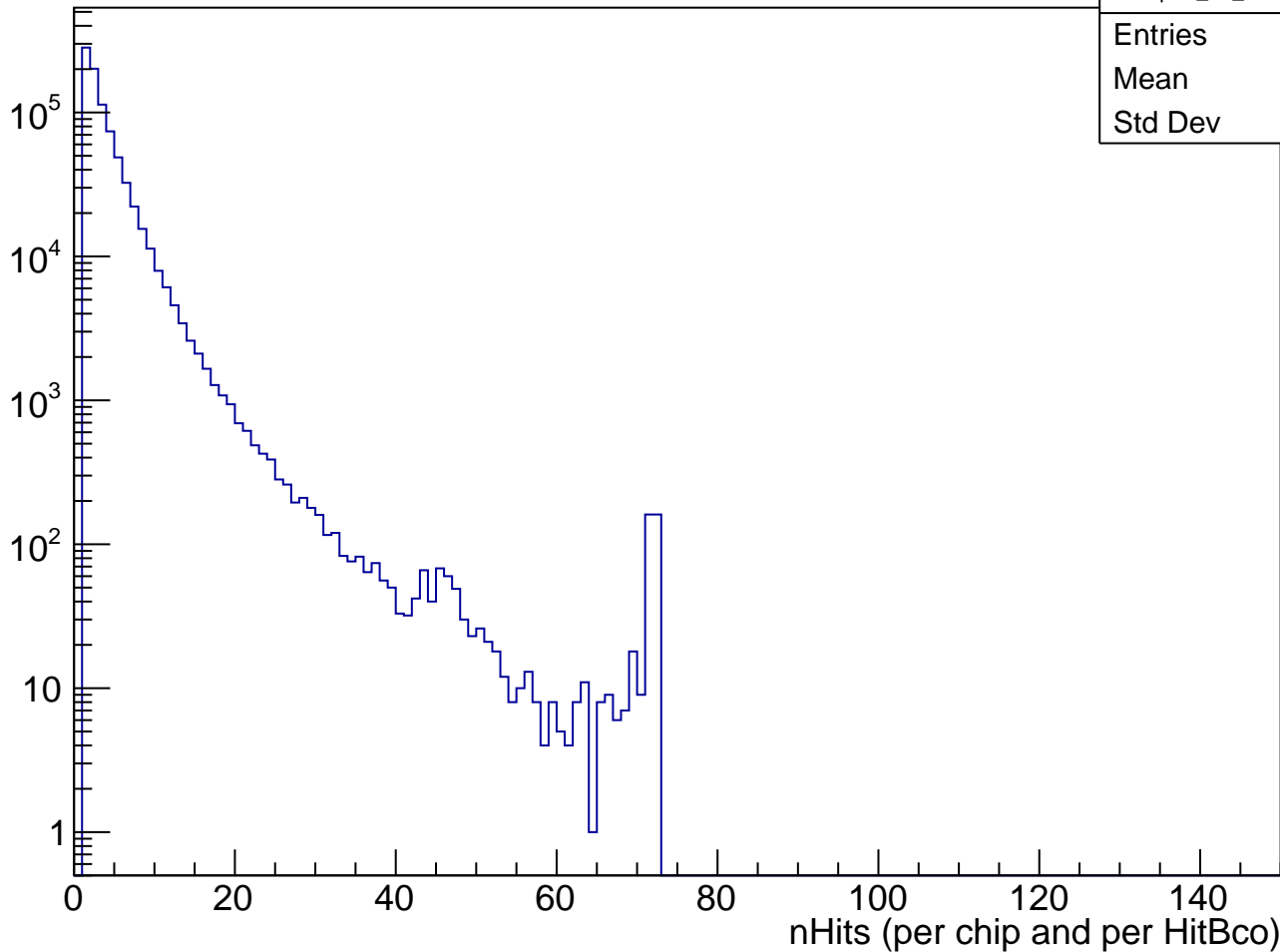
Entries 760653

Mean 3.198

Std Dev 3.769

nChipHit_F5_Fch10_Chip17

Entries

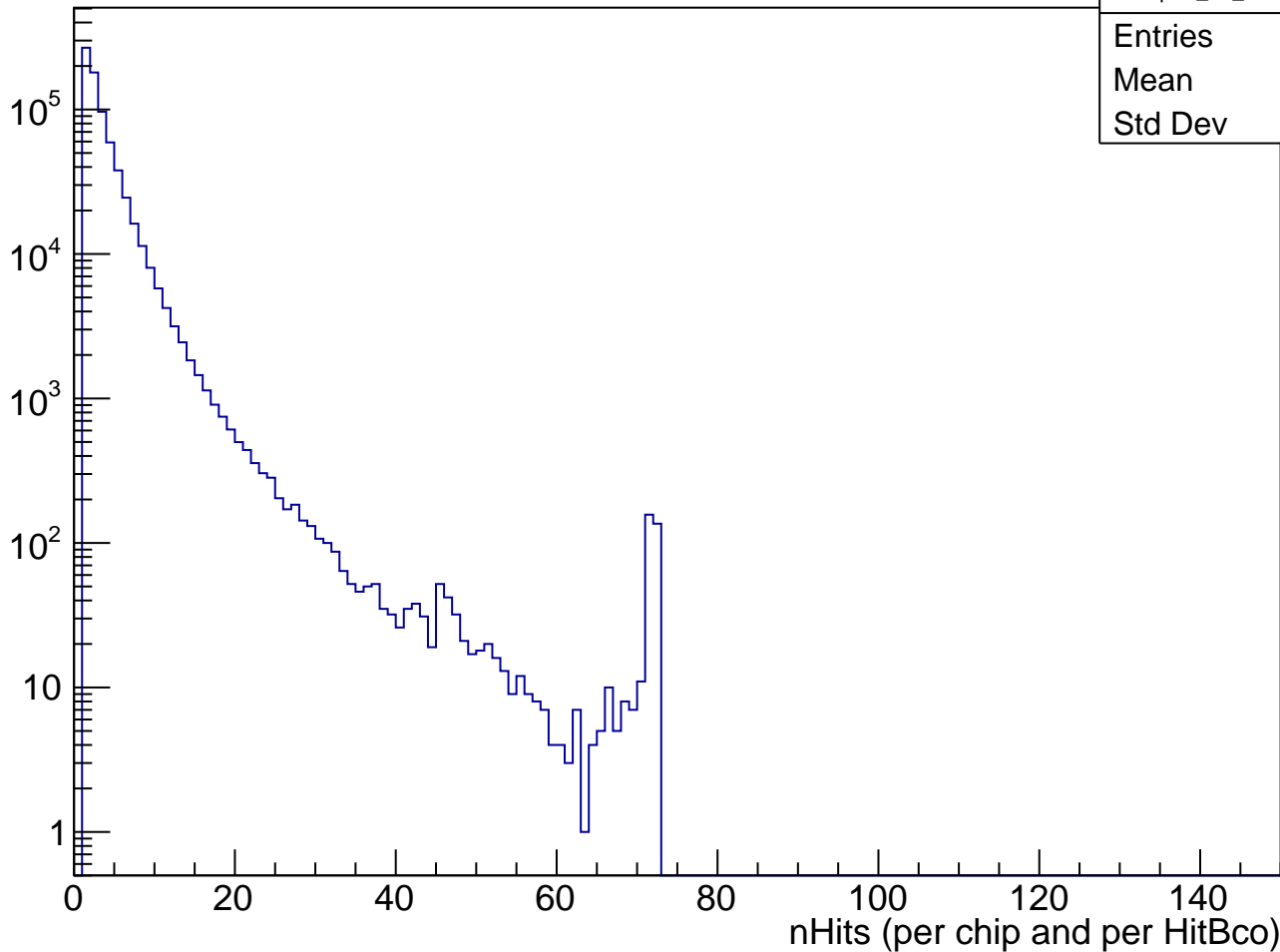


nChipHit_F5_Fch10_Chip17	
Entries	838627
Mean	3.234
Std Dev	3.694

nHits (per chip and per HitBco)

nChipHit_F5_Fch10_Chip18

Entries



nChipHit_F5_Fch10_Chip18

Entries 728091

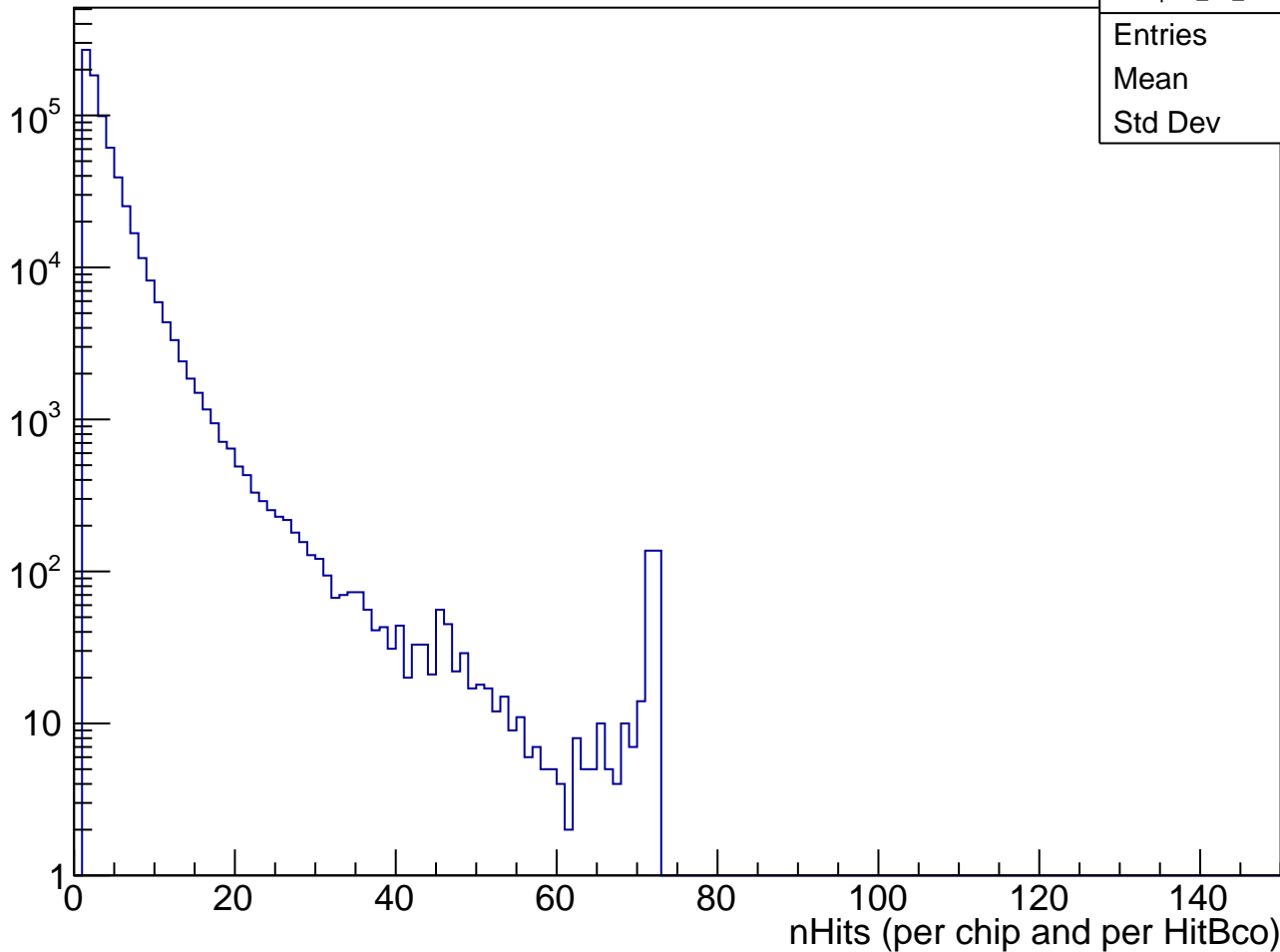
Mean 2.991

Std Dev 3.491

nHits (per chip and per HitBco)

nChipHit_F5_Fch10_Chip19

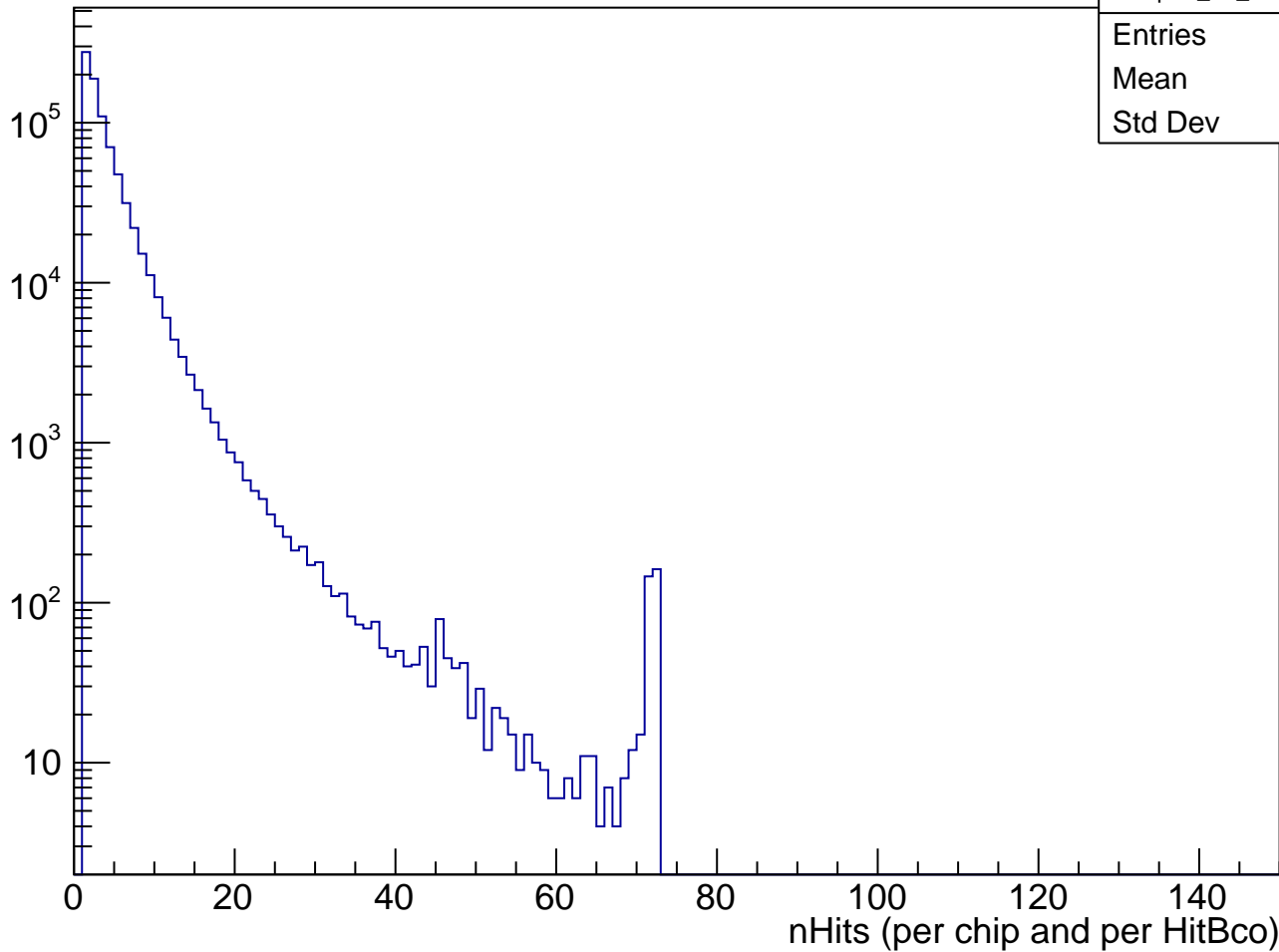
Entries



nChipHit_F5_Fch10_Chip19	
Entries	741355
Mean	3.002
Std Dev	3.474

nChipHit_F5_Fch10_Chip2

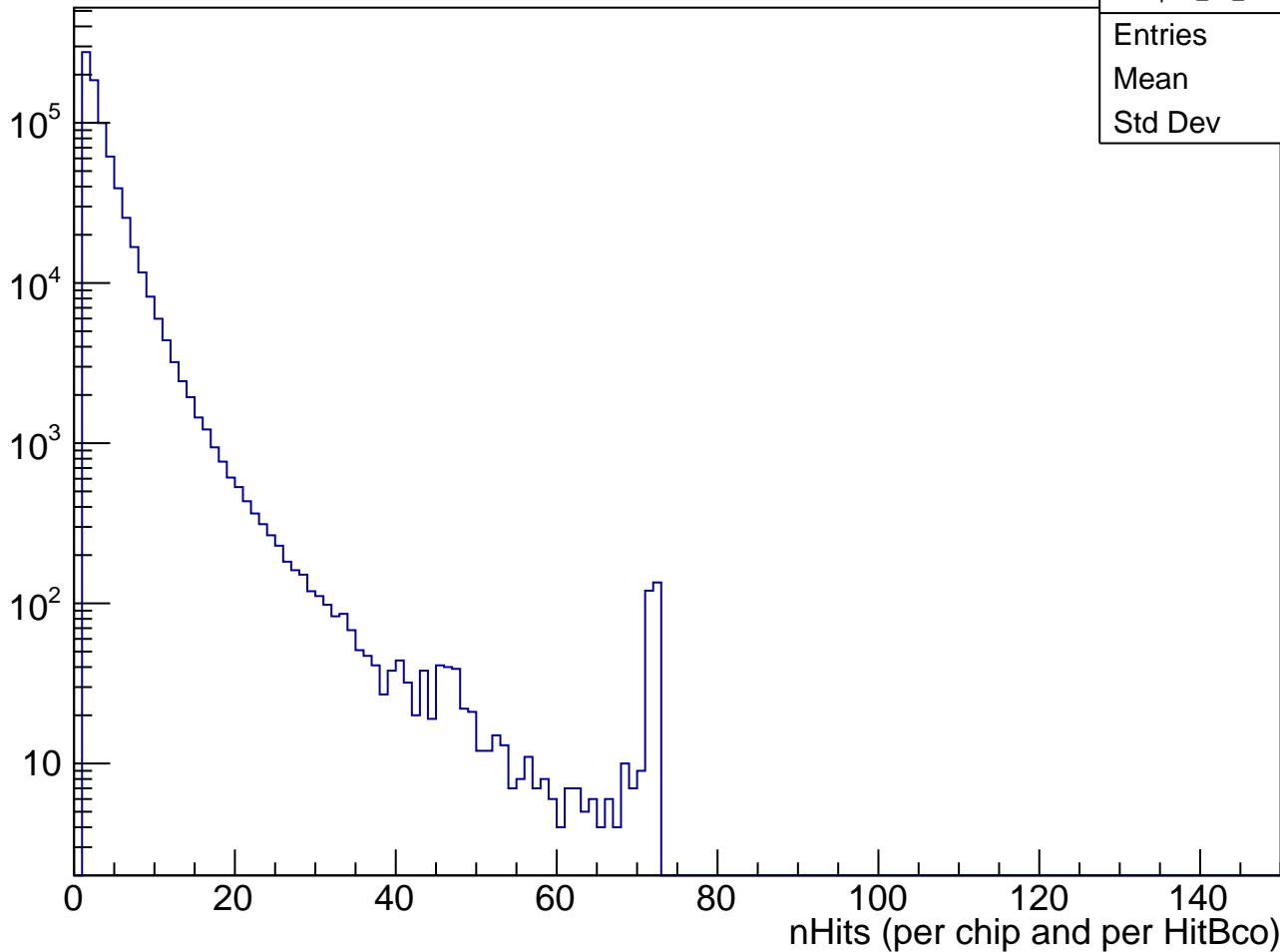
Entries



nChipHit_F5_Fch10_Chip2	
Entries	810008
Mean	3.259
Std Dev	3.752

nChipHit_F5_Fch10_Chip20

Entries

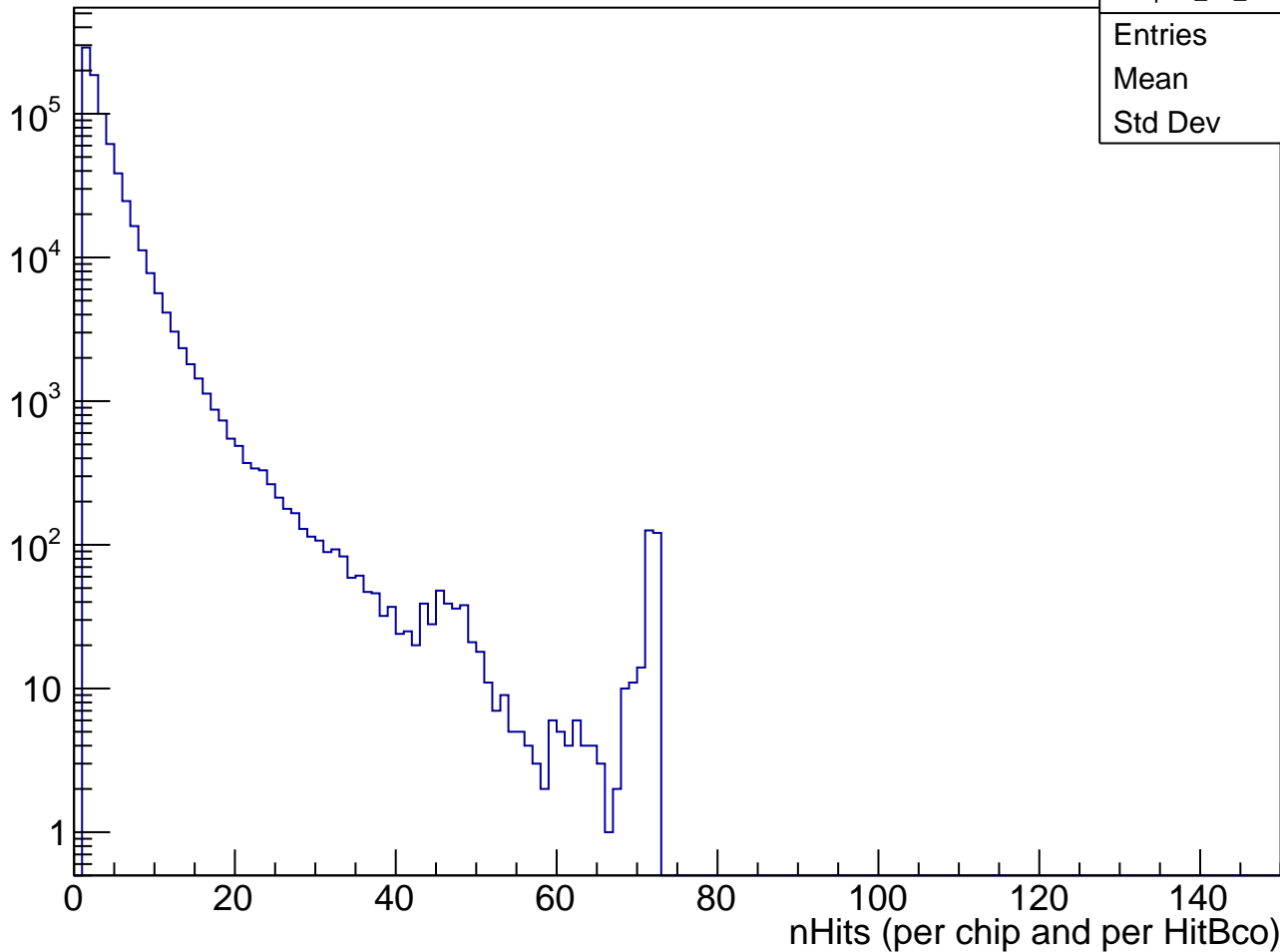


nChipHit_F5_Fch10_Chip20	
Entries	750815
Mean	2.983
Std Dev	3.432

nHits (per chip and per HitBco)

nChipHit_F5_Fch10_Chip21

Entries

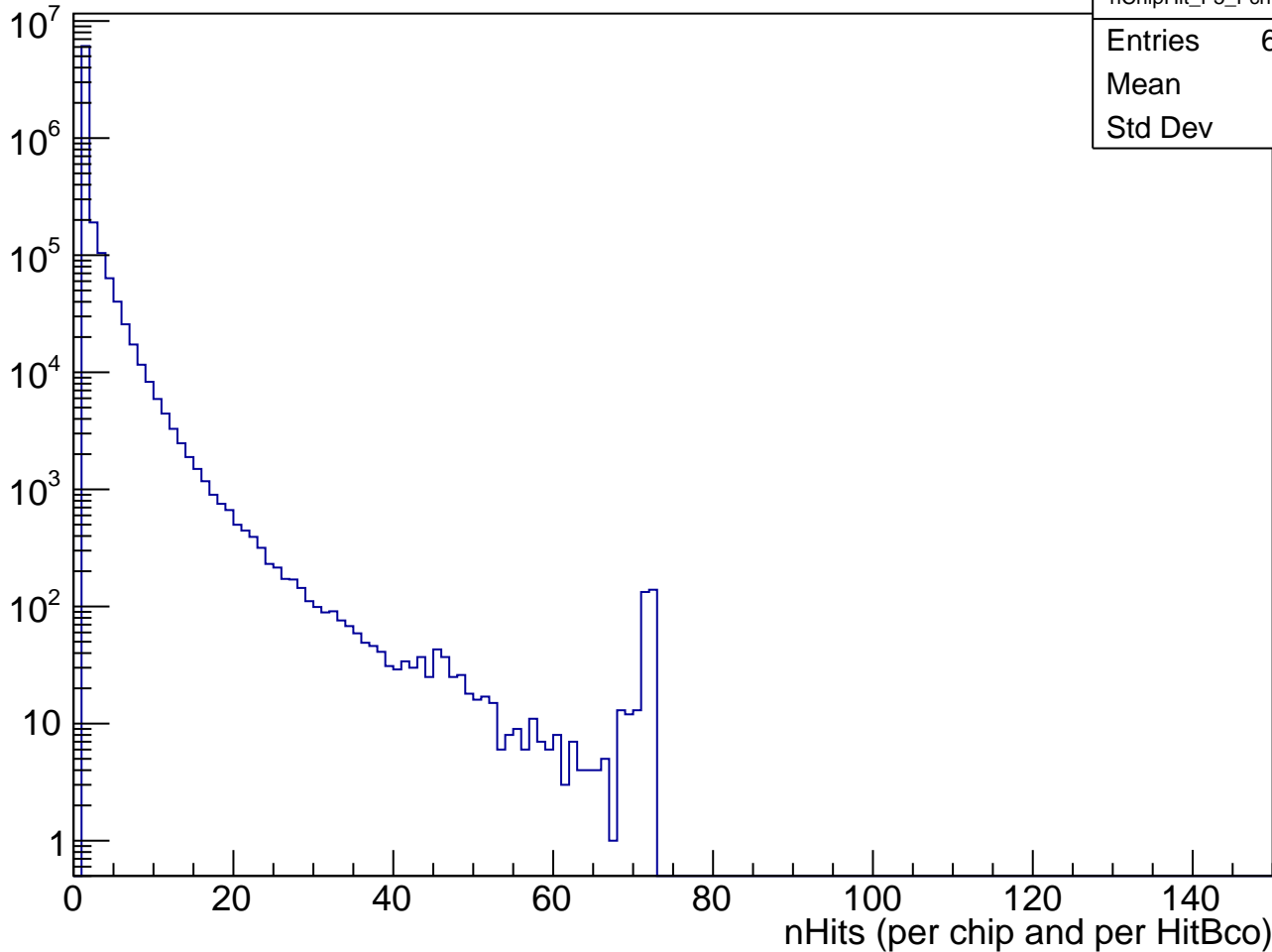


nChipHit_F5_Fch10_Chip21	
Entries	760628
Mean	2.914
Std Dev	3.353

nHits (per chip and per HitBco)

nChipHit_F5_Fch10_Chip22

Entries



nChipHit_F5_Fch10_Chip22

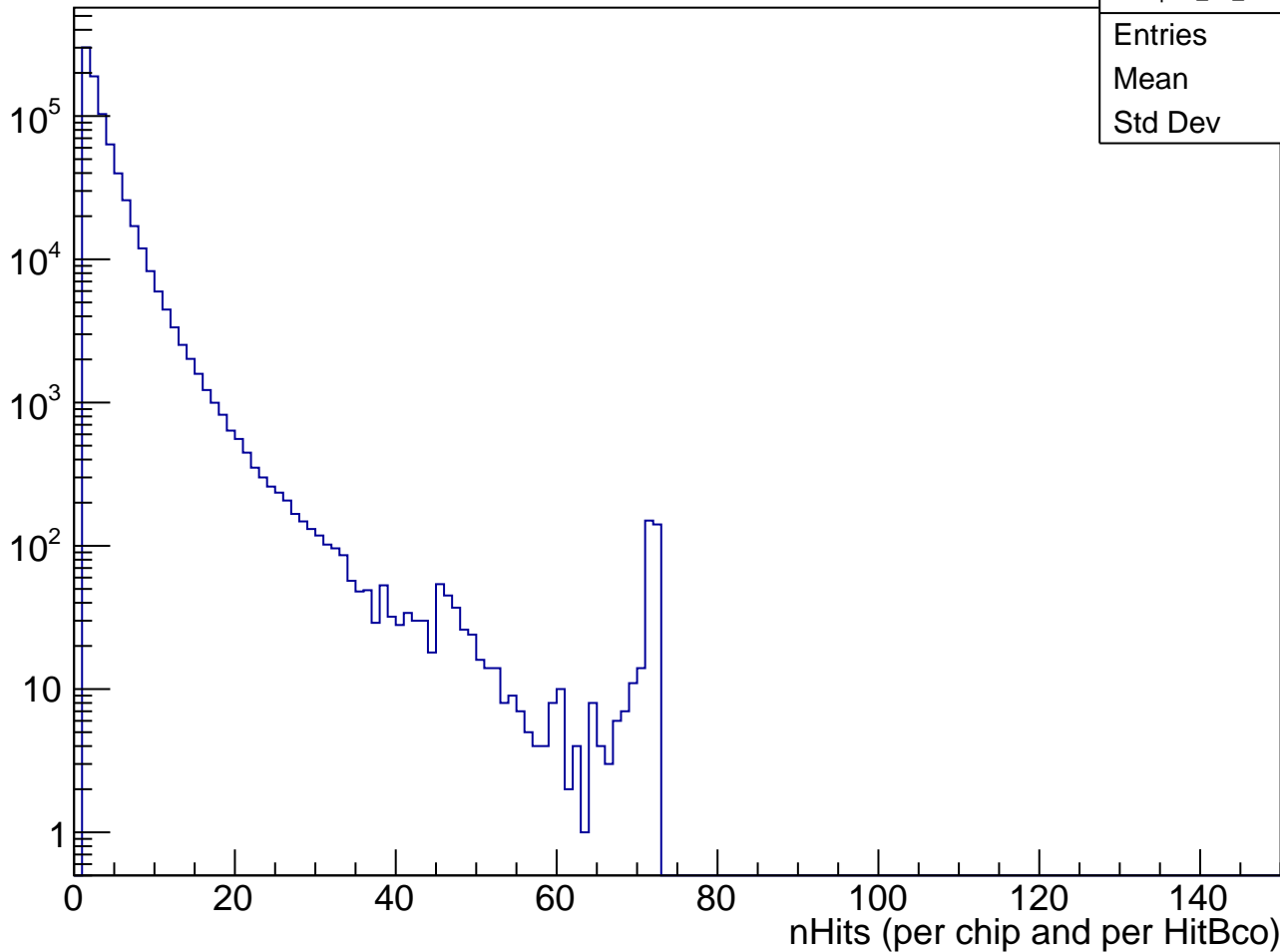
Entries 6600323

Mean 1.23

Std Dev 1.326

nChipHit_F5_Fch10_Chip23

Entries



nChipHit_F5_Fch10_Chip23

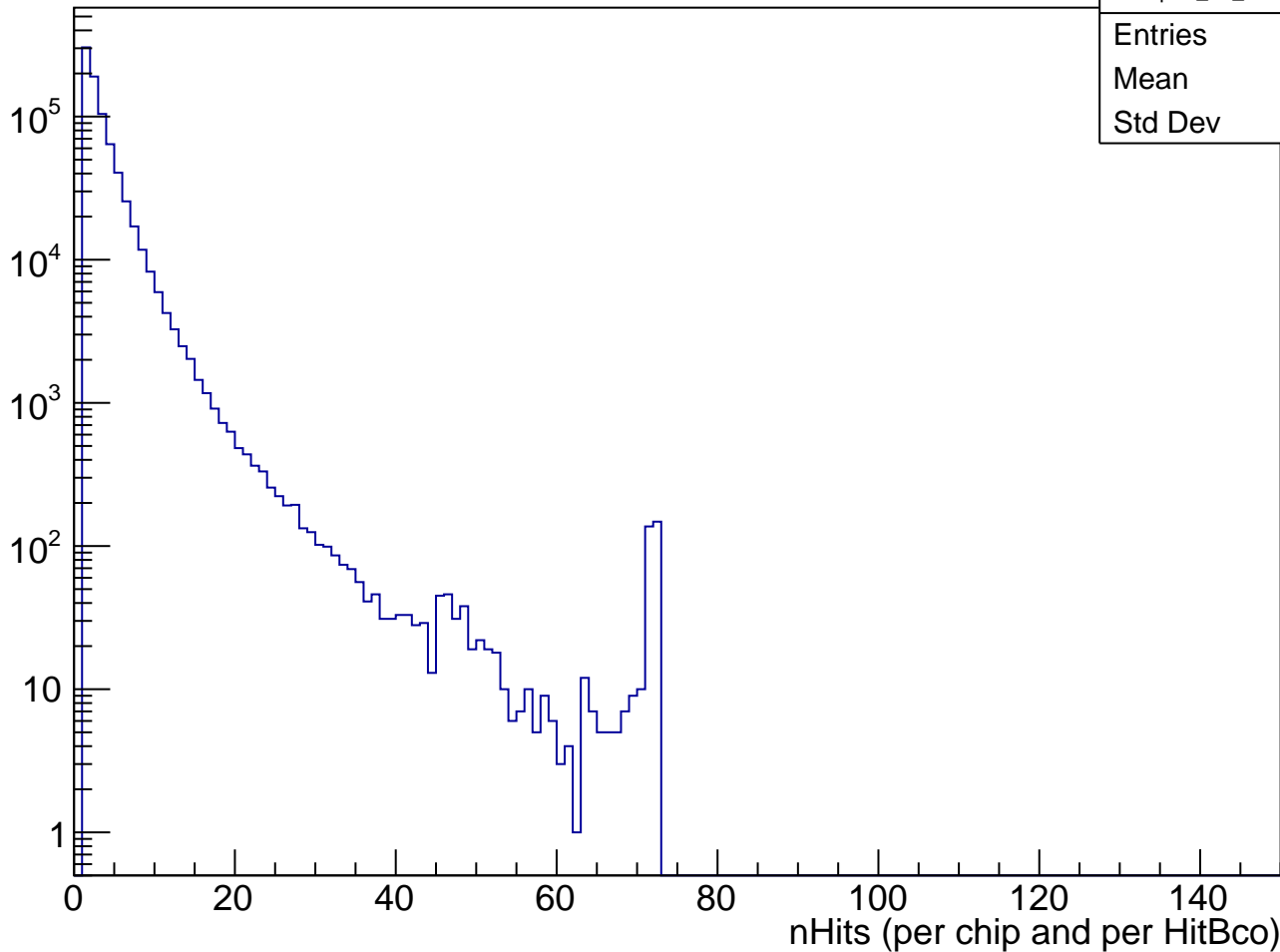
Entries 788012

Mean 2.939

Std Dev 3.429

nChipHit_F5_Fch10_Chip24

Entries



nChipHit_F5_Fch10_Chip24

Entries 793122

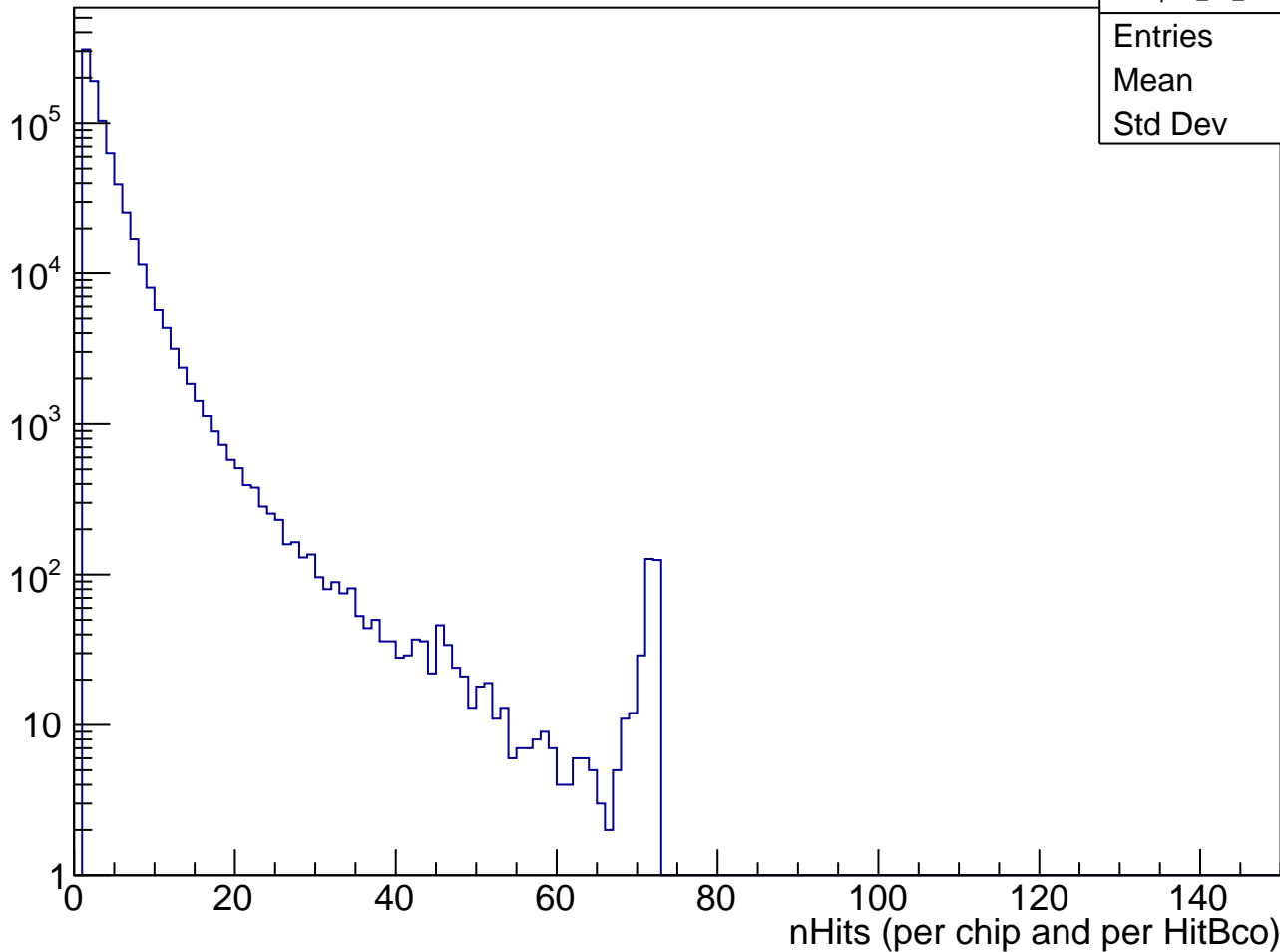
Mean 2.918

Std Dev 3.392

nHits (per chip and per HitBco)

nChipHit_F5_Fch10_Chip25

Entries

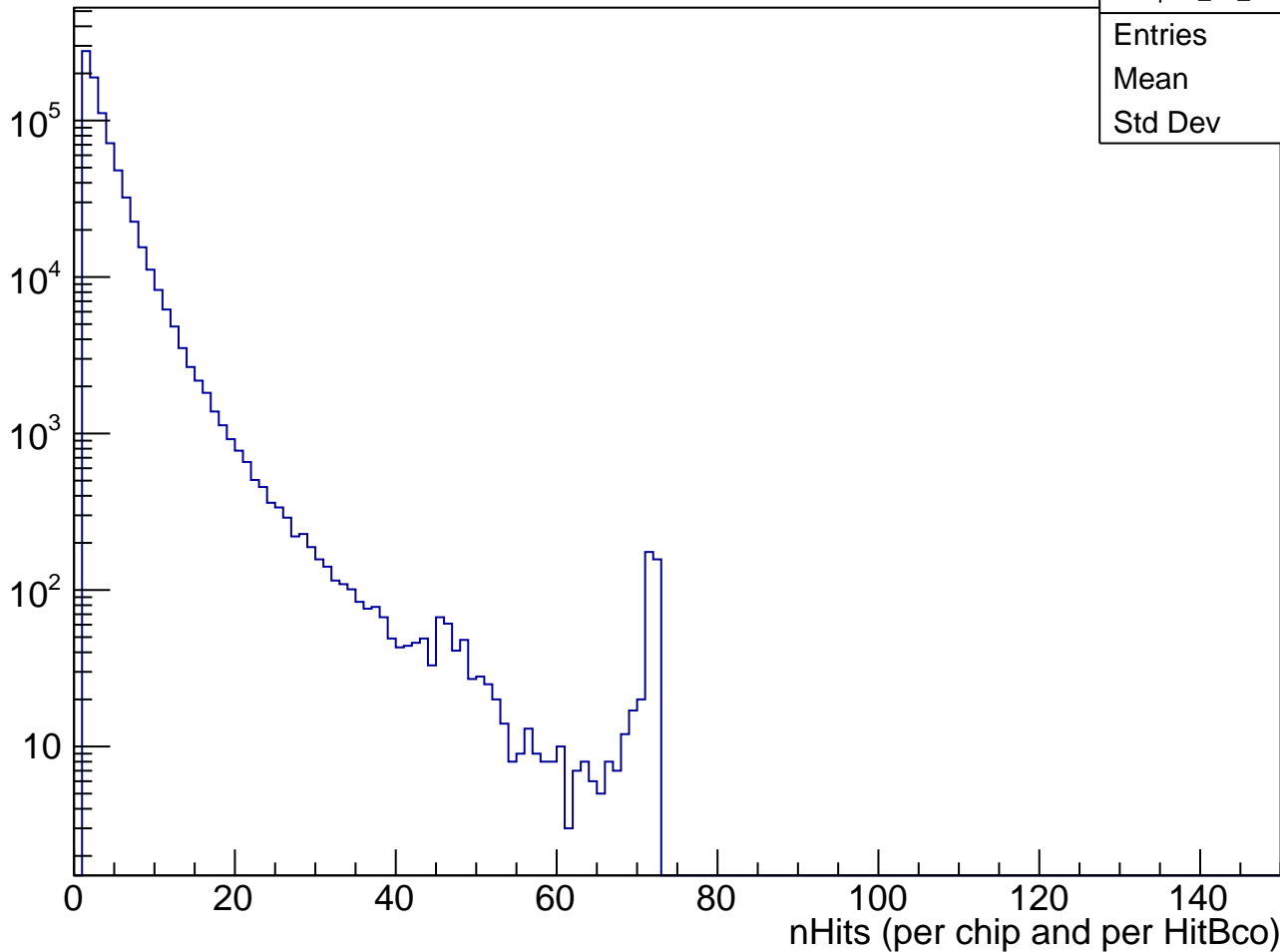


nChipHit_F5_Fch10_Chip25	
Entries	792151
Mean	2.885
Std Dev	3.344

nHits (per chip and per HitBco)

nChipHit_F5_Fch10_Chip3

Entries

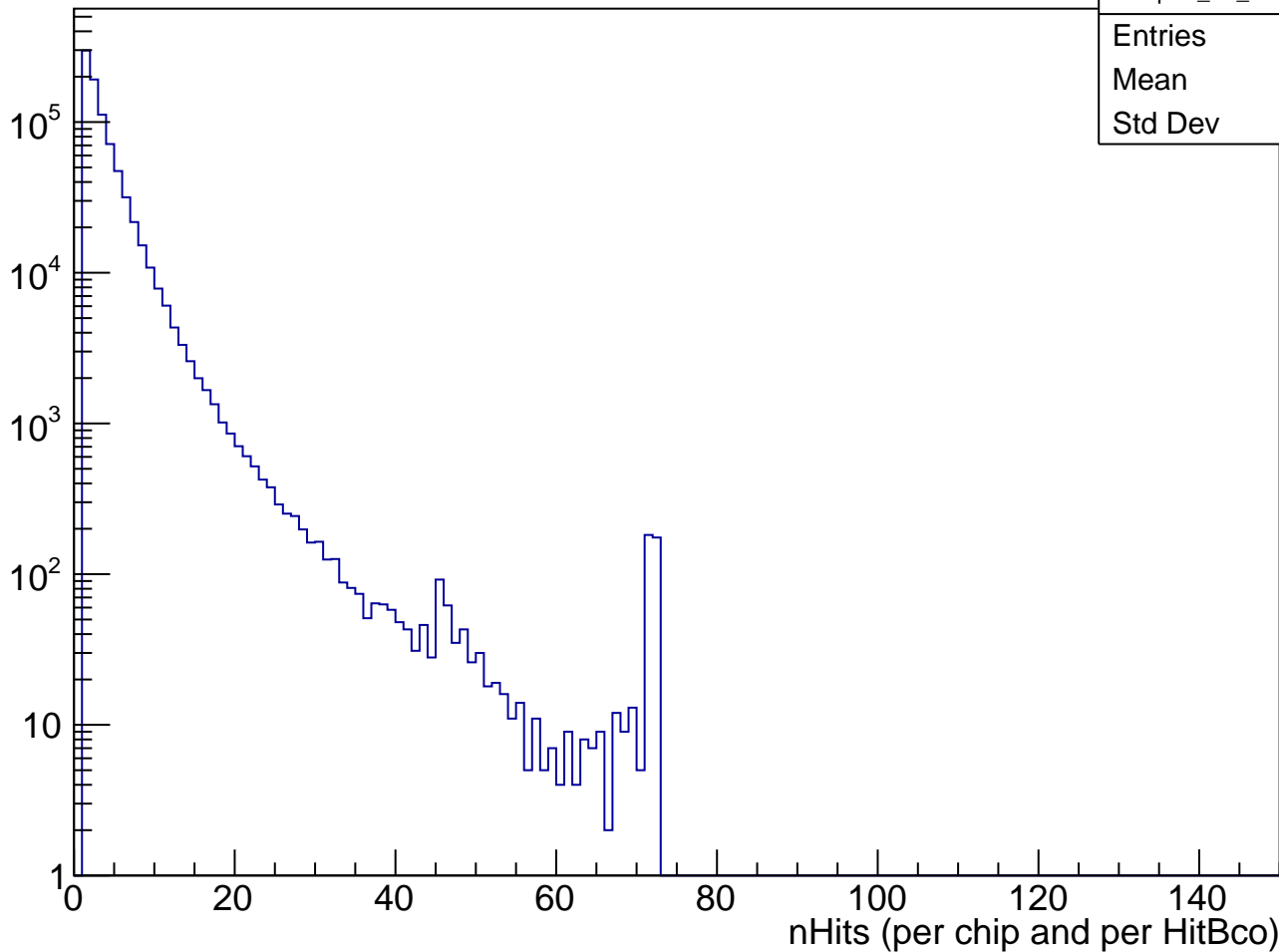


nChipHit_F5_Fch10_Chip3	
Entries	817674
Mean	3.29
Std Dev	3.814

nHits (per chip and per HitBco)

nChipHit_F5_Fch10_Chip4

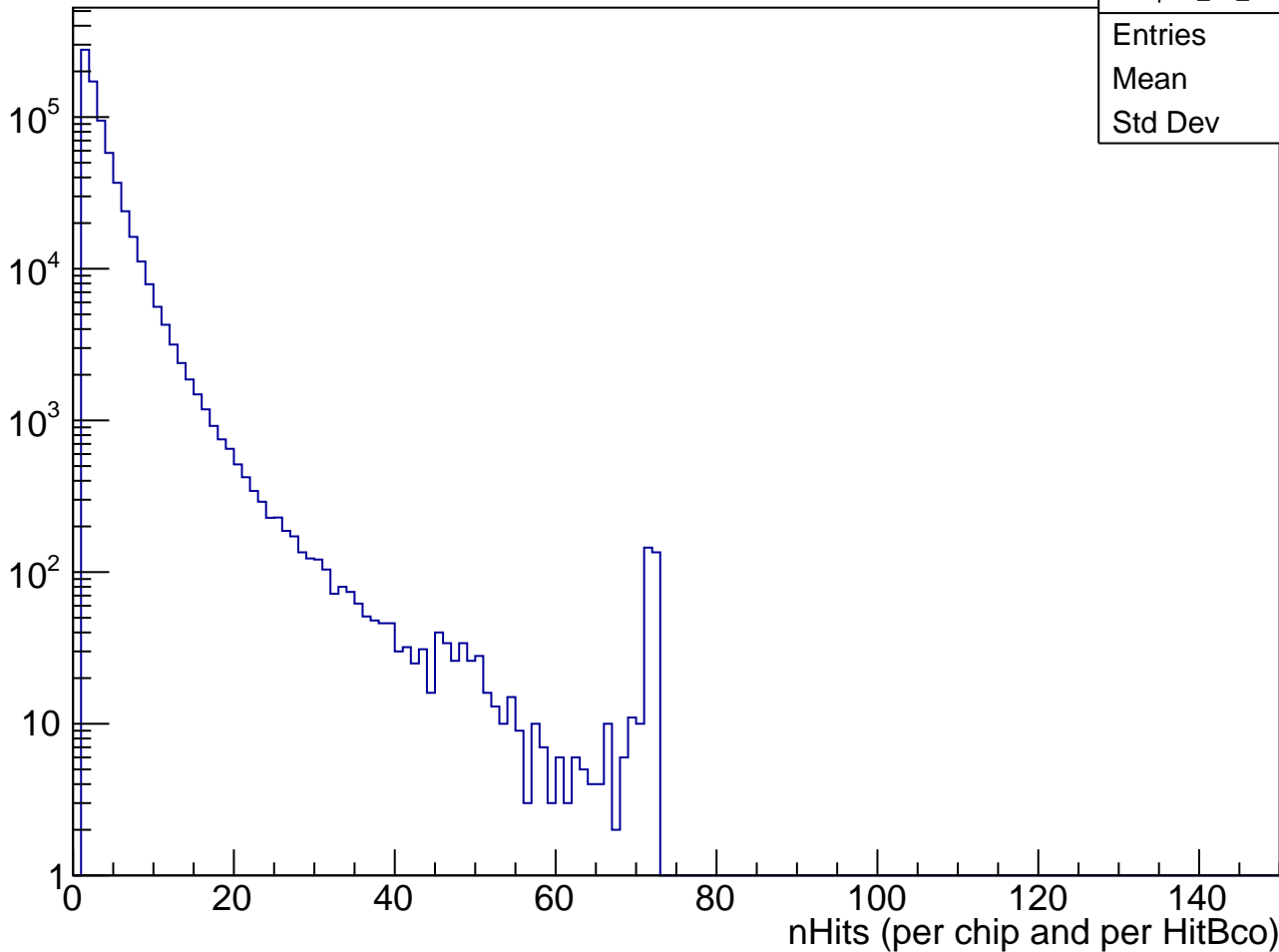
Entries



nChipHit_F5_Fch10_Chip4	
Entries	837013
Mean	3.186
Std Dev	3.722

nChipHit_F5_Fch10_Chip5

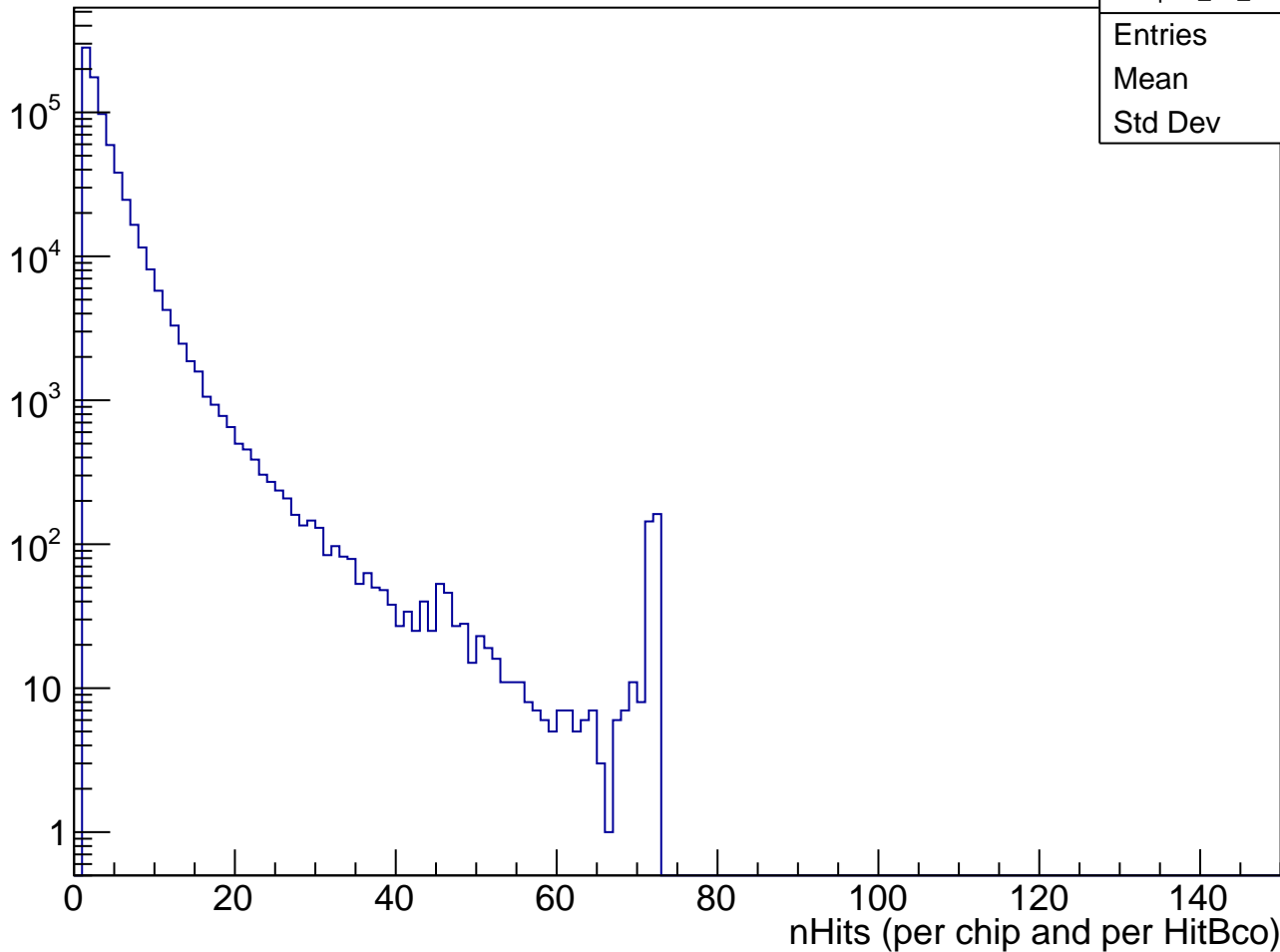
Entries



nChipHit_F5_Fch10_Chip5	
Entries	724895
Mean	2.965
Std Dev	3.497

nChipHit_F5_Fch10_Chip6

Entries

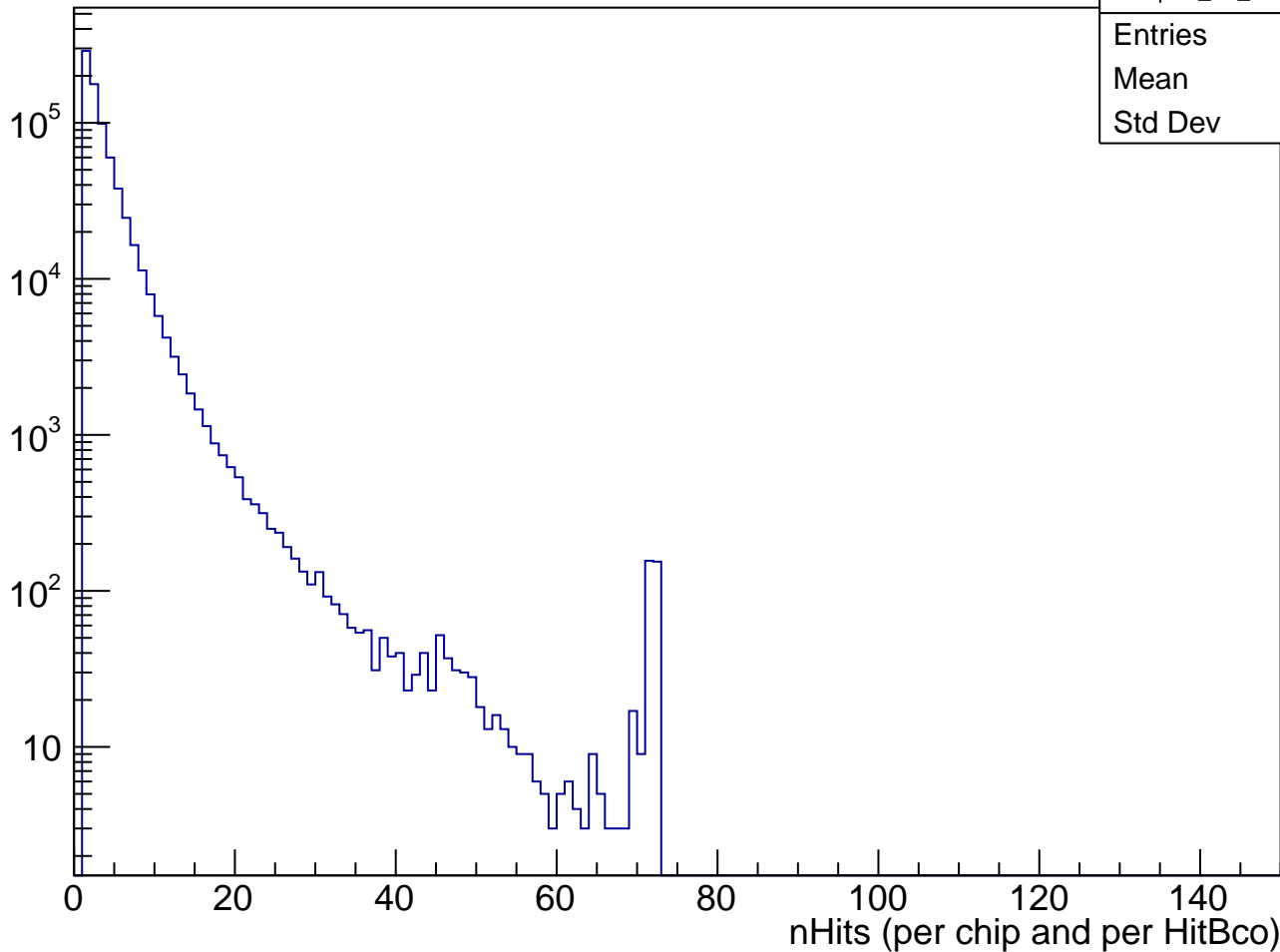


nChipHit_F5_Fch10_Chip6	
Entries	739664
Mean	2.978
Std Dev	3.528

nHits (per chip and per HitBco)

nChipHit_F5_Fch10_Chip7

Entries

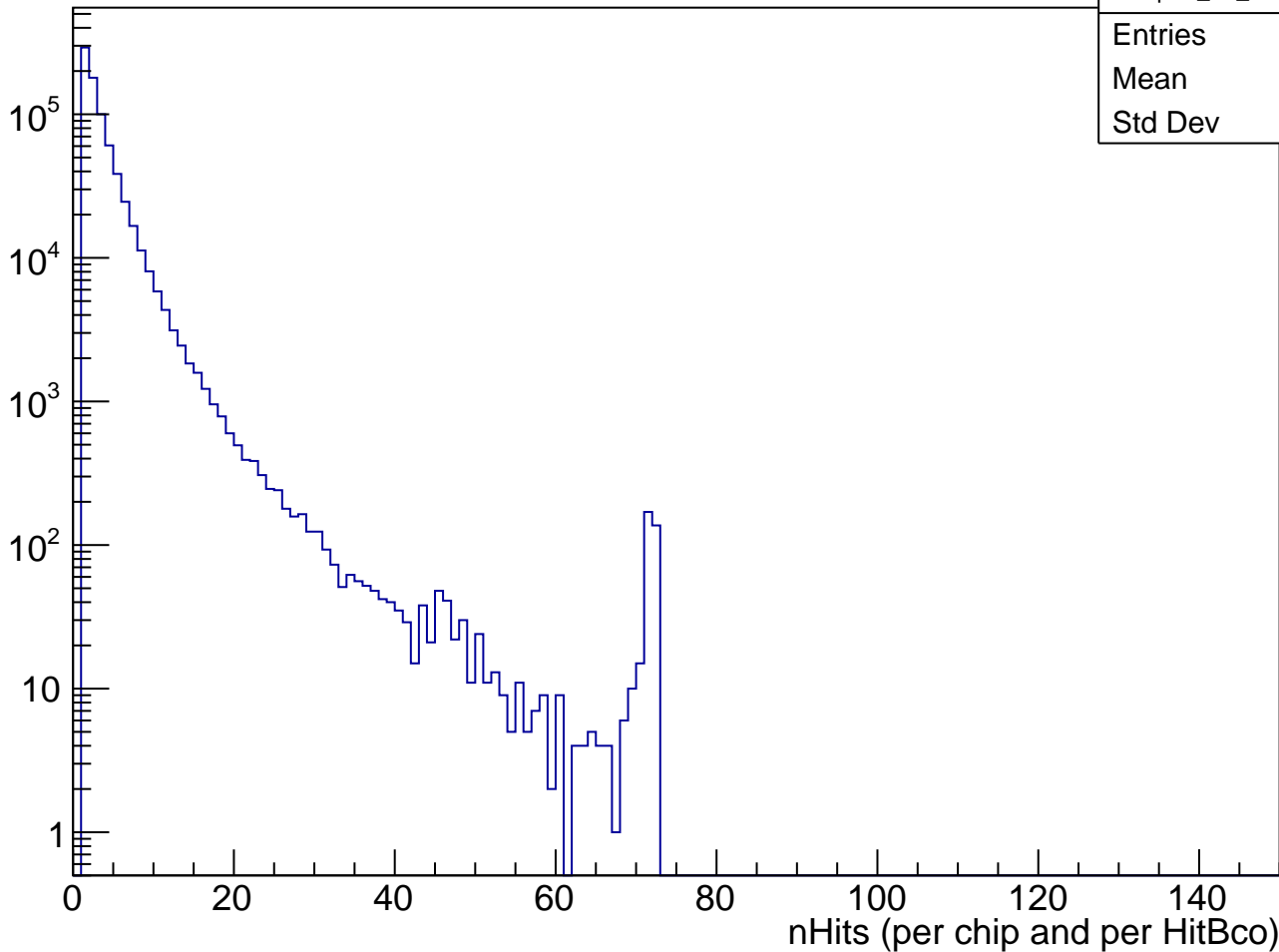


nChipHit_F5_Fch10_Chip7	
Entries	749365
Mean	2.942
Std Dev	3.476

nHits (per chip and per HitBco)

nChipHit_F5_Fch10_Chip8

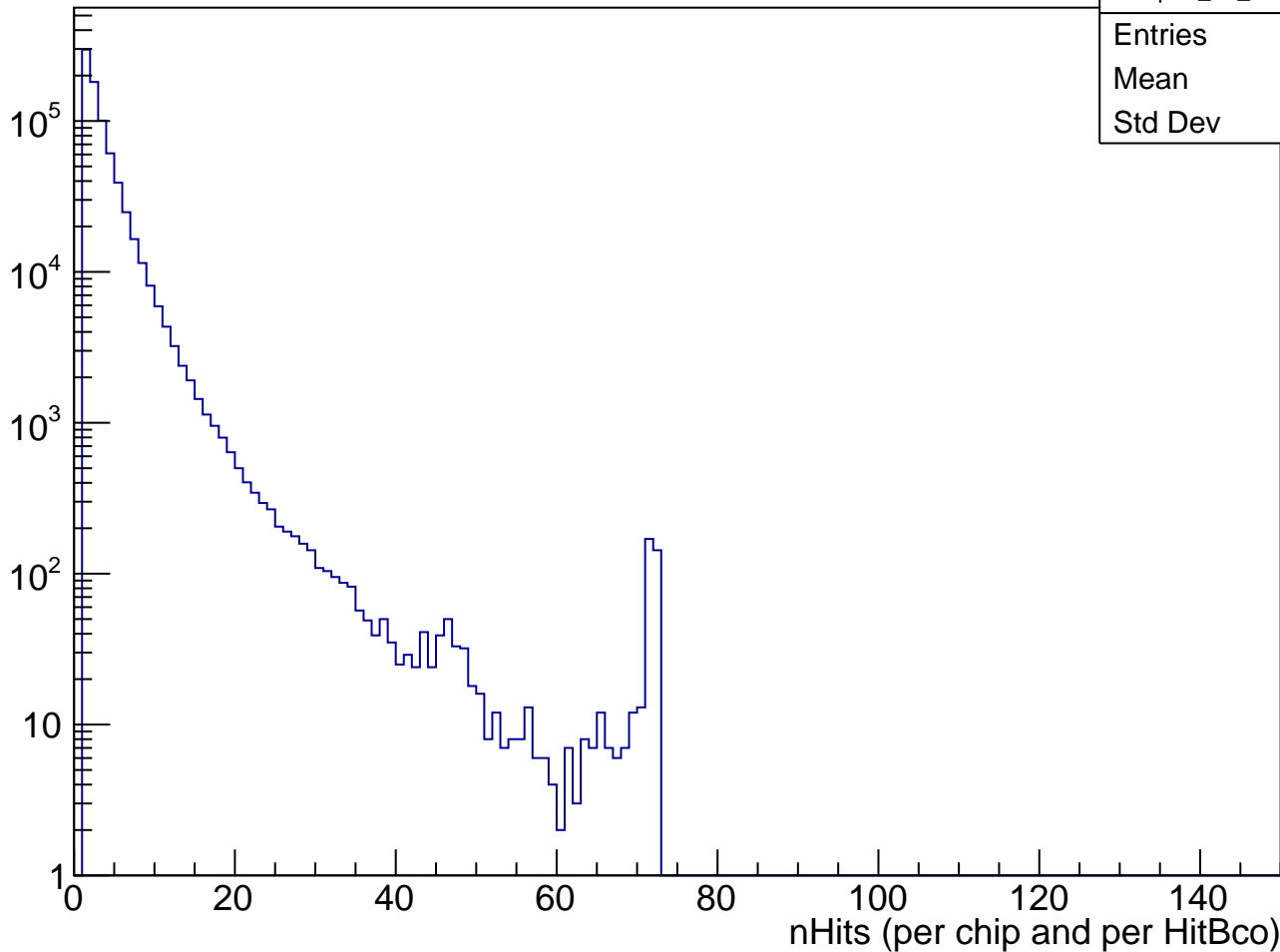
Entries



nChipHit_F5_Fch10_Chip8	
Entries	758028
Mean	2.939
Std Dev	3.448

nChipHit_F5_Fch10_Chip9

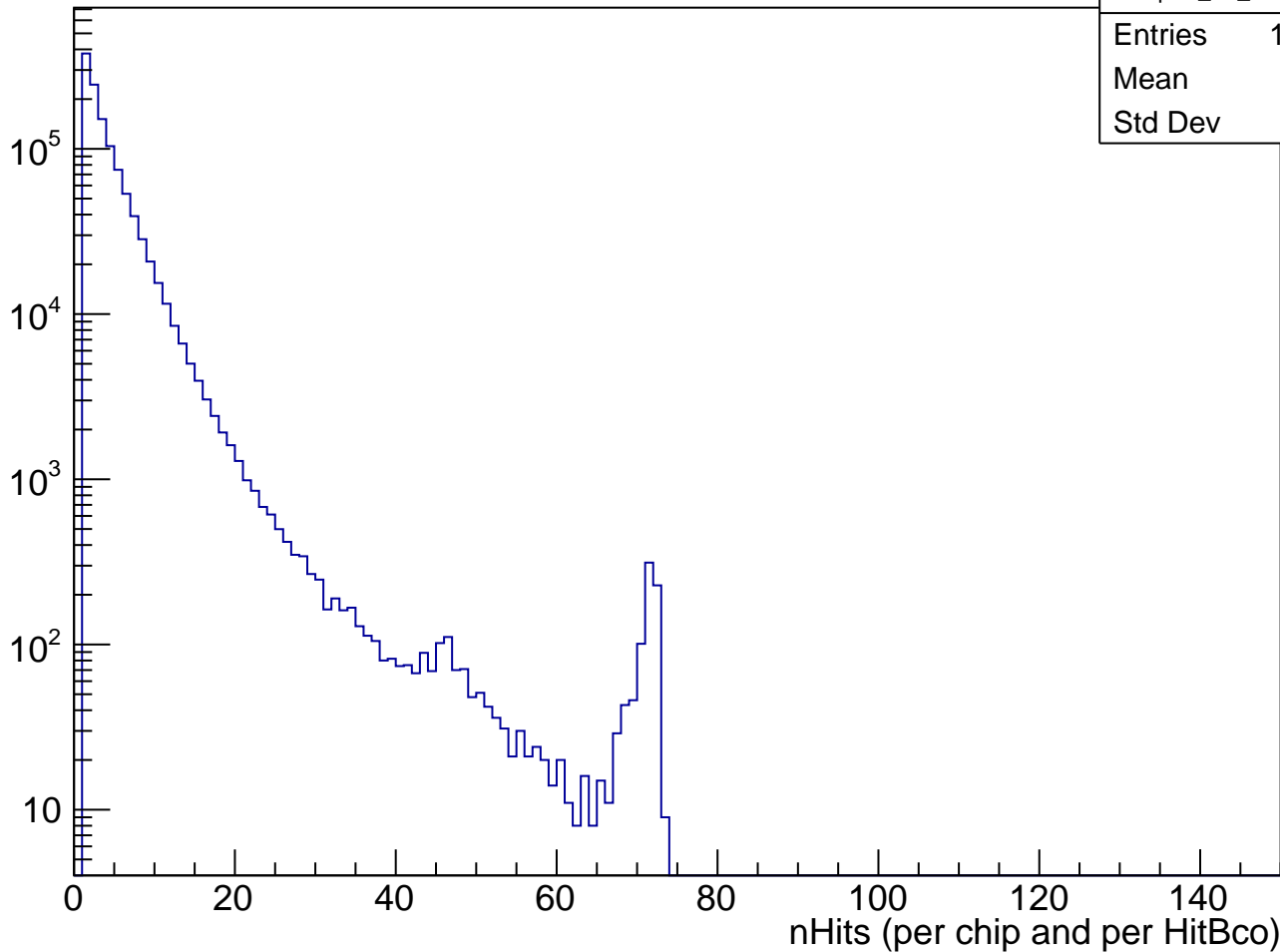
Entries



nChipHit_F5_Fch10_Chip9	
Entries	767539
Mean	2.93
Std Dev	3.471

nChipHit_F5_Fch11_Chip0

Entries

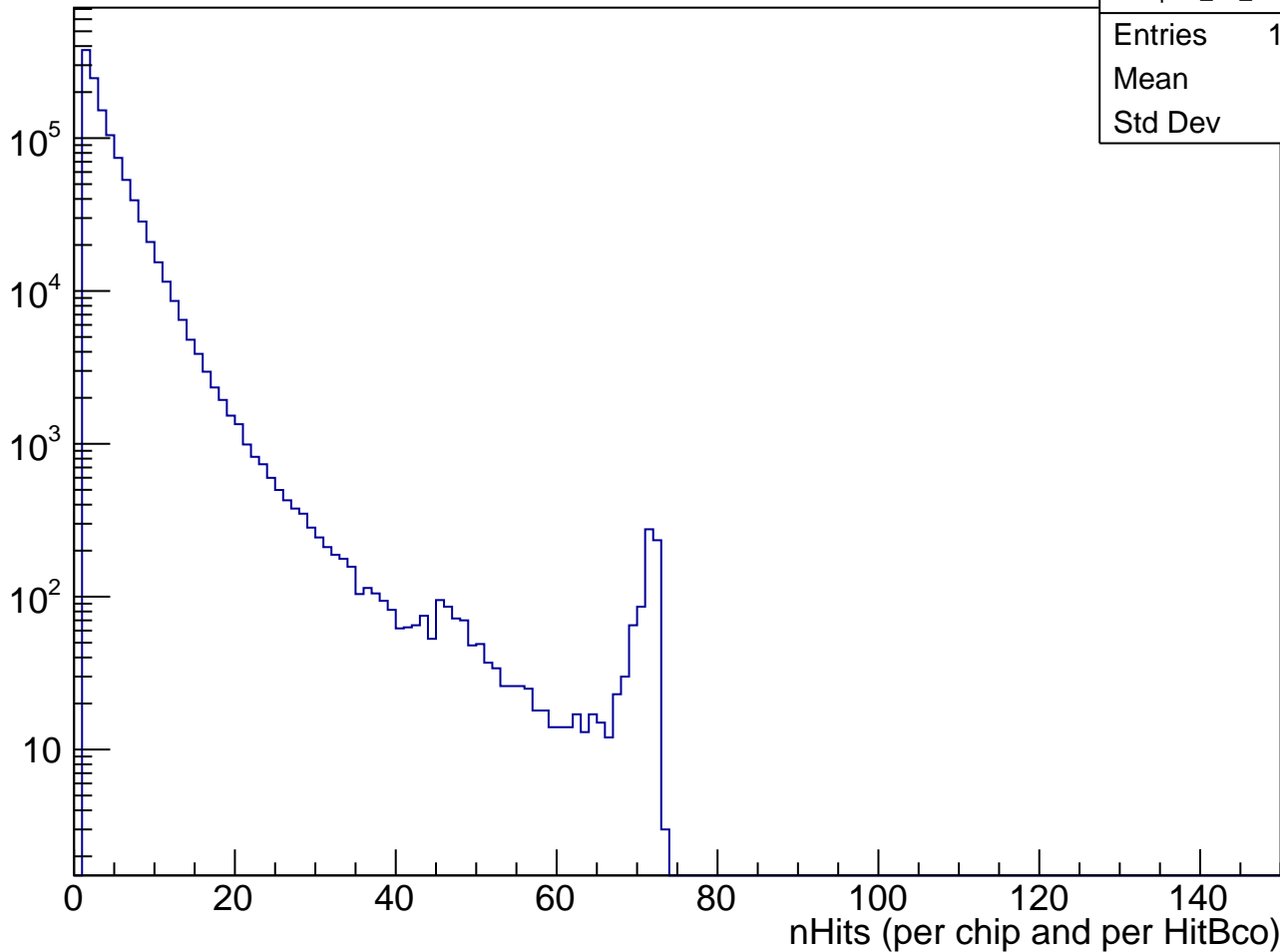


nChipHit_F5_Fch11_Chip0	
Entries	1163790
Mean	3.589
Std Dev	4.151

nHits (per chip and per HitBco)

nChipHit_F5_Fch11_Chip1

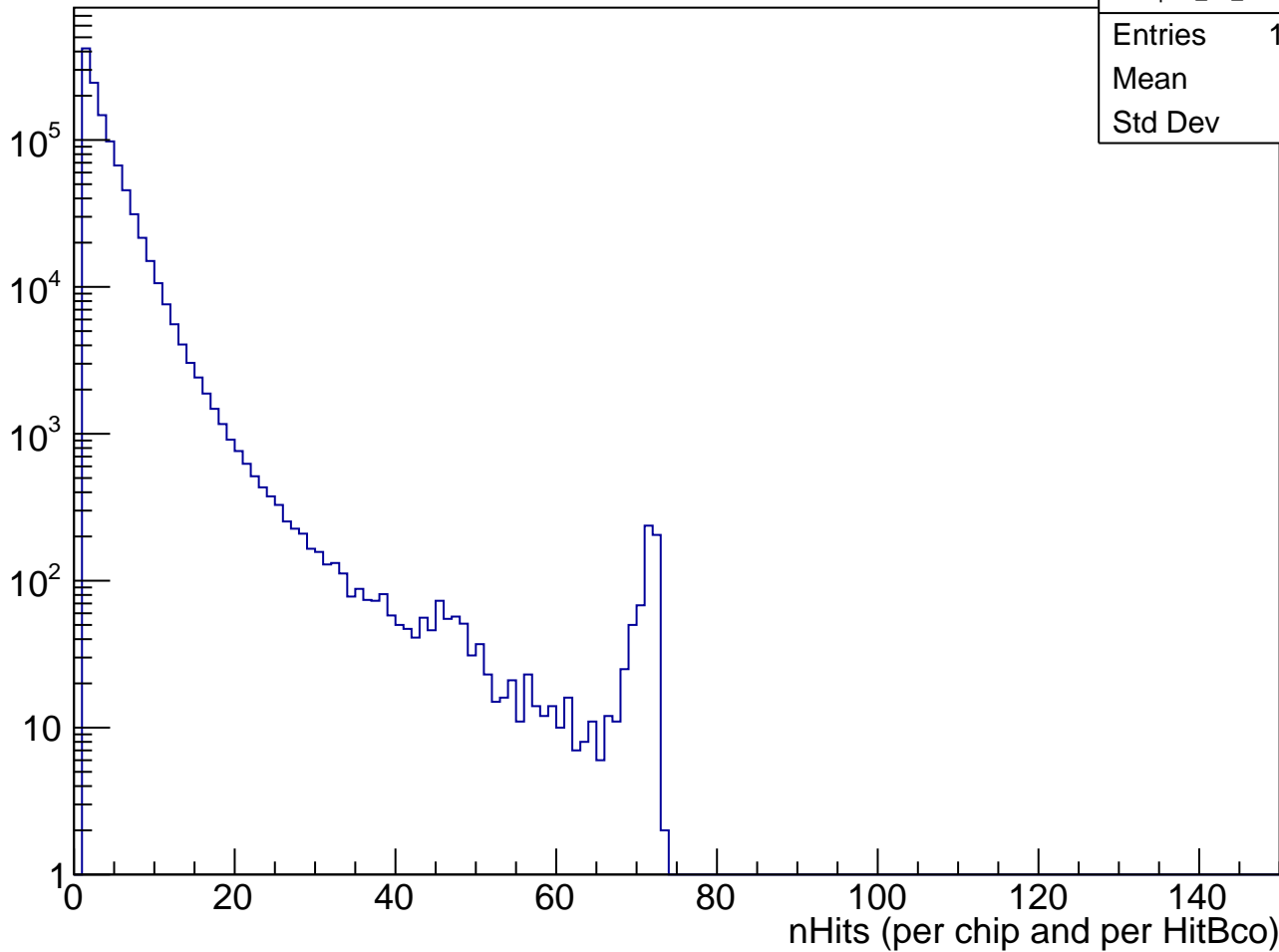
Entries



nChipHit_F5_Fch11_Chip1	
Entries	1165166
Mean	3.579
Std Dev	4.111

nChipHit_F5_Fch11_Chip10

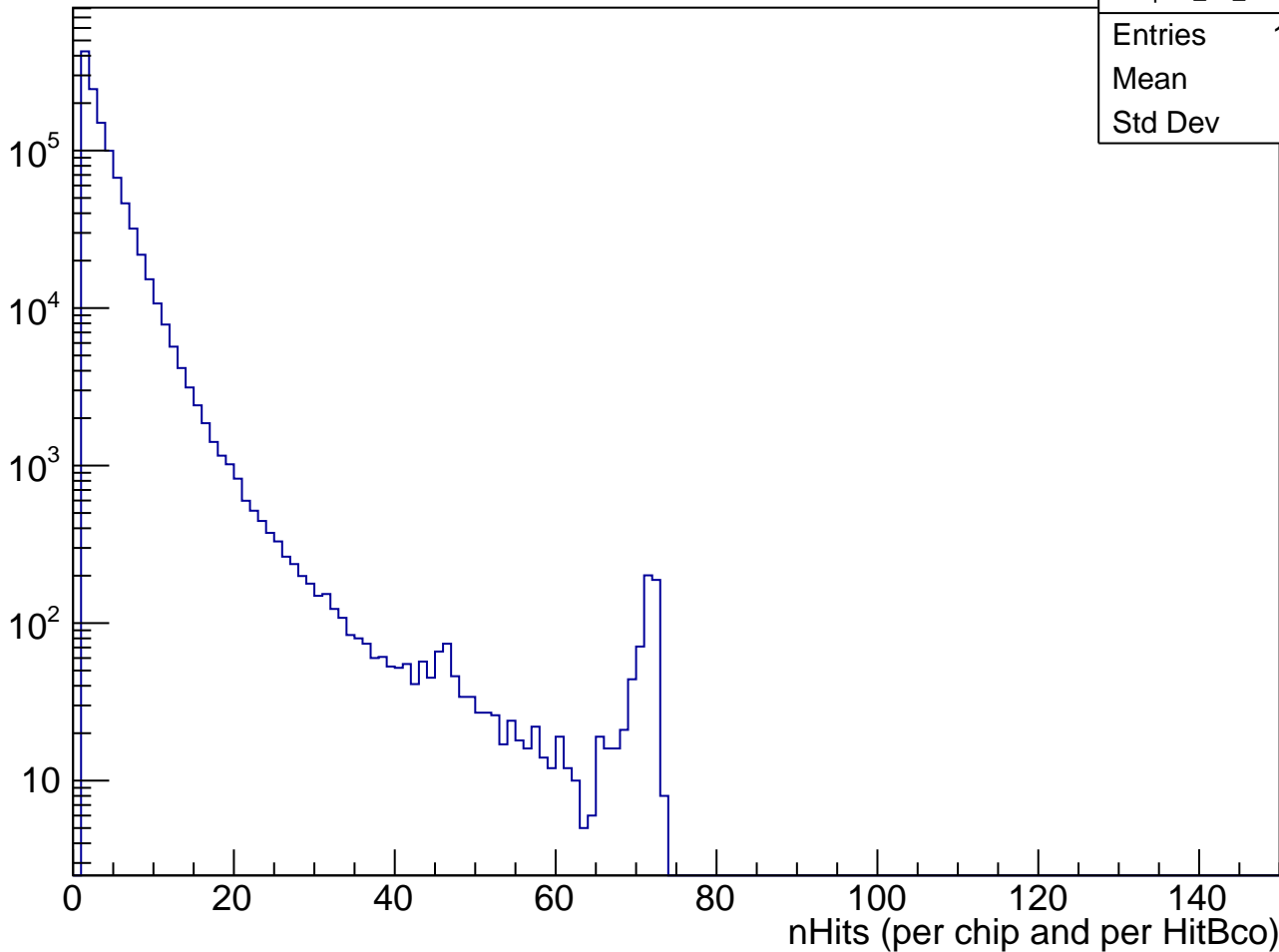
Entries



nChipHit_F5_Fch11_Chip10	
Entries	1135870
Mean	3.137
Std Dev	3.615

nChipHit_F5_Fch11_Chip11

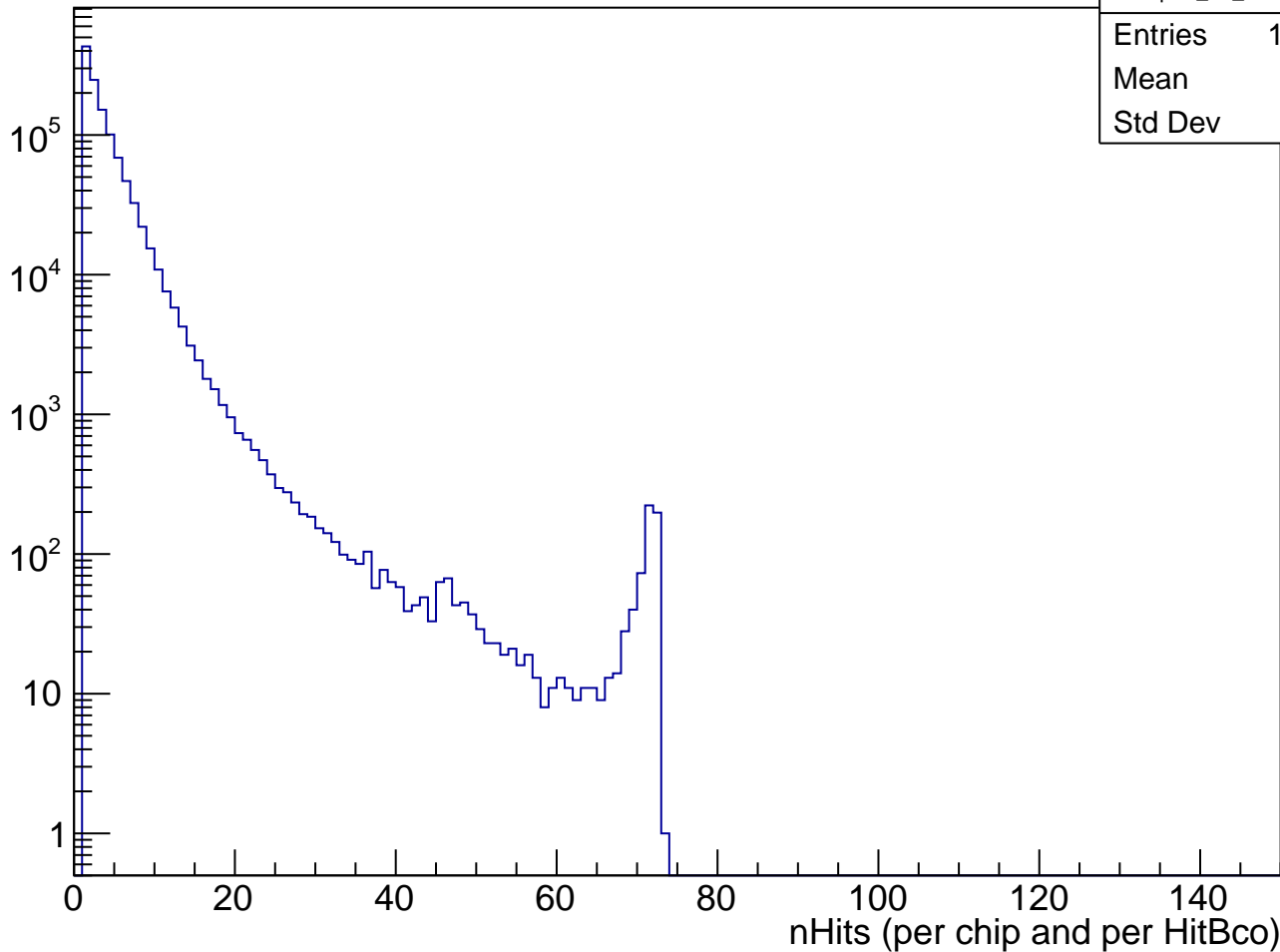
Entries



nChipHit_F5_Fch11_Chip11	
Entries	1149471
Mean	3.137
Std Dev	3.591

nChipHit_F5_Fch11_Chip12

Entries



nChipHit_F5_Fch11_Chip12

Entries 1162688

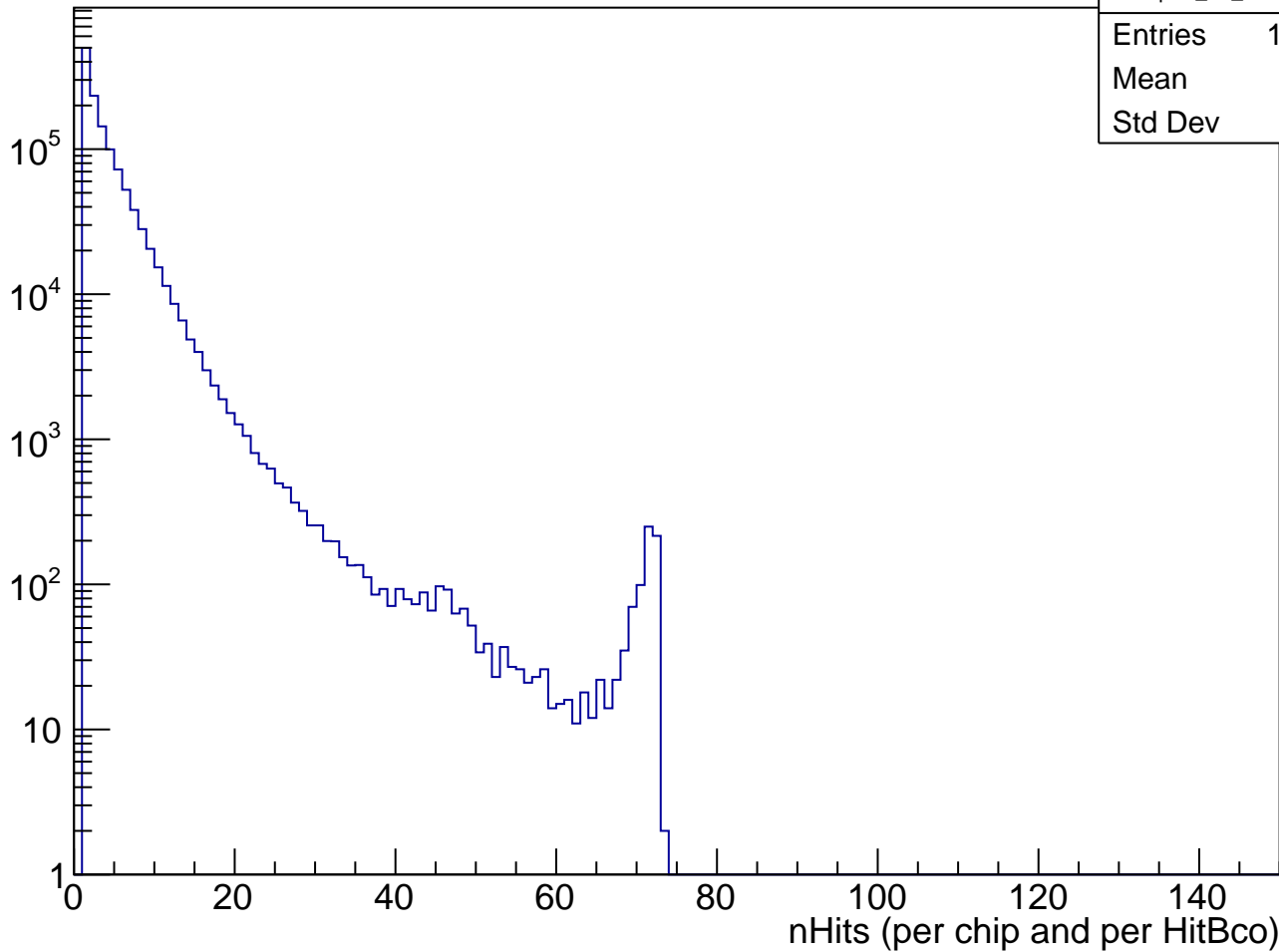
Mean 3.136

Std Dev 3.584

nHits (per chip and per HitBco)

nChipHit_F5_Fch11_Chip13

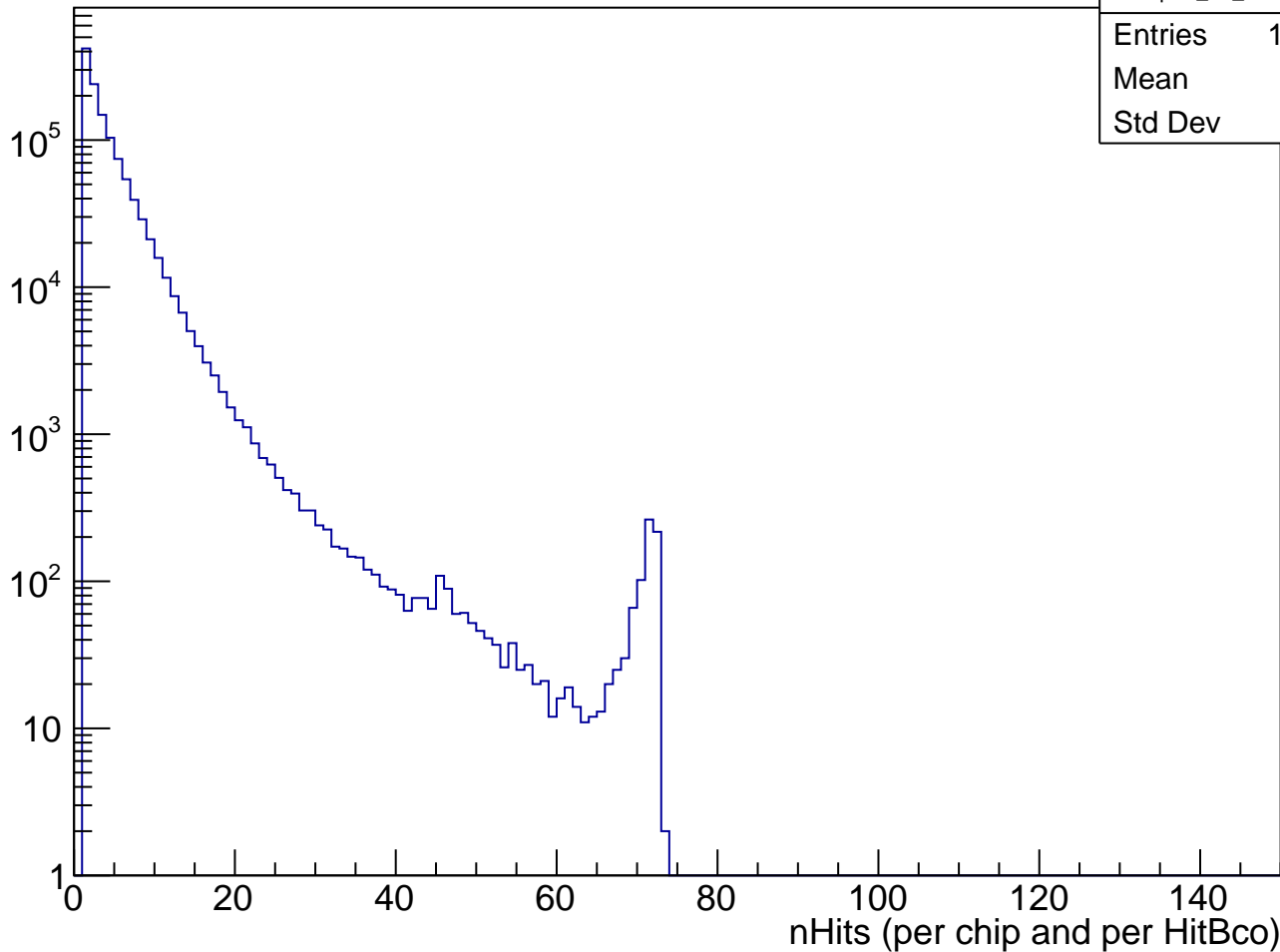
Entries



nChipHit_F5_Fch11_Chip13	
Entries	1255852
Mean	3.339
Std Dev	4.023

nChipHit_F5_Fch11_Chip14

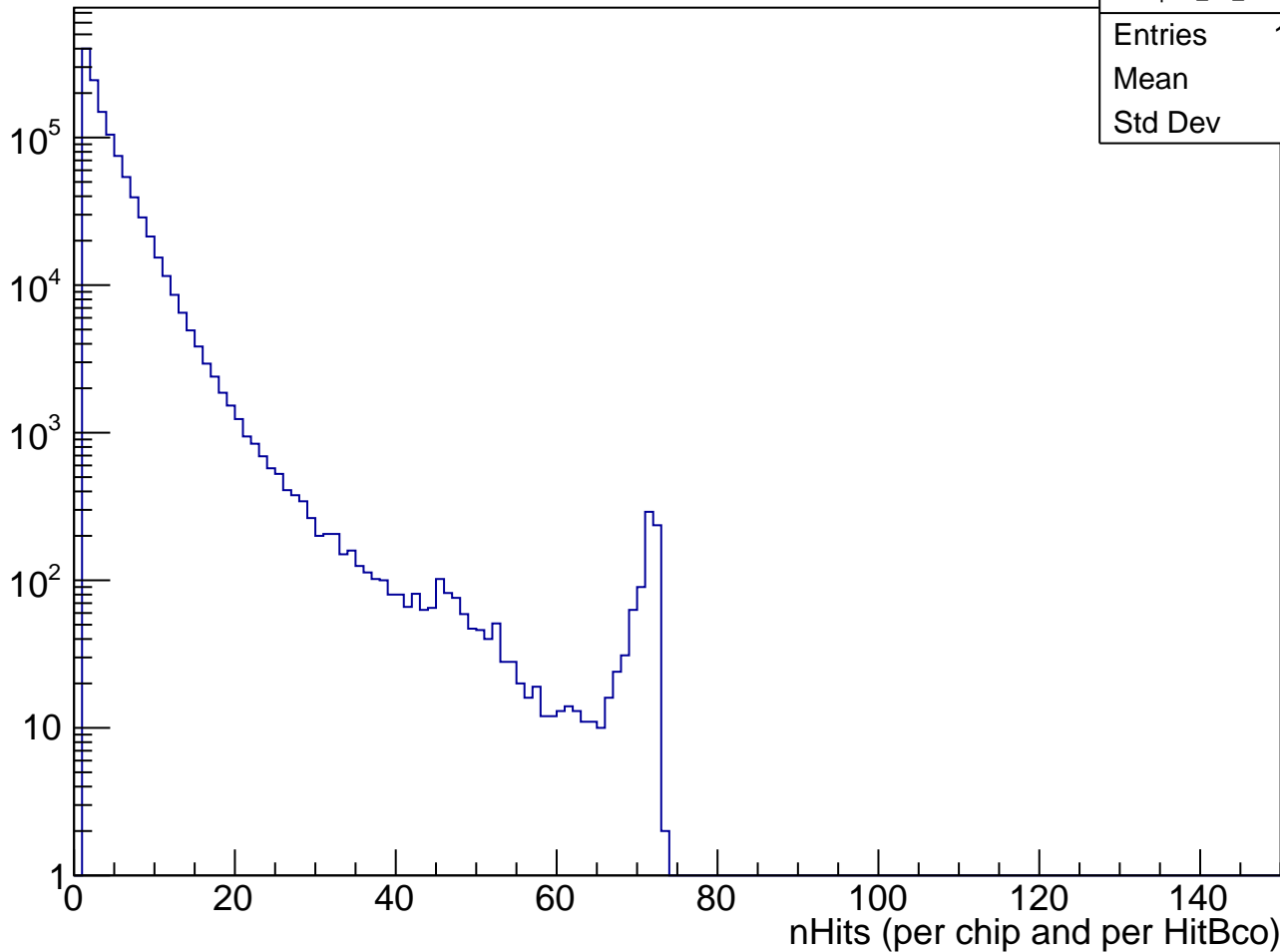
Entries



nChipHit_F5_Fch11_Chip14	
Entries	1200008
Mean	3.516
Std Dev	4.105

nChipHit_F5_Fch11_Chip15

Entries



nChipHit_F5_Fch11_Chip15

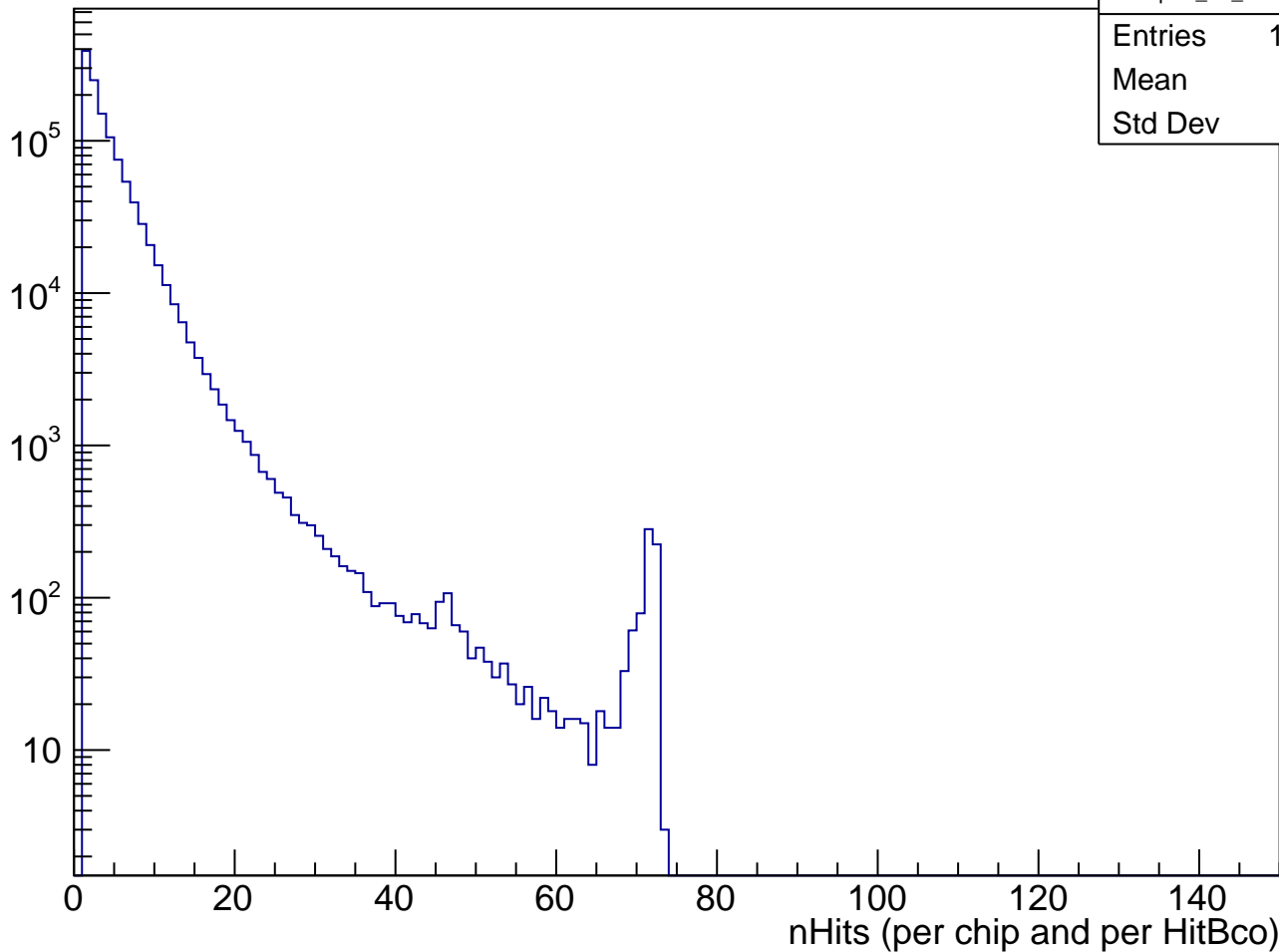
Entries 1186131

Mean 3.535

Std Dev 4.09

nChipHit_F5_Fch11_Chip16

Entries



nChipHit_F5_Fch11_Chip16

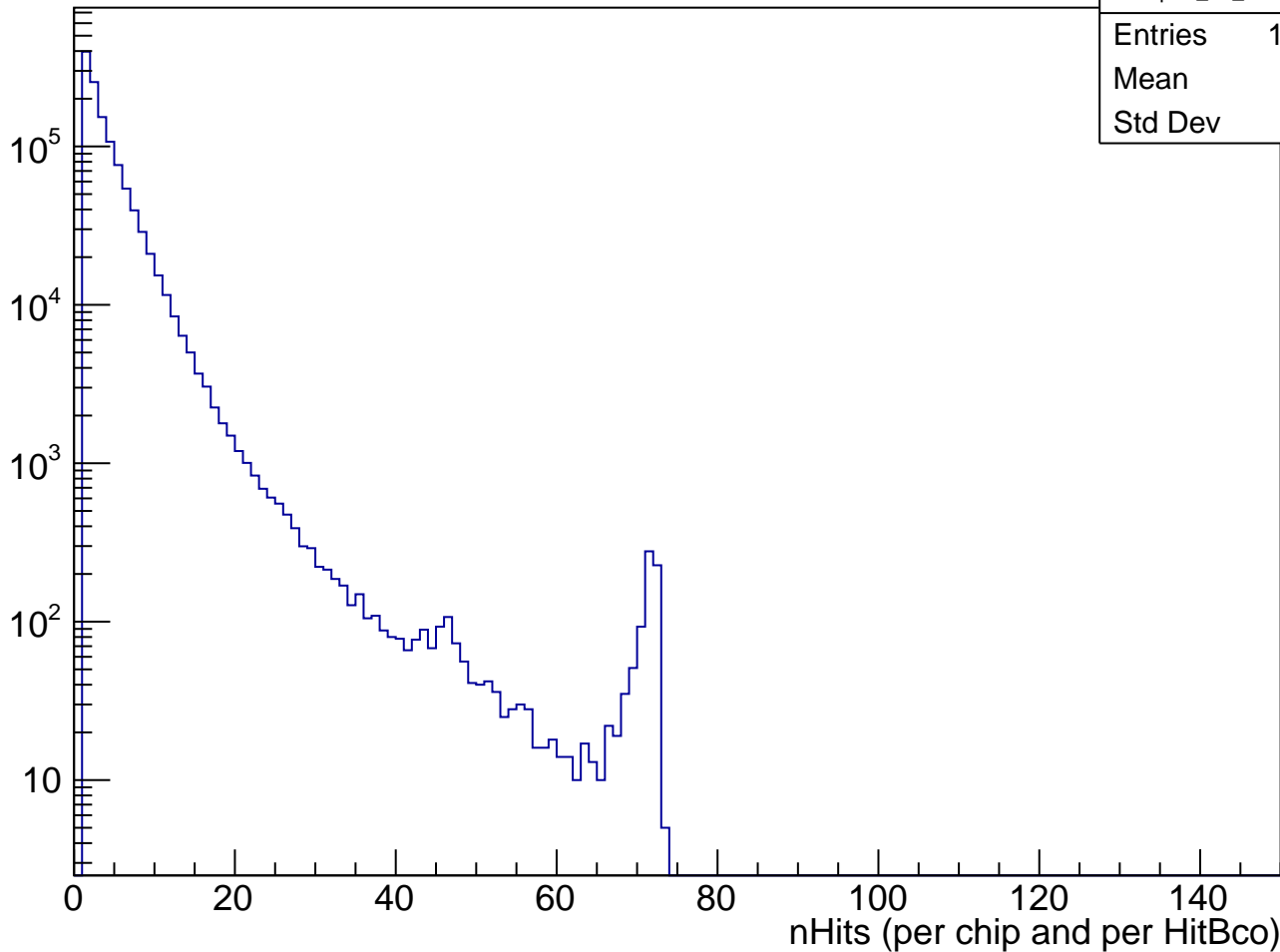
Entries 1181033

Mean 3.542

Std Dev 4.078

nChipHit_F5_Fch11_Chip17

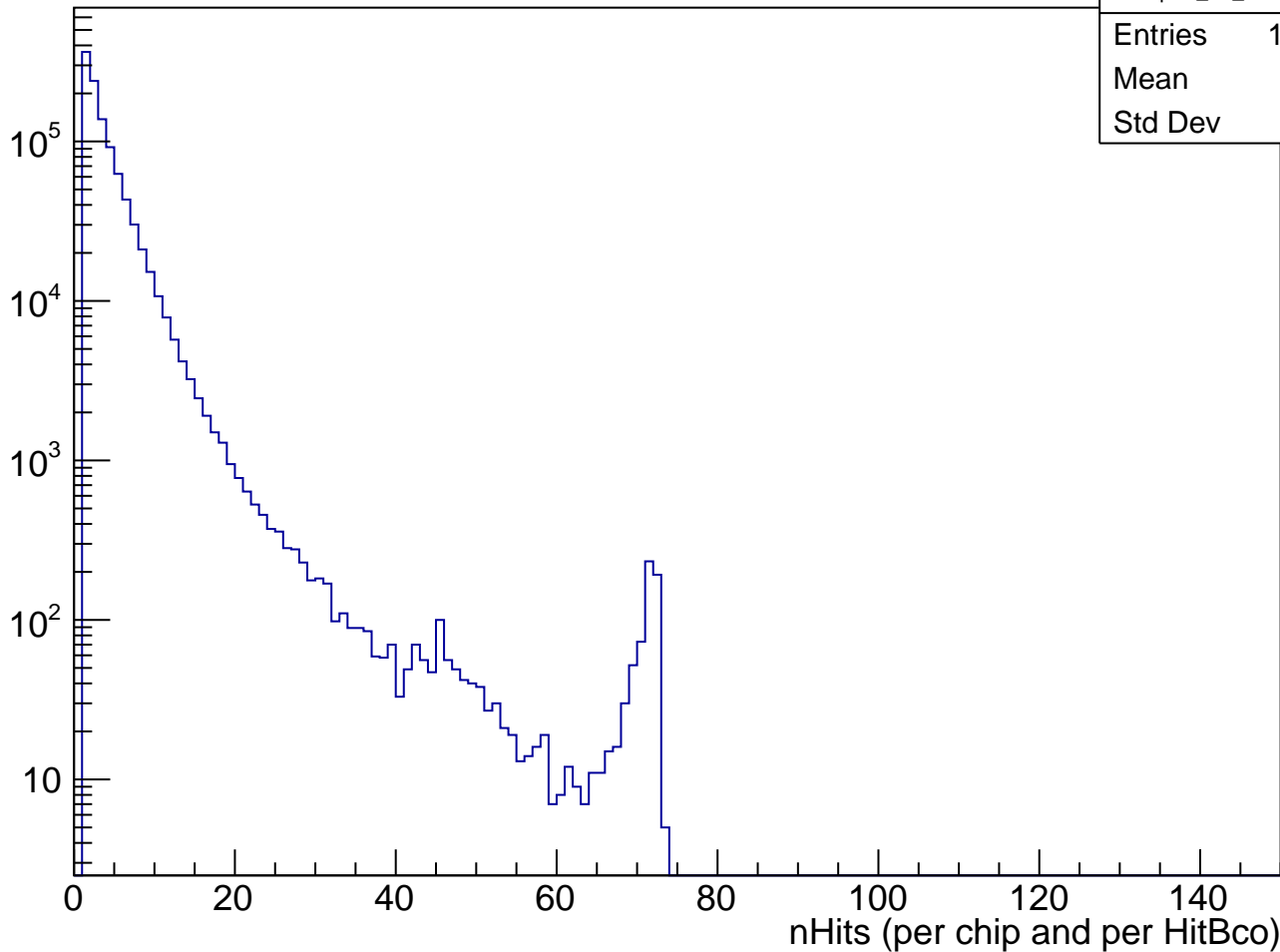
Entries



nChipHit_F5_Fch11_Chip17	
Entries	1200159
Mean	3.528
Std Dev	4.064

nChipHit_F5_Fch11_Chip18

Entries



nChipHit_F5_Fch11_Chip18

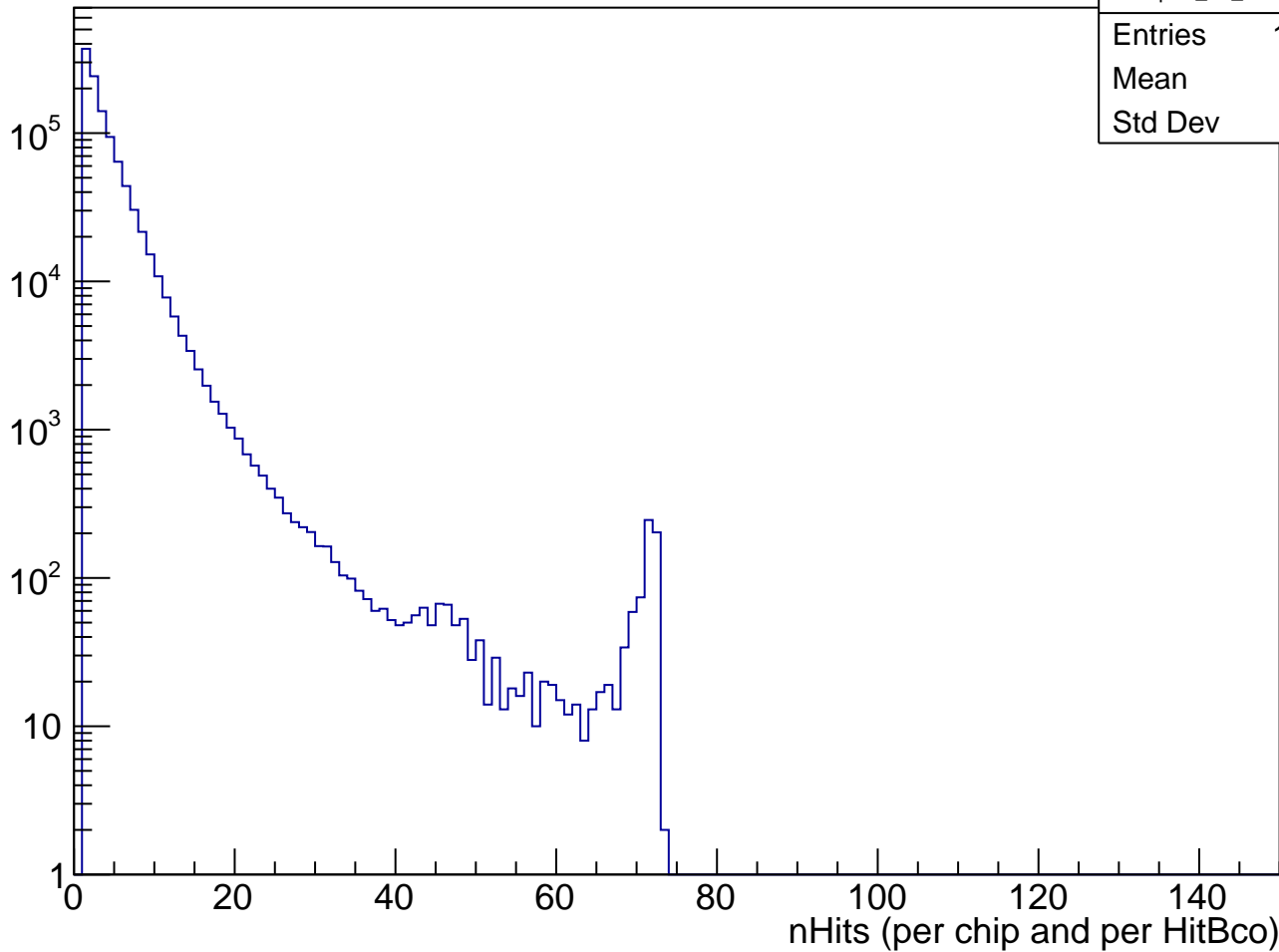
Entries 1052069

Mean 3.251

Std Dev 3.759

nChipHit_F5_Fch11_Chip19

Entries



nChipHit_F5_Fch11_Chip19

Entries 1069631

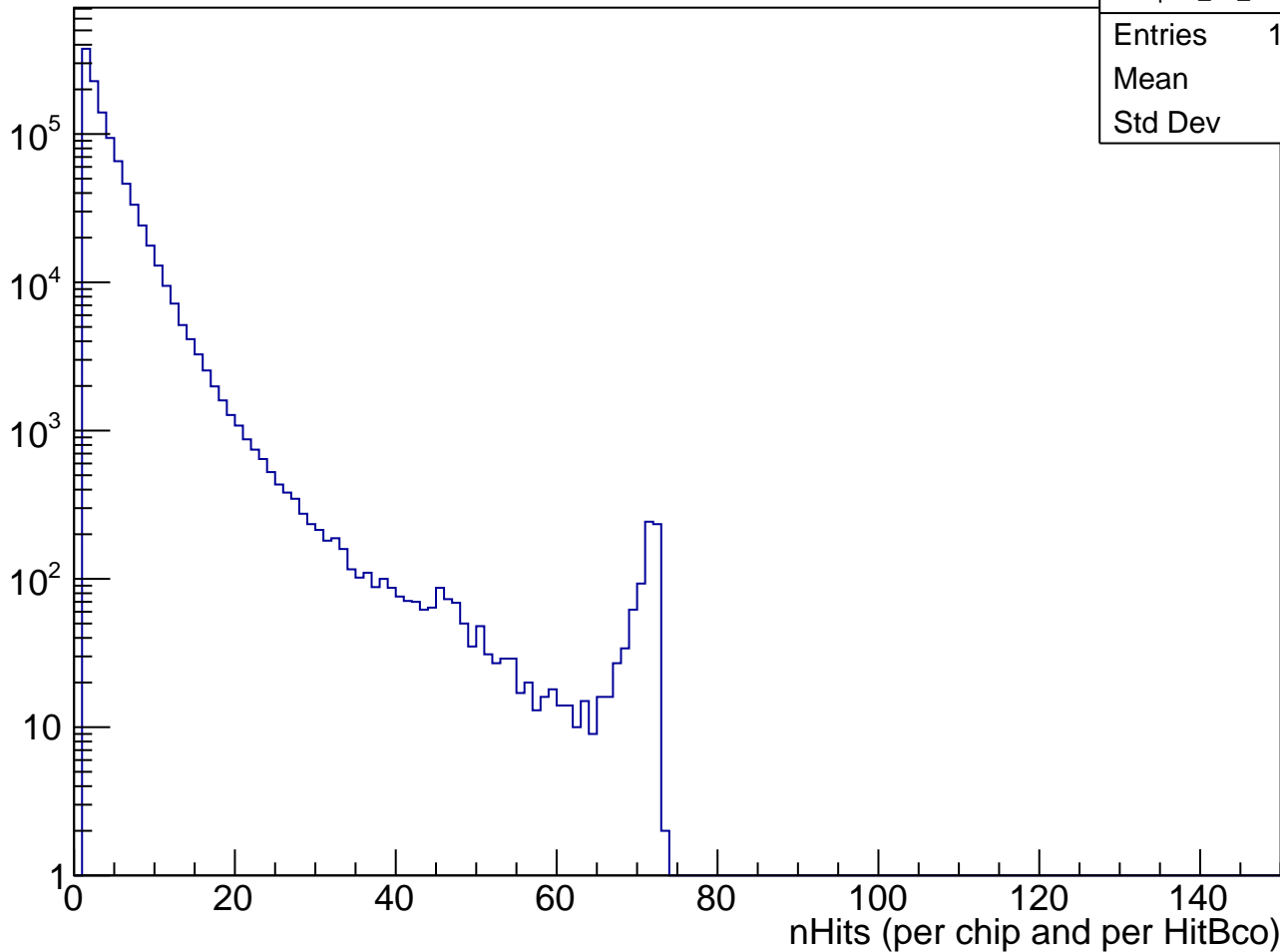
Mean 3.256

Std Dev 3.775

nHits (per chip and per HitBco)

nChipHit_F5_Fch11_Chip2

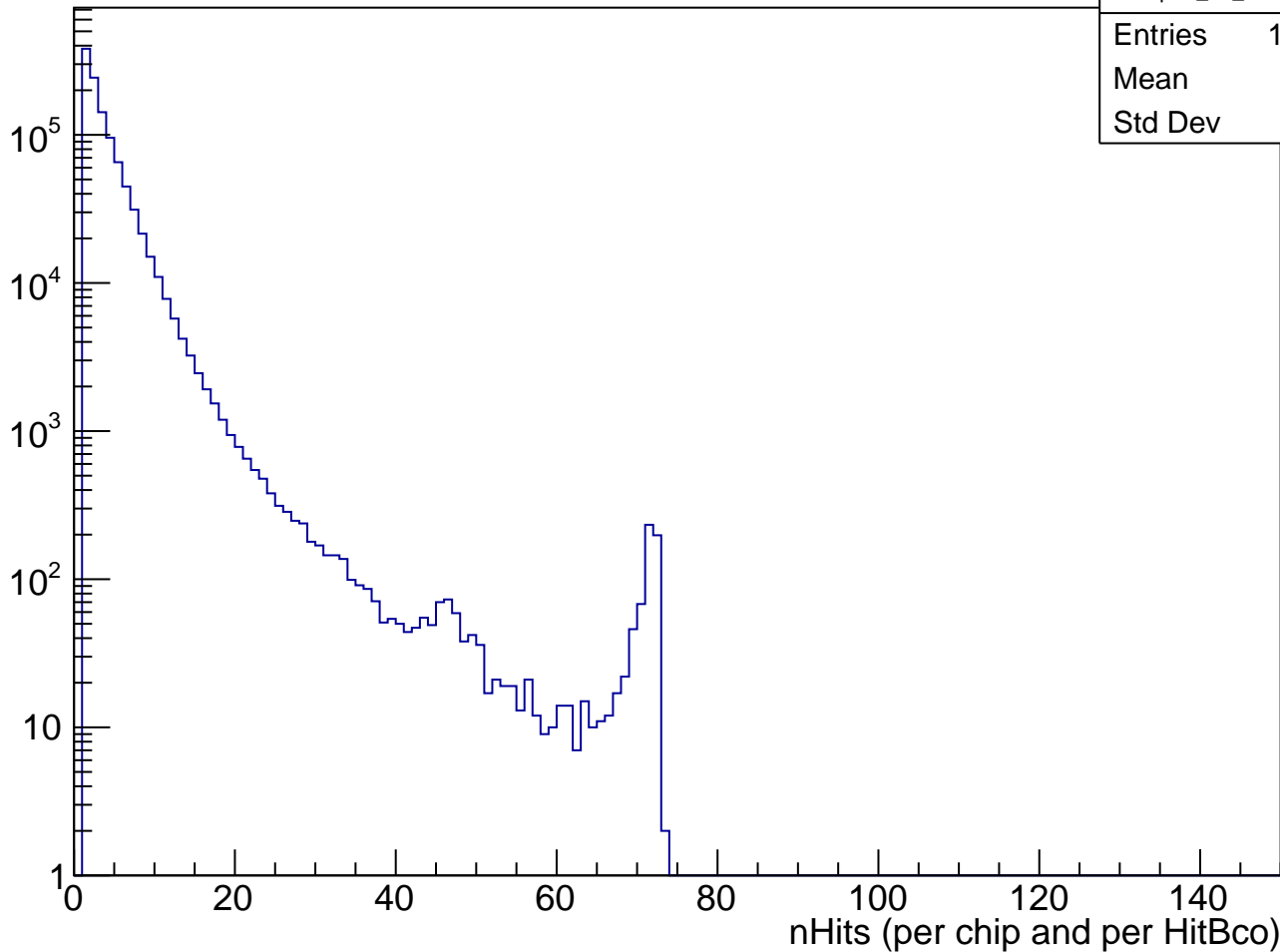
Entries



nChipHit_F5_Fch11_Chip2	
Entries	1081754
Mean	3.428
Std Dev	4.049

nChipHit_F5_Fch11_Chip20

Entries



nChipHit_F5_Fch11_Chip20

Entries 1086988

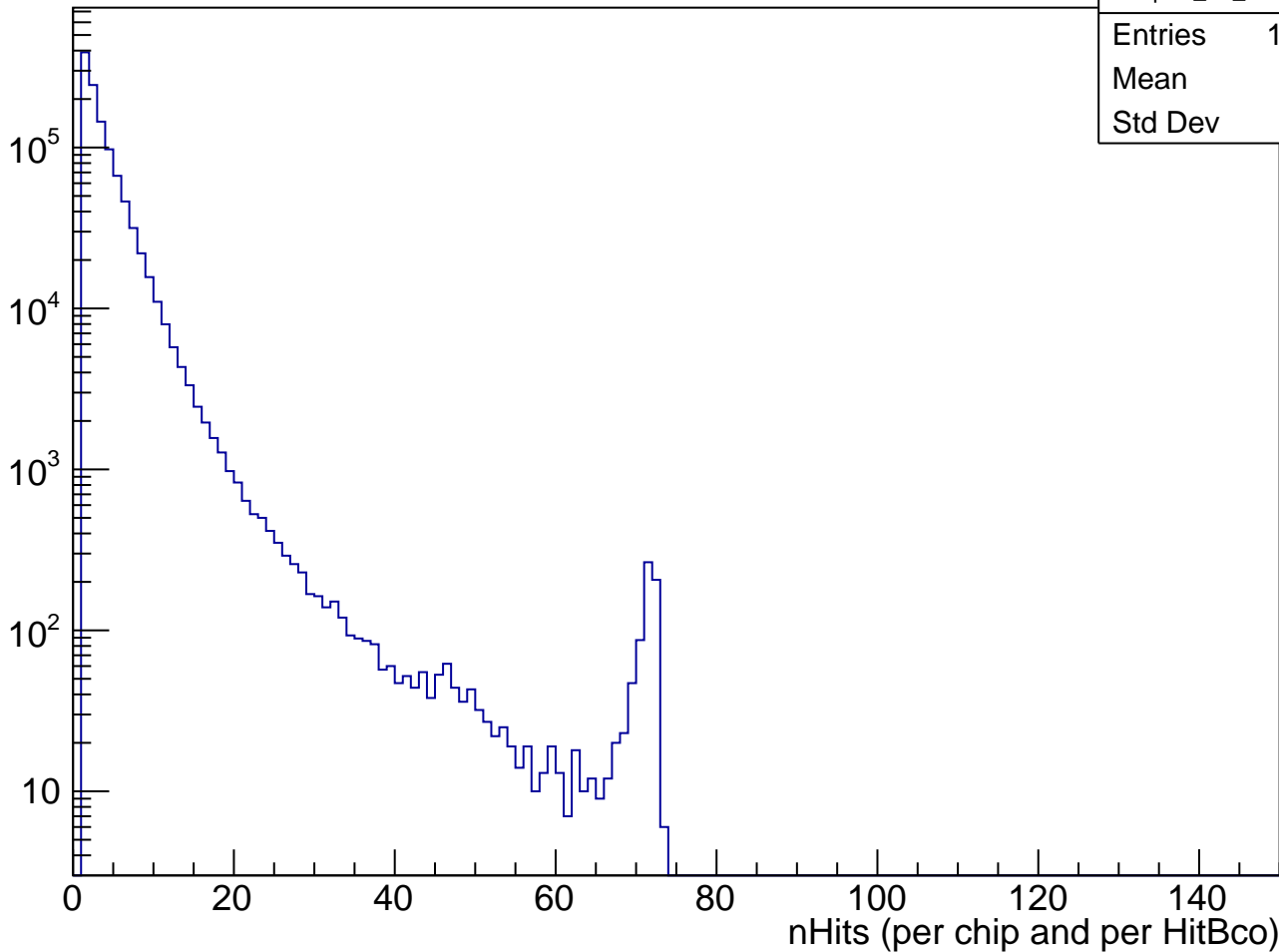
Mean 3.226

Std Dev 3.703

nHits (per chip and per HitBco)

nChipHit_F5_Fch11_Chip21

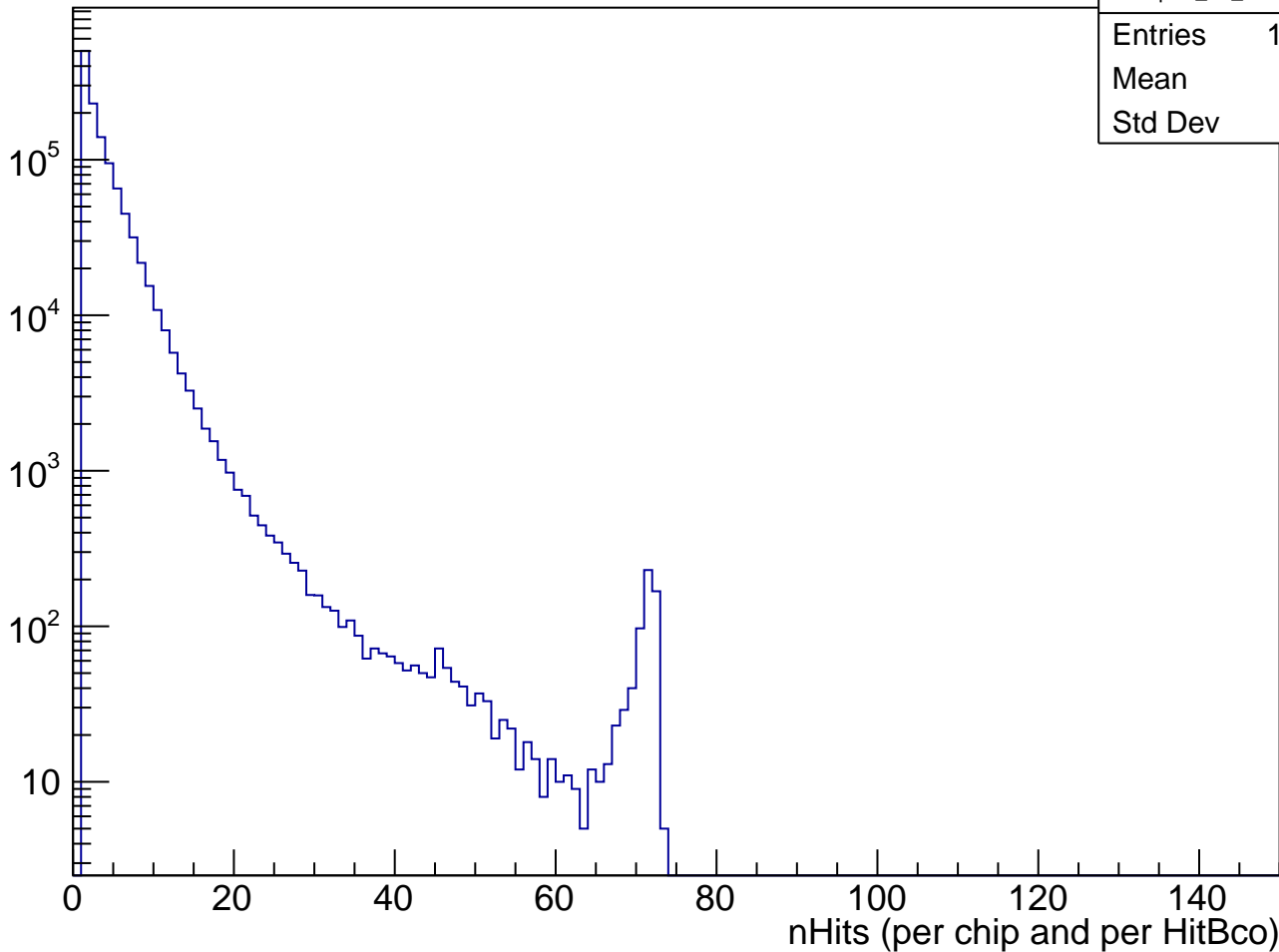
Entries



nChipHit_F5_Fch11_Chip21	
Entries	1106545
Mean	3.23
Std Dev	3.729

nChipHit_F5_Fch11_Chip22

Entries

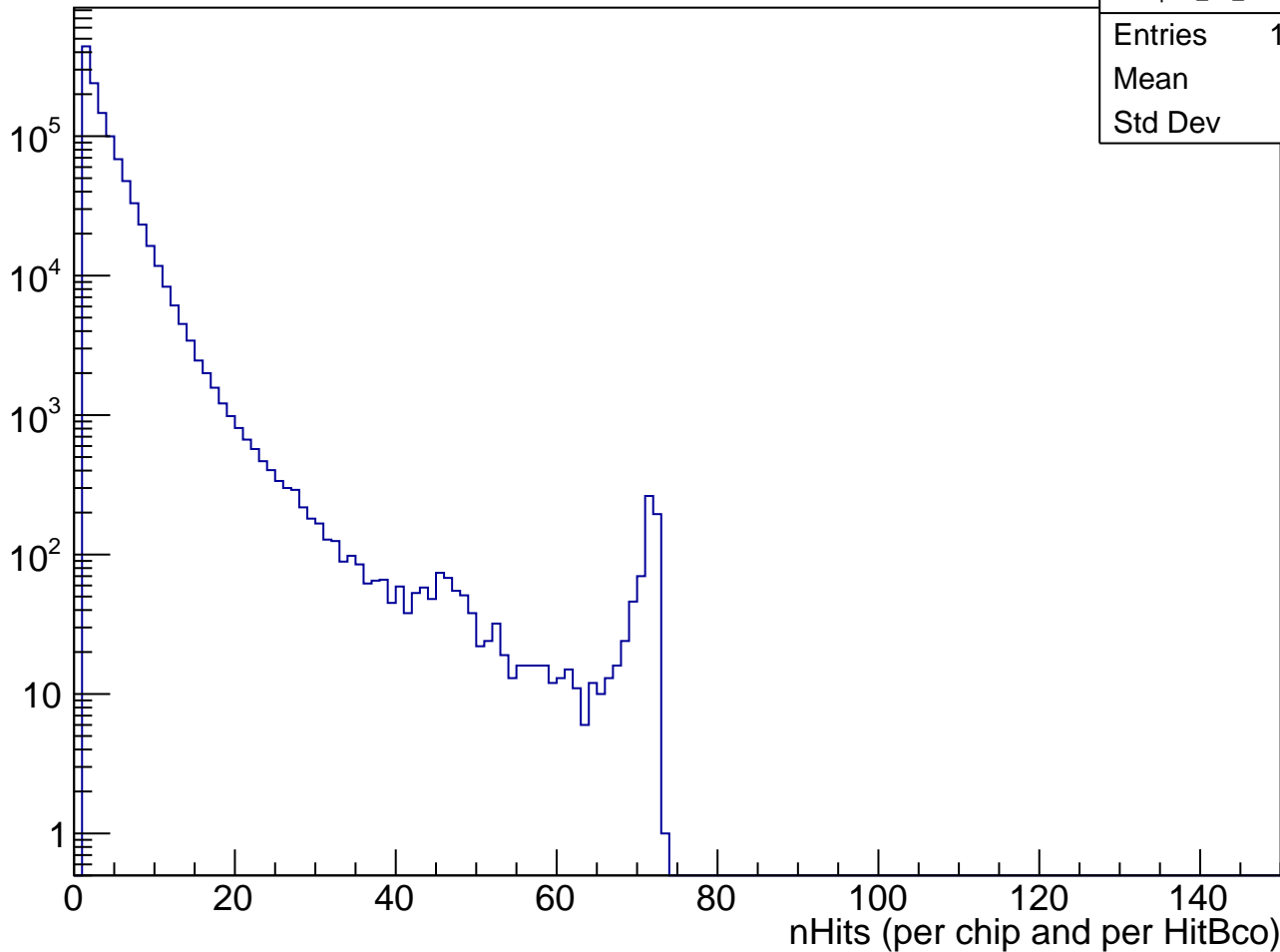


nChipHit_F5_Fch11_Chip22	
Entries	1191949
Mean	3.018
Std Dev	3.592

nHits (per chip and per HitBco)

nChipHit_F5_Fch11_Chip23

Entries



nChipHit_F5_Fch11_Chip23

Entries 1164500

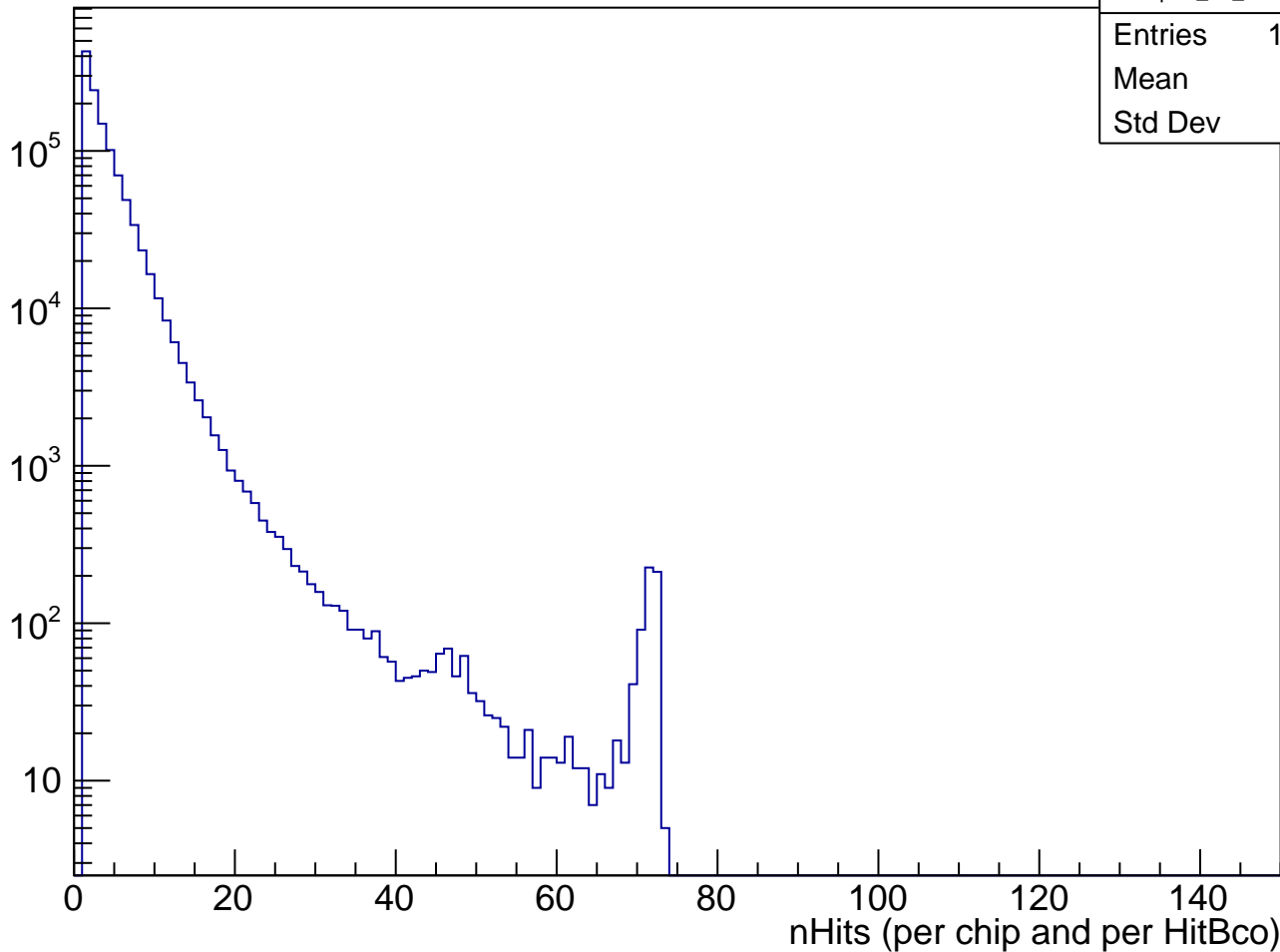
Mean 3.168

Std Dev 3.664

nHits (per chip and per HitBco)

nChipHit_F5_Fch11_Chip24

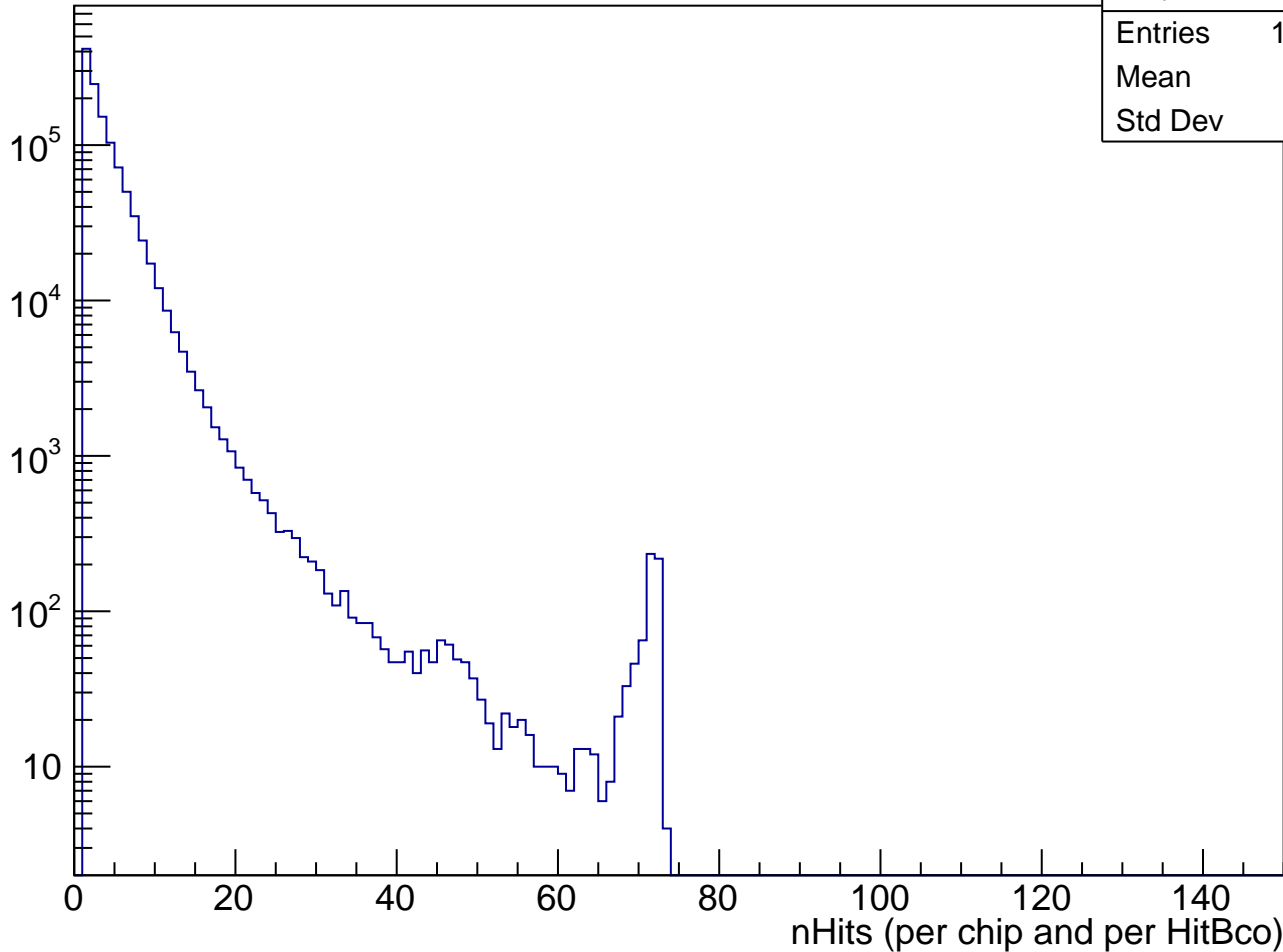
Entries



nChipHit_F5_Fch11_Chip24	
Entries	1163625
Mean	3.196
Std Dev	3.662

nChipHit_F5_Fch11_Chip25

Entries



nChipHit_F5_Fch11_Chip25

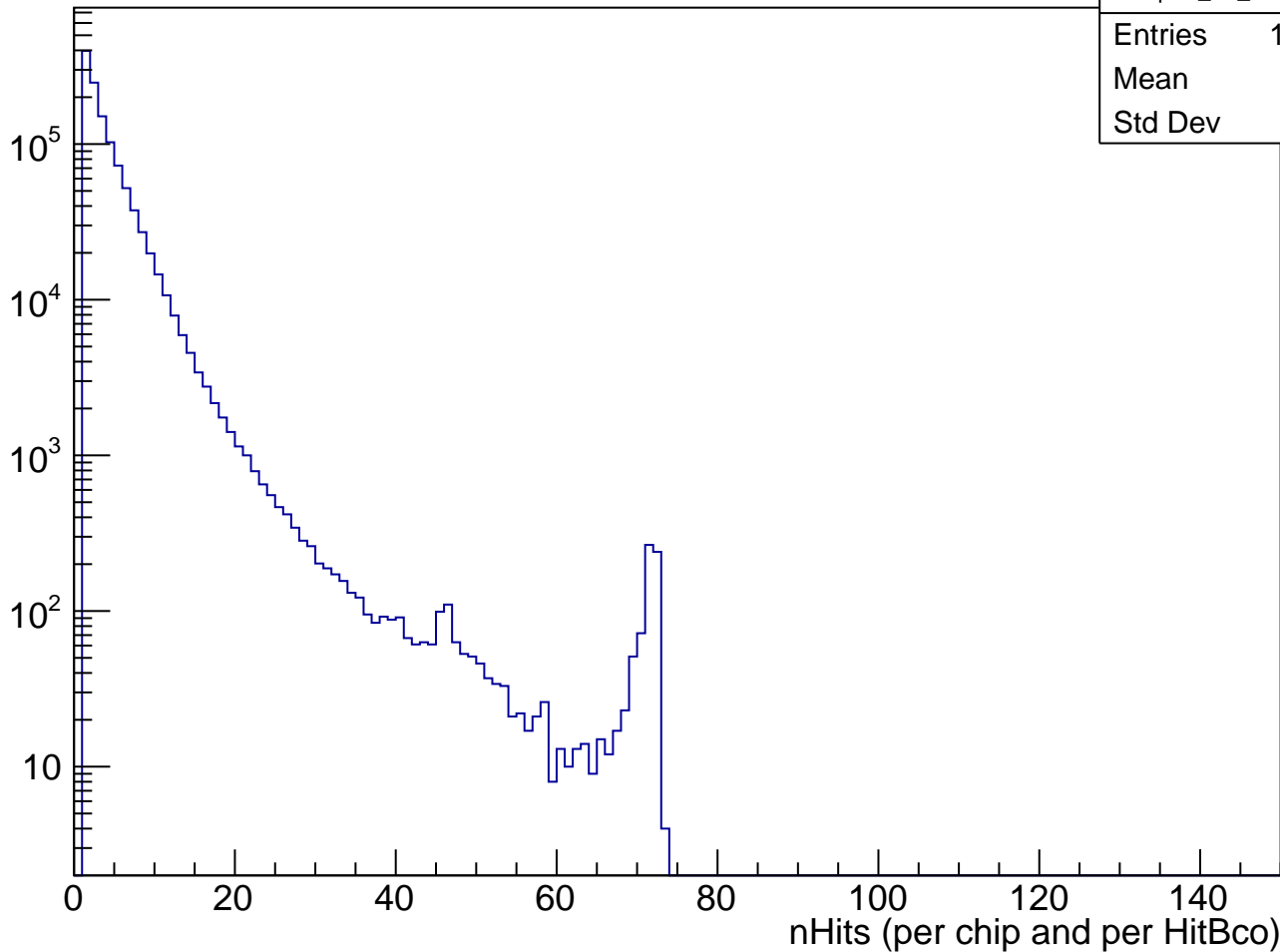
Entries 1168322

Mean 3.246

Std Dev 3.674

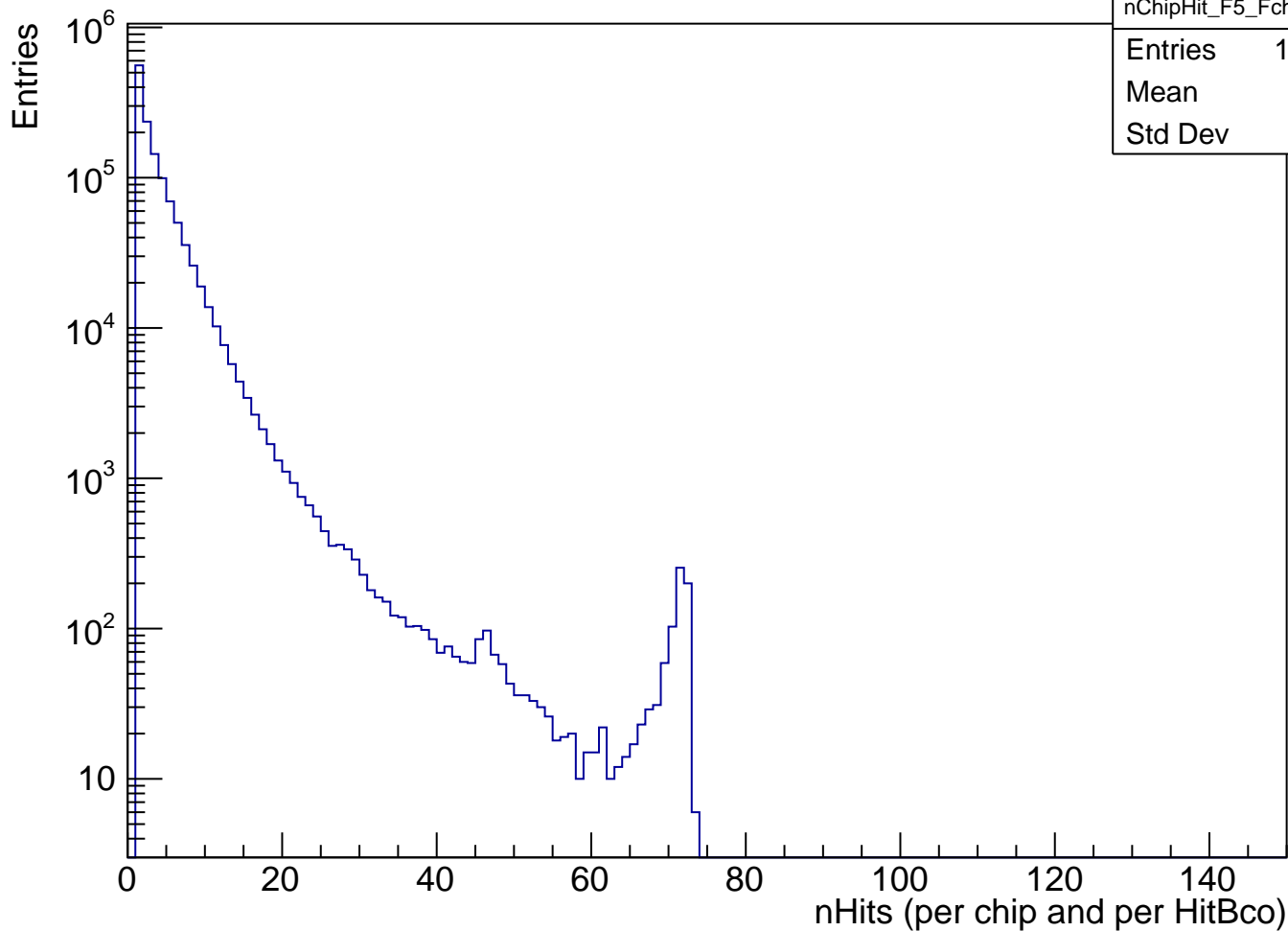
nChipHit_F5_Fch11_Chip3

Entries



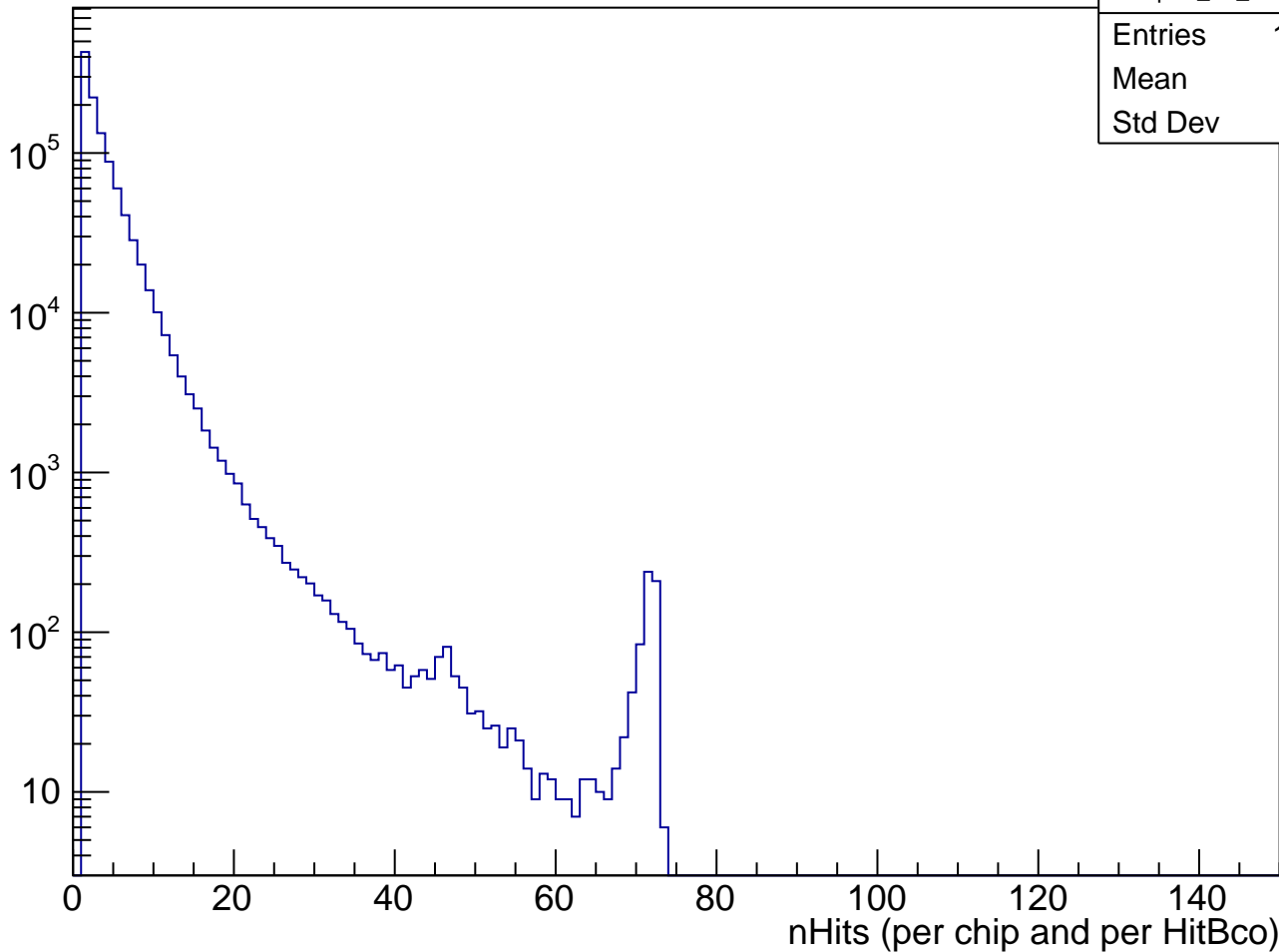
nChipHit_F5_Fch11_Chip3	
Entries	1172270
Mean	3.465
Std Dev	4.003

nChipHit_F5_Fch11_Chip4



nChipHit_F5_Fch11_Chip5

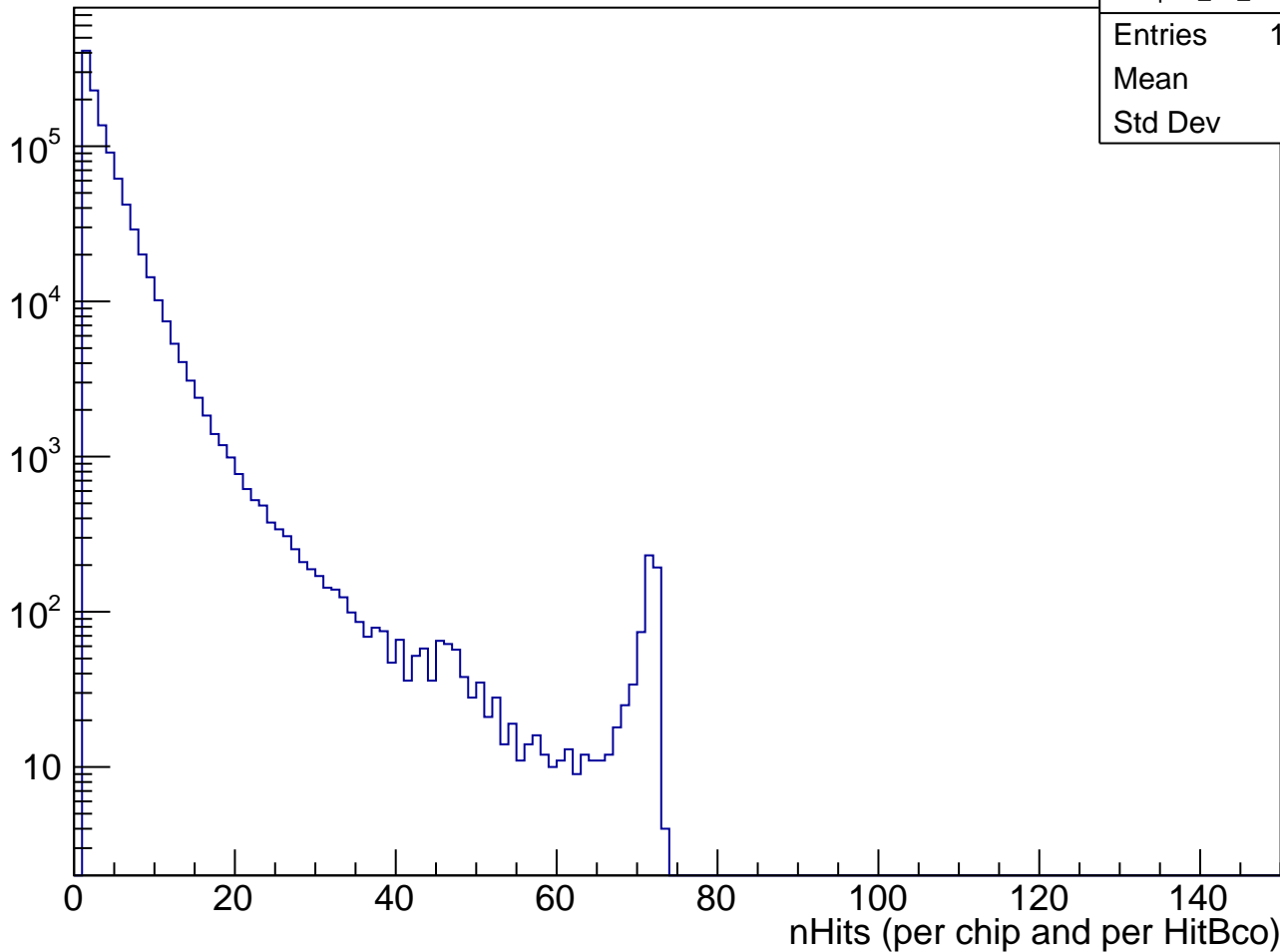
Entries



nChipHit_F5_Fch11_Chip5	
Entries	1081001
Mean	3.091
Std Dev	3.717

nChipHit_F5_Fch11_Chip6

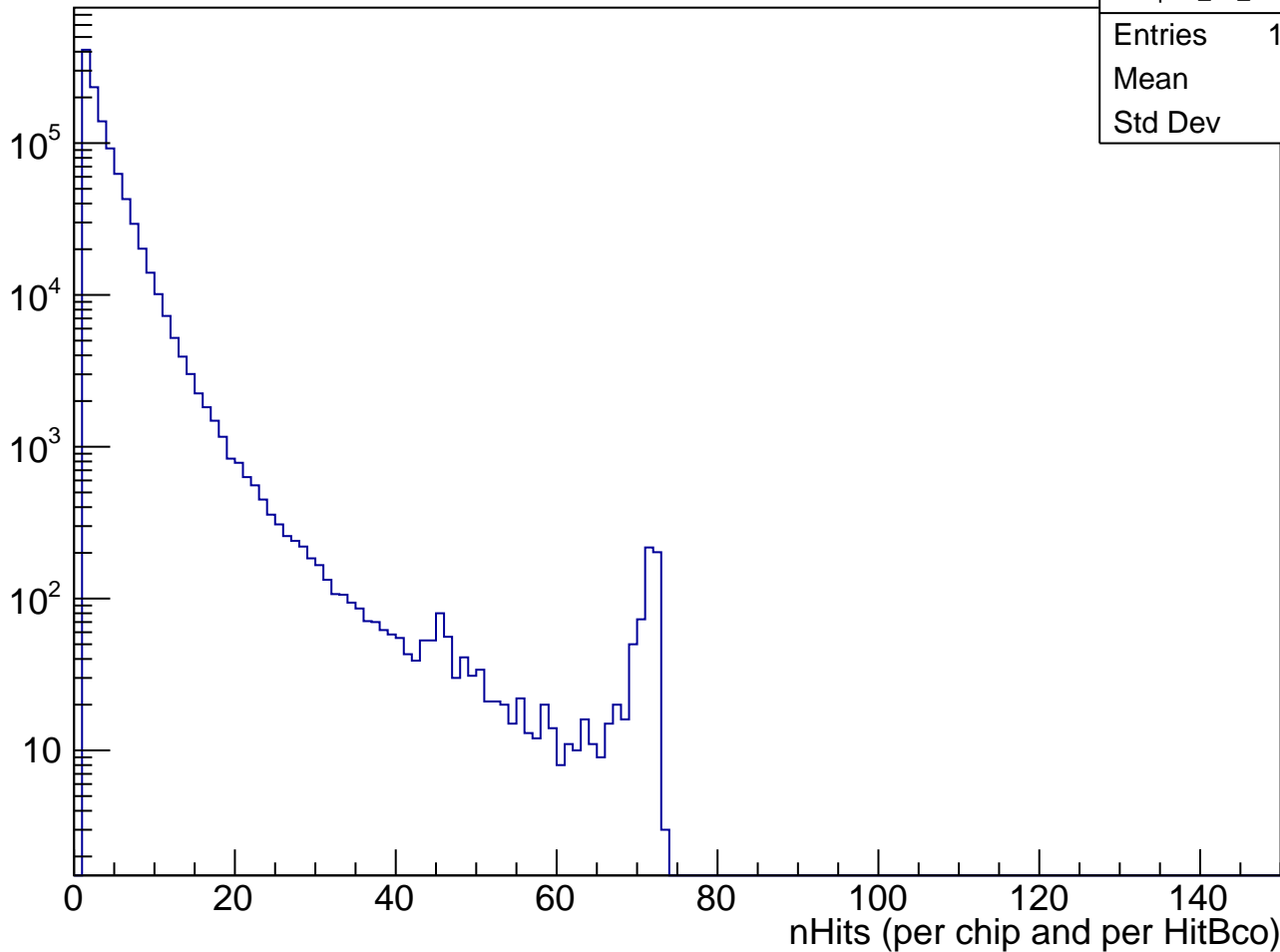
Entries



nChipHit_F5_Fch11_Chip6	
Entries	1080353
Mean	3.126
Std Dev	3.68

nChipHit_F5_Fch11_Chip7

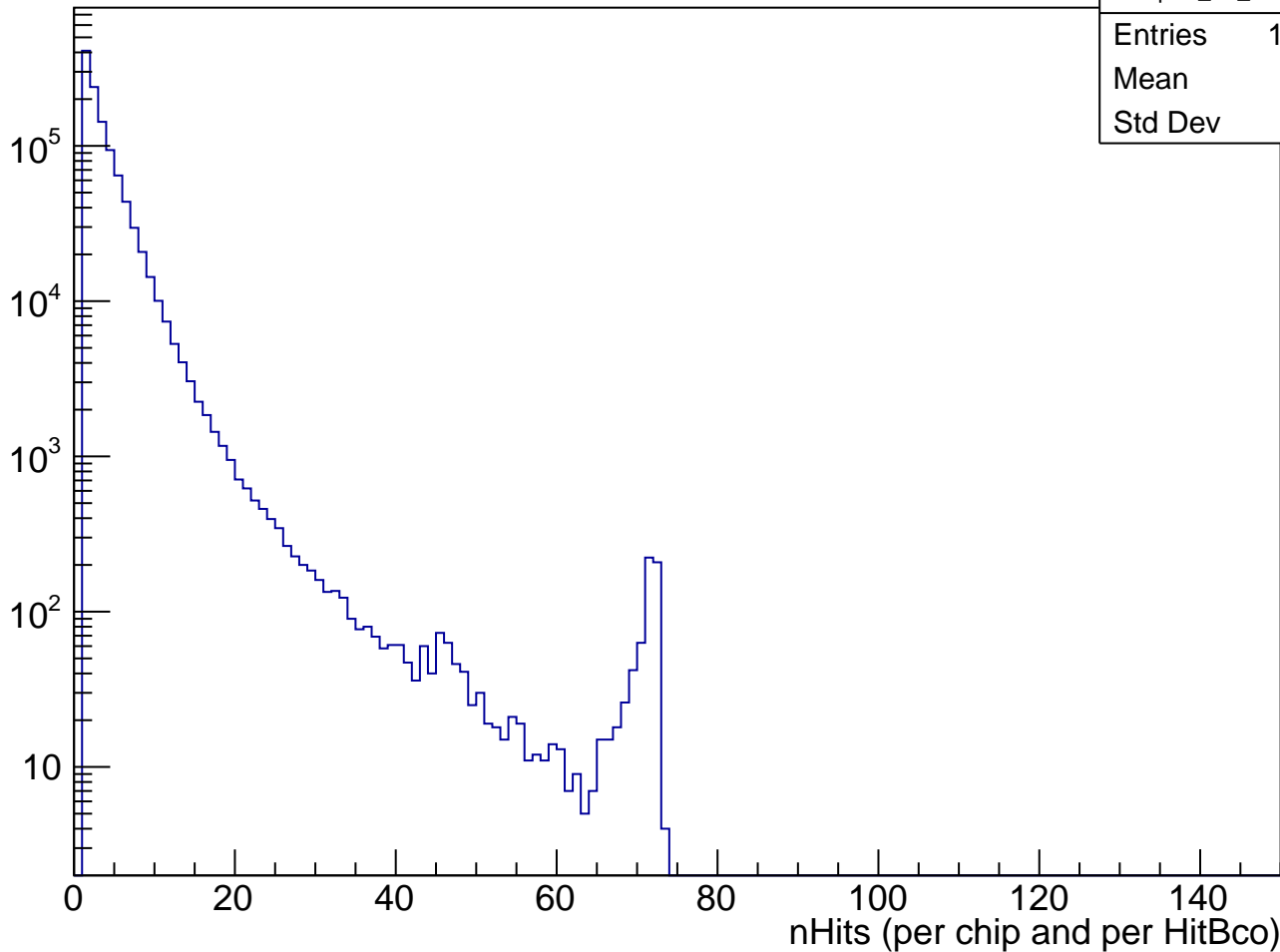
Entries



nChipHit_F5_Fch11_Chip7	
Entries	1089289
Mean	3.115
Std Dev	3.642

nChipHit_F5_Fch11_Chip8

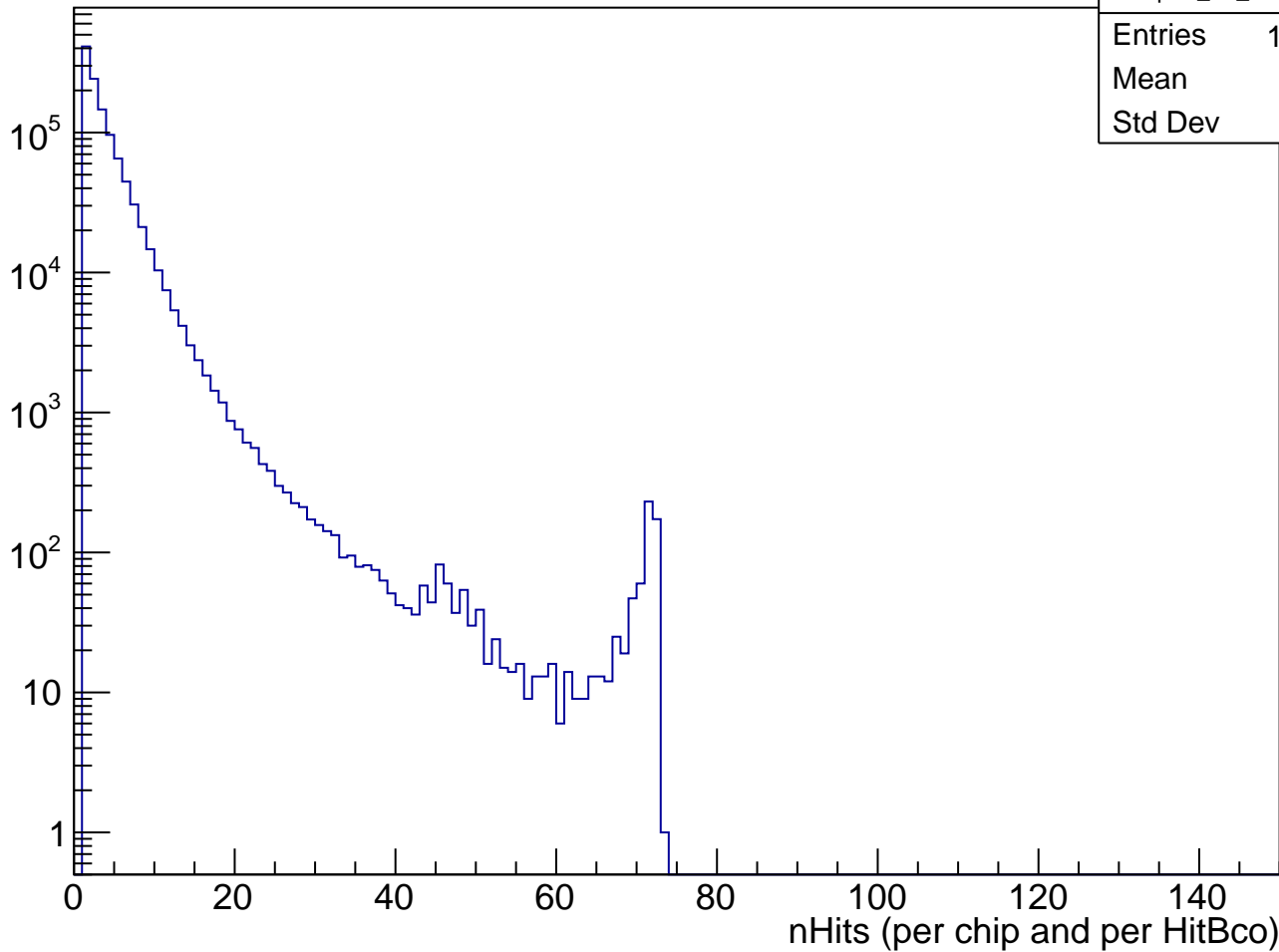
Entries



nChipHit_F5_Fch11_Chip8	
Entries	1102699
Mean	3.127
Std Dev	3.626

nChipHit_F5_Fch11_Chip9

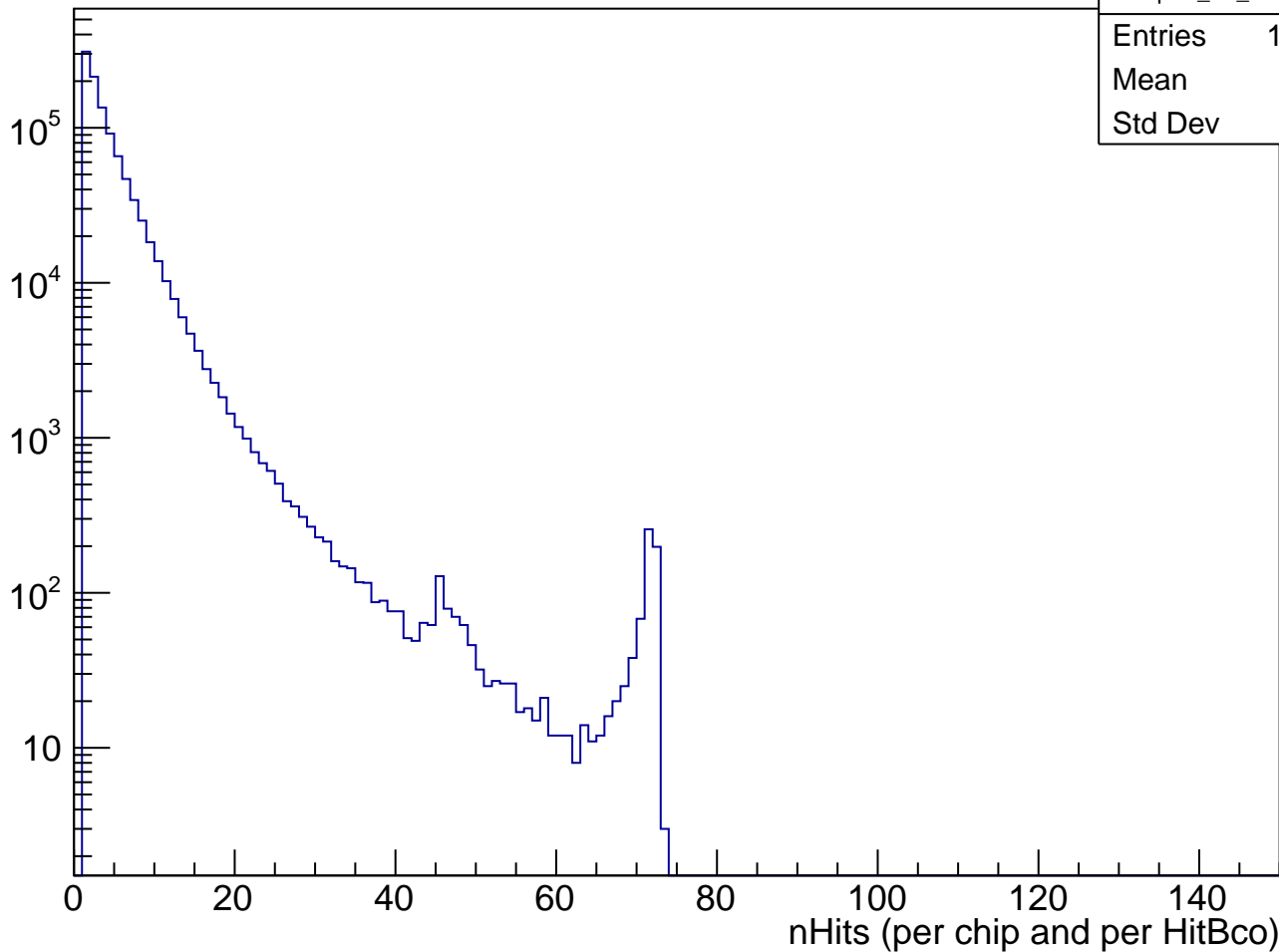
Entries



nChipHit_F5_Fch11_Chip9	
Entries	1117172
Mean	3.134
Std Dev	3.596

nChipHit_F5_Fch12_Chip0

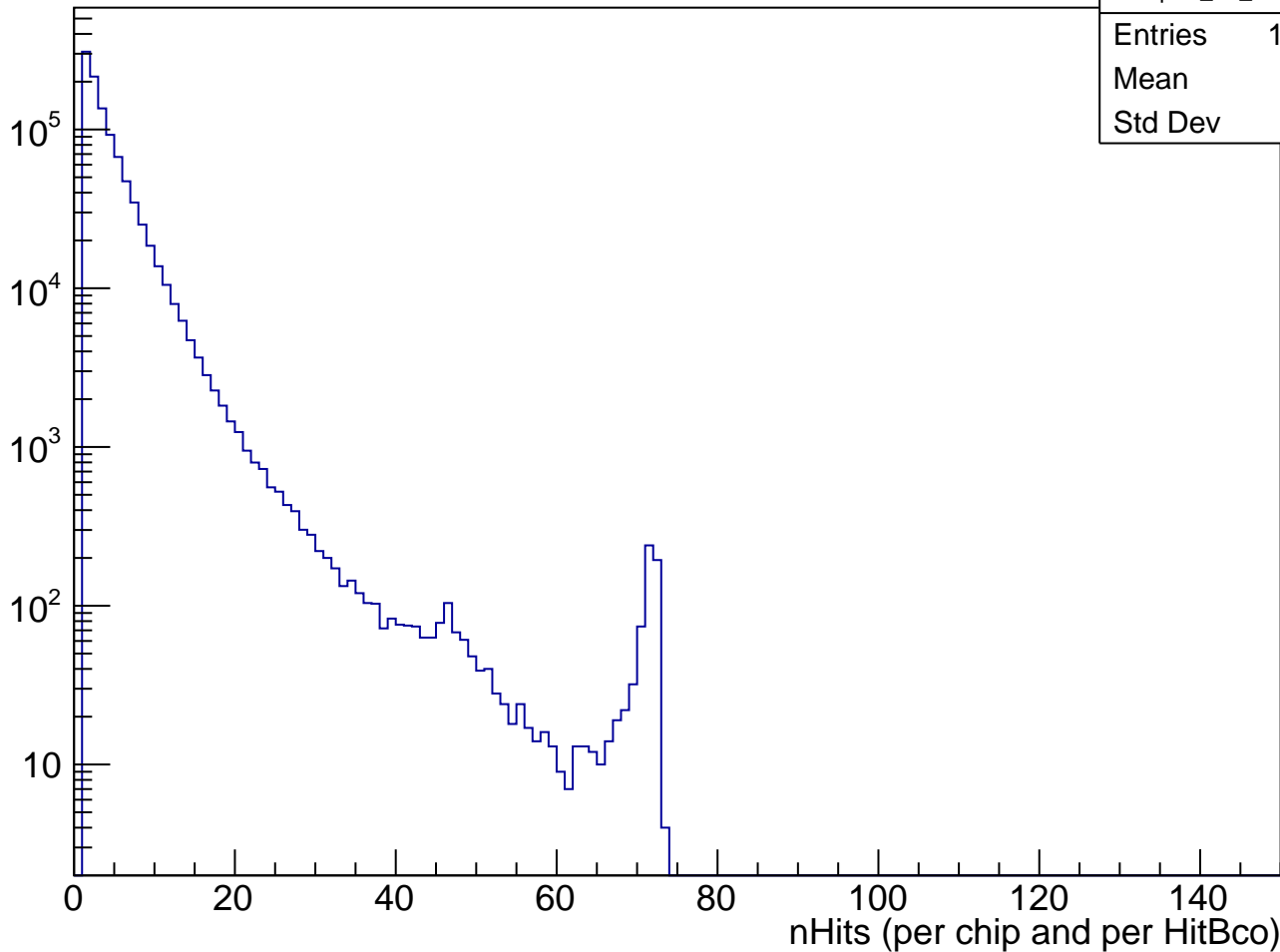
Entries



nChipHit_F5_Fch12_Chip0	
Entries	1004164
Mean	3.673
Std Dev	4.193

nChipHit_F5_Fch12_Chip1

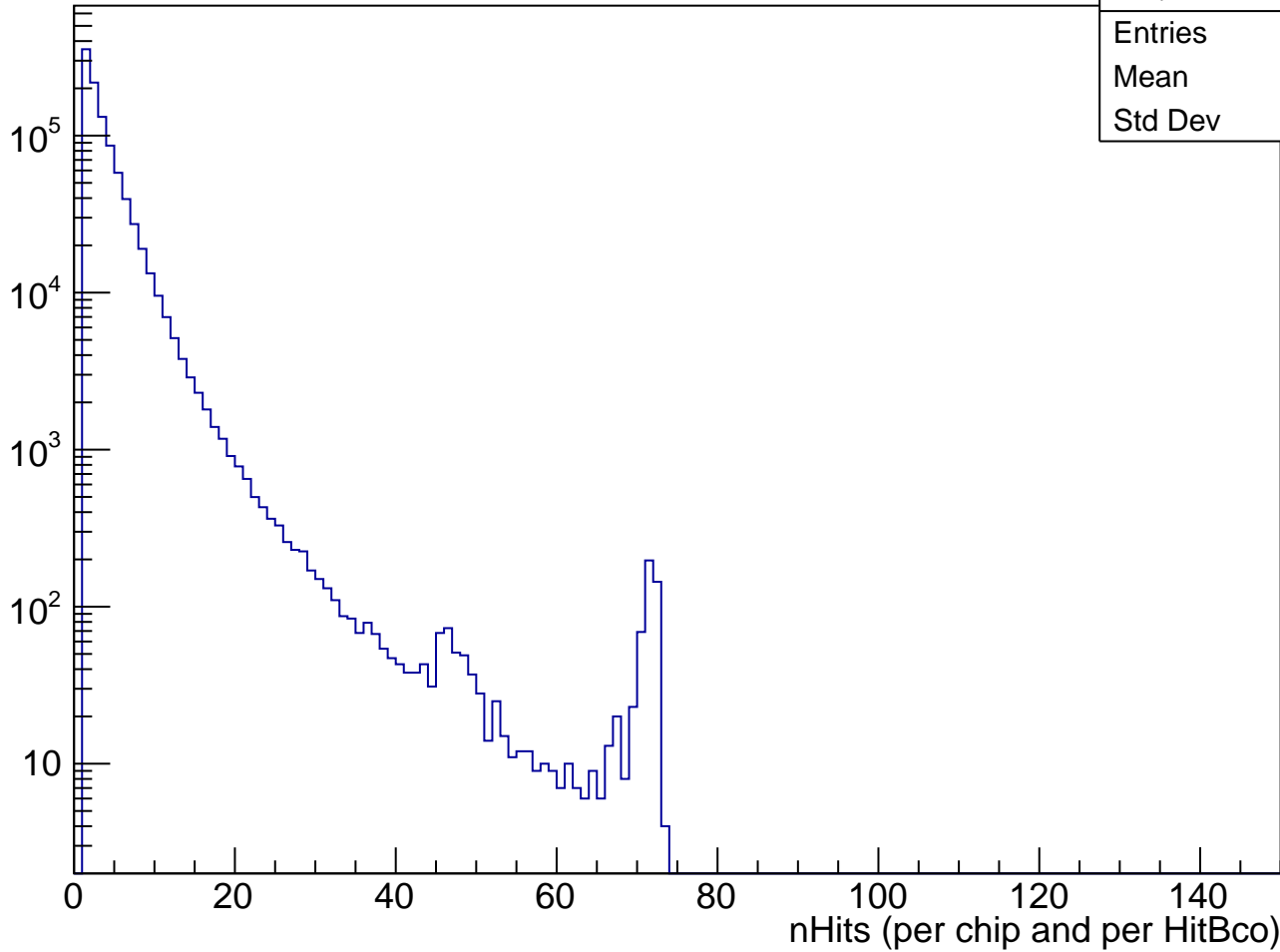
Entries



nChipHit_F5_Fch12_Chip1	
Entries	1009659
Mean	3.685
Std Dev	4.183

nChipHit_F5_Fch12_Chip10

Entries



nChipHit_F5_Fch12_Chip10

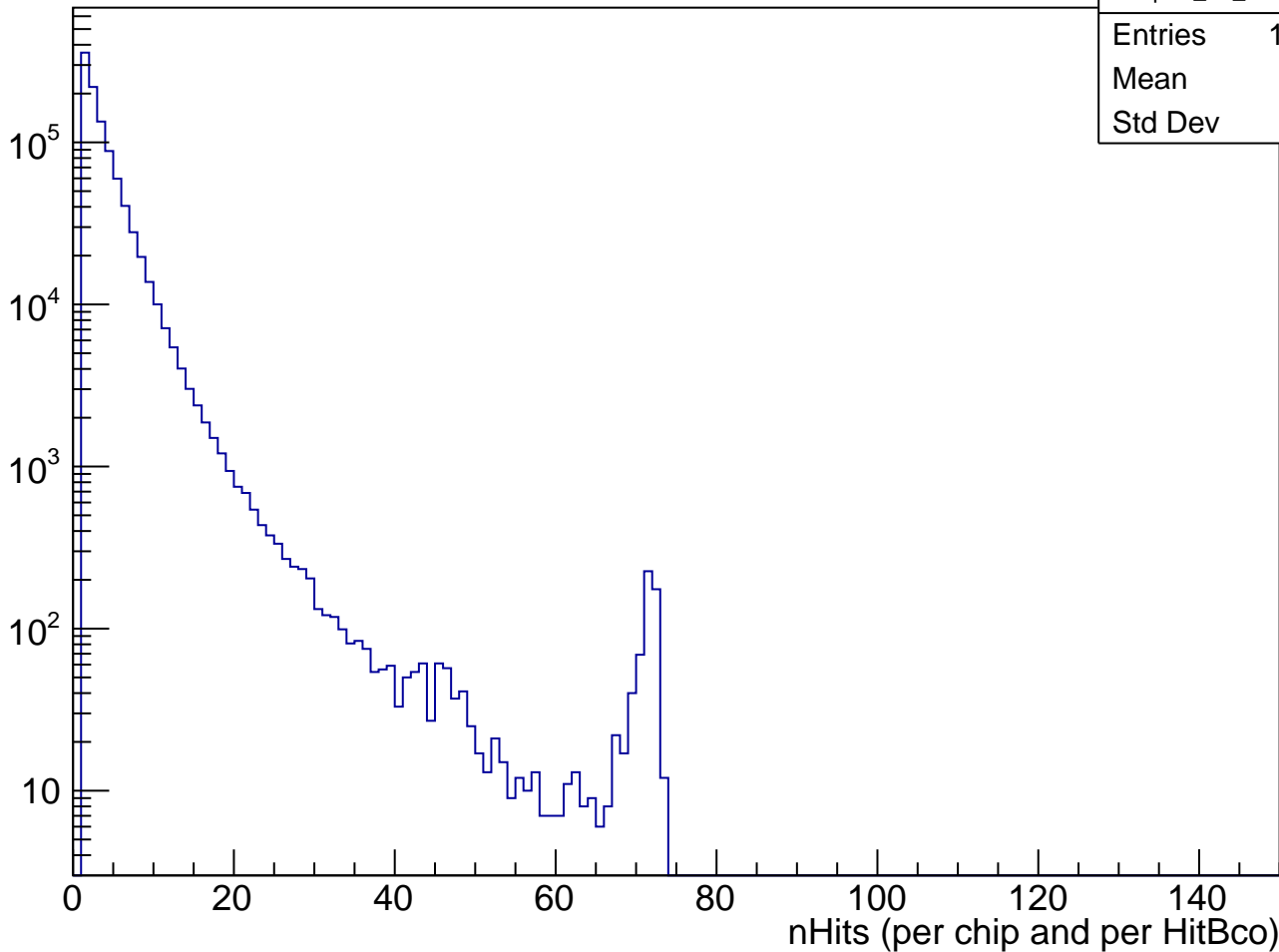
Entries 989428

Mean 3.189

Std Dev 3.643

nChipHit_F5_Fch12_Chip11

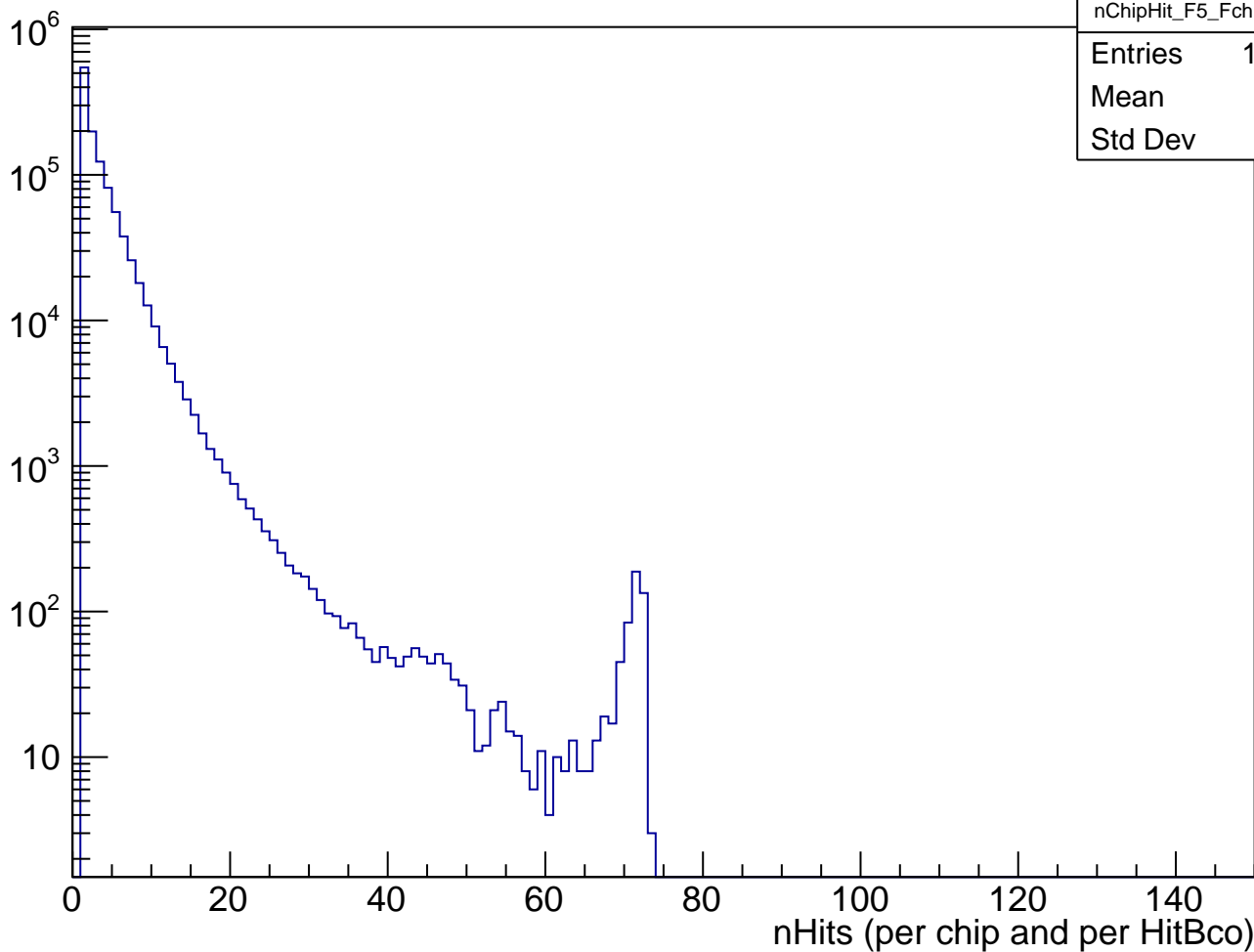
Entries



nChipHit_F5_Fch12_Chip11	
Entries	1005363
Mean	3.218
Std Dev	3.701

nChipHit_F5_Fch12_Chip12

Entries



nChipHit_F5_Fch12_Chip12

Entries 1140697

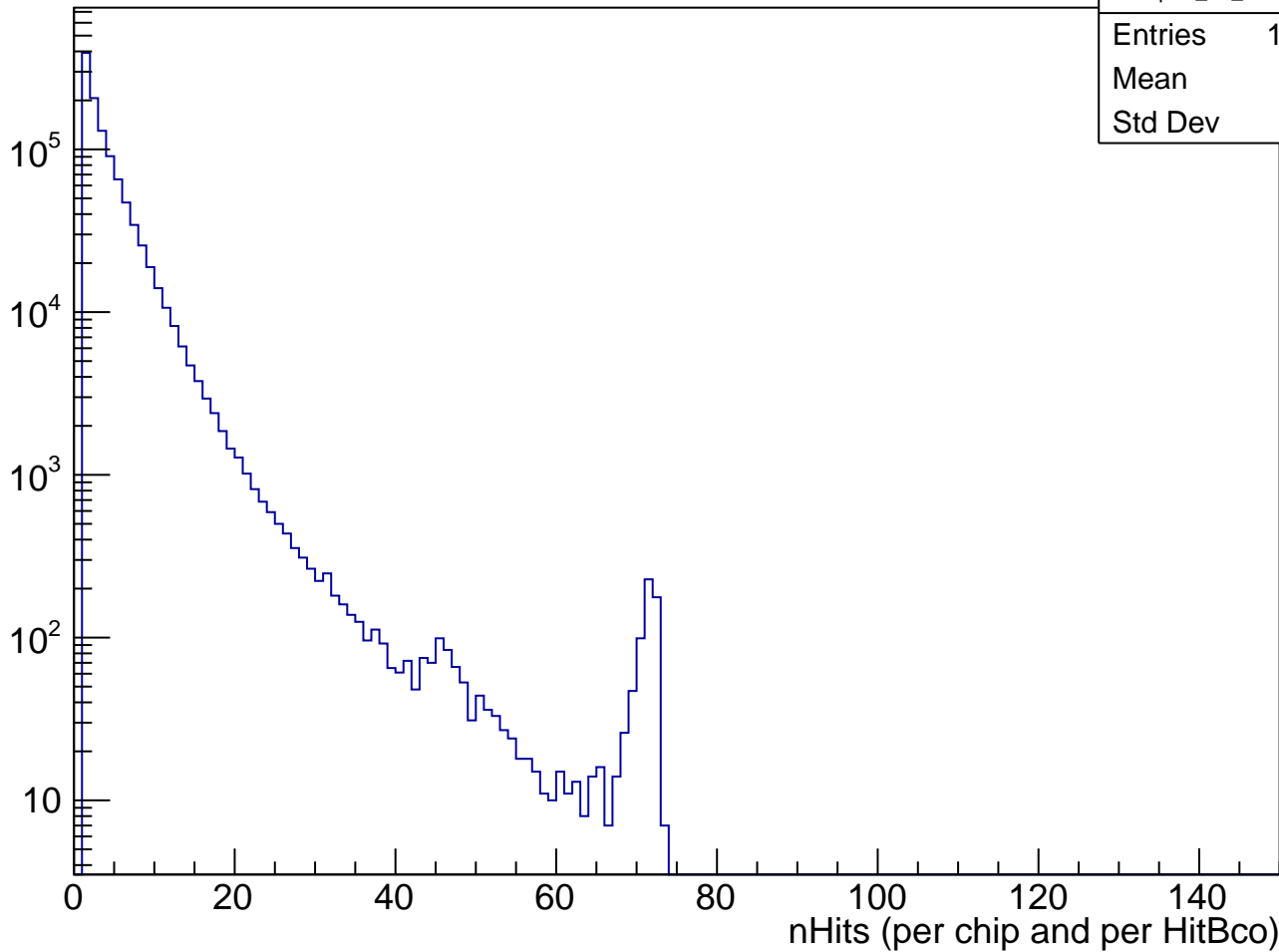
Mean 2.804

Std Dev 3.454

nHits (per chip and per HitBco)

nChipHit_F5_Fch12_Chip13

Entries



nChipHit_F5_Fch12_Chip13

Entries 1076376

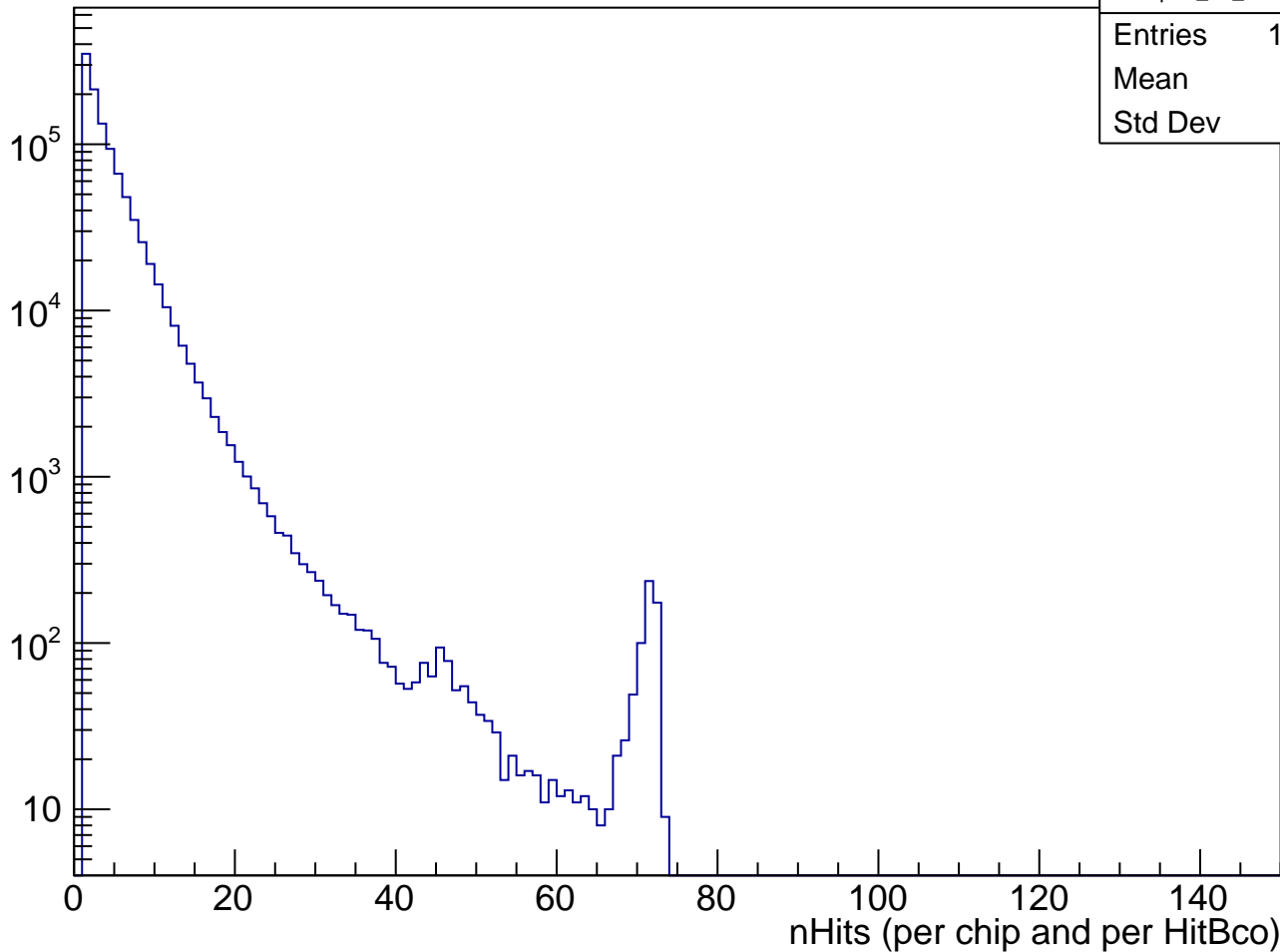
Mean 3.506

Std Dev 4.13

nHits (per chip and per HitBco)

nChipHit_F5_Fch12_Chip14

Entries



nChipHit_F5_Fch12_Chip14

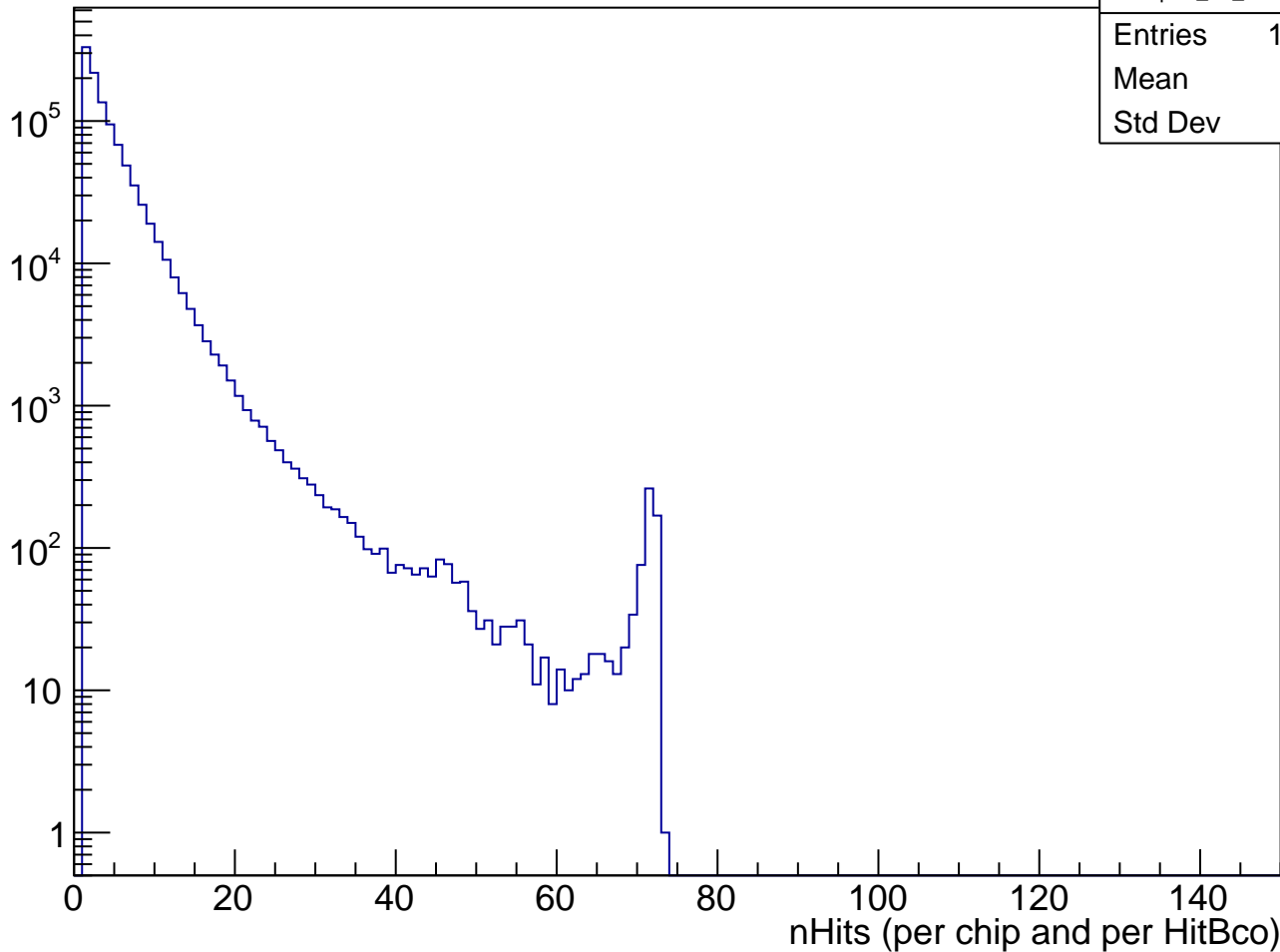
Entries 1050644

Mean 3.599

Std Dev 4.141

nChipHit_F5_Fch12_Chip15

Entries



nChipHit_F5_Fch12_Chip15

Entries 1040315

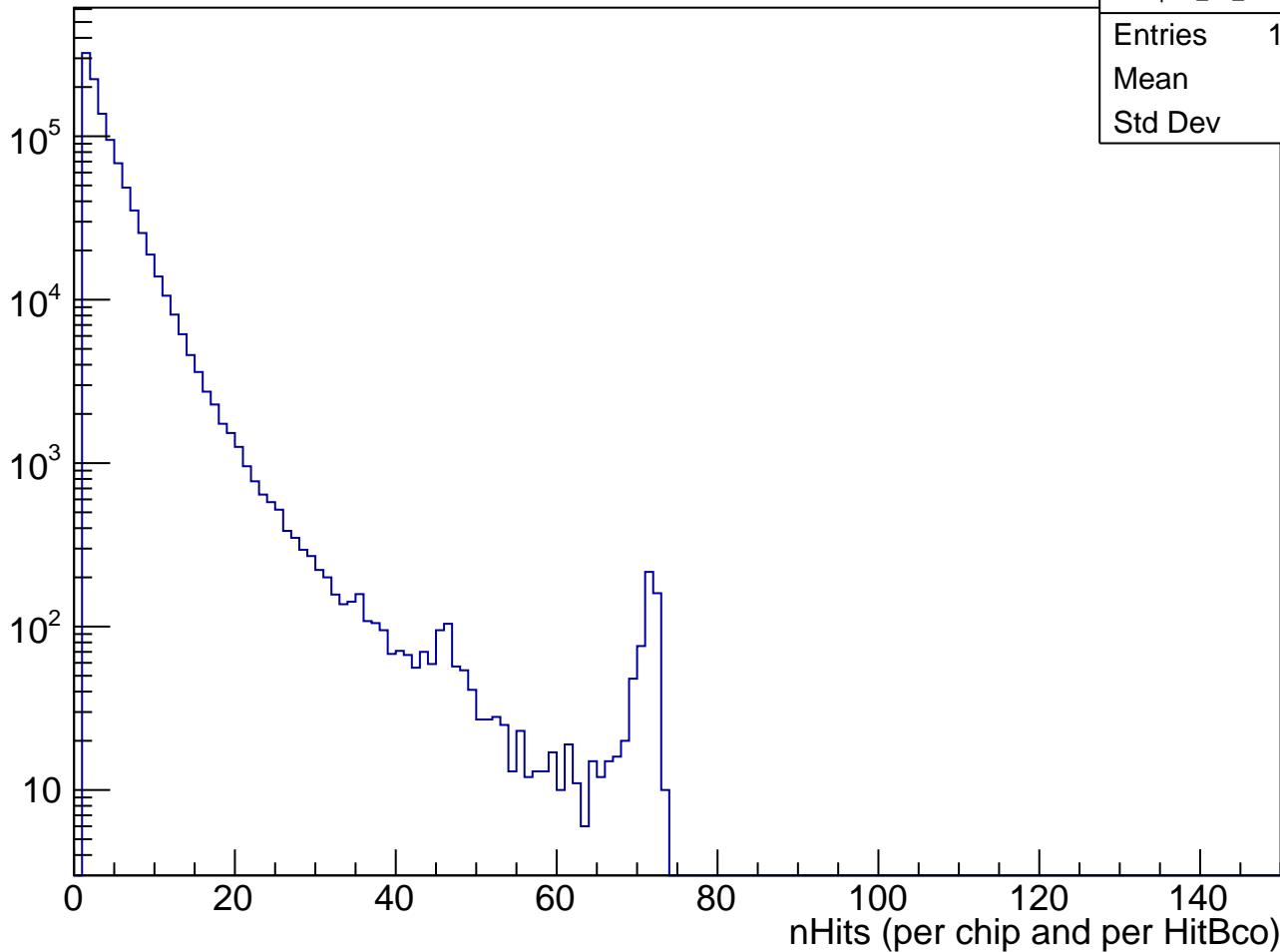
Mean 3.639

Std Dev 4.138

nHits (per chip and per HitBco)

nChipHit_F5_Fch12_Chip16

Entries



nChipHit_F5_Fch12_Chip16

Entries 1039646

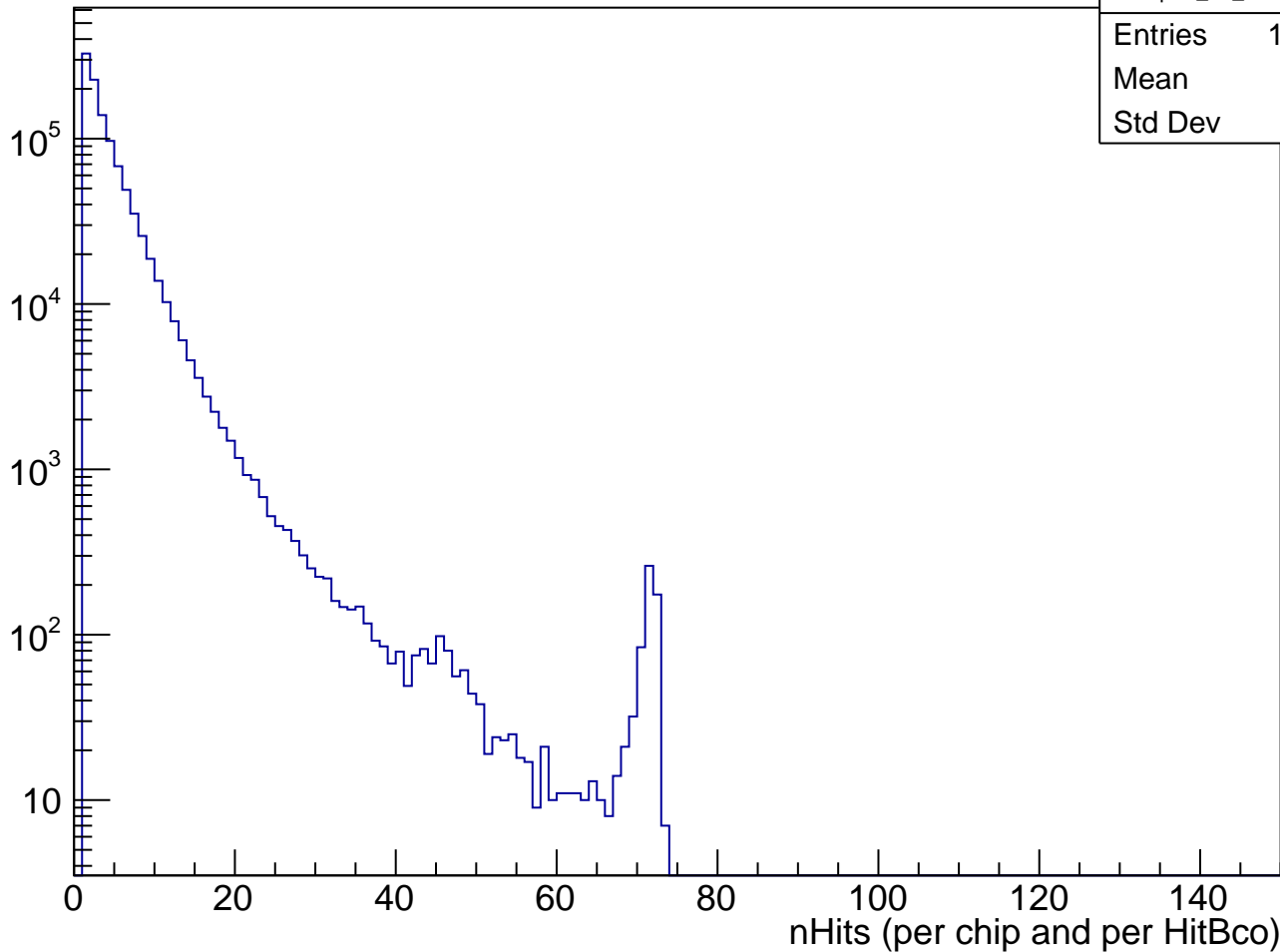
Mean 3.636

Std Dev 4.1

nHits (per chip and per HitBco)

nChipHit_F5_Fch12_Chip17

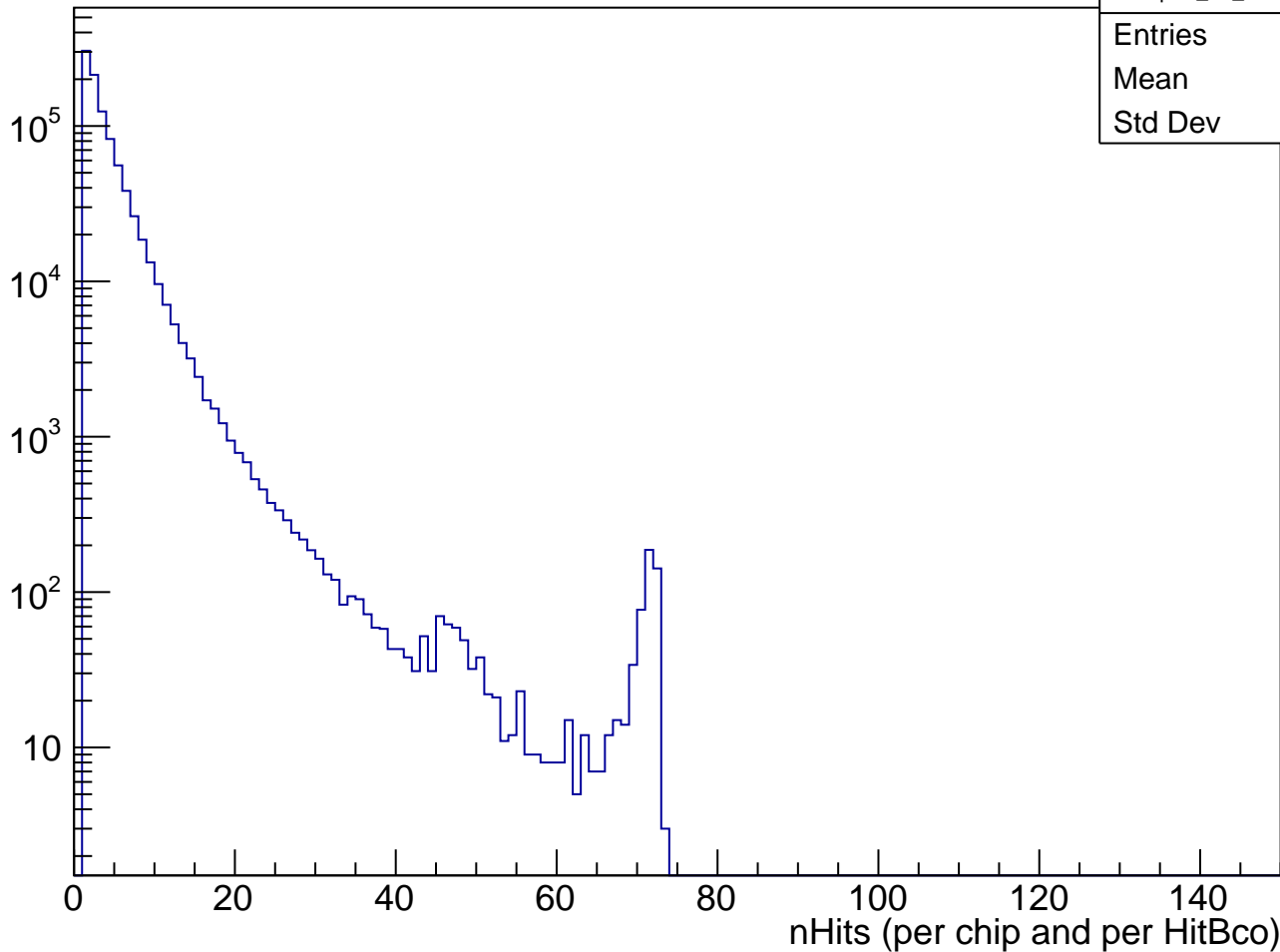
Entries



nChipHit_F5_Fch12_Chip17	
Entries	1050332
Mean	3.617
Std Dev	4.1

nChipHit_F5_Fch12_Chip18

Entries



nChipHit_F5_Fch12_Chip18

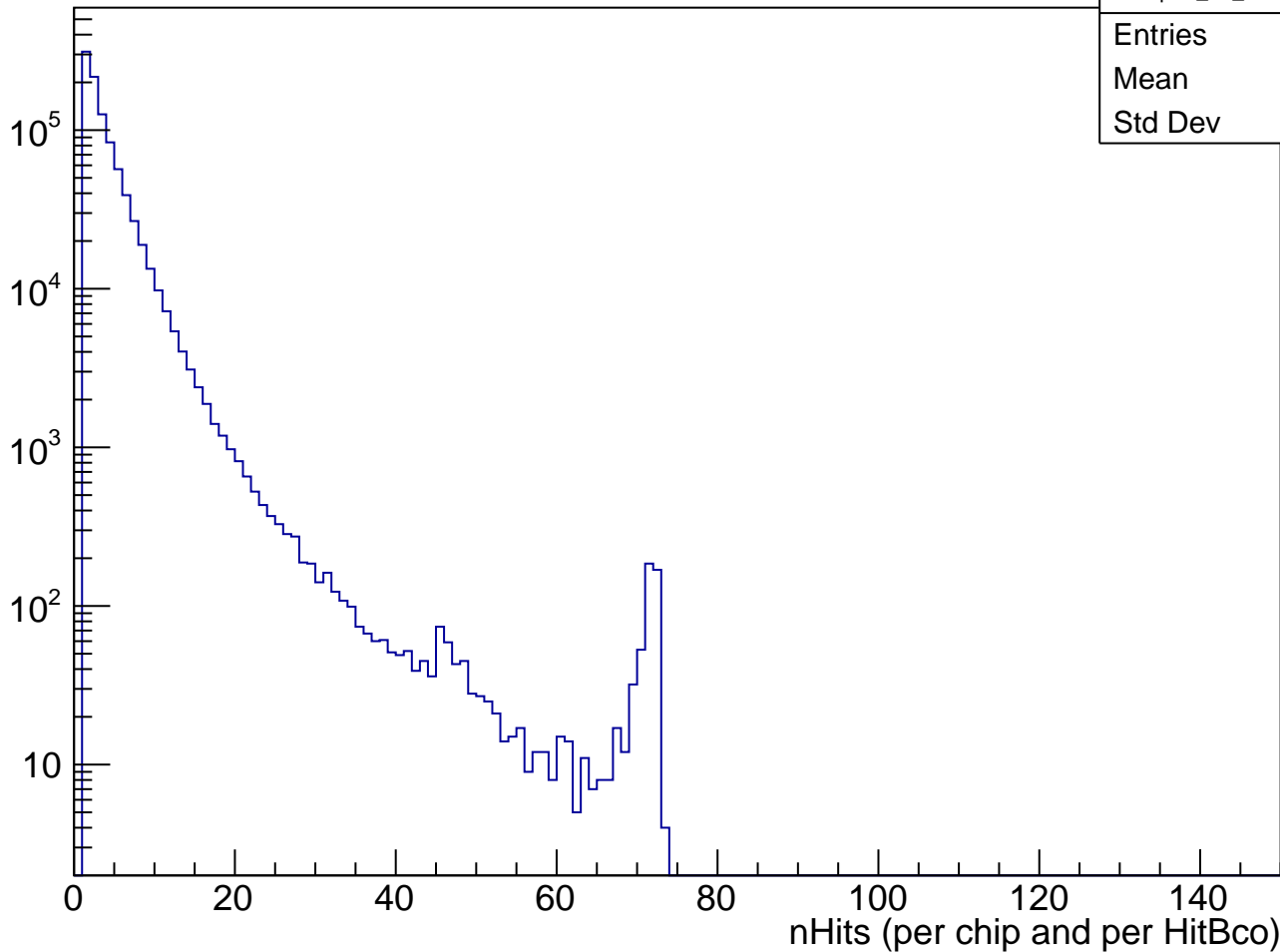
Entries 920436

Mean 3.315

Std Dev 3.778

nChipHit_F5_Fch12_Chip19

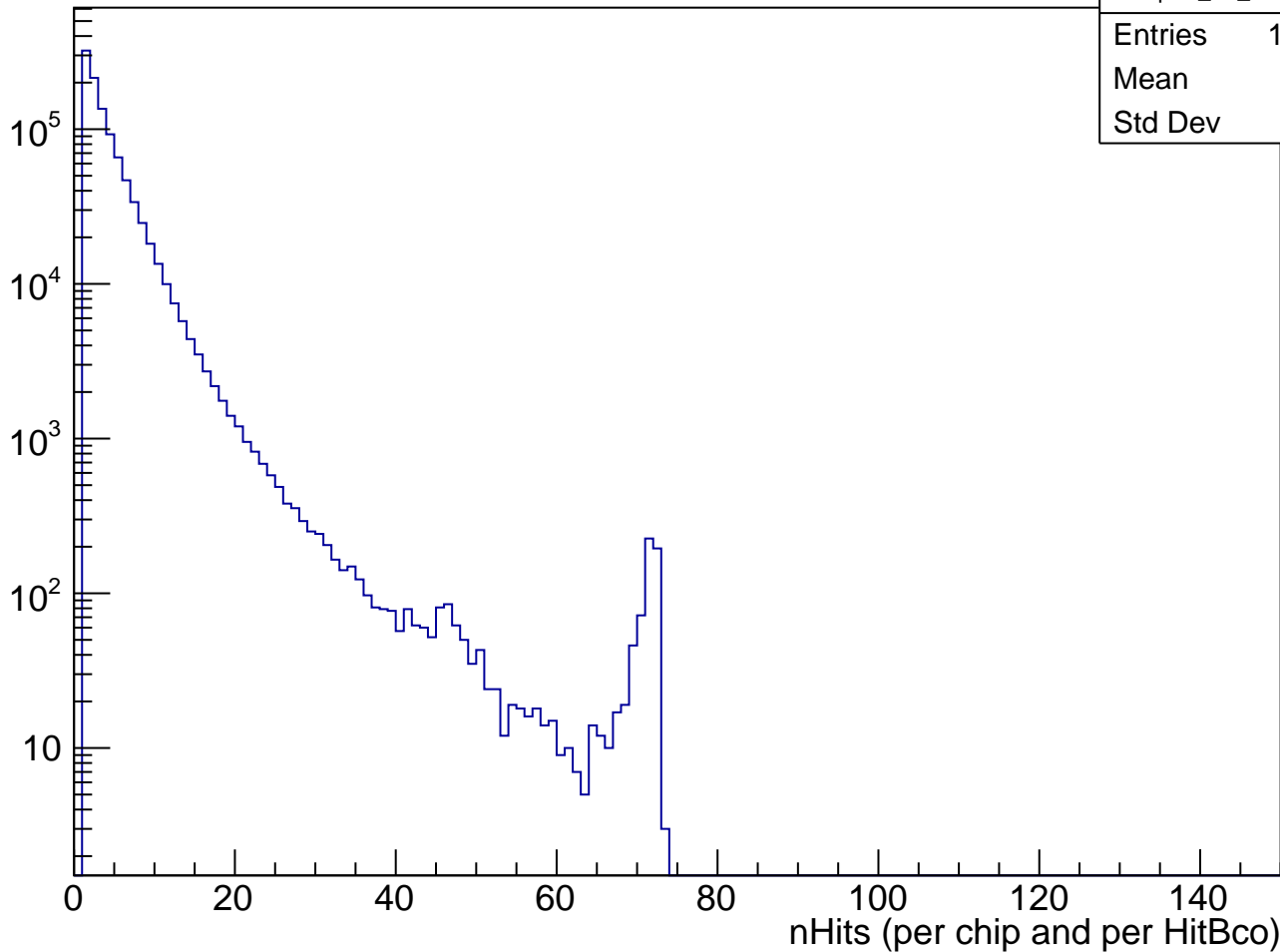
Entries



nChipHit_F5_Fch12_Chip19	
Entries	936892
Mean	3.302
Std Dev	3.758

nChipHit_F5_Fch12_Chip2

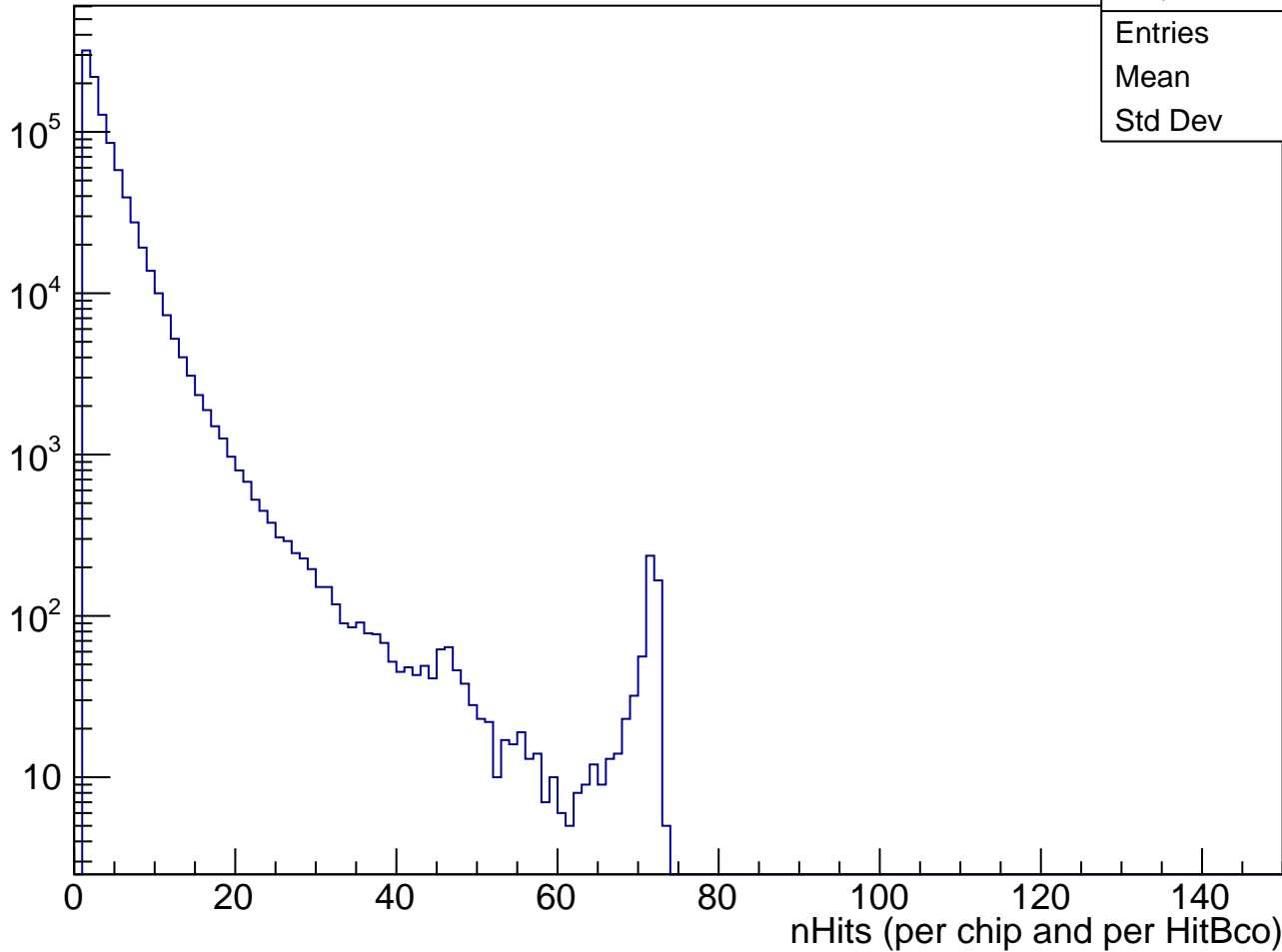
Entries



nChipHit_F5_Fch12_Chip2	
Entries	1015756
Mean	3.609
Std Dev	4.104

nChipHit_F5_Fch12_Chip20

Entries



nChipHit_F5_Fch12_Chip20

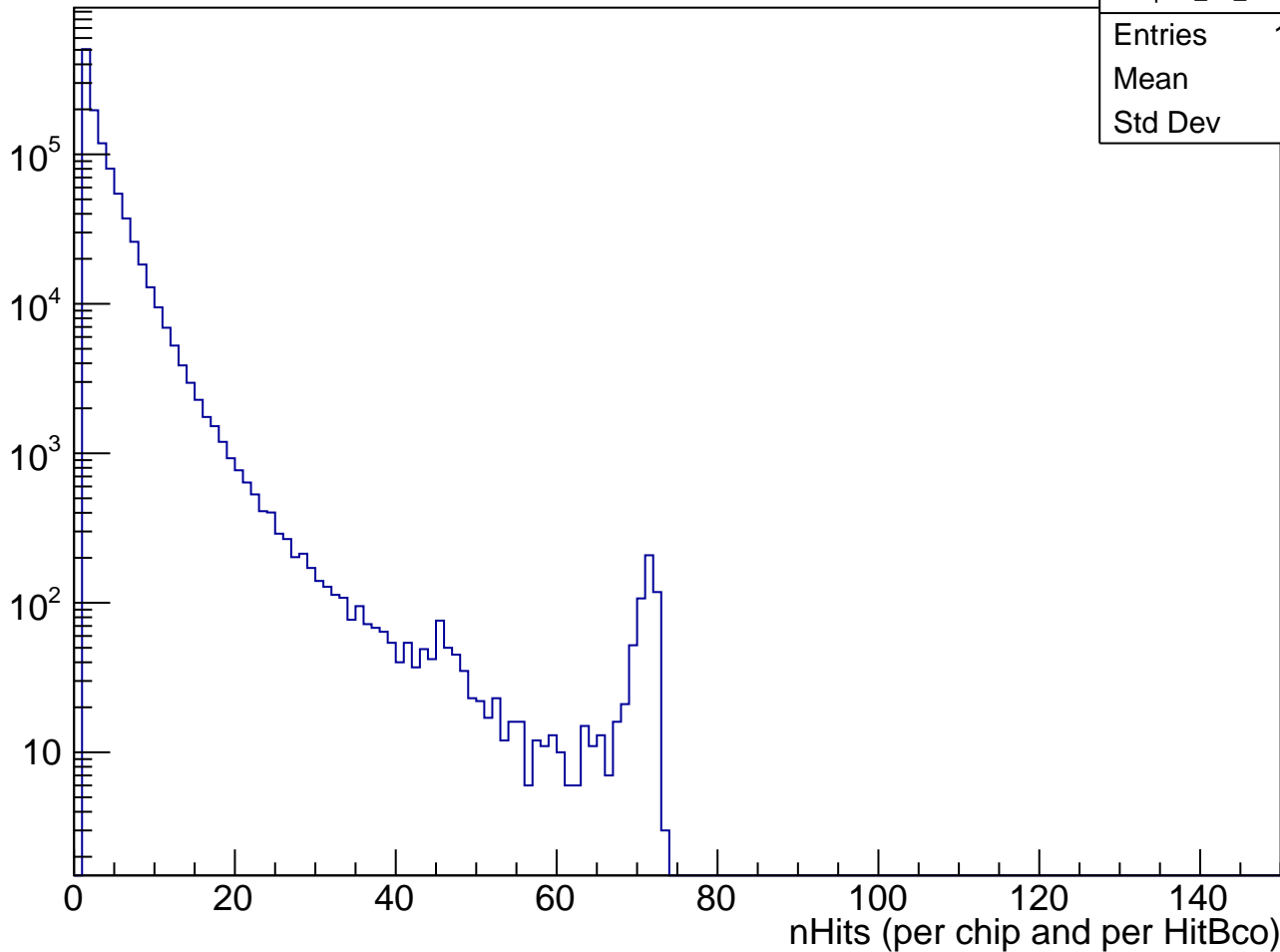
Entries 953908

Mean 3.299

Std Dev 3.778

nChipHit_F5_Fch12_Chip21

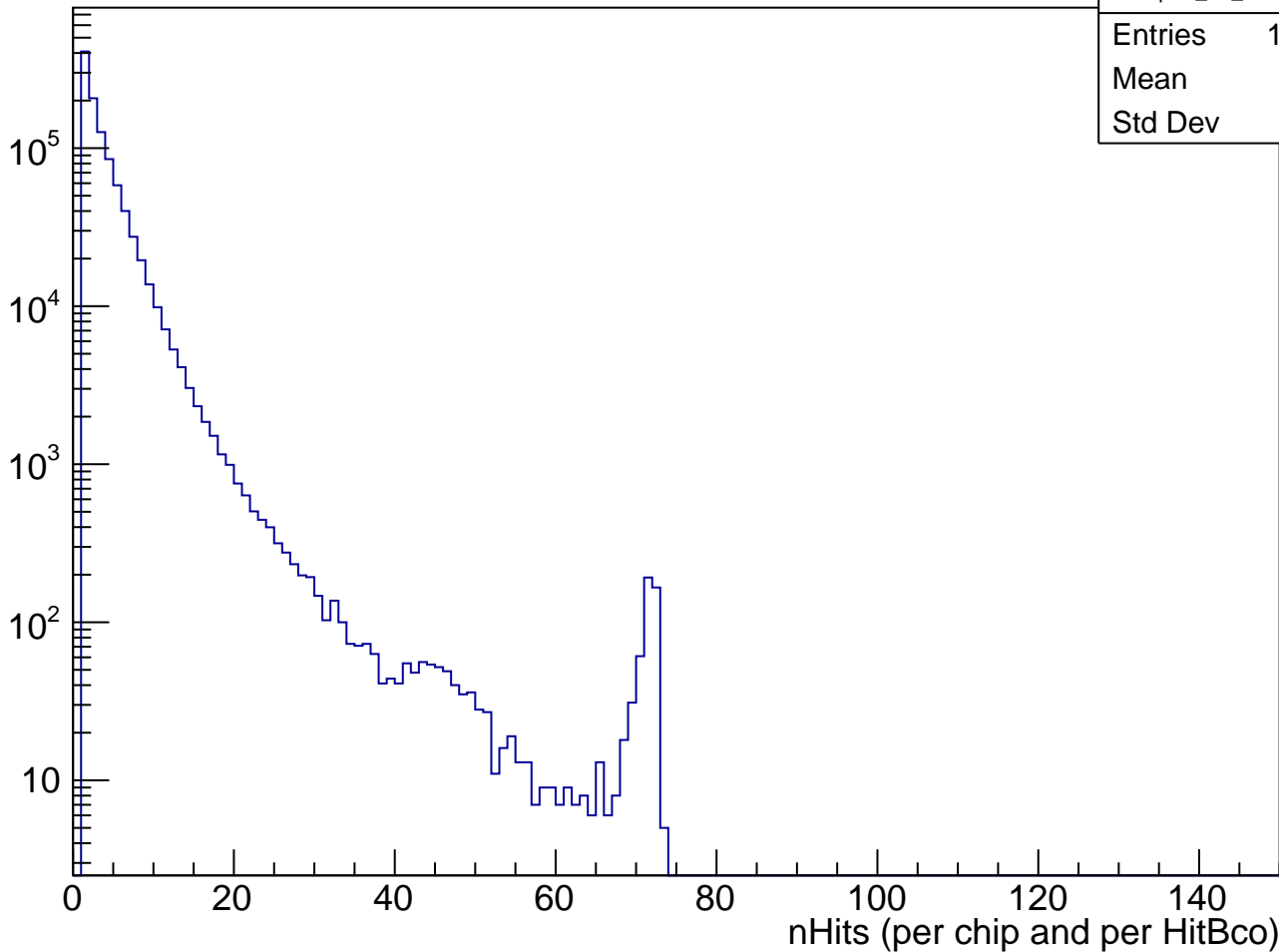
Entries



nChipHit_F5_Fch12_Chip21	
Entries	1092761
Mean	2.893
Std Dev	3.575

nChipHit_F5_Fch12_Chip22

Entries



nChipHit_F5_Fch12_Chip22

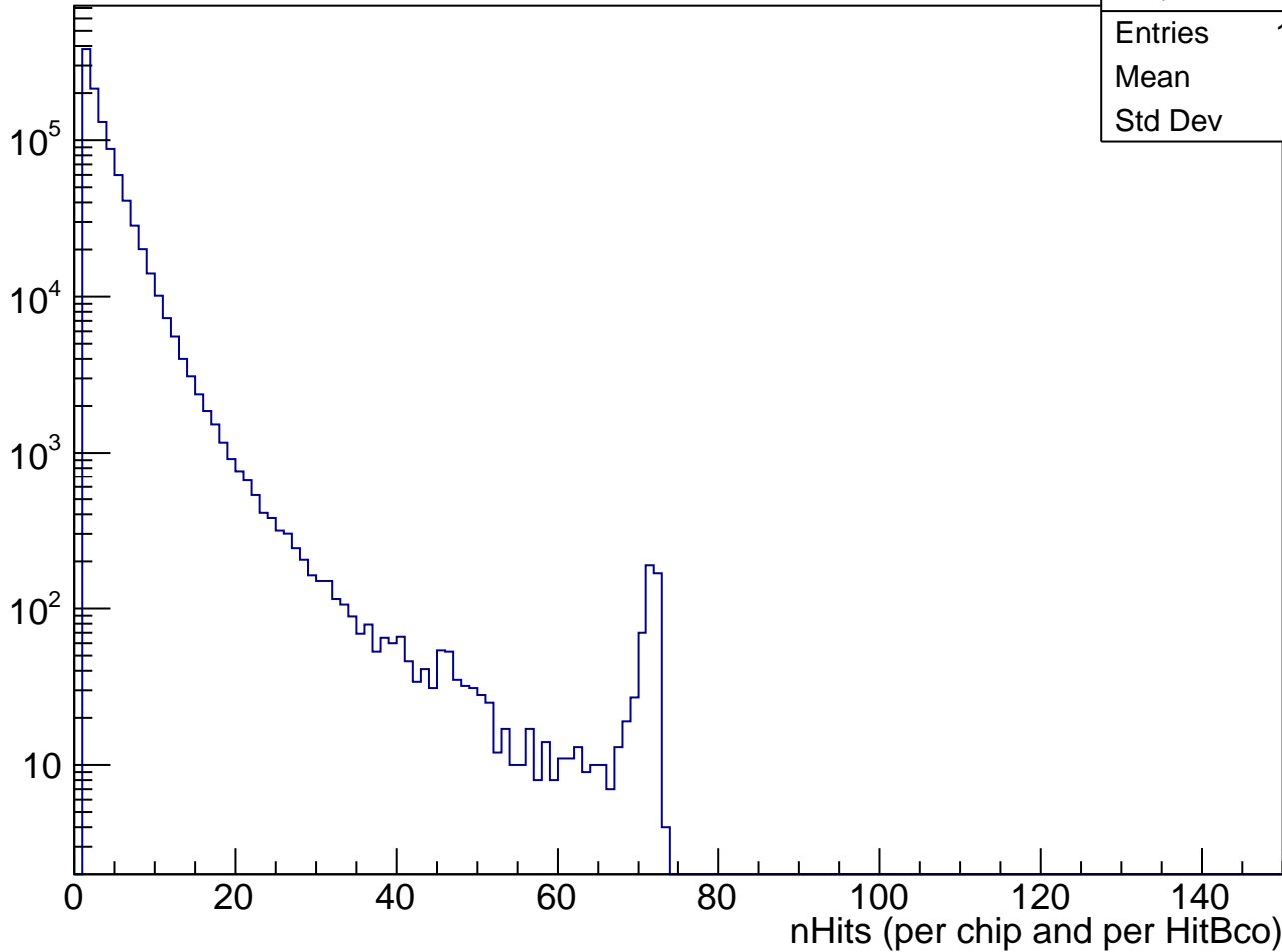
Entries 1029436

Mean 3.108

Std Dev 3.633

nChipHit_F5_Fch12_Chip23

Entries



nChipHit_F5_Fch12_Chip23

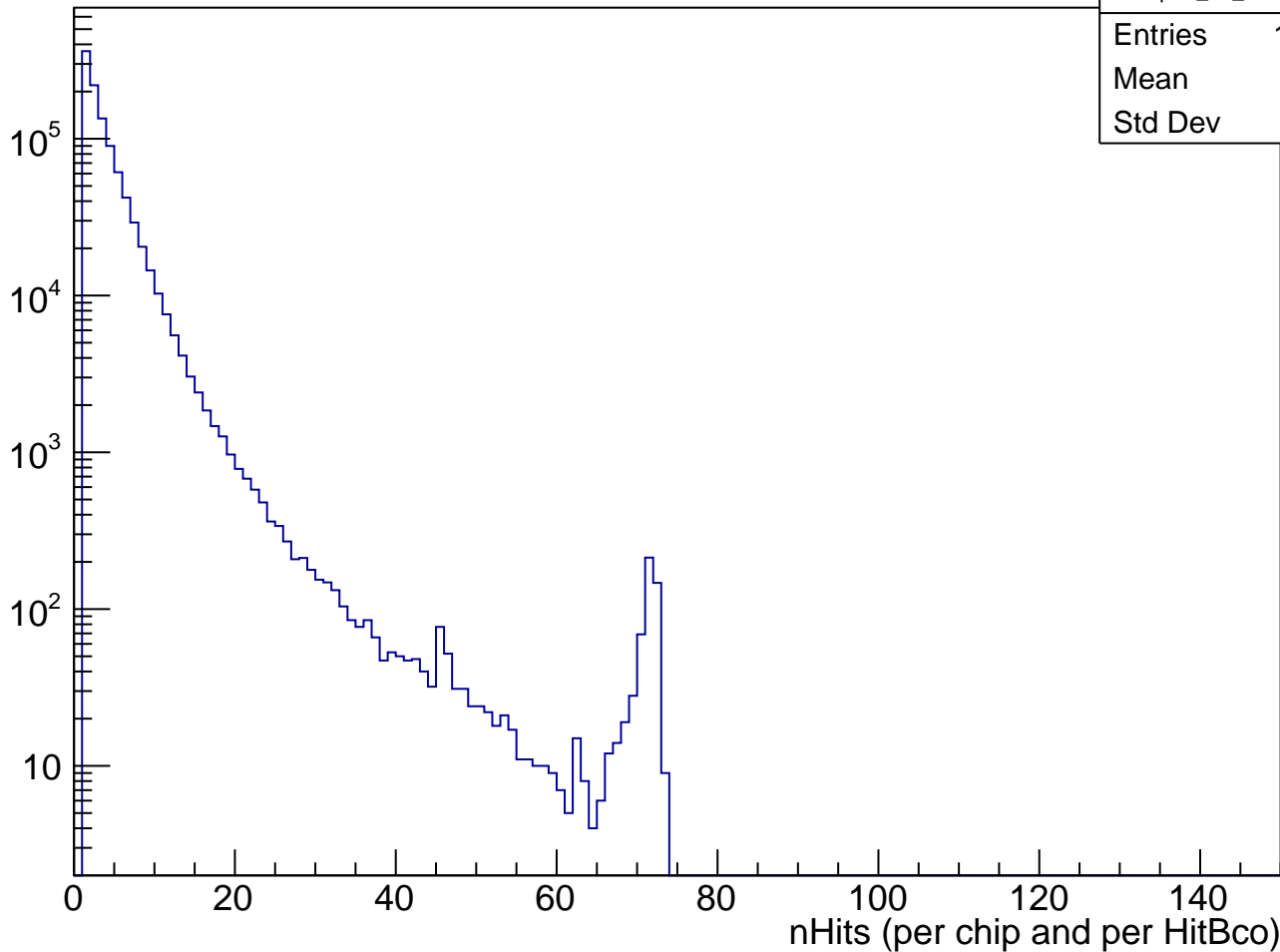
Entries 1022011

Mean 3.178

Std Dev 3.655

nChipHit_F5_Fch12_Chip24

Entries

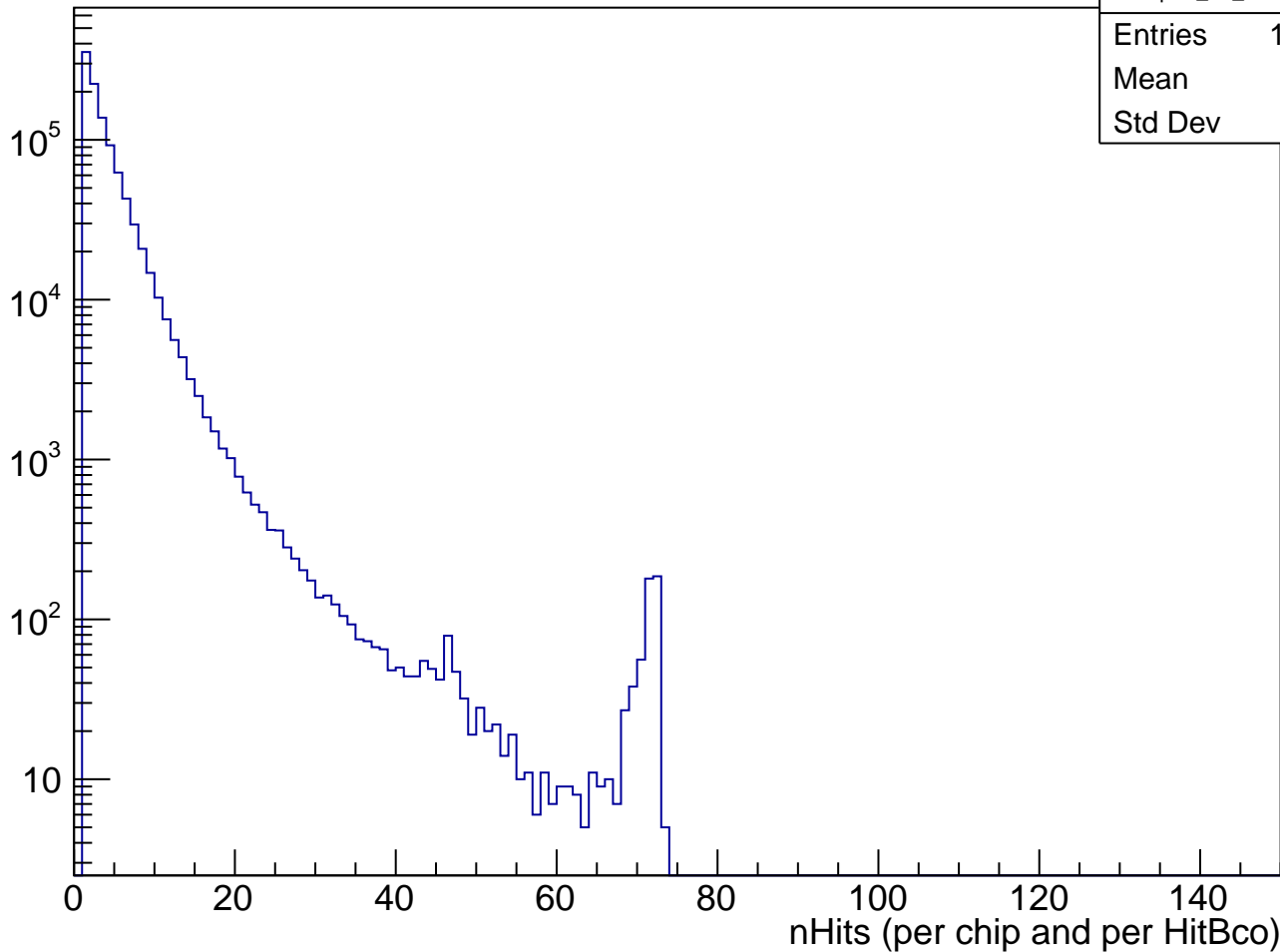


nChipHit_F5_Fch12_Chip24	
Entries	1018321
Mean	3.237
Std Dev	3.672

nHits (per chip and per HitBco)

nChipHit_F5_Fch12_Chip25

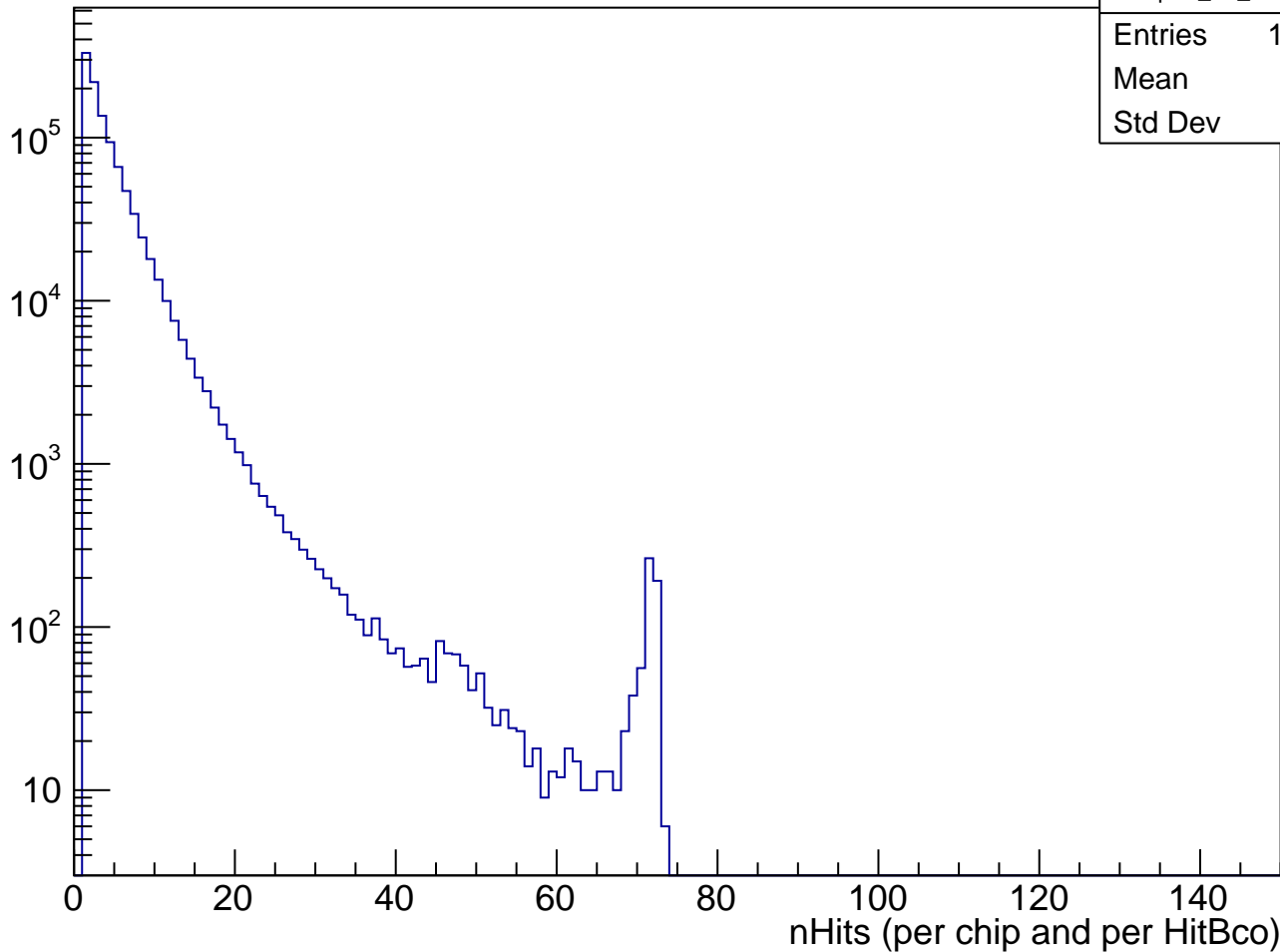
Entries



nChipHit_F5_Fch12_Chip25	
Entries	1023810
Mean	3.261
Std Dev	3.671

nChipHit_F5_Fch12_Chip3

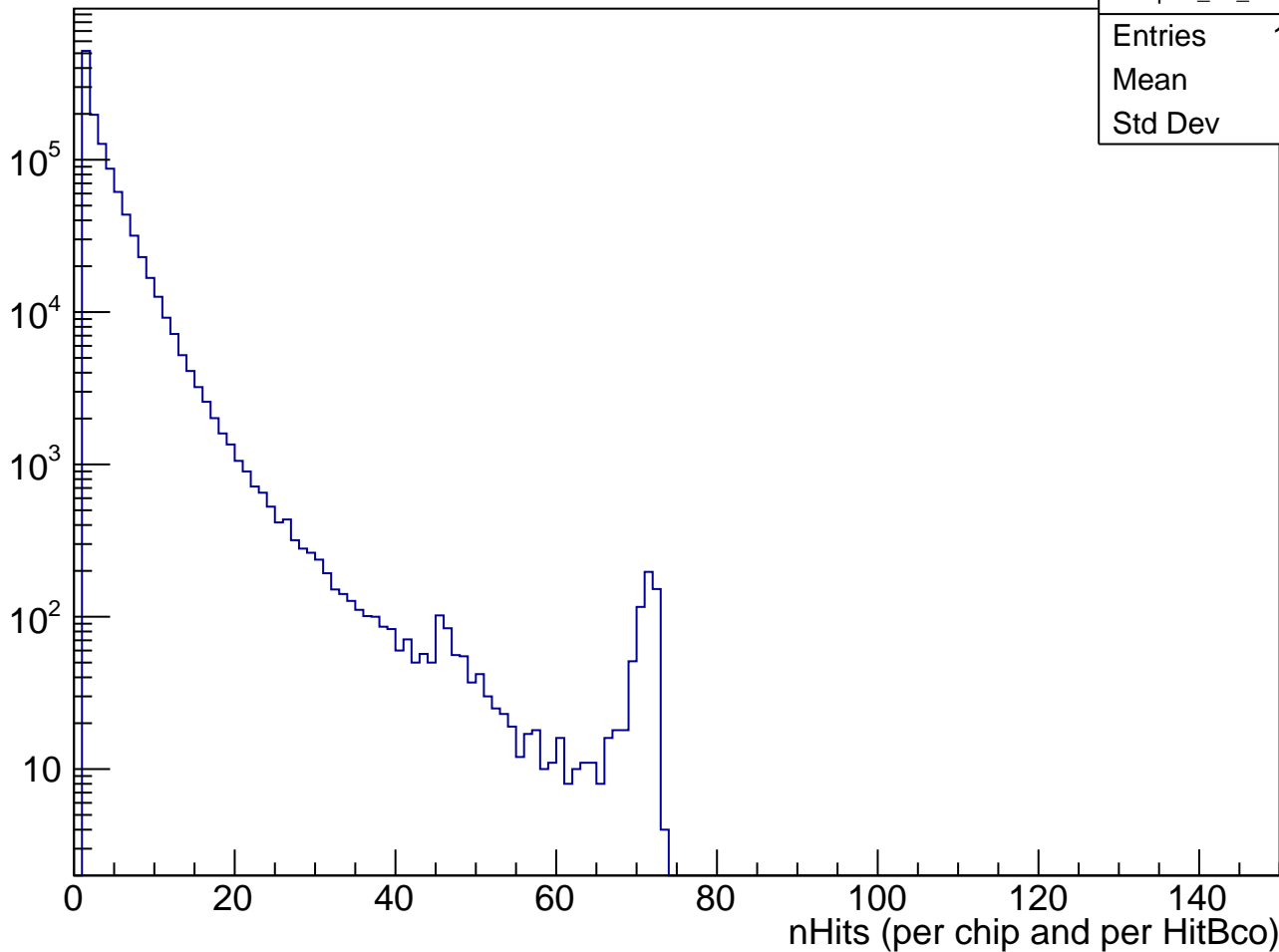
Entries



nChipHit_F5_Fch12_Chip3	
Entries	1030497
Mean	3.583
Std Dev	4.102

nChipHit_F5_Fch12_Chip4

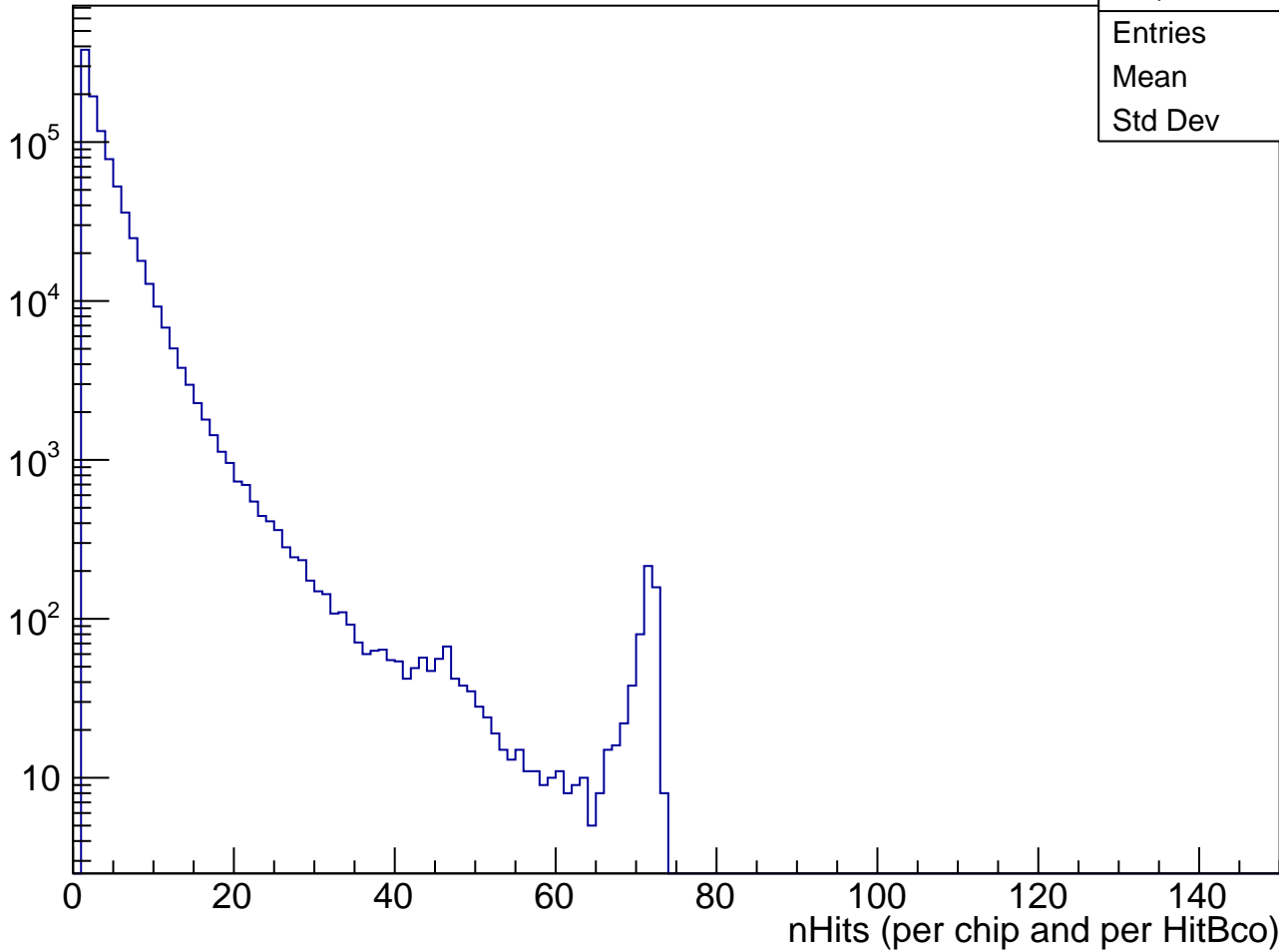
Entries



nChipHit_F5_Fch12_Chip4	
Entries	1164051
Mean	3.13
Std Dev	3.873

nChipHit_F5_Fch12_Chip5

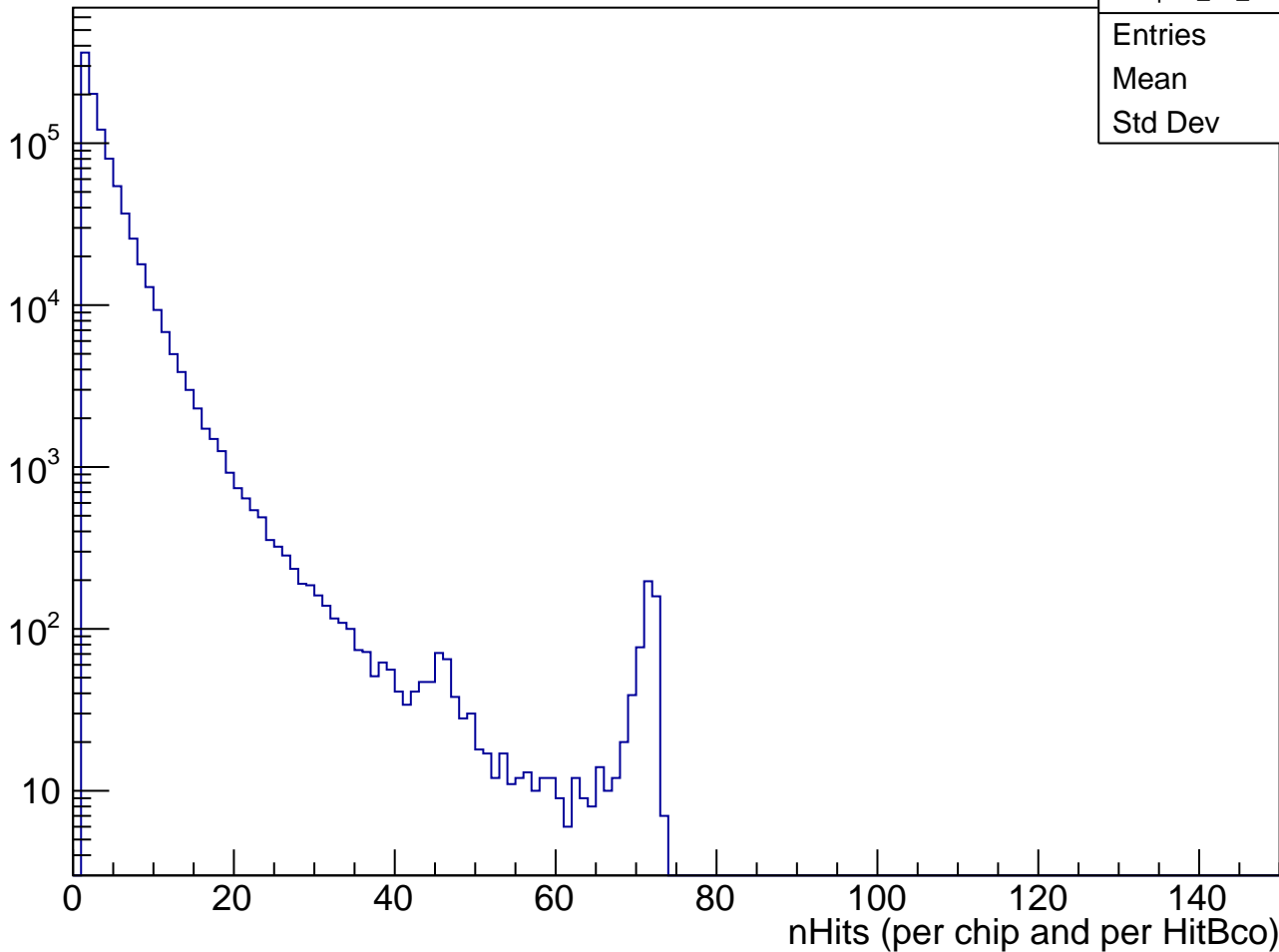
Entries



nChipHit_F5_Fch12_Chip5	
Entries	955926
Mean	3.124
Std Dev	3.77

nChipHit_F5_Fch12_Chip6

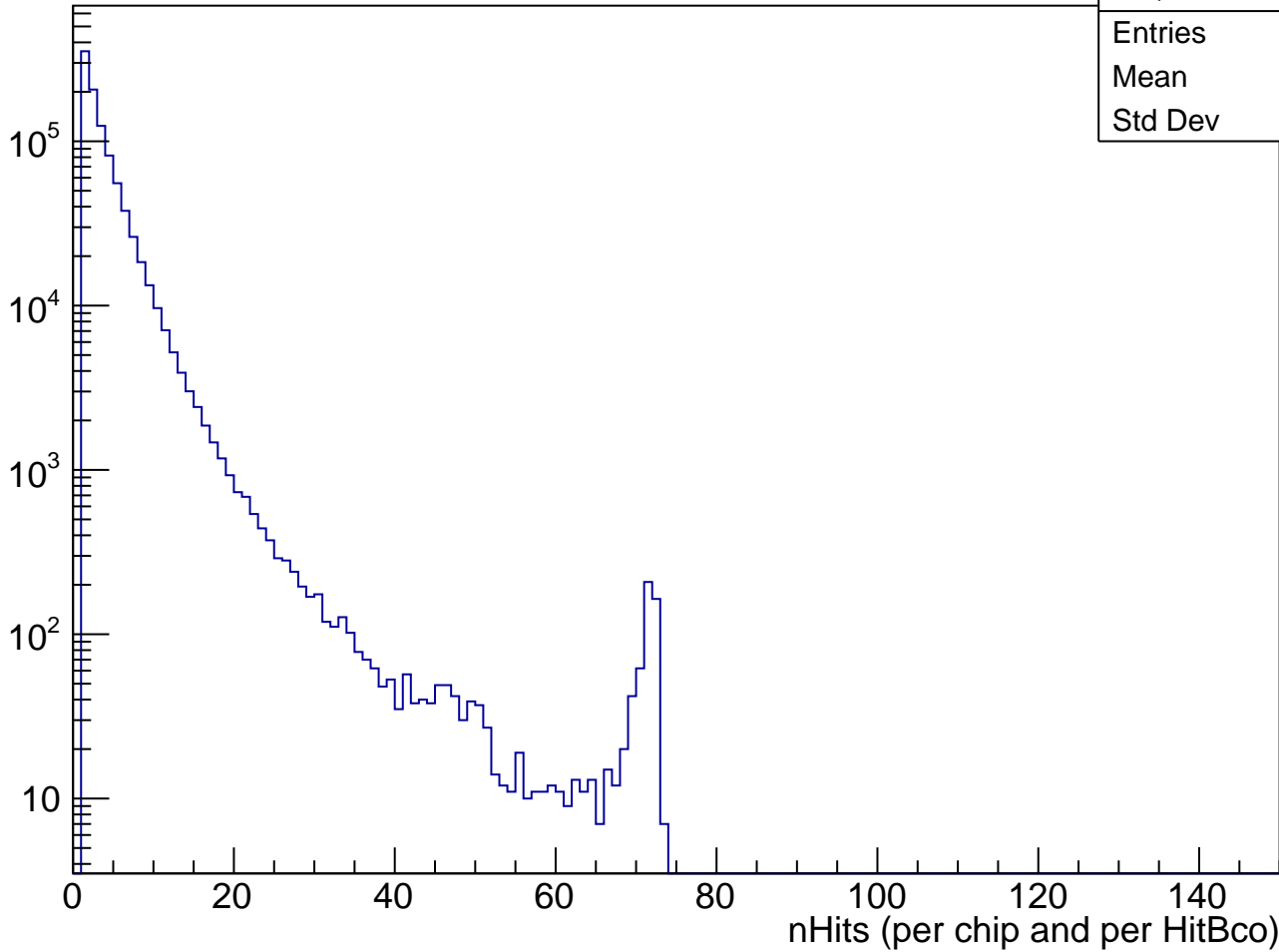
Entries



nChipHit_F5_Fch12_Chip6	
Entries	955715
Mean	3.162
Std Dev	3.731

nChipHit_F5_Fch12_Chip7

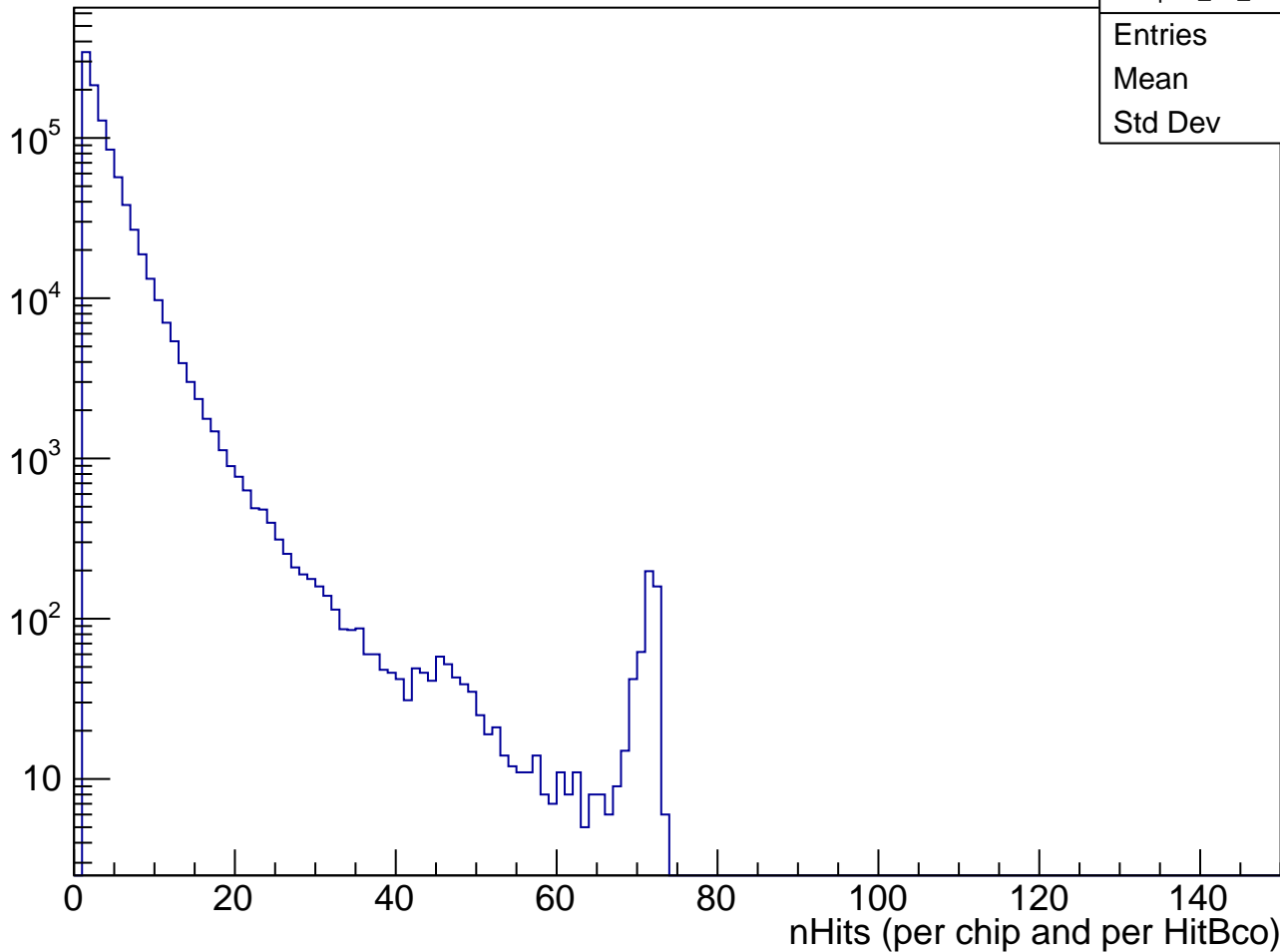
Entries



nChipHit_F5_Fch12_Chip7	
Entries	959640
Mean	3.201
Std Dev	3.743

nChipHit_F5_Fch12_Chip8

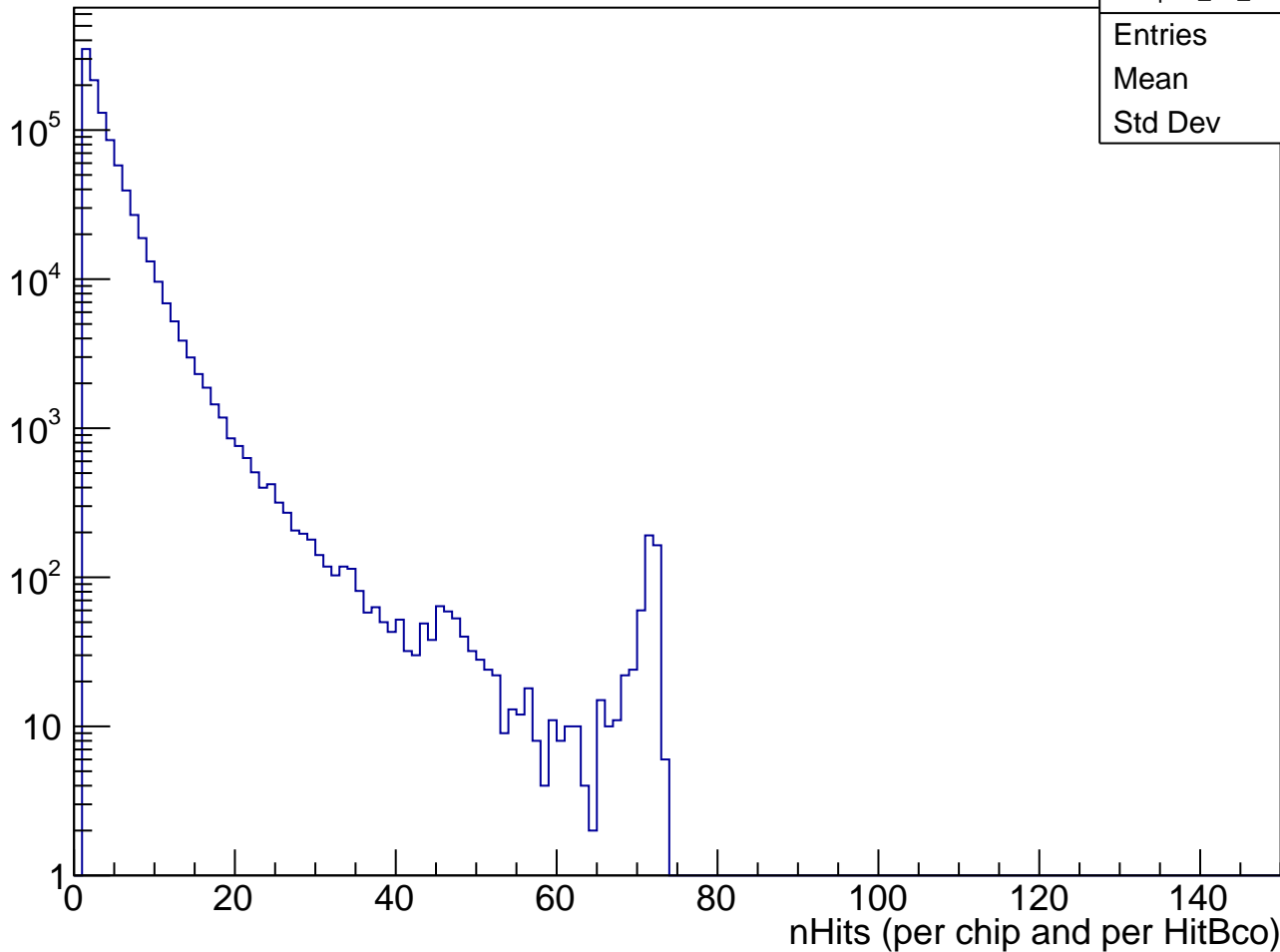
Entries



nChipHit_F5_Fch12_Chip8	
Entries	966240
Mean	3.216
Std Dev	3.685

nChipHit_F5_Fch12_Chip9

Entries

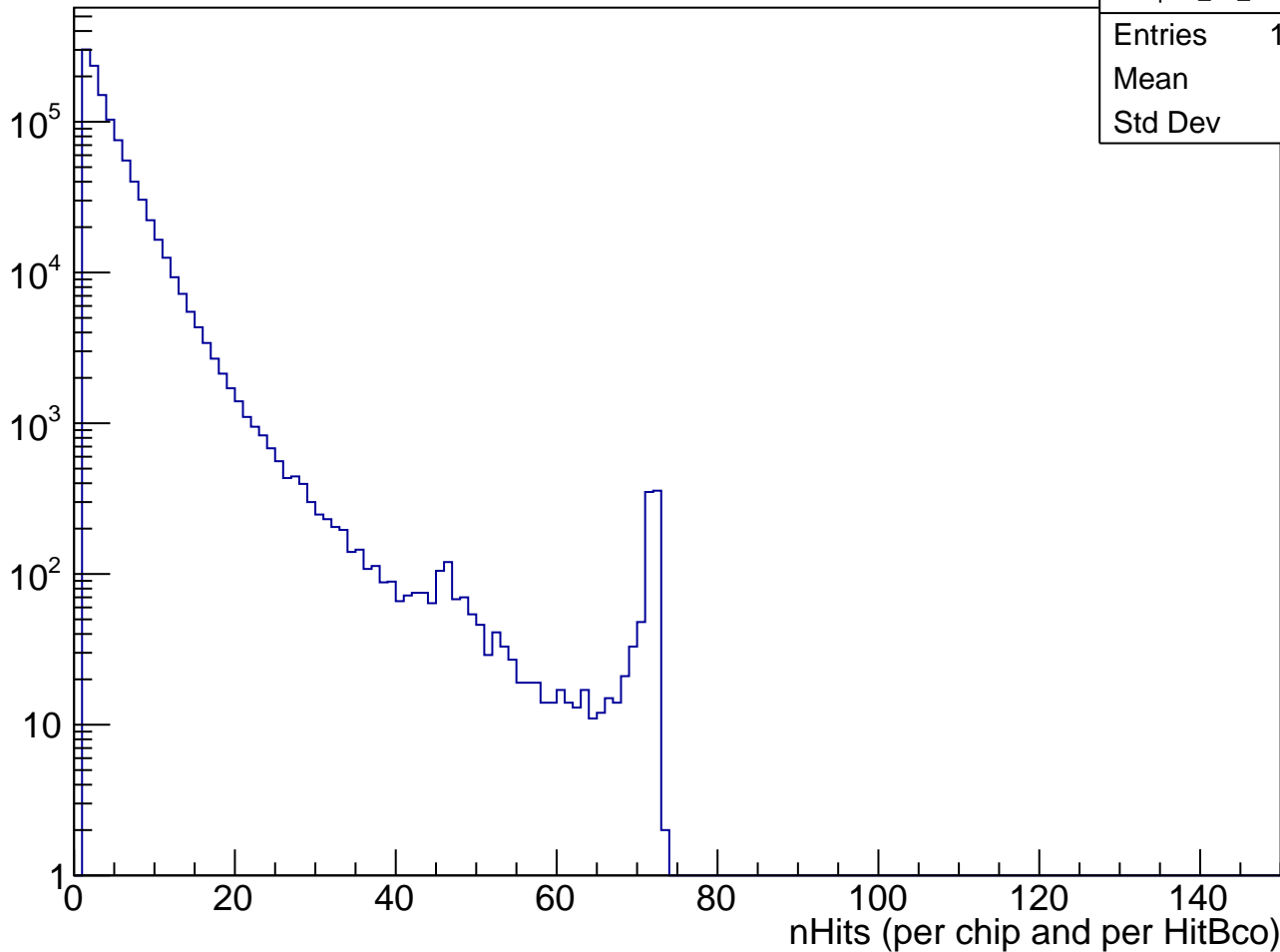


nChipHit_F5_Fch12_Chip9	
Entries	980208
Mean	3.201
Std Dev	3.661

nHits (per chip and per HitBco)

nChipHit_F5_Fch13_Chip0

Entries

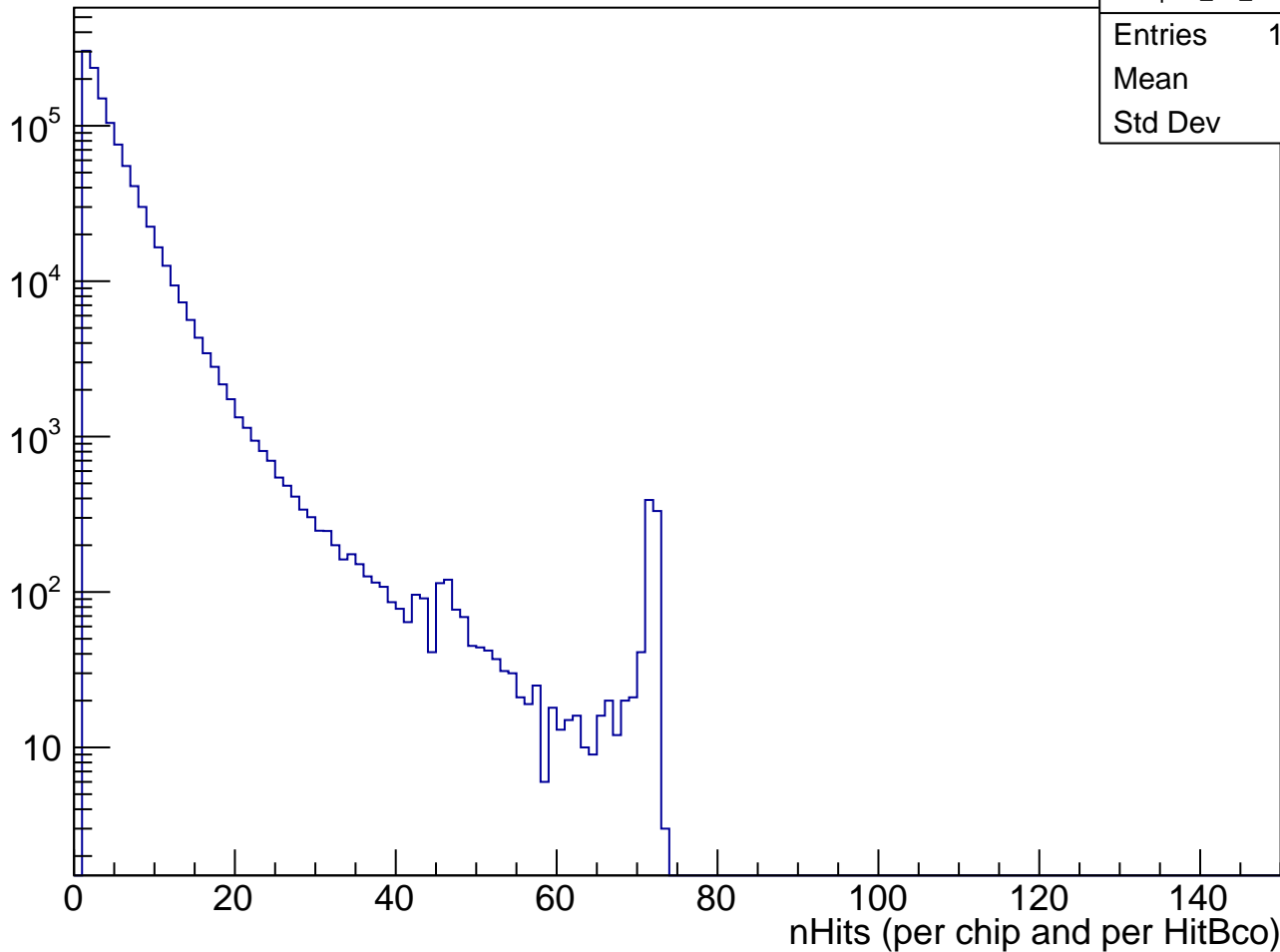


nChipHit_F5_Fch13_Chip0	
Entries	1090243
Mean	3.872
Std Dev	4.358

nHits (per chip and per HitBco)

nChipHit_F5_Fch13_Chip1

Entries

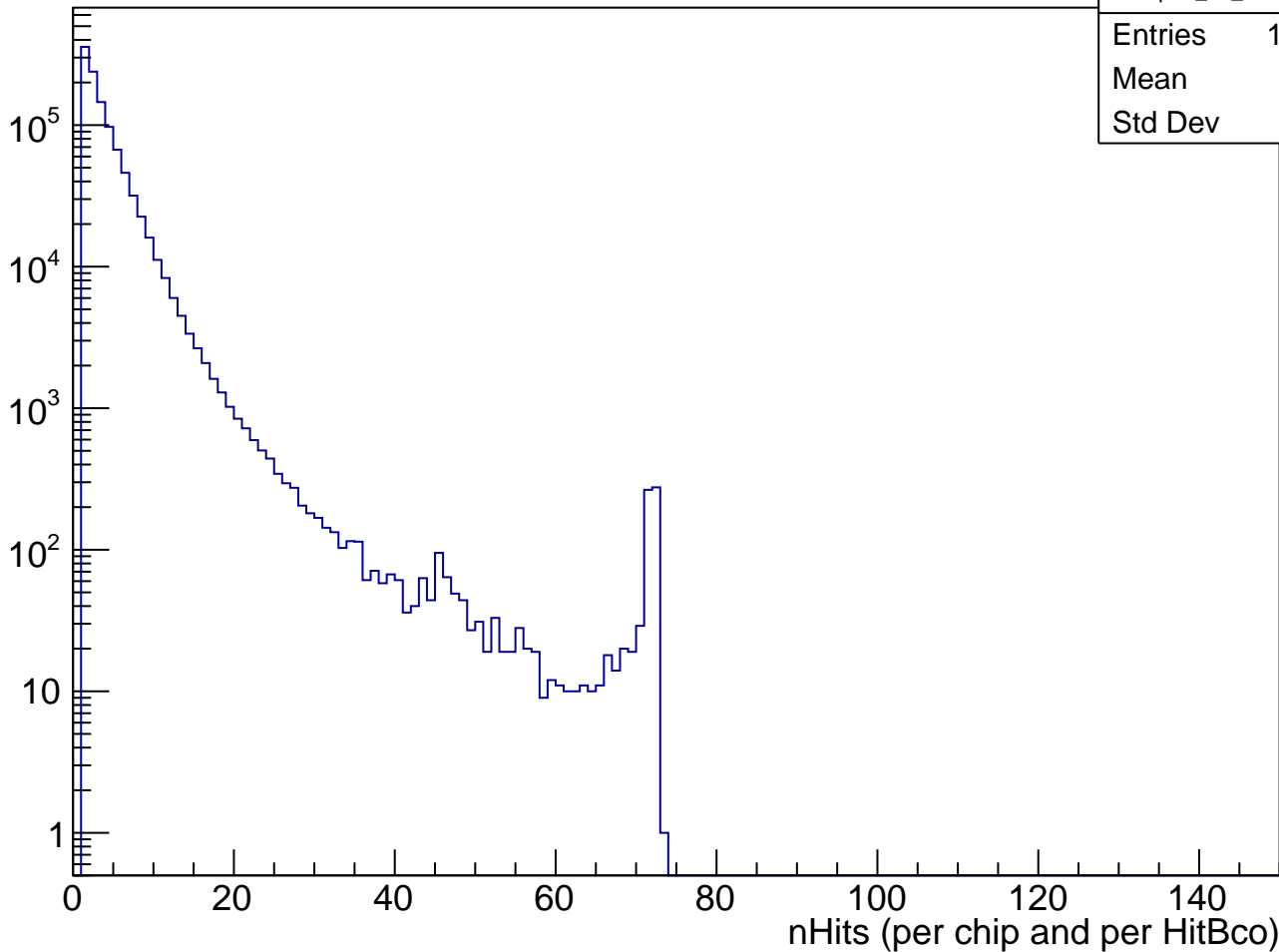


nChipHit_F5_Fch13_Chip1	
Entries	1094686
Mean	3.874
Std Dev	4.365

nHits (per chip and per HitBco)

nChipHit_F5_Fch13_Chip10

Entries



nChipHit_F5_Fch13_Chip10

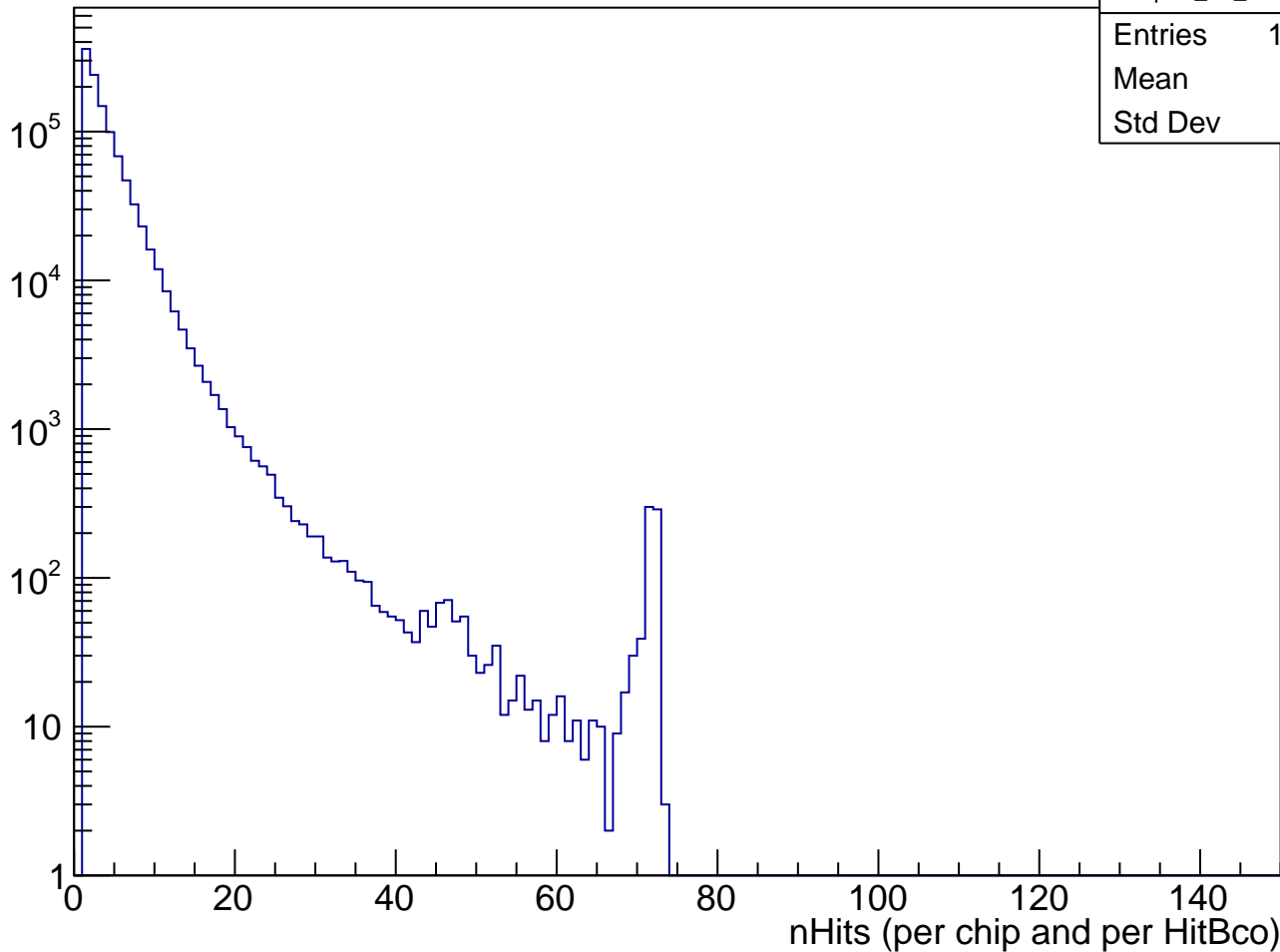
Entries 1070408

Mean 3.328

Std Dev 3.797

nChipHit_F5_Fch13_Chip11

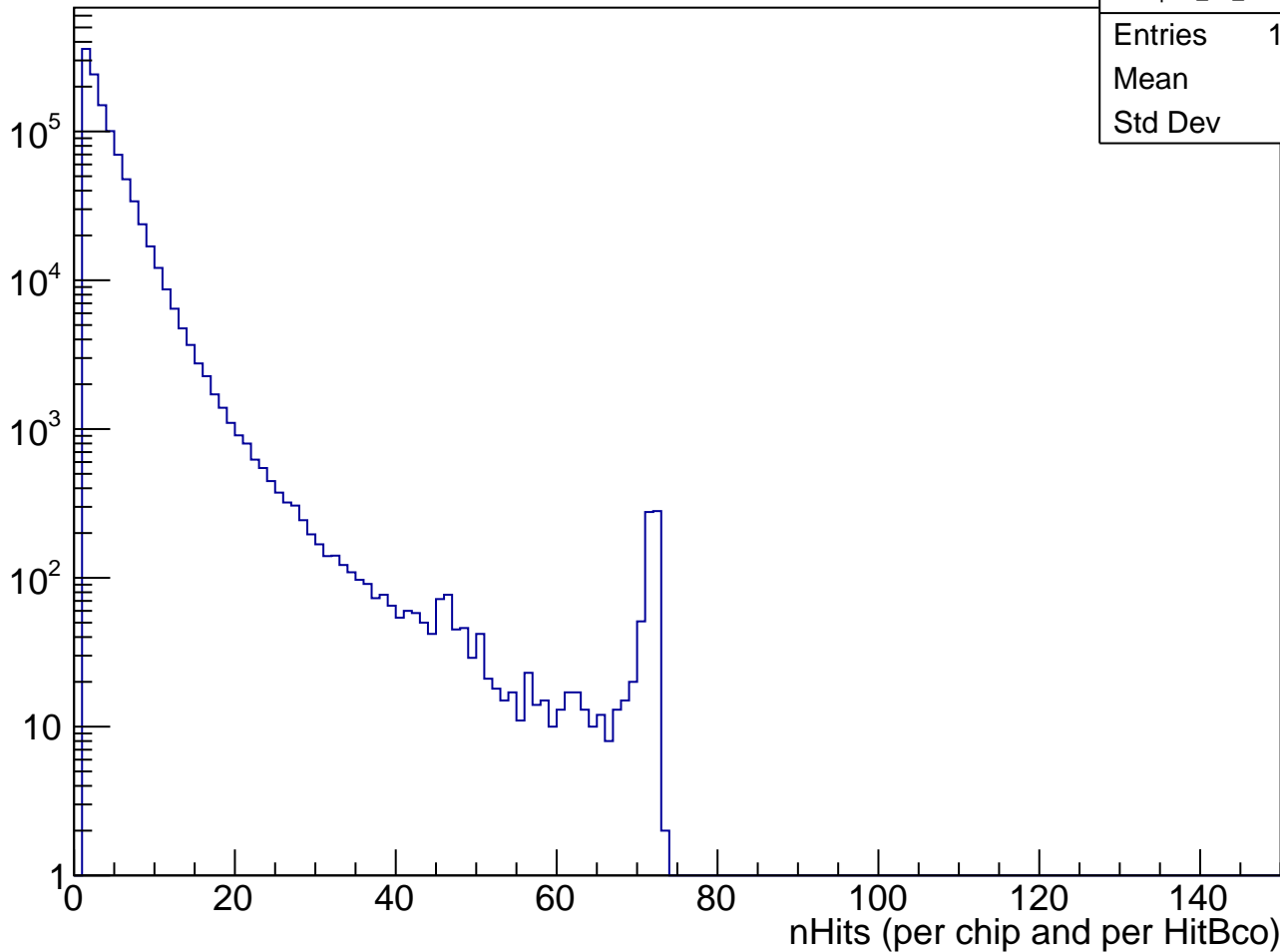
Entries



nChipHit_F5_Fch13_Chip11	
Entries	1085196
Mean	3.347
Std Dev	3.821

nChipHit_F5_Fch13_Chip12

Entries



nChipHit_F5_Fch13_Chip12

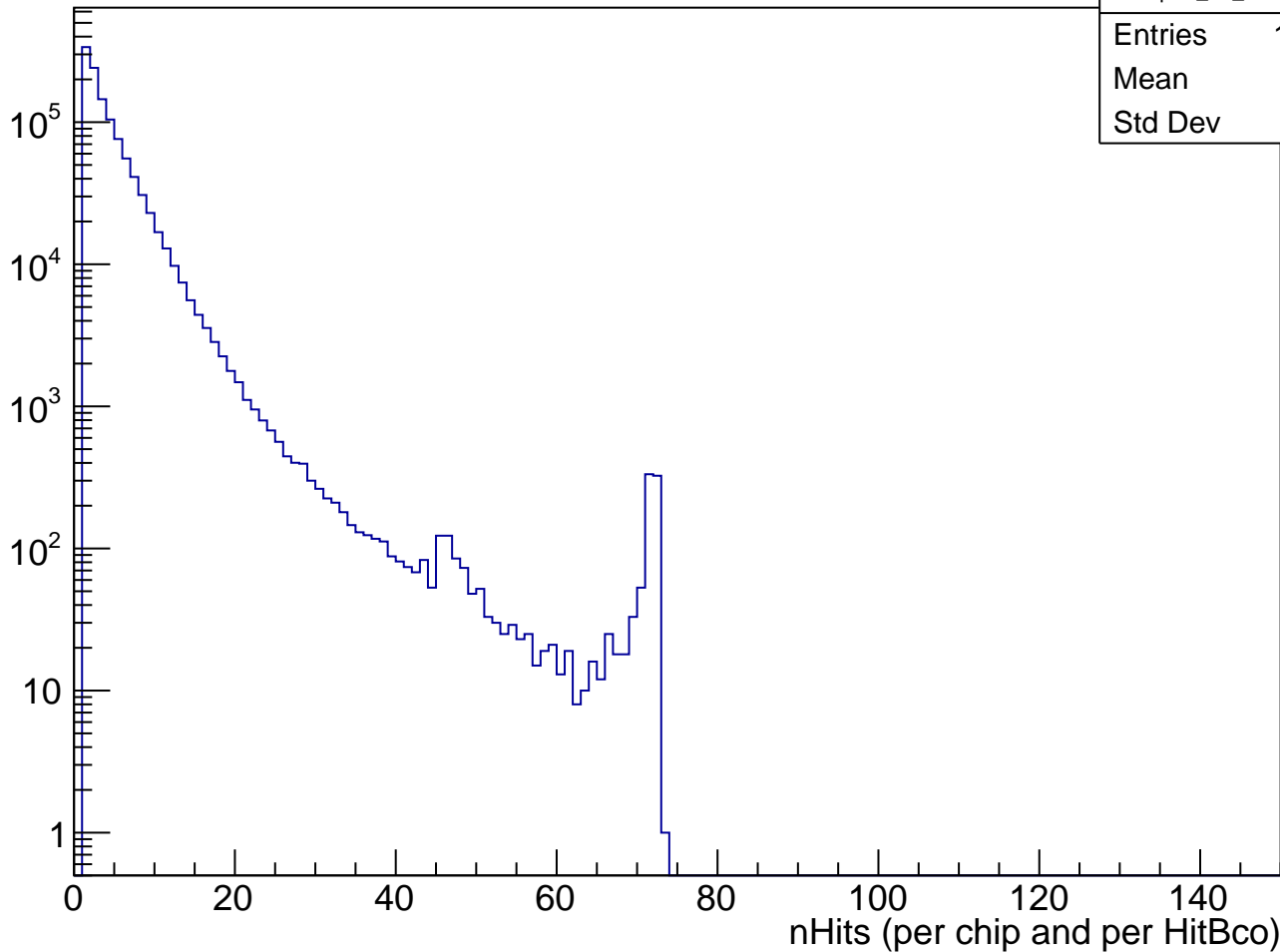
Entries 1094994

Mean 3.38

Std Dev 3.839

nChipHit_F5_Fch13_Chip13

Entries



nChipHit_F5_Fch13_Chip13

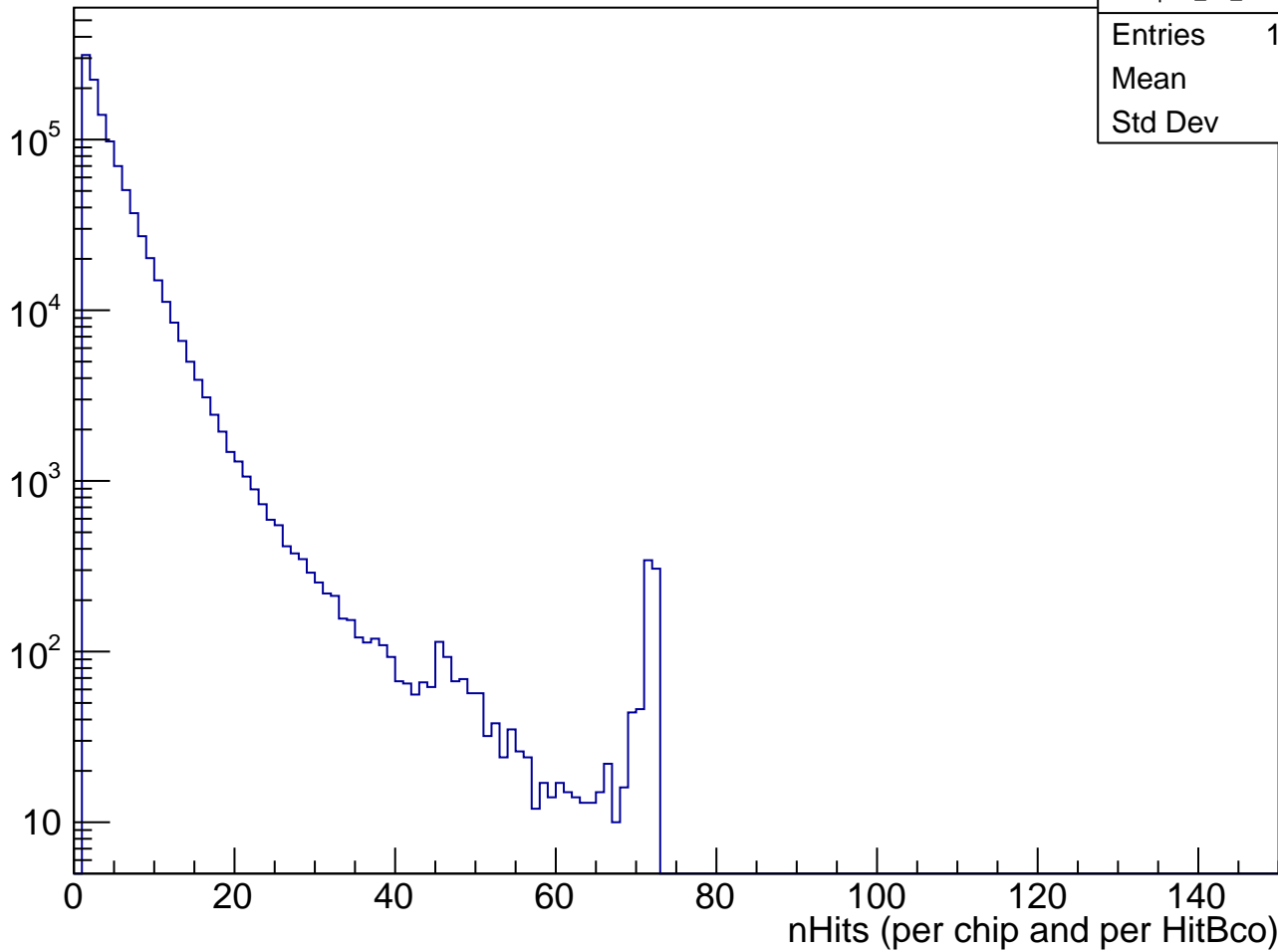
Entries 1132691

Mean 3.801

Std Dev 4.324

nChipHit_F5_Fch13_Chip14

Entries



nChipHit_F5_Fch13_Chip14

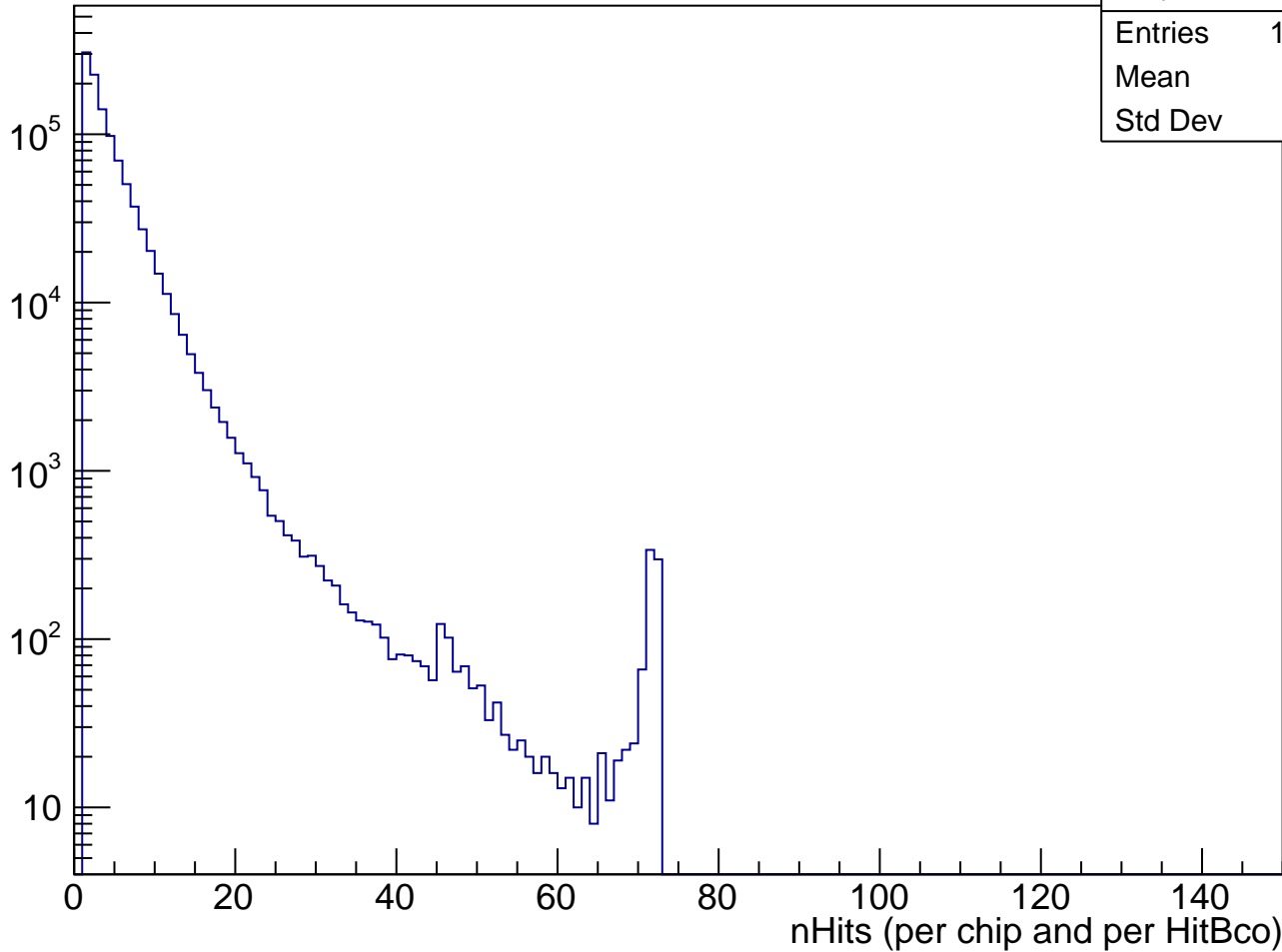
Entries 1048379

Mean 3.755

Std Dev 4.319

nChipHit_F5_Fch13_Chip15

Entries



nChipHit_F5_Fch13_Chip15

Entries 1044876

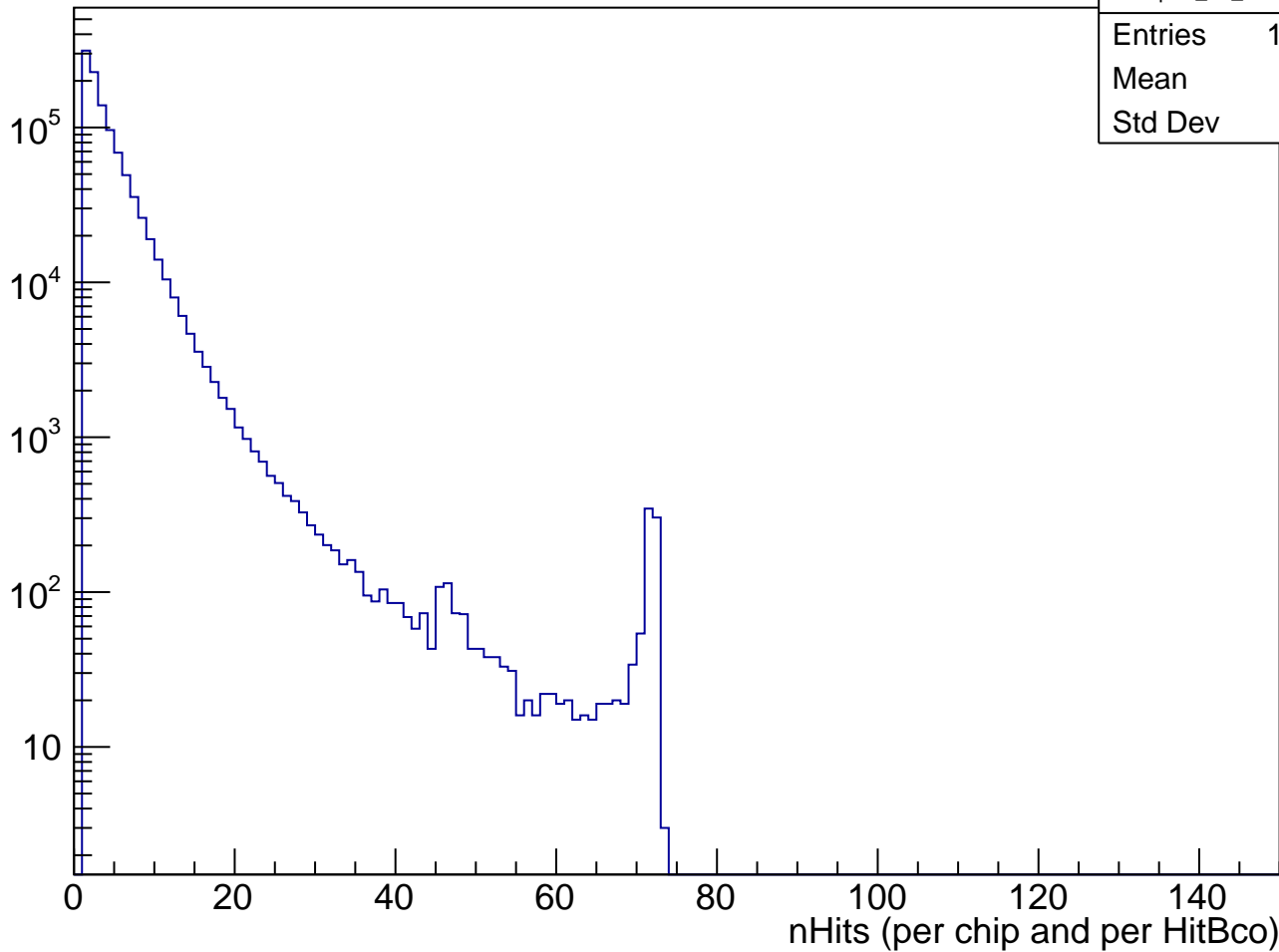
Mean 3.766

Std Dev 4.32

nHits (per chip and per HitBco)

nChipHit_F5_Fch13_Chip16

Entries



nChipHit_F5_Fch13_Chip16

Entries 1039984

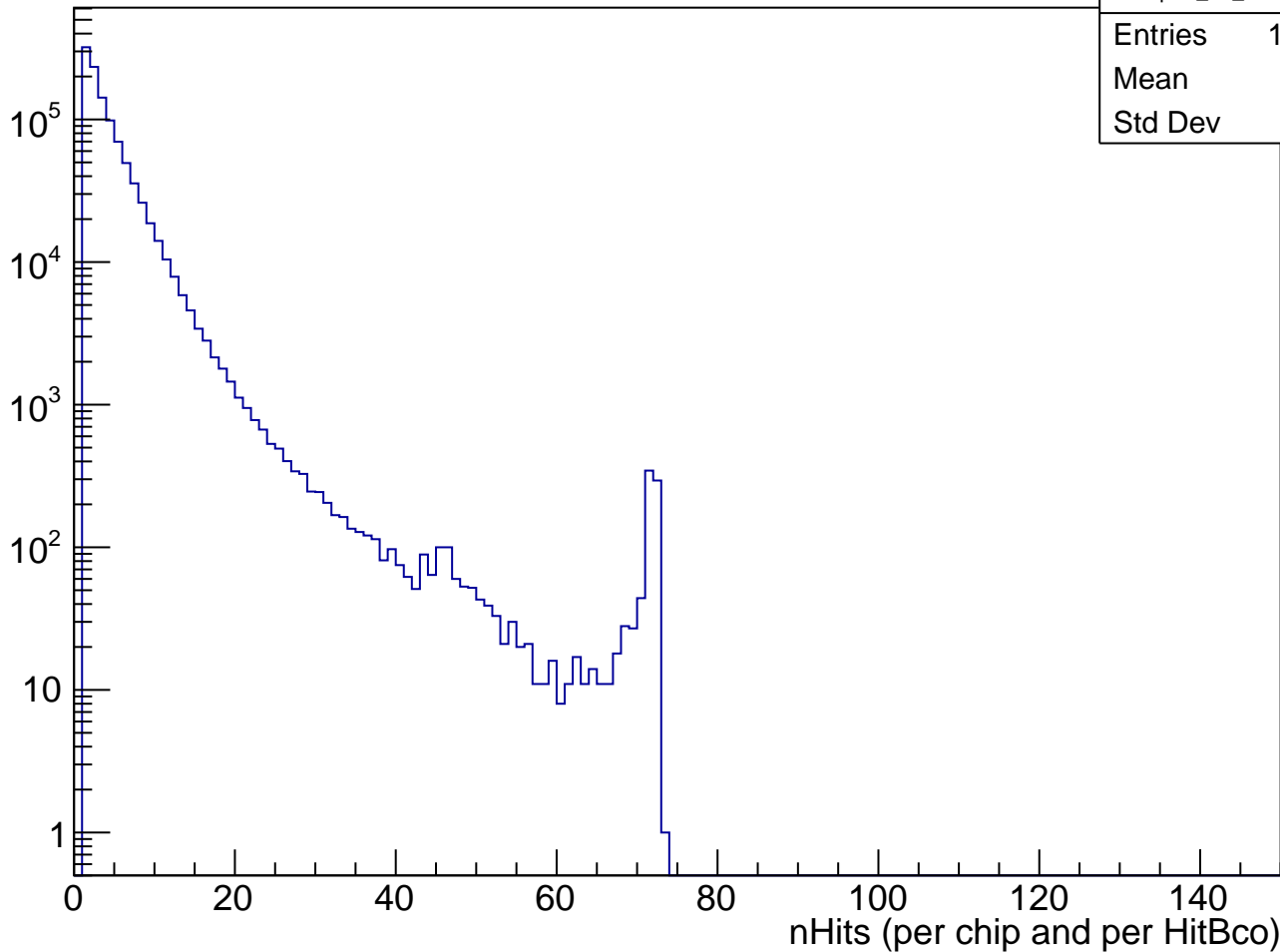
Mean 3.687

Std Dev 4.281

nHits (per chip and per HitBco)

nChipHit_F5_Fch13_Chip17

Entries



nChipHit_F5_Fch13_Chip17

Entries 1058295

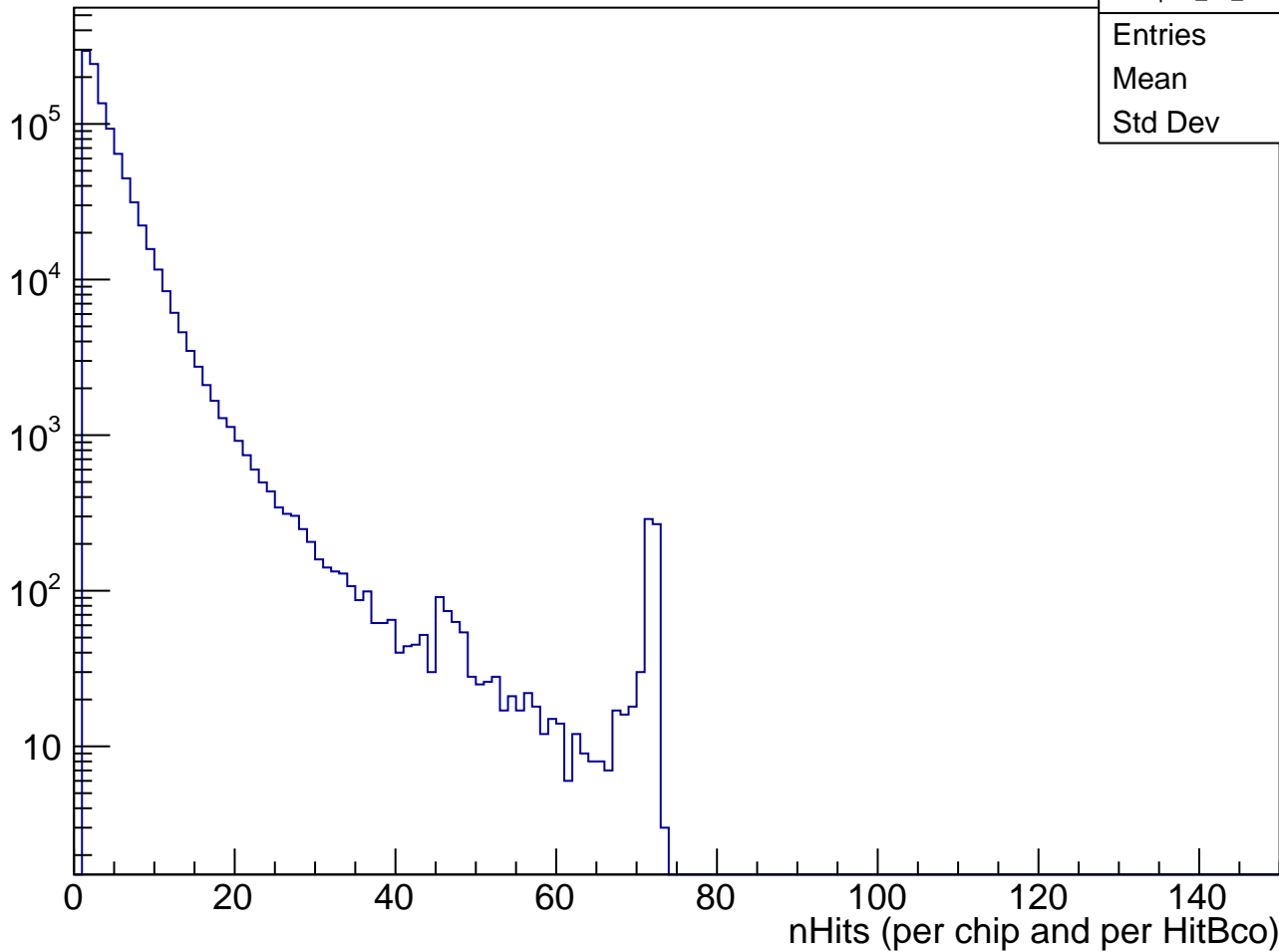
Mean 3.639

Std Dev 4.187

nHits (per chip and per HitBco)

nChipHit_F5_Fch13_Chip18

Entries



nChipHit_F5_Fch13_Chip18

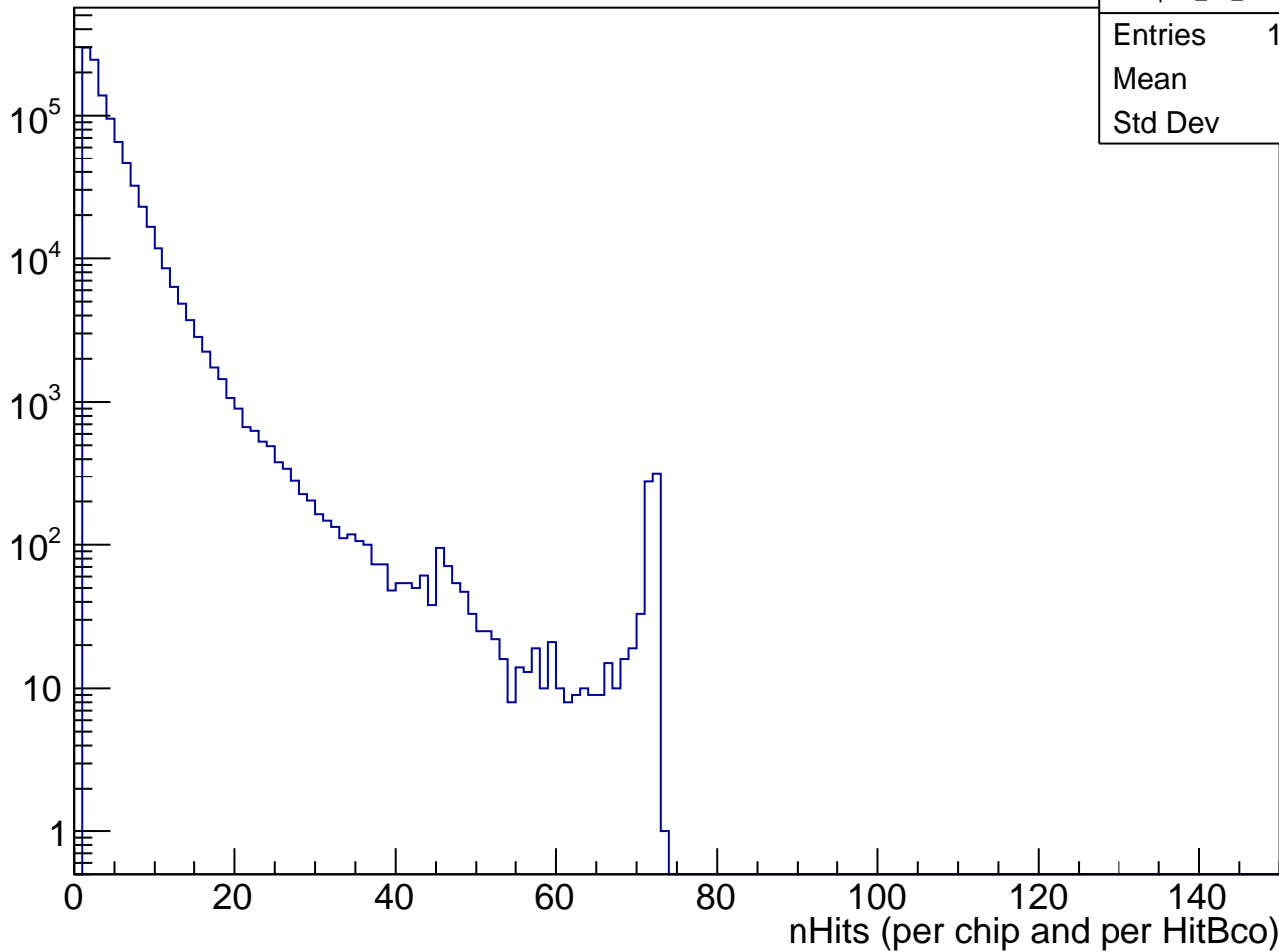
Entries 995520

Mean 3.469

Std Dev 3.914

nChipHit_F5_Fch13_Chip19

Entries



nChipHit_F5_Fch13_Chip19

Entries 1011688

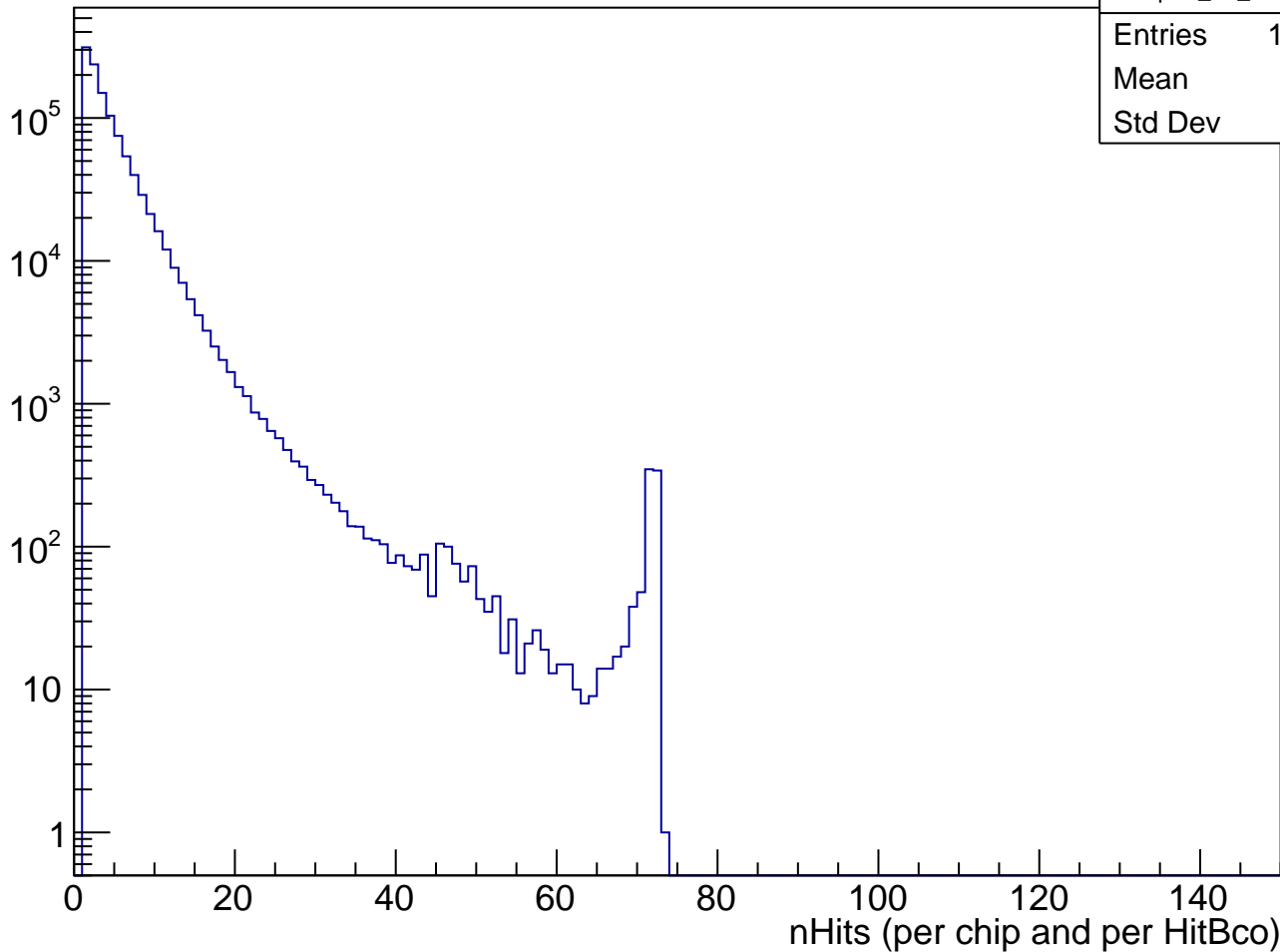
Mean 3.492

Std Dev 3.938

nHits (per chip and per HitBco)

nChipHit_F5_Fch13_Chip2

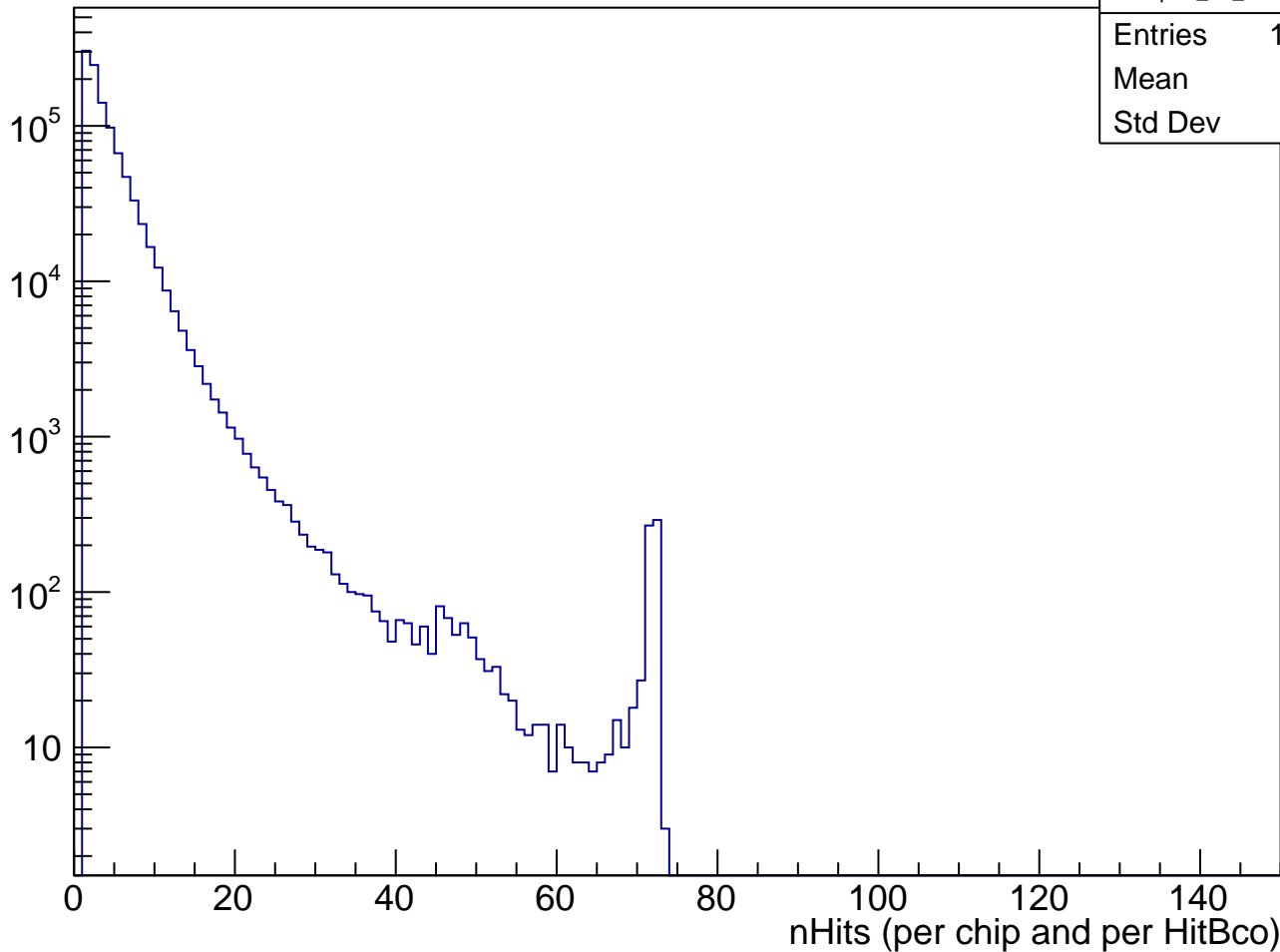
Entries



nChipHit_F5_Fch13_Chip2	
Entries	1095814
Mean	3.804
Std Dev	4.309

nChipHit_F5_Fch13_Chip20

Entries



nChipHit_F5_Fch13_Chip20

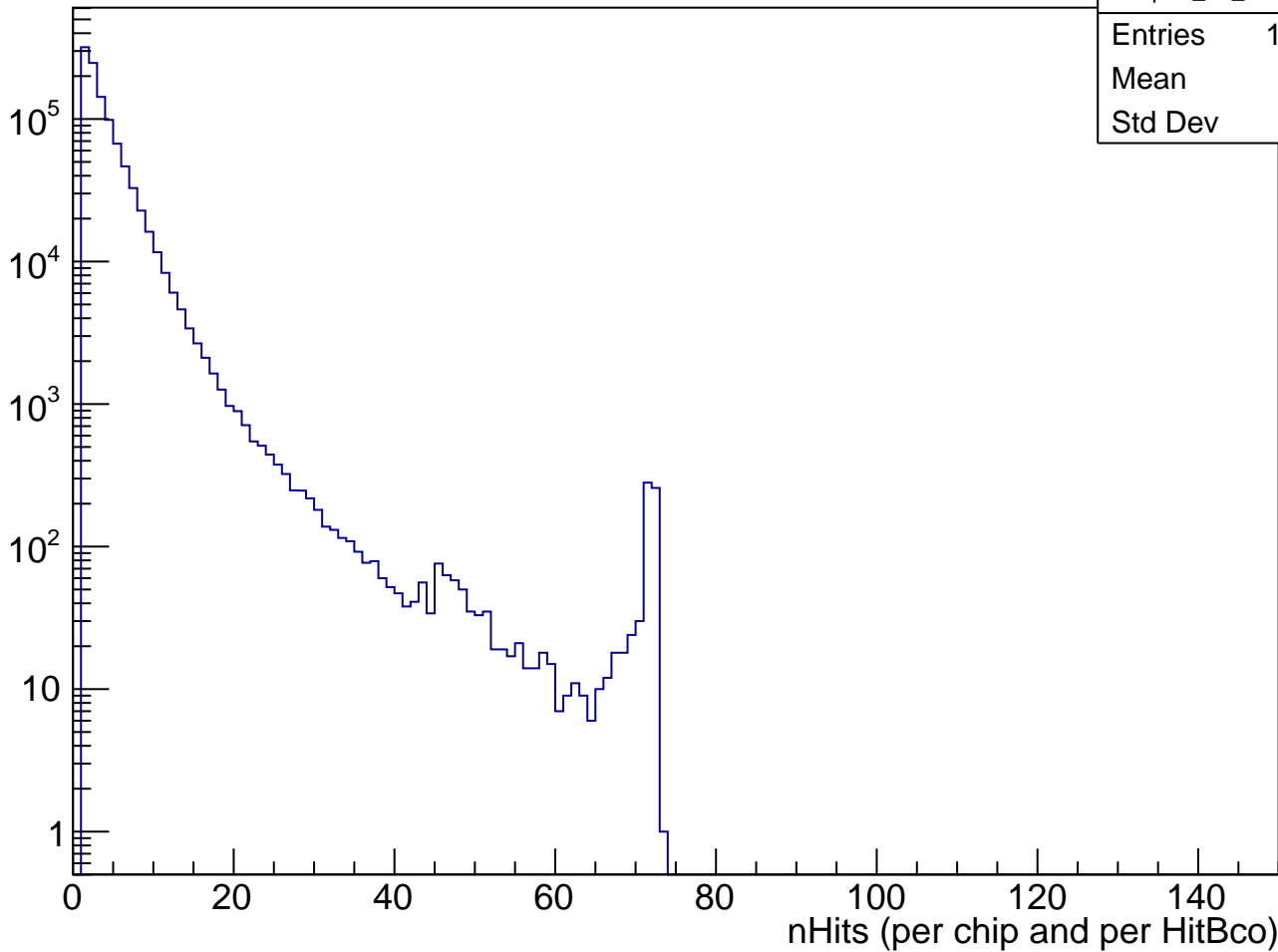
Entries 1028680

Mean 3.491

Std Dev 3.914

nChipHit_F5_Fch13_Chip21

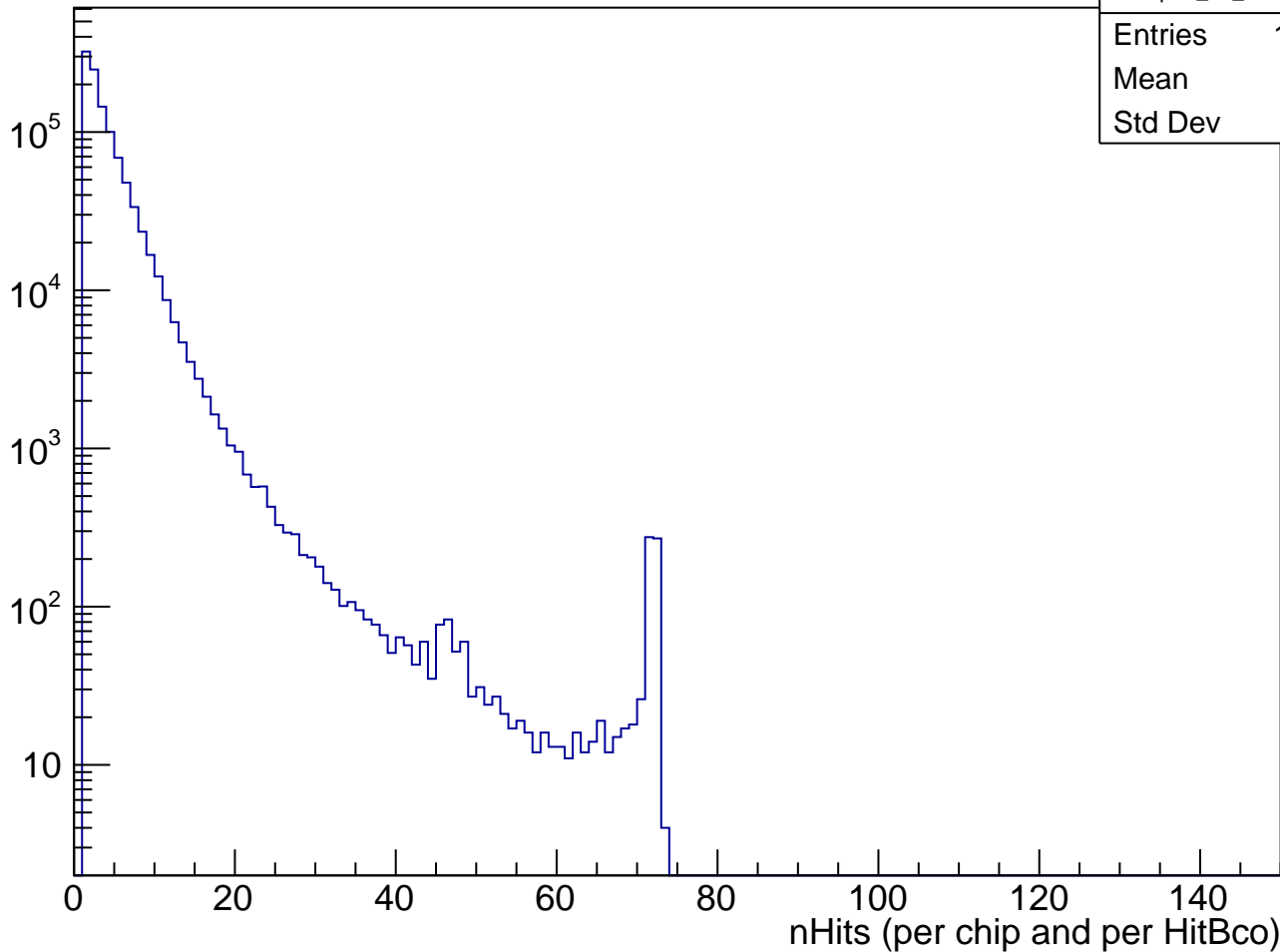
Entries



nChipHit_F5_Fch13_Chip21	
Entries	1043192
Mean	3.415
Std Dev	3.826

nChipHit_F5_Fch13_Chip22

Entries



nChipHit_F5_Fch13_Chip22

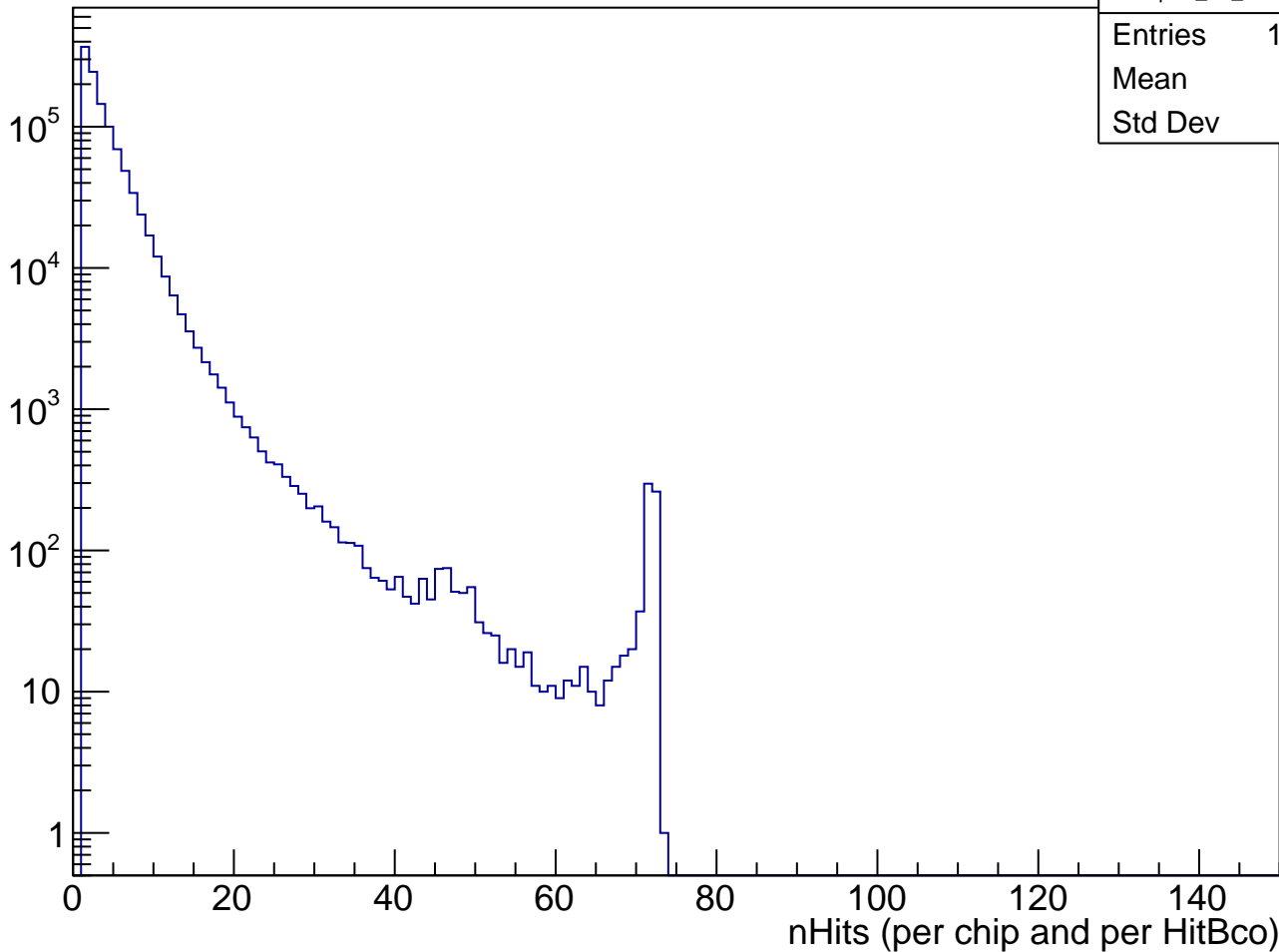
Entries 1057791

Mean 3.436

Std Dev 3.844

nChipHit_F5_Fch13_Chip23

Entries



nChipHit_F5_Fch13_Chip23

Entries 1102118

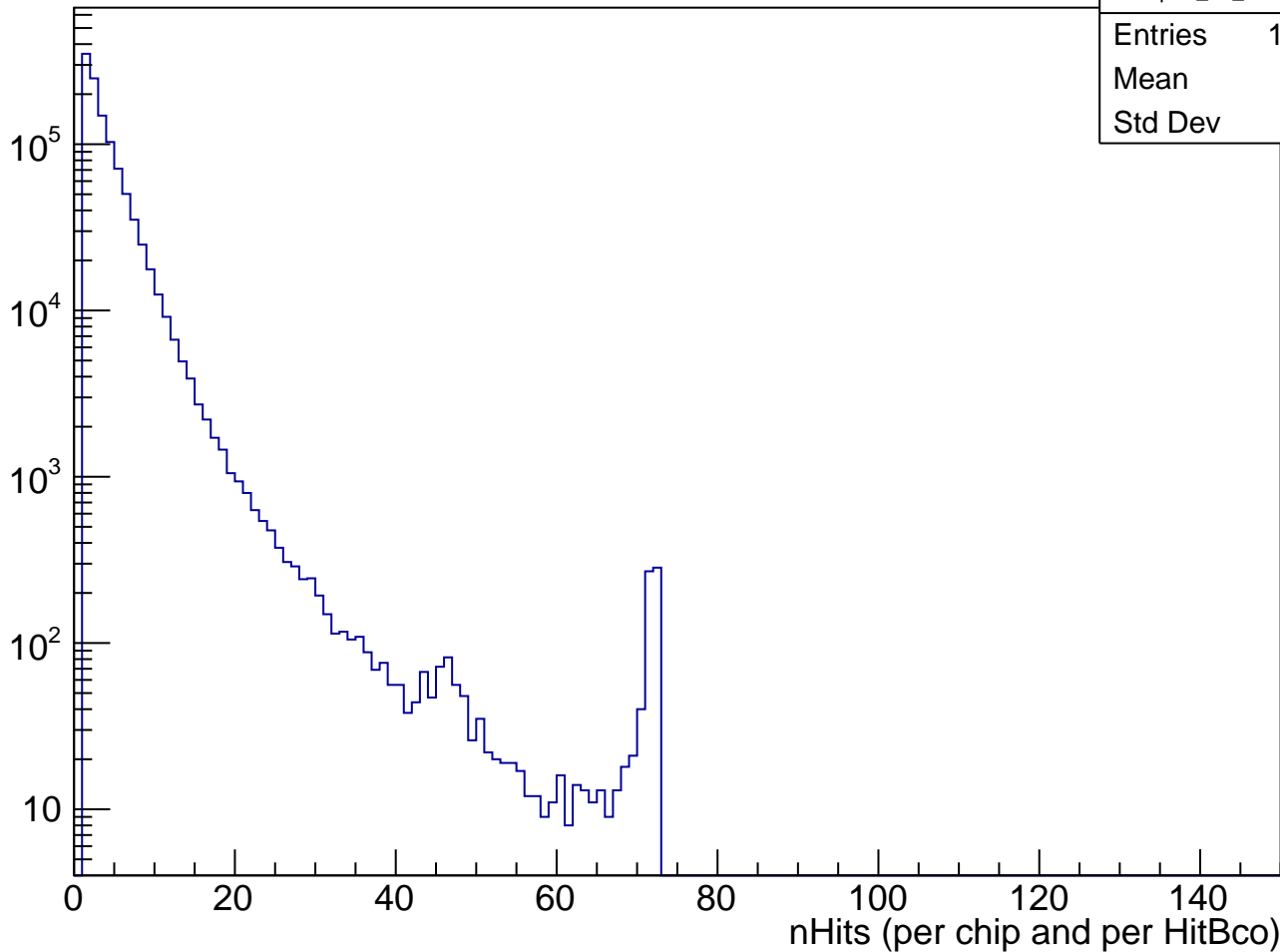
Mean 3.356

Std Dev 3.824

nHits (per chip and per HitBco)

nChipHit_F5_Fch13_Chip24

Entries



nChipHit_F5_Fch13_Chip24

Entries 1103465

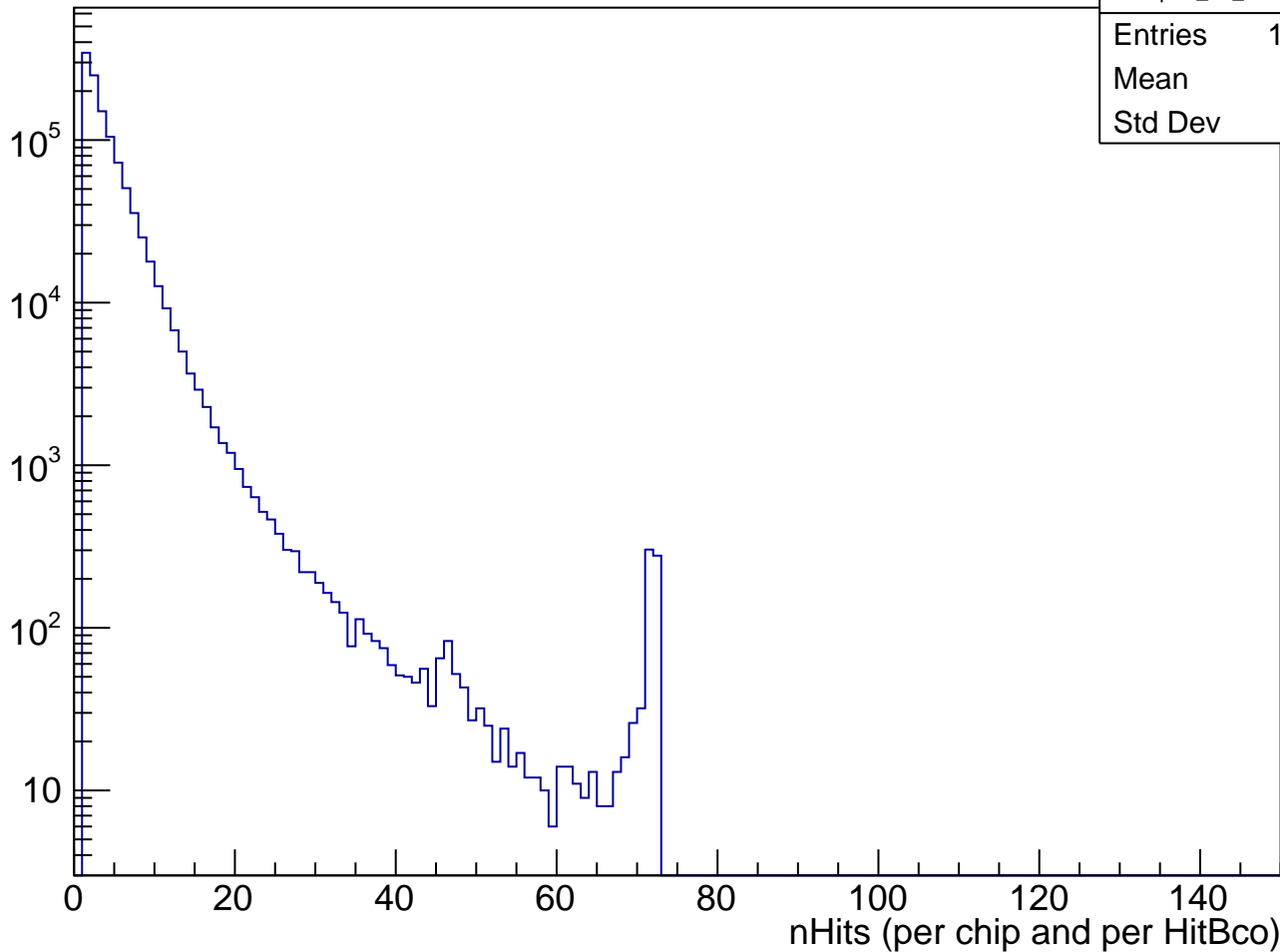
Mean 3.423

Std Dev 3.833

nHits (per chip and per HitBco)

nChipHit_F5_Fch13_Chip25

Entries



nChipHit_F5_Fch13_Chip25

Entries 1105212

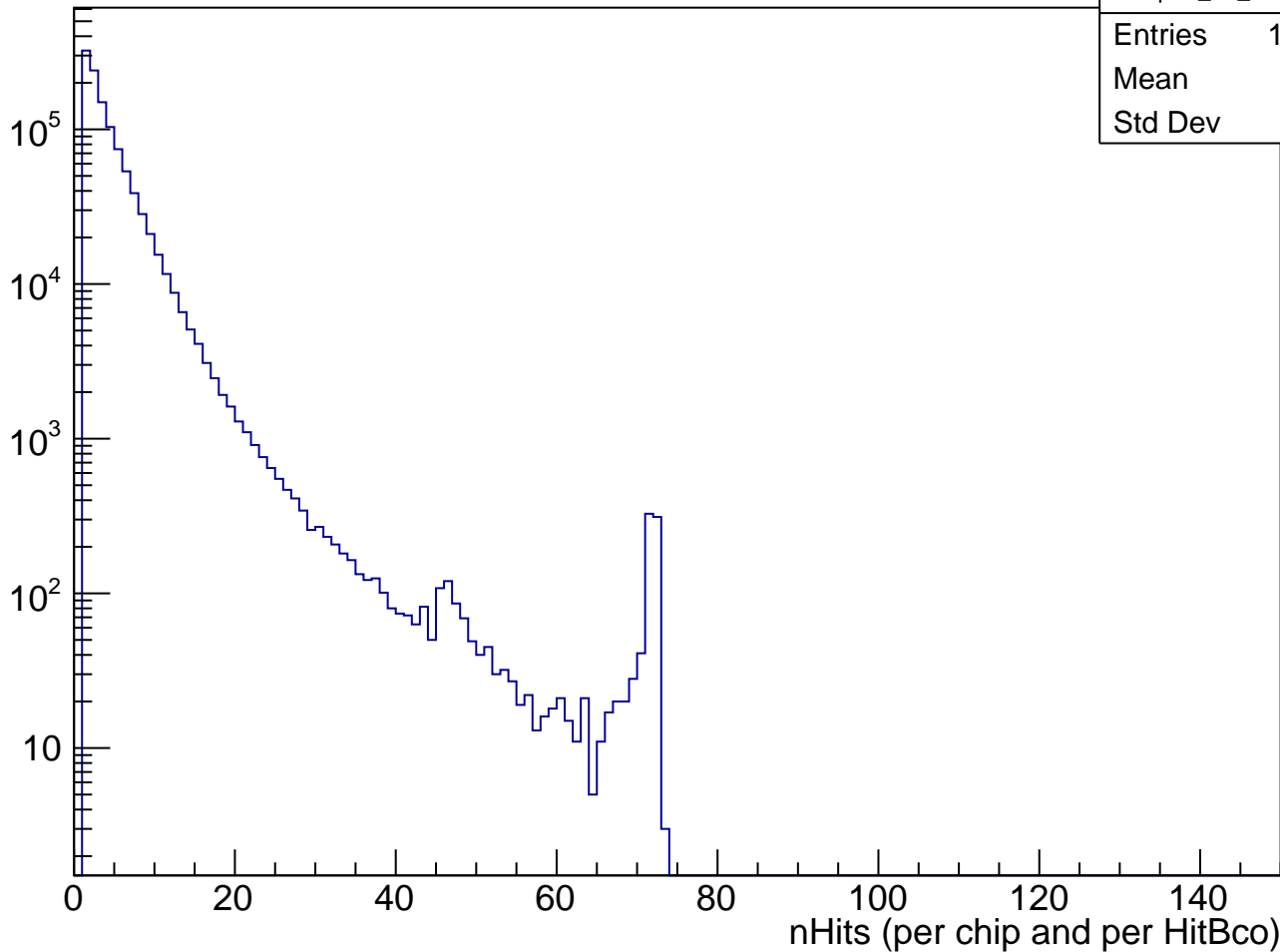
Mean 3.443

Std Dev 3.833

nHits (per chip and per HitBco)

nChipHit_F5_Fch13_Chip3

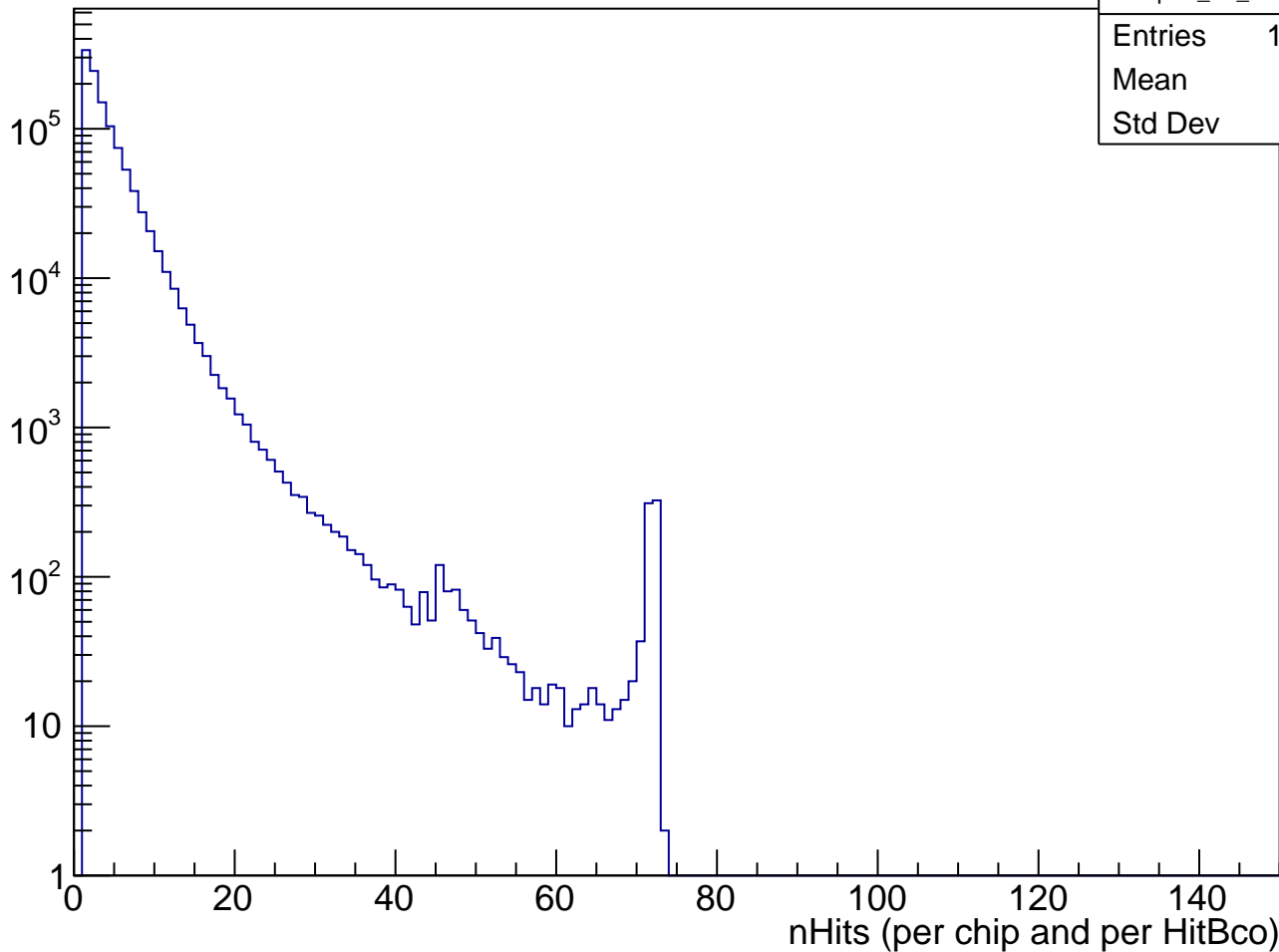
Entries



nChipHit_F5_Fch13_Chip3	
Entries	1103728
Mean	3.74
Std Dev	4.254

nChipHit_F5_Fch13_Chip4

Entries



nChipHit_F5_Fch13_Chip4

Entries 1116718

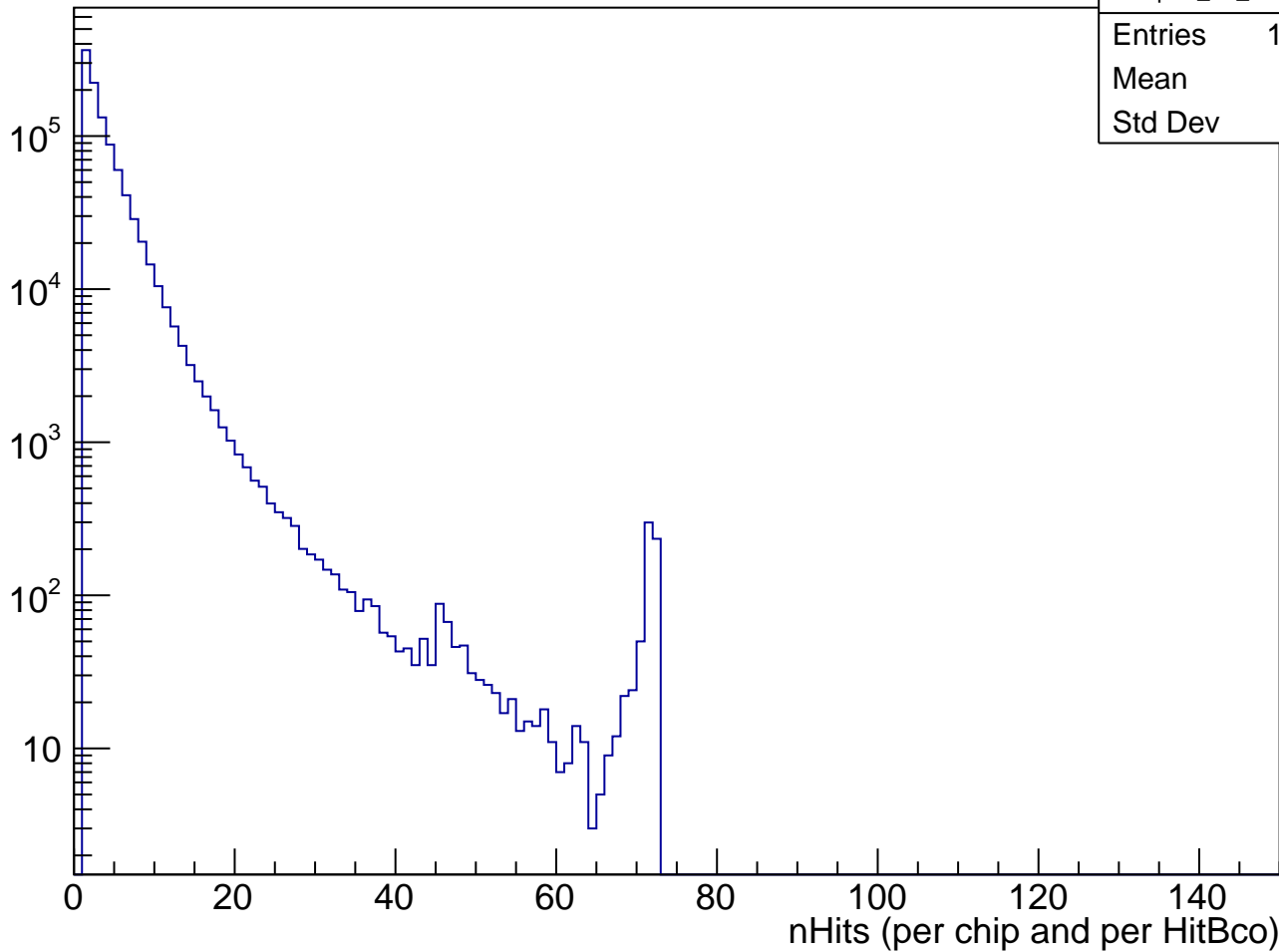
Mean 3.66

Std Dev 4.162

nHits (per chip and per HitBco)

nChipHit_F5_Fch13_Chip5

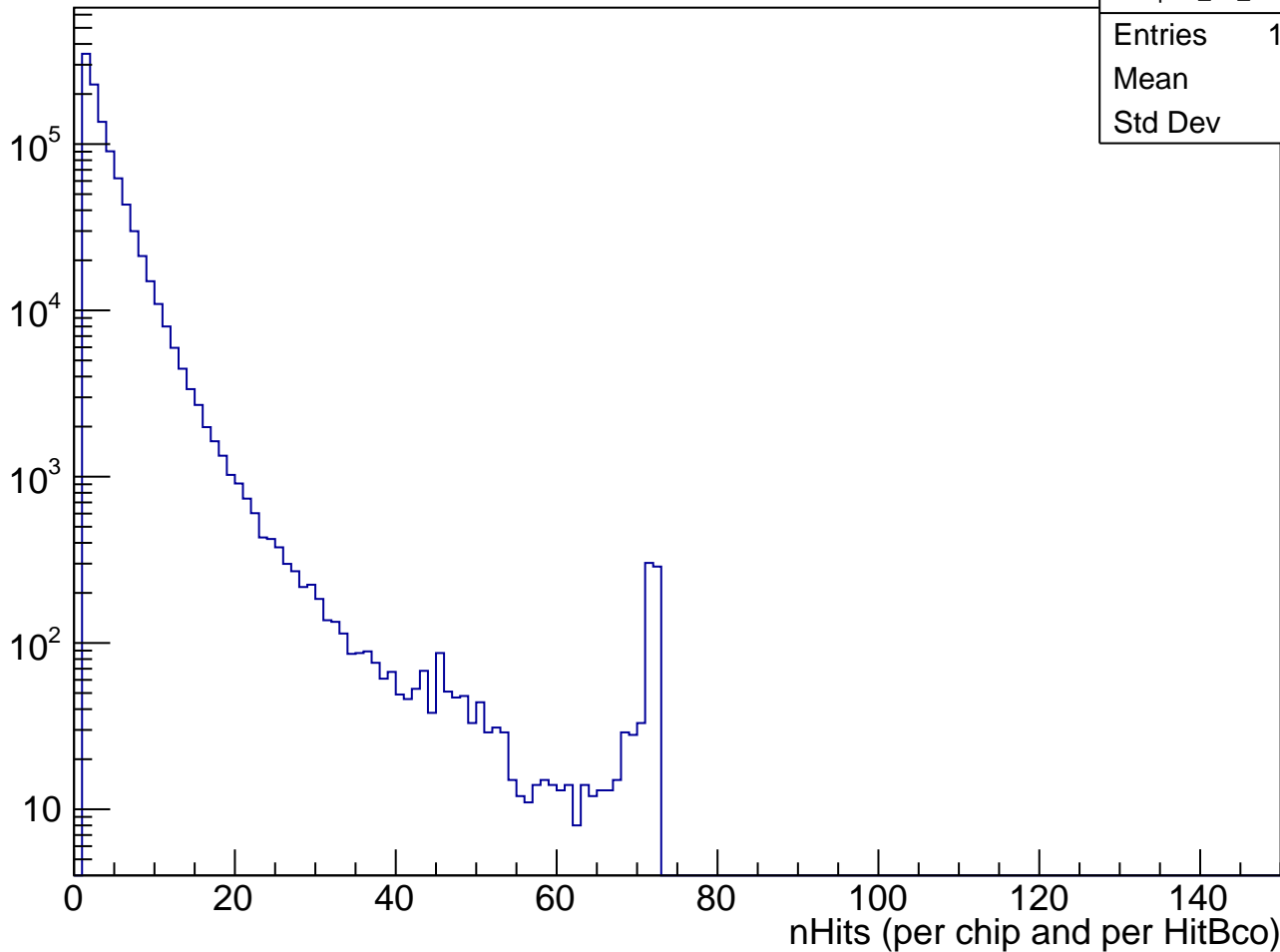
Entries



nChipHit_F5_Fch13_Chip5	
Entries	1018268
Mean	3.252
Std Dev	3.826

nChipHit_F5_Fch13_Chip6

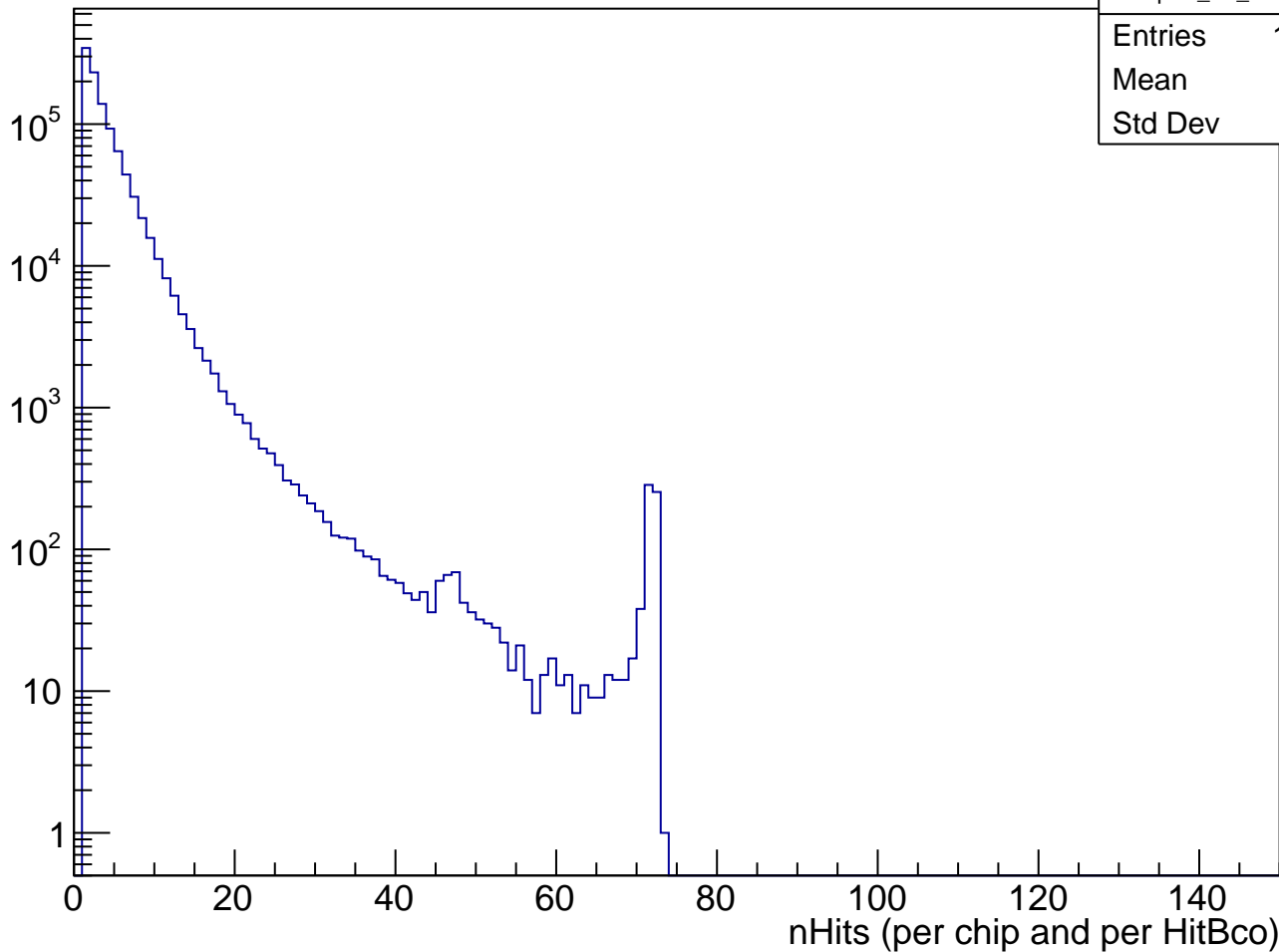
Entries



nChipHit_F5_Fch13_Chip6	
Entries	1023755
Mean	3.327
Std Dev	3.903

nChipHit_F5_Fch13_Chip7

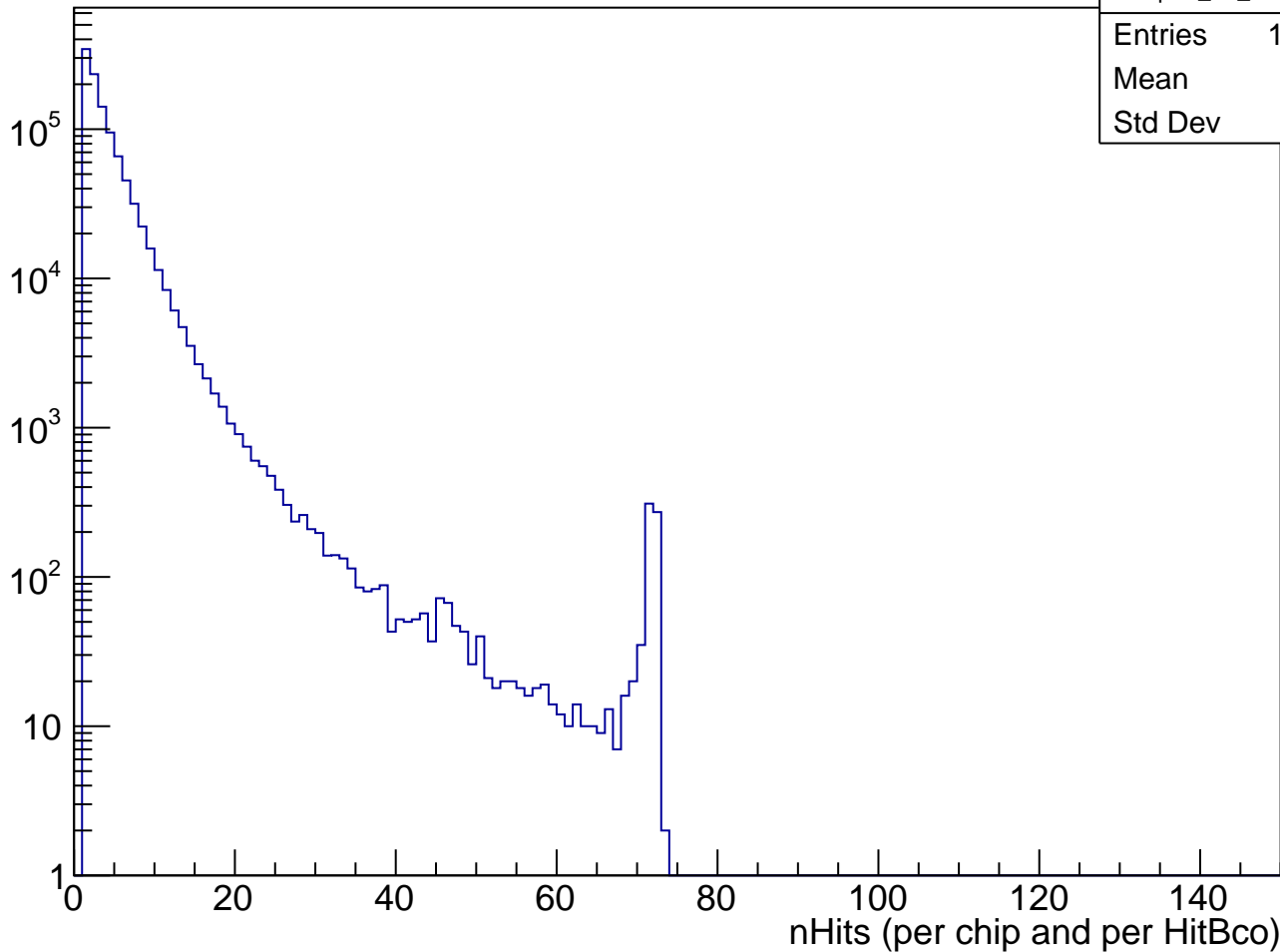
Entries



nChipHit_F5_Fch13_Chip7	
Entries	1034441
Mean	3.358
Std Dev	3.864

nChipHit_F5_Fch13_Chip8

Entries

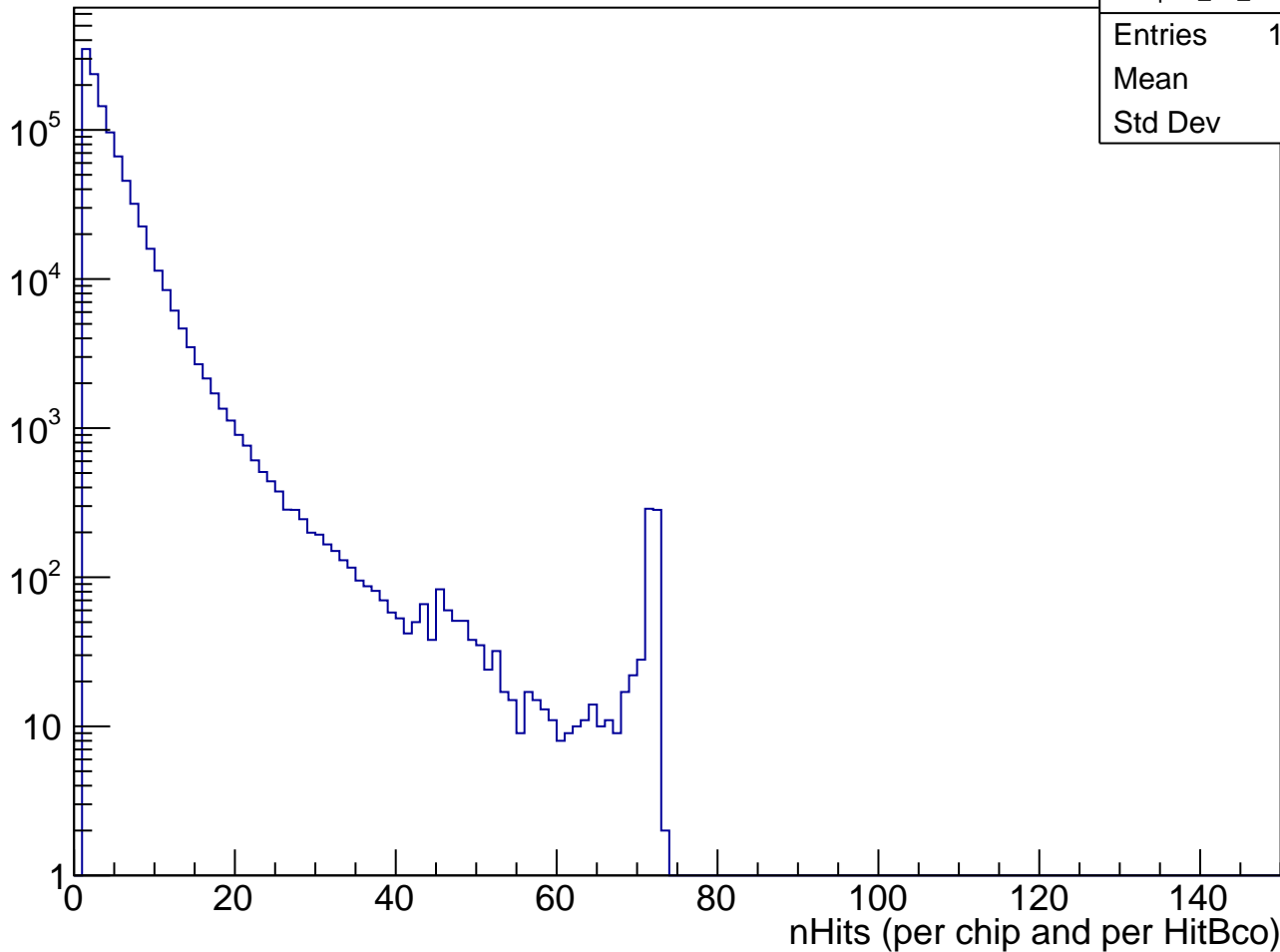


nChipHit_F5_Fch13_Chip8	
Entries	1045589
Mean	3.374
Std Dev	3.879

nHits (per chip and per HitBco)

nChipHit_F5_Fch13_Chip9

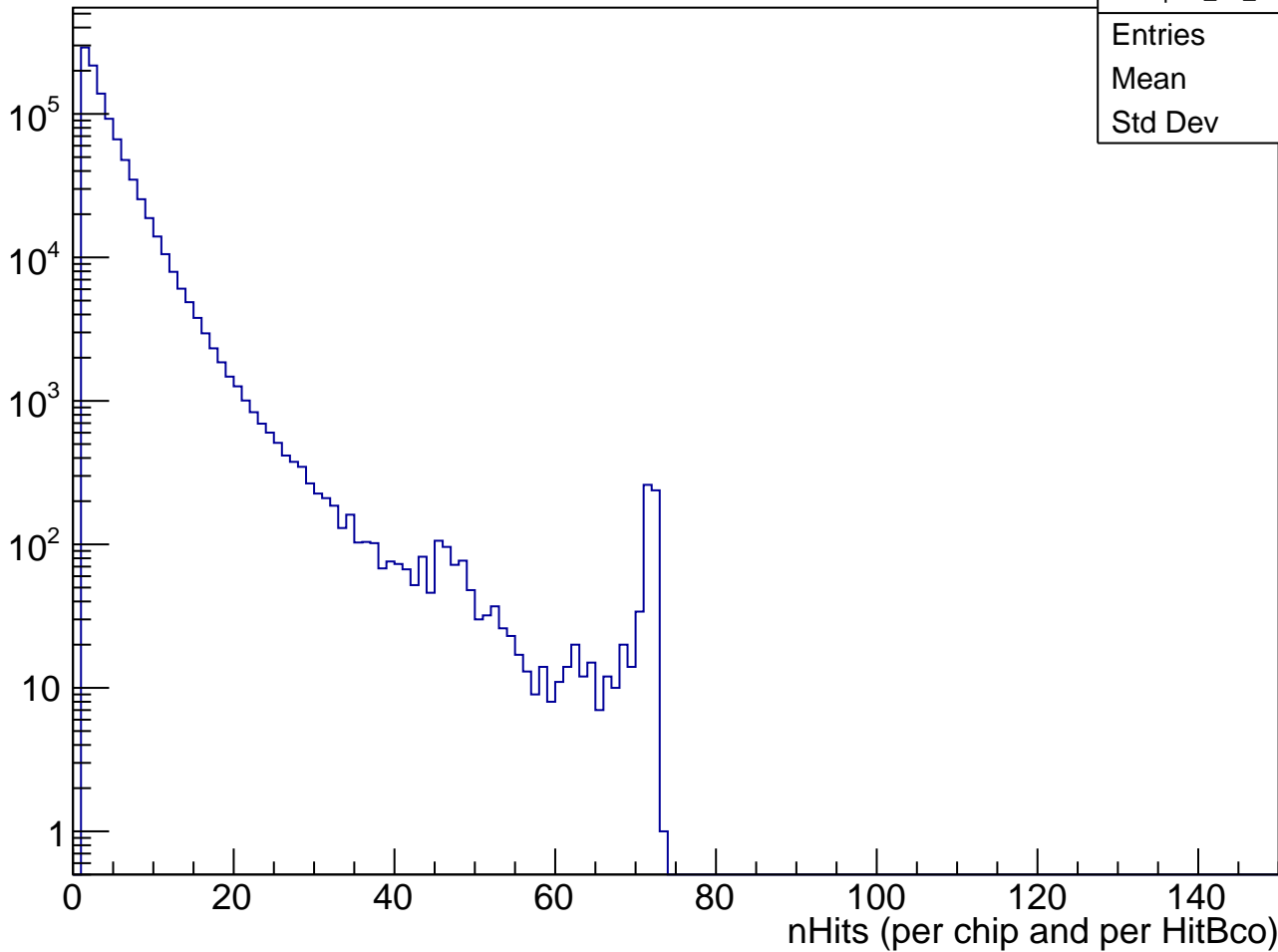
Entries



nChipHit_F5_Fch13_Chip9	
Entries	1058709
Mean	3.364
Std Dev	3.855

nChipHit_F5_Fch1_Chip0

Entries



nChipHit_F5_Fch1_Chip0

Entries 997297

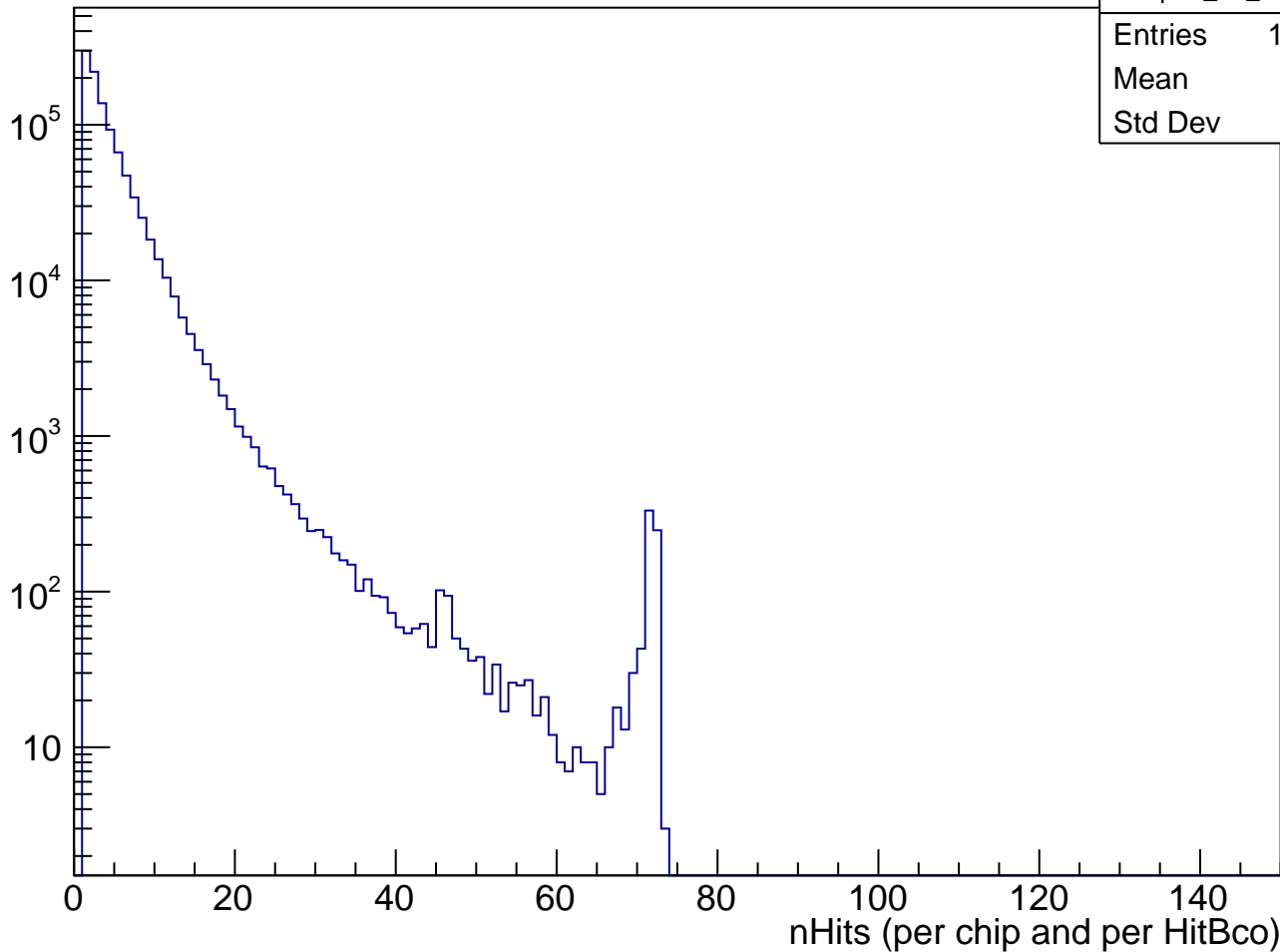
Mean 3.741

Std Dev 4.207

nHits (per chip and per HitBco)

nChipHit_F5_Fch1_Chip1

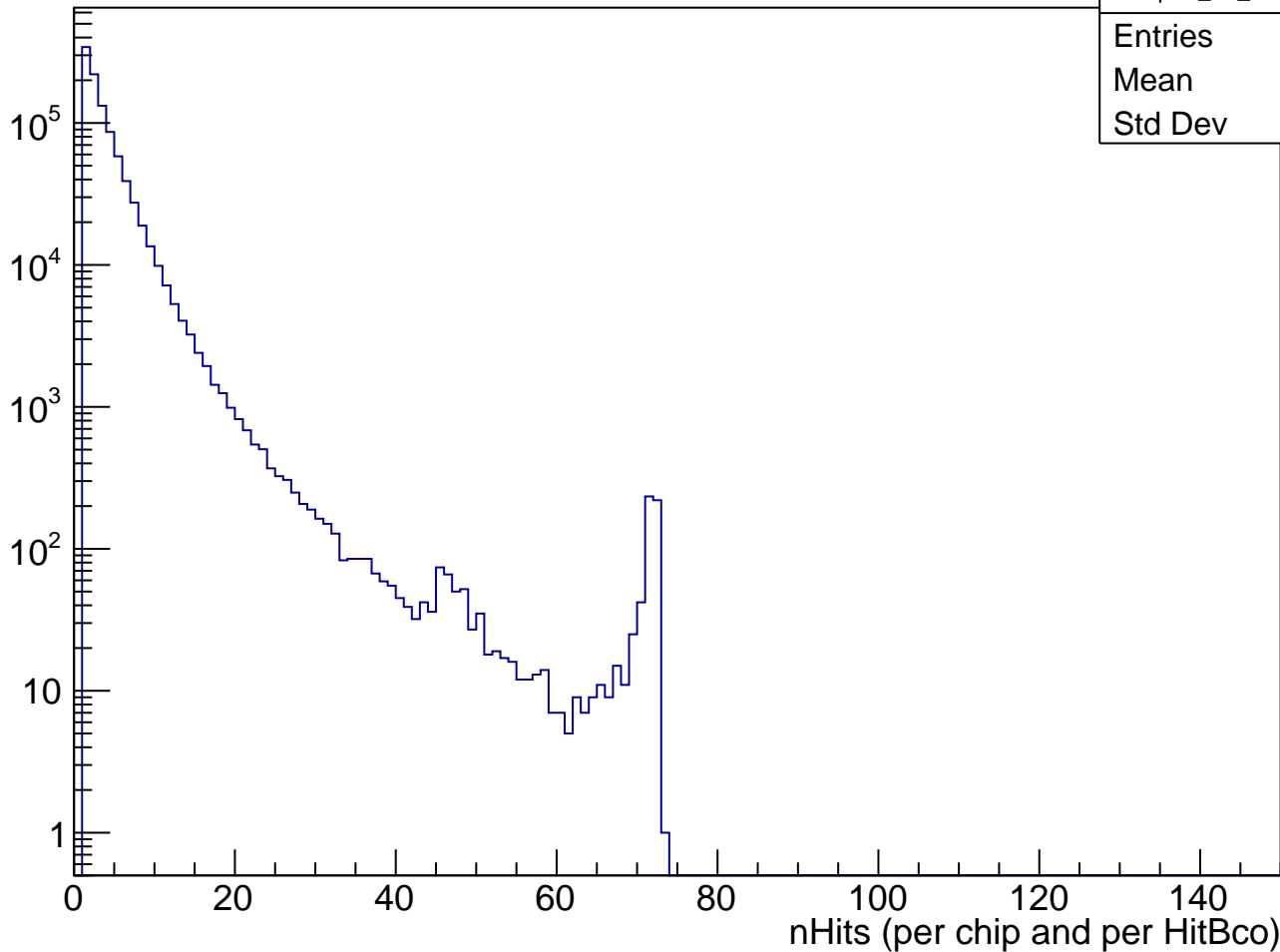
Entries



nChipHit_F5_Fch1_Chip1	
Entries	1002926
Mean	3.695
Std Dev	4.209

nChipHit_F5_Fch1_Chip10

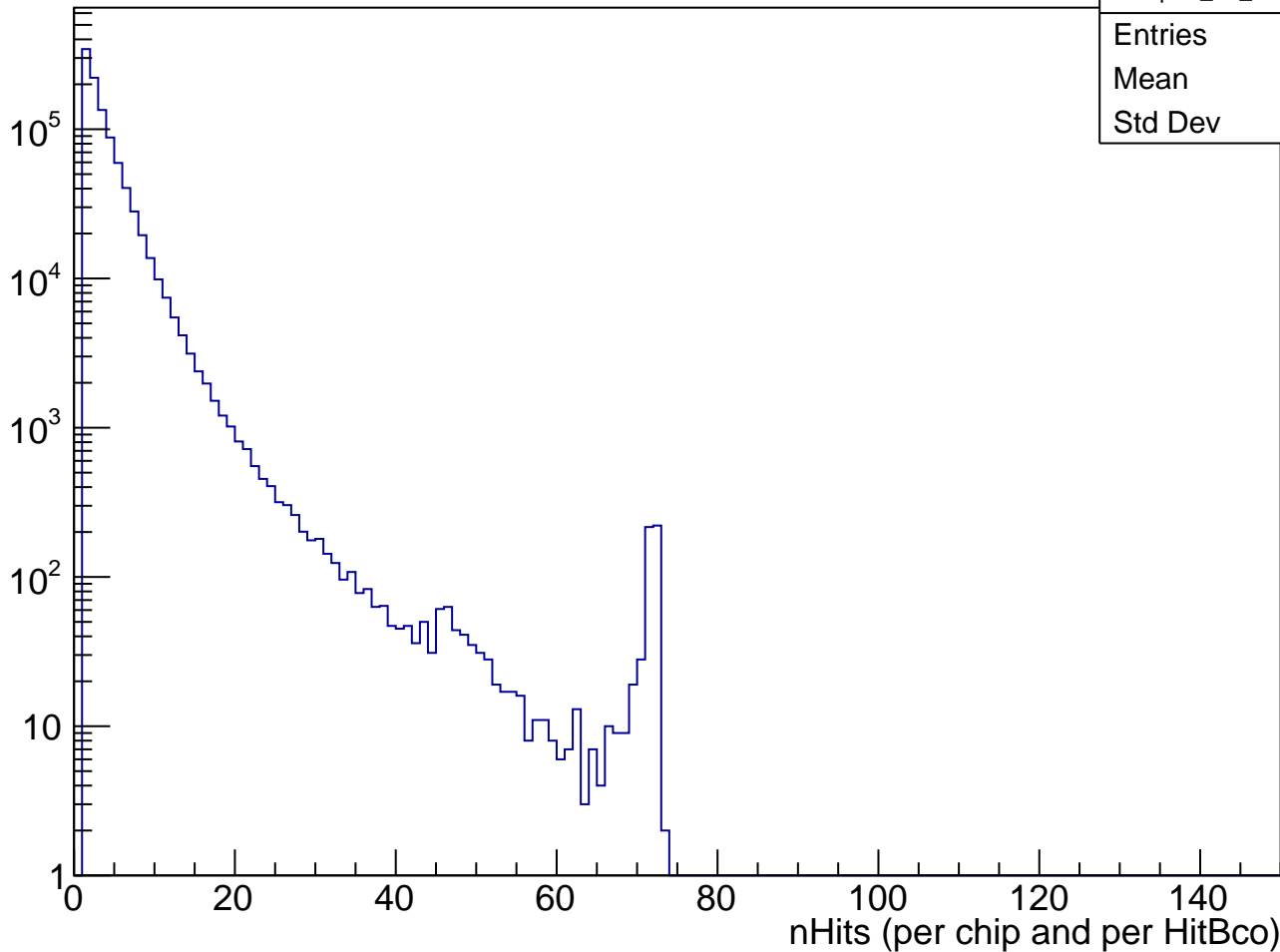
Entries



nChipHit_F5_Fch1_Chip10	
Entries	983141
Mean	3.244
Std Dev	3.756

nChipHit_F5_Fch1_Chip11

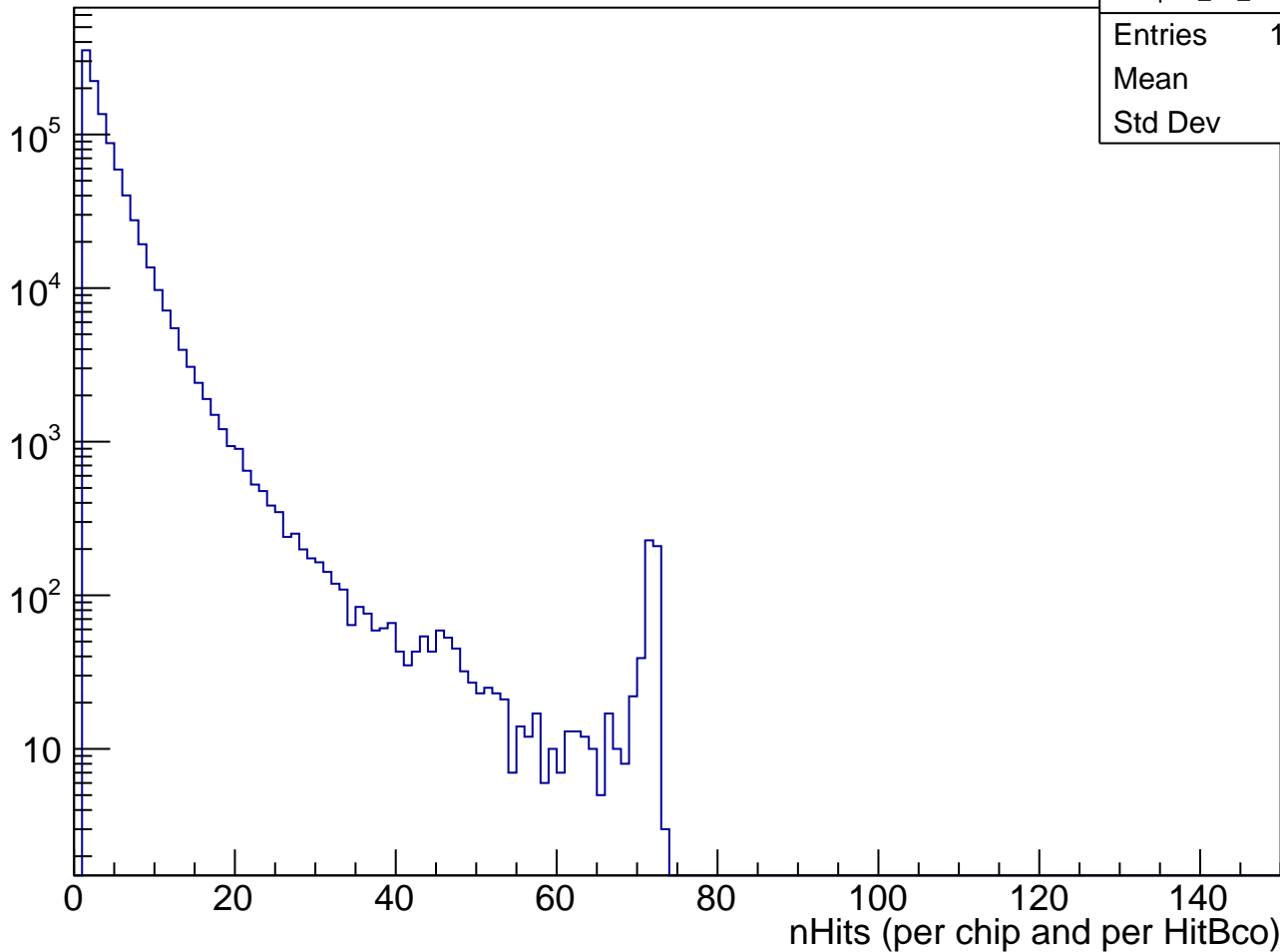
Entries



nChipHit_F5_Fch1_Chip11	
Entries	993949
Mean	3.255
Std Dev	3.718

nChipHit_F5_Fch1_Chip12

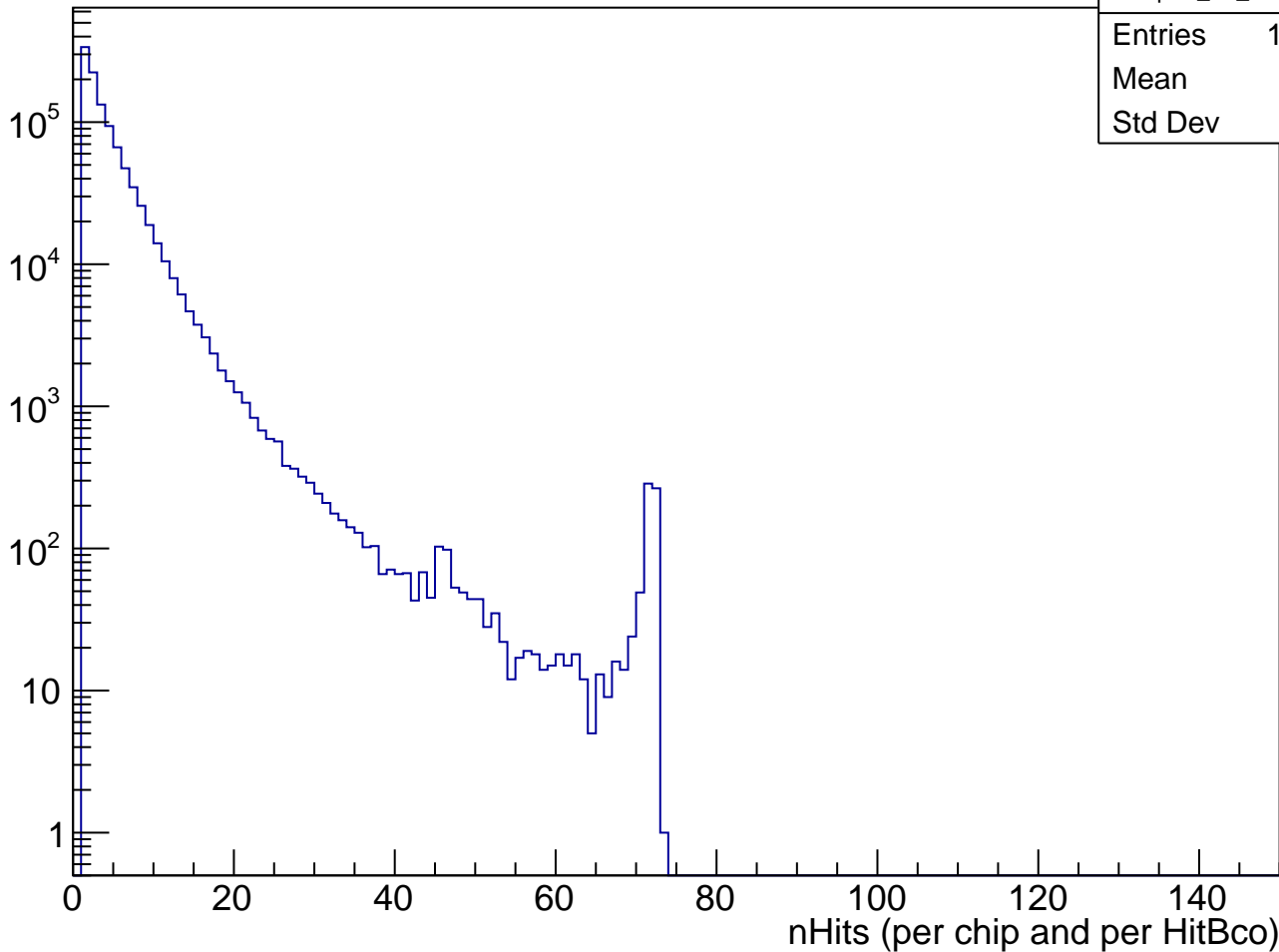
Entries



nChipHit_F5_Fch1_Chip12	
Entries	1003263
Mean	3.218
Std Dev	3.7

nChipHit_F5_Fch1_Chip13

Entries

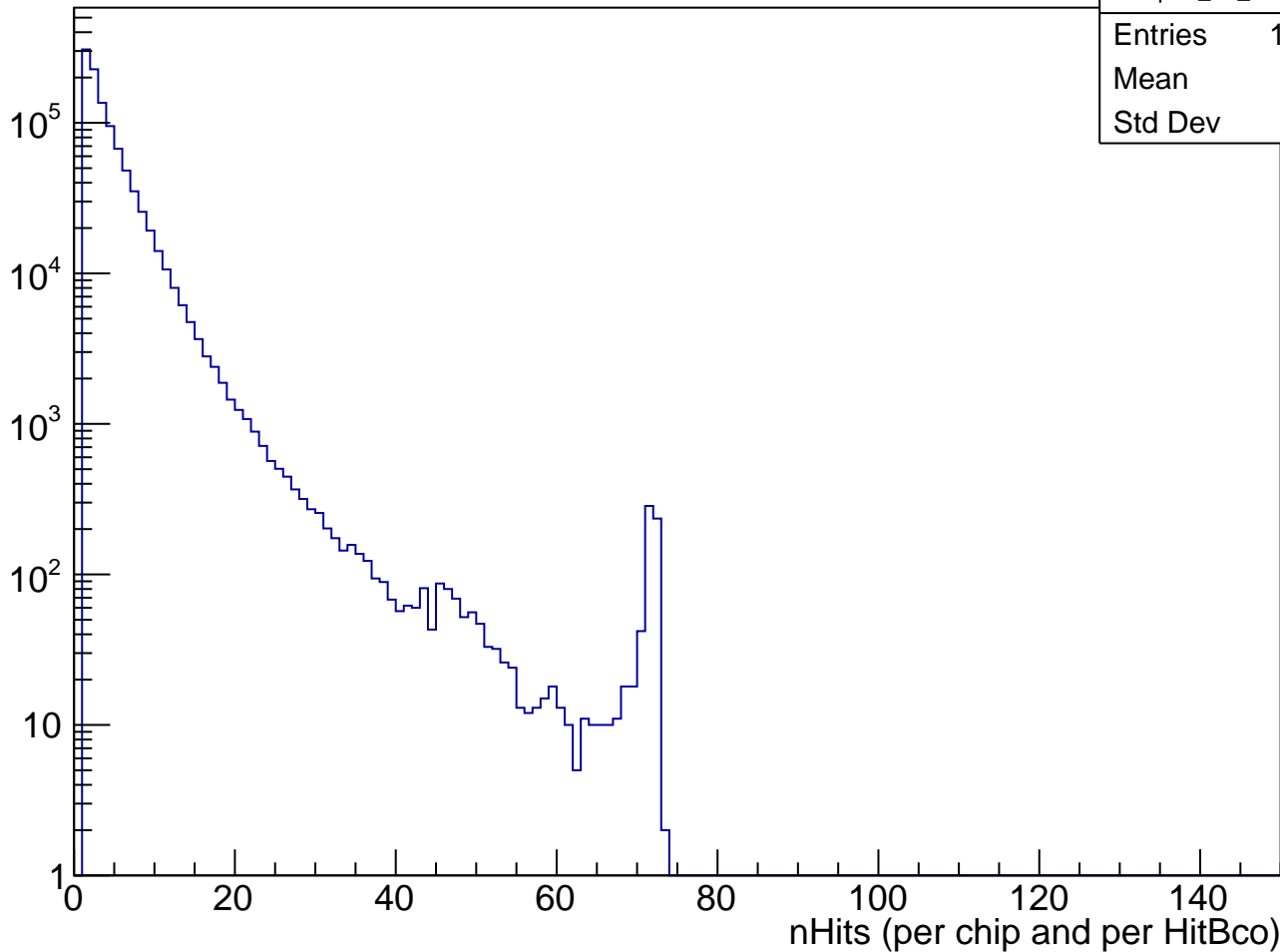


nChipHit_F5_Fch1_Chip13	
Entries	1046488
Mean	3.616
Std Dev	4.179

nHits (per chip and per HitBco)

nChipHit_F5_Fch1_Chip14

Entries

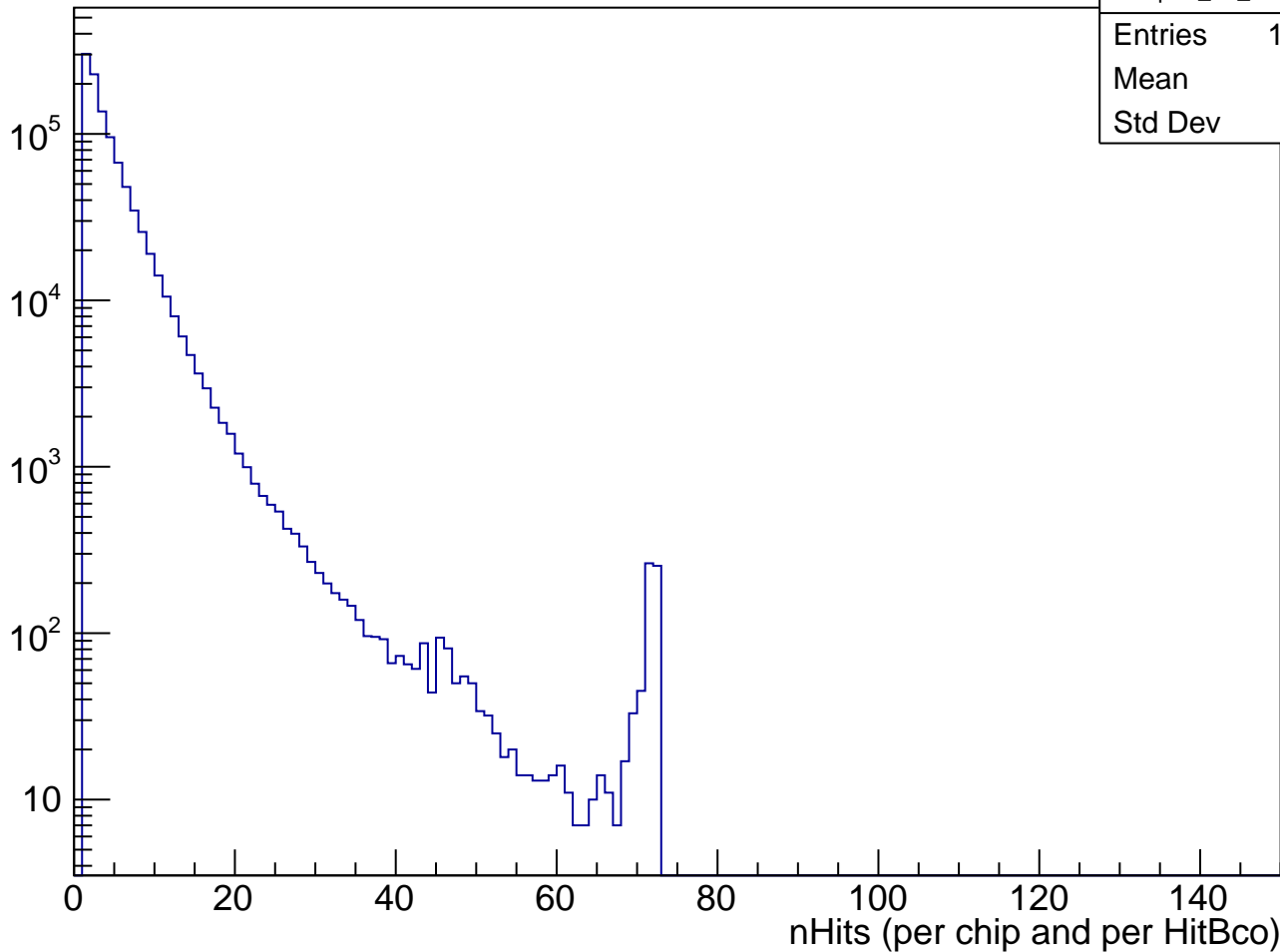


nChipHit_F5_Fch1_Chip14	
Entries	1025930
Mean	3.69
Std Dev	4.178

nHits (per chip and per HitBco)

nChipHit_F5_Fch1_Chip15

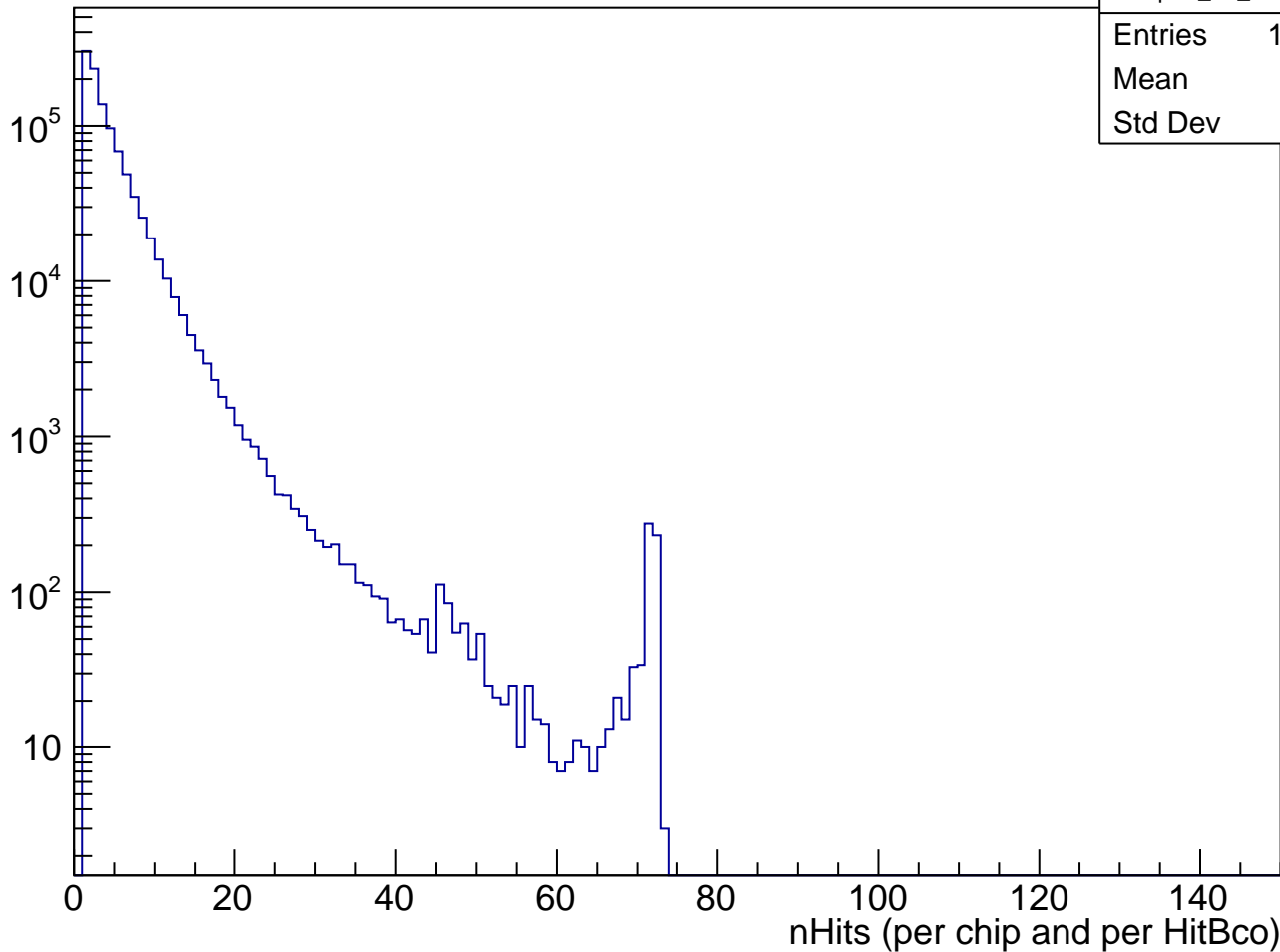
Entries



nChipHit_F5_Fch1_Chip15	
Entries	1022758
Mean	3.691
Std Dev	4.167

nChipHit_F5_Fch1_Chip16

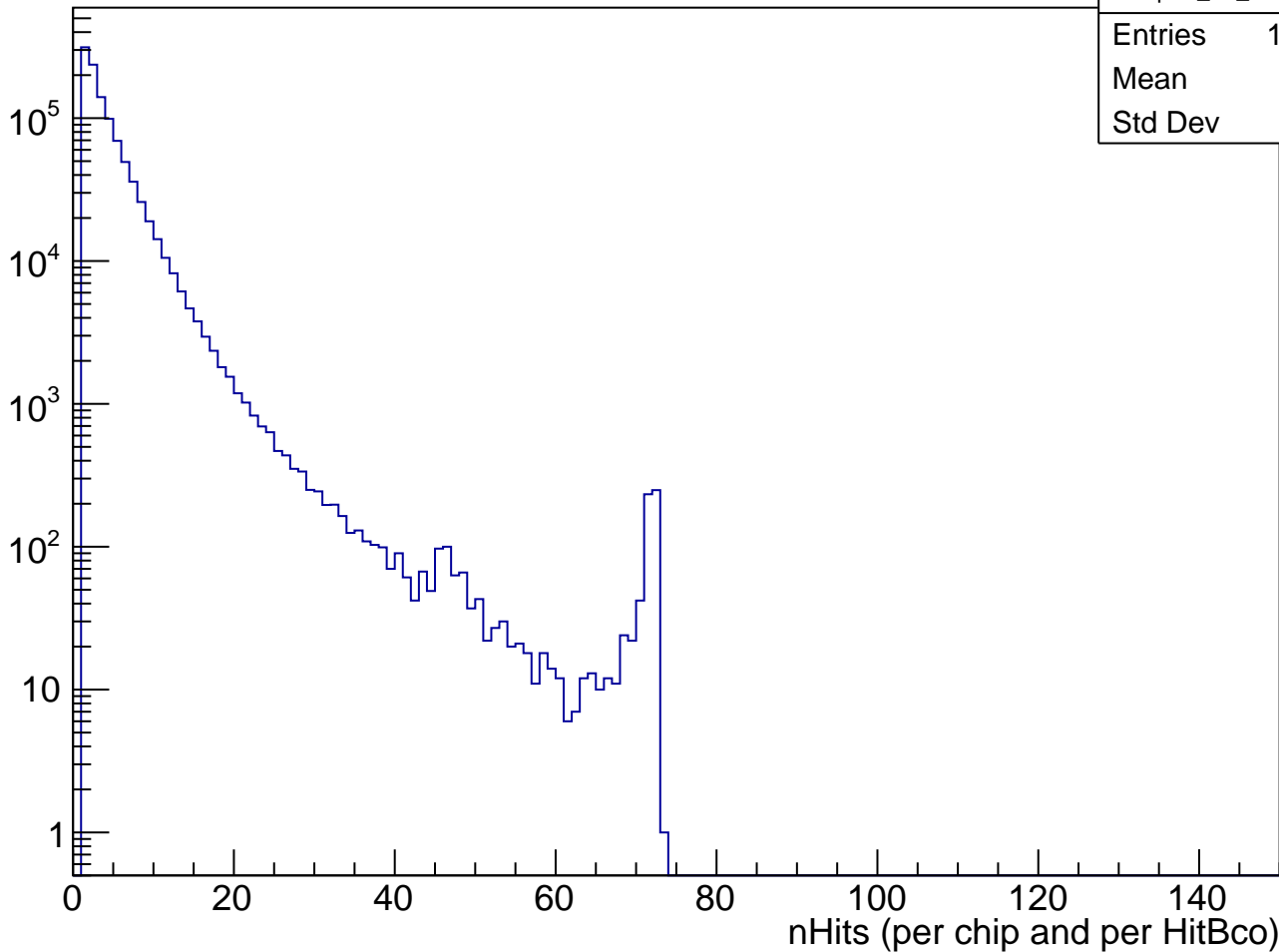
Entries



nChipHit_F5_Fch1_Chip16	
Entries	1031374
Mean	3.67
Std Dev	4.126

nChipHit_F5_Fch1_Chip17

Entries

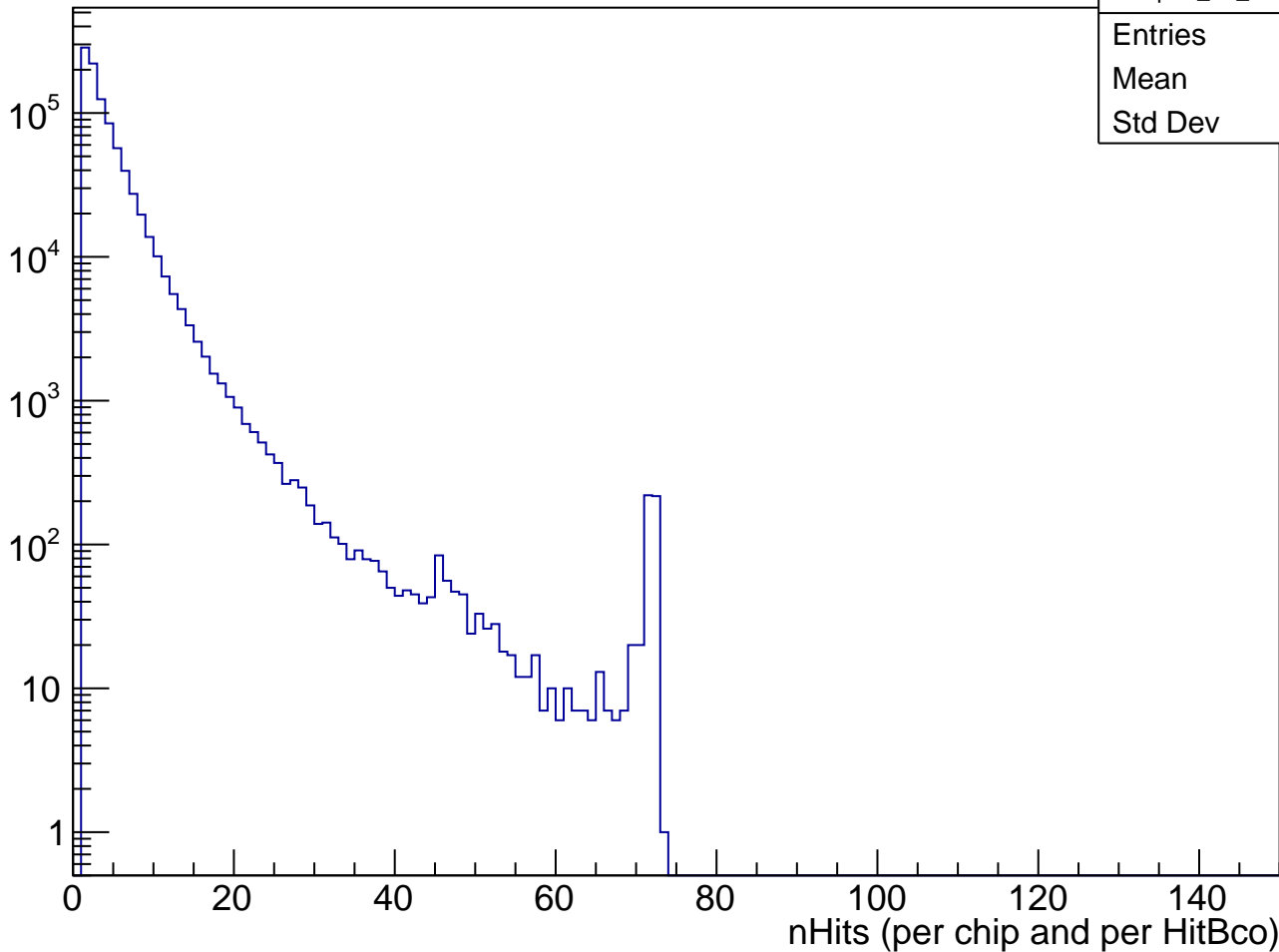


nChipHit_F5_Fch1_Chip17	
Entries	1054208
Mean	3.664
Std Dev	4.118

nHits (per chip and per HitBco)

nChipHit_F5_Fch1_Chip18

Entries

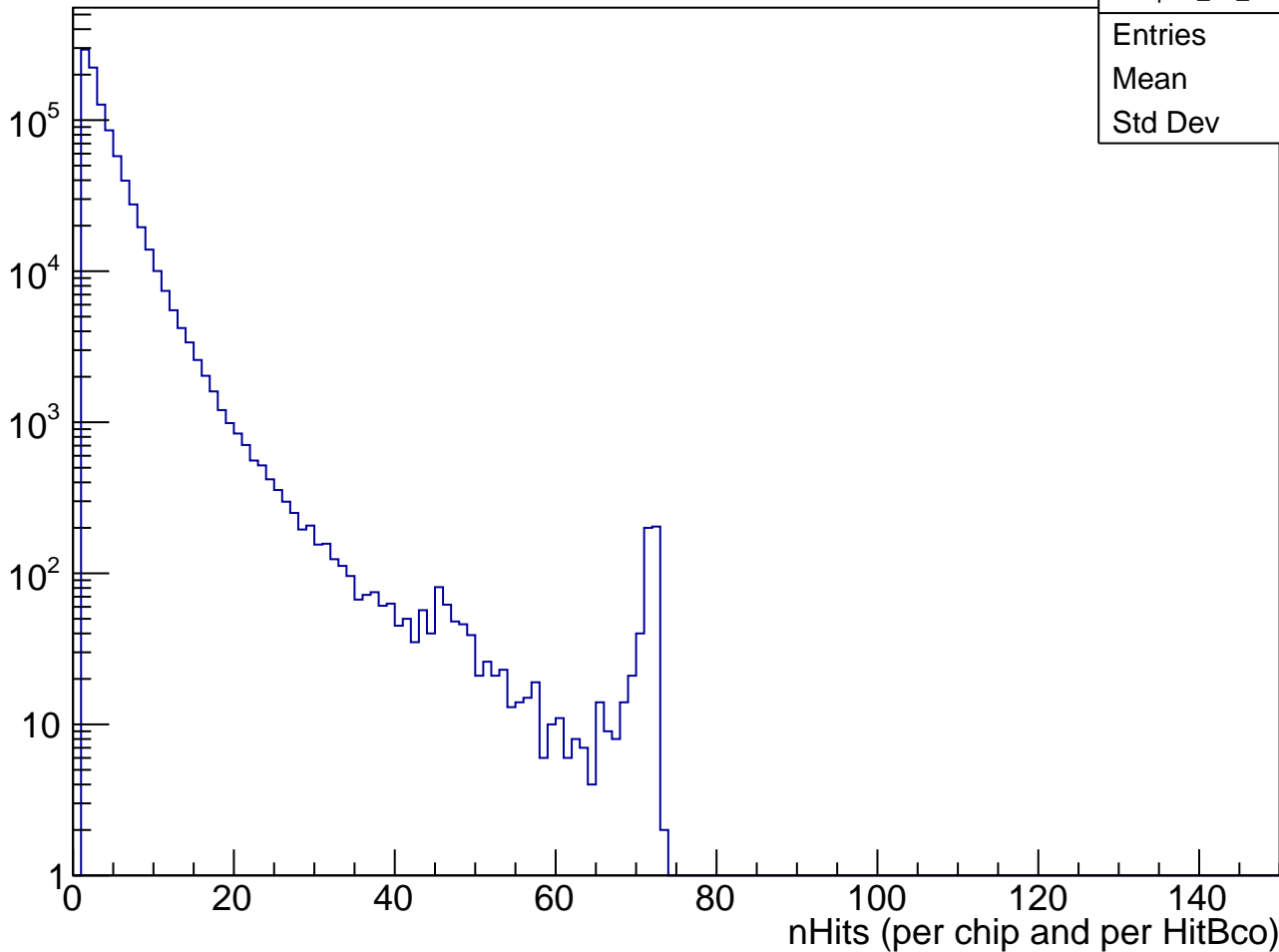


nChipHit_F5_Fch1_Chip18	
Entries	918716
Mean	3.409
Std Dev	3.852

nHits (per chip and per HitBco)

nChipHit_F5_Fch1_Chip19

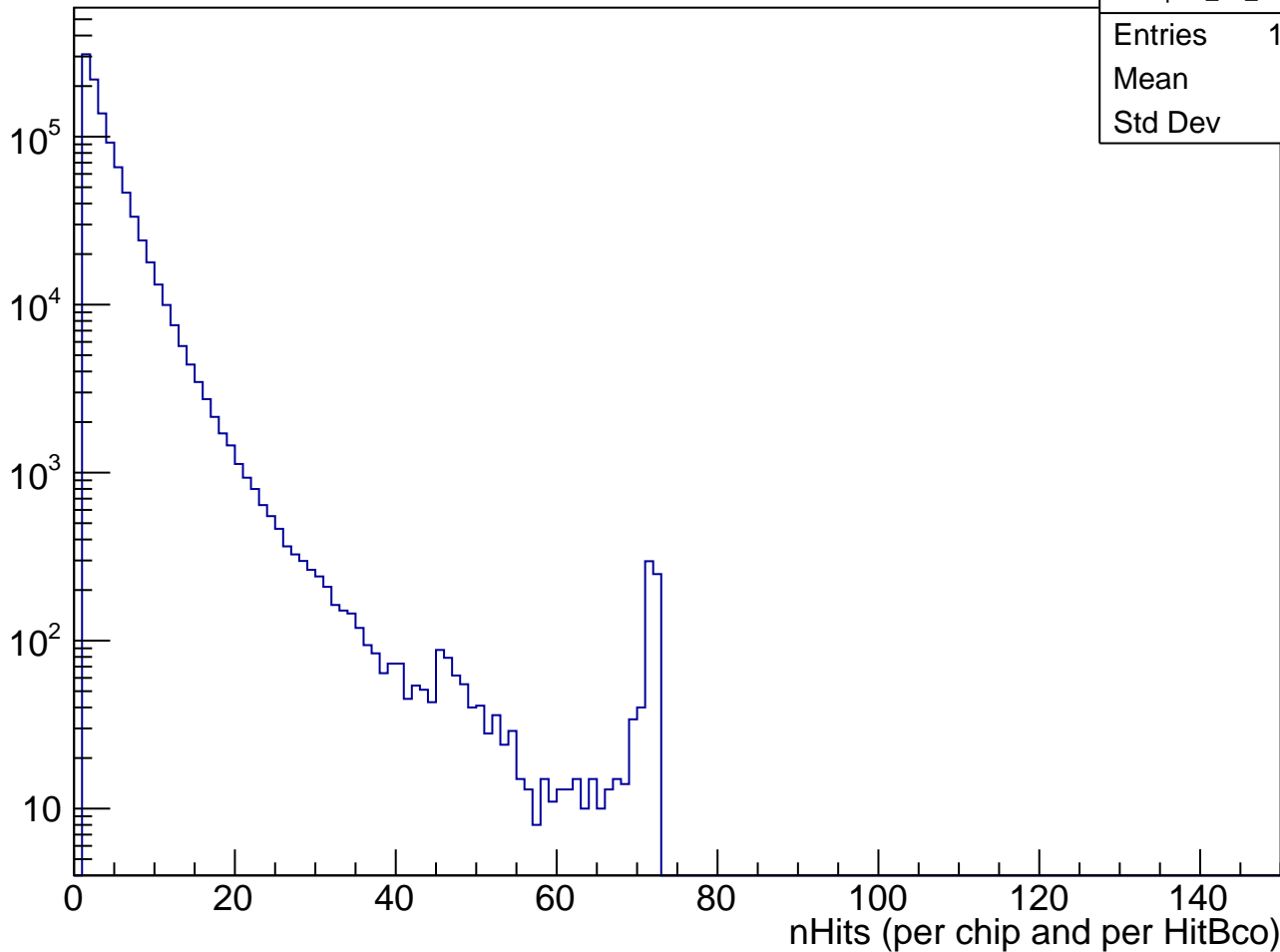
Entries



nChipHit_F5_Fch1_Chip19	
Entries	932156
Mean	3.385
Std Dev	3.829

nChipHit_F5_Fch1_Chip2

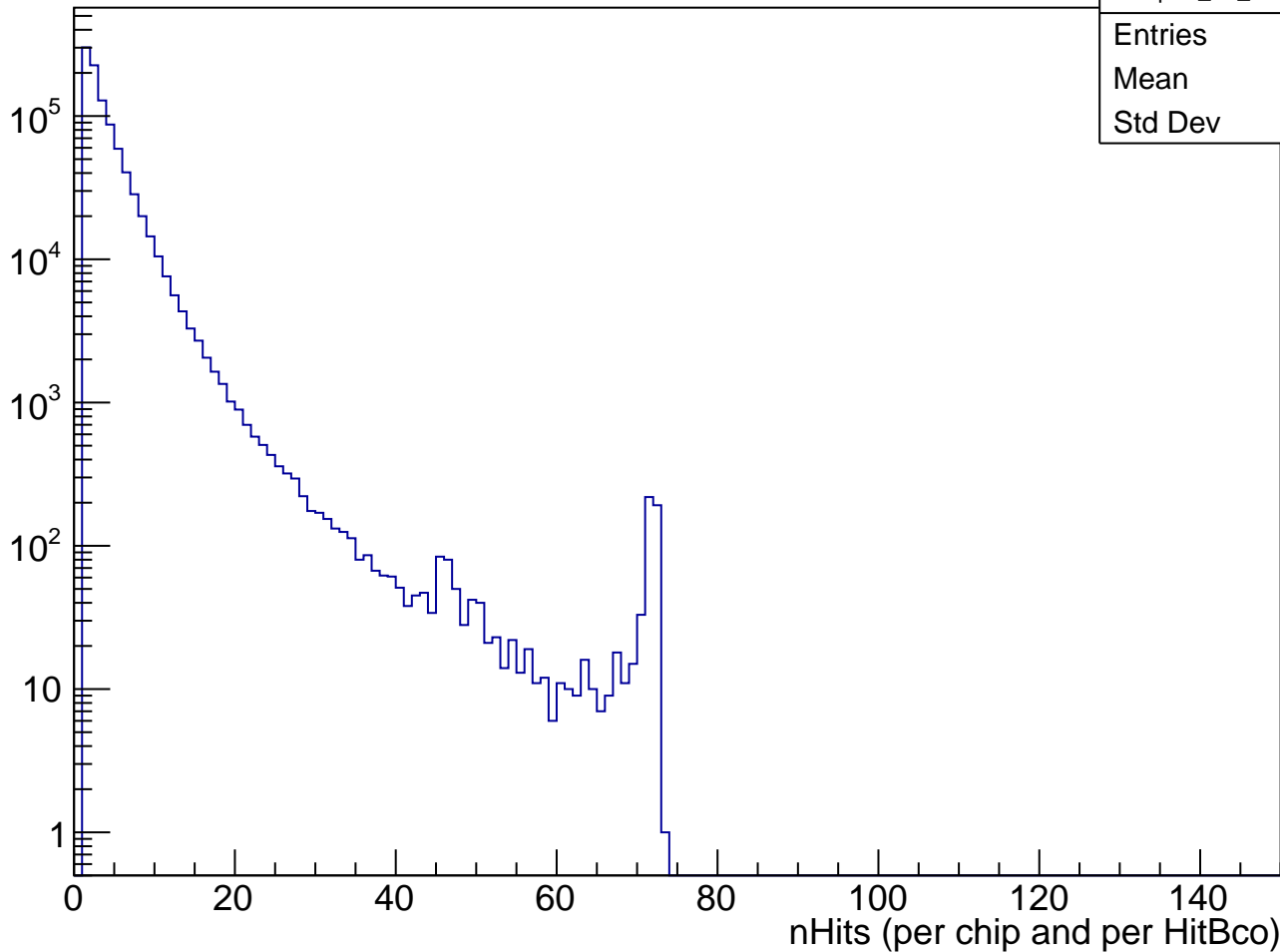
Entries



nChipHit_F5_Fch1_Chip2	
Entries	1006442
Mean	3.625
Std Dev	4.144

nChipHit_F5_Fch1_Chip20

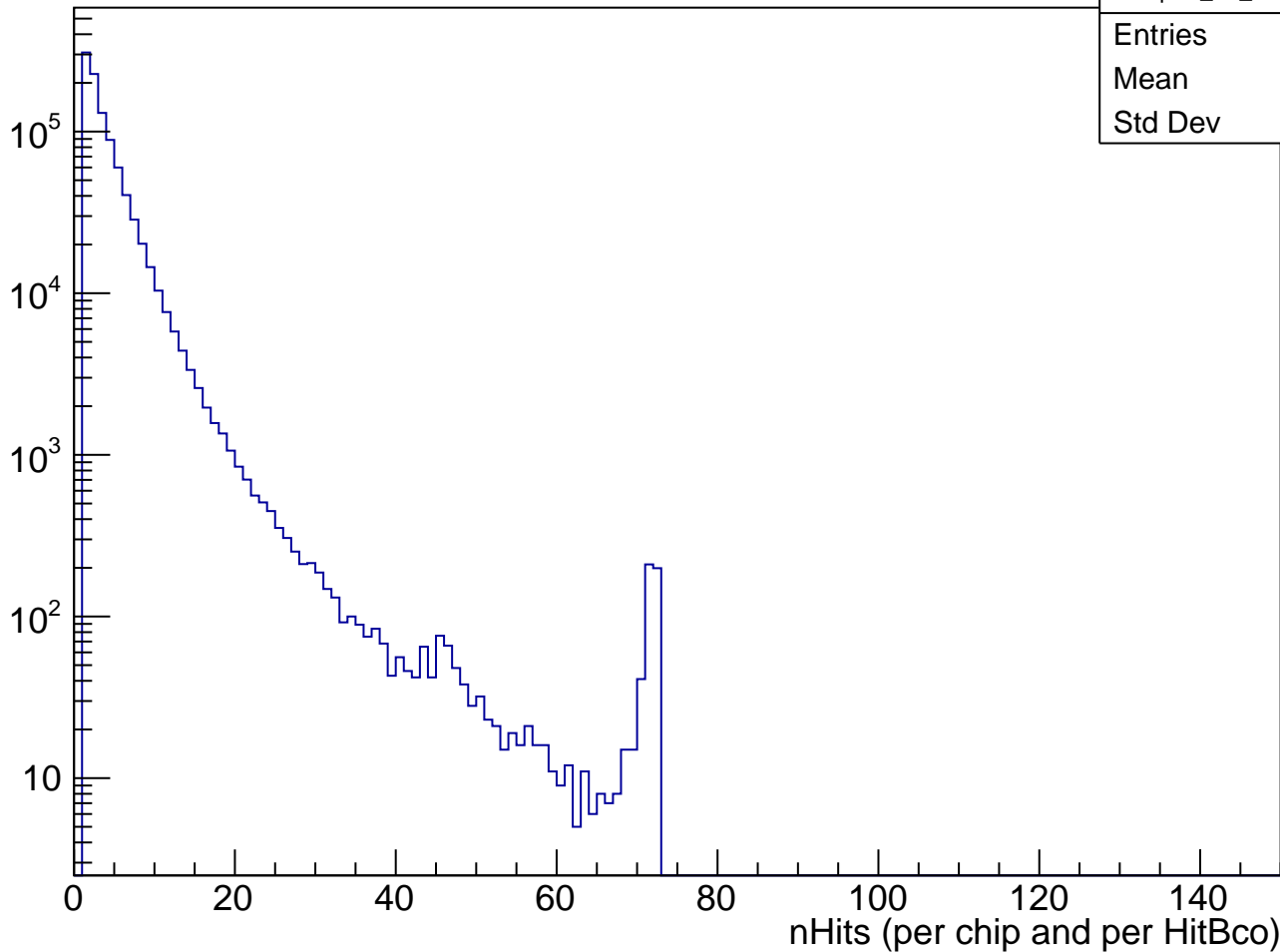
Entries



nChipHit_F5_Fch1_Chip20	
Entries	952390
Mean	3.39
Std Dev	3.84

nChipHit_F5_Fch1_Chip21

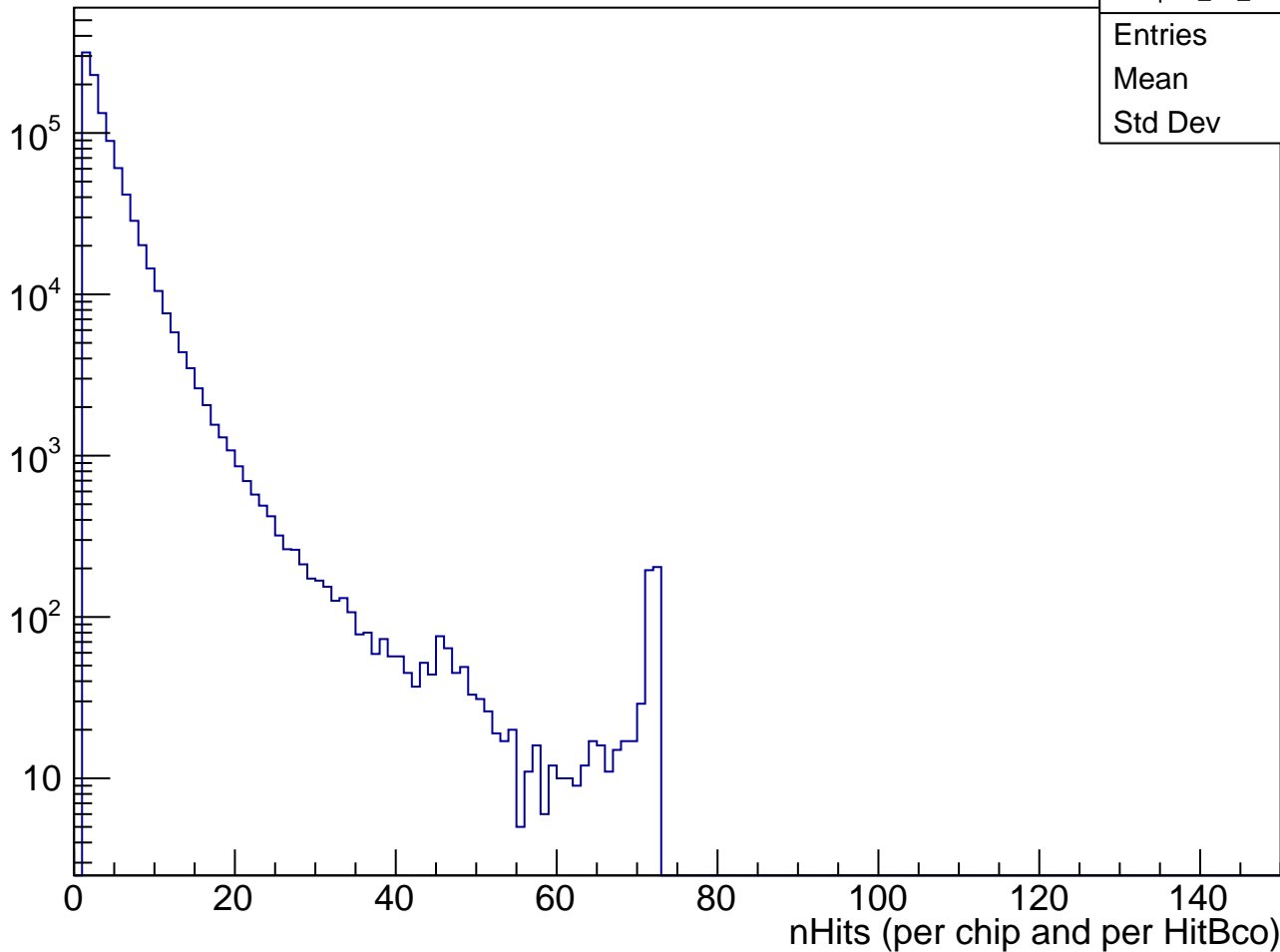
Entries



nChipHit_F5_Fch1_Chip21	
Entries	965155
Mean	3.371
Std Dev	3.811

nChipHit_F5_Fch1_Chip22

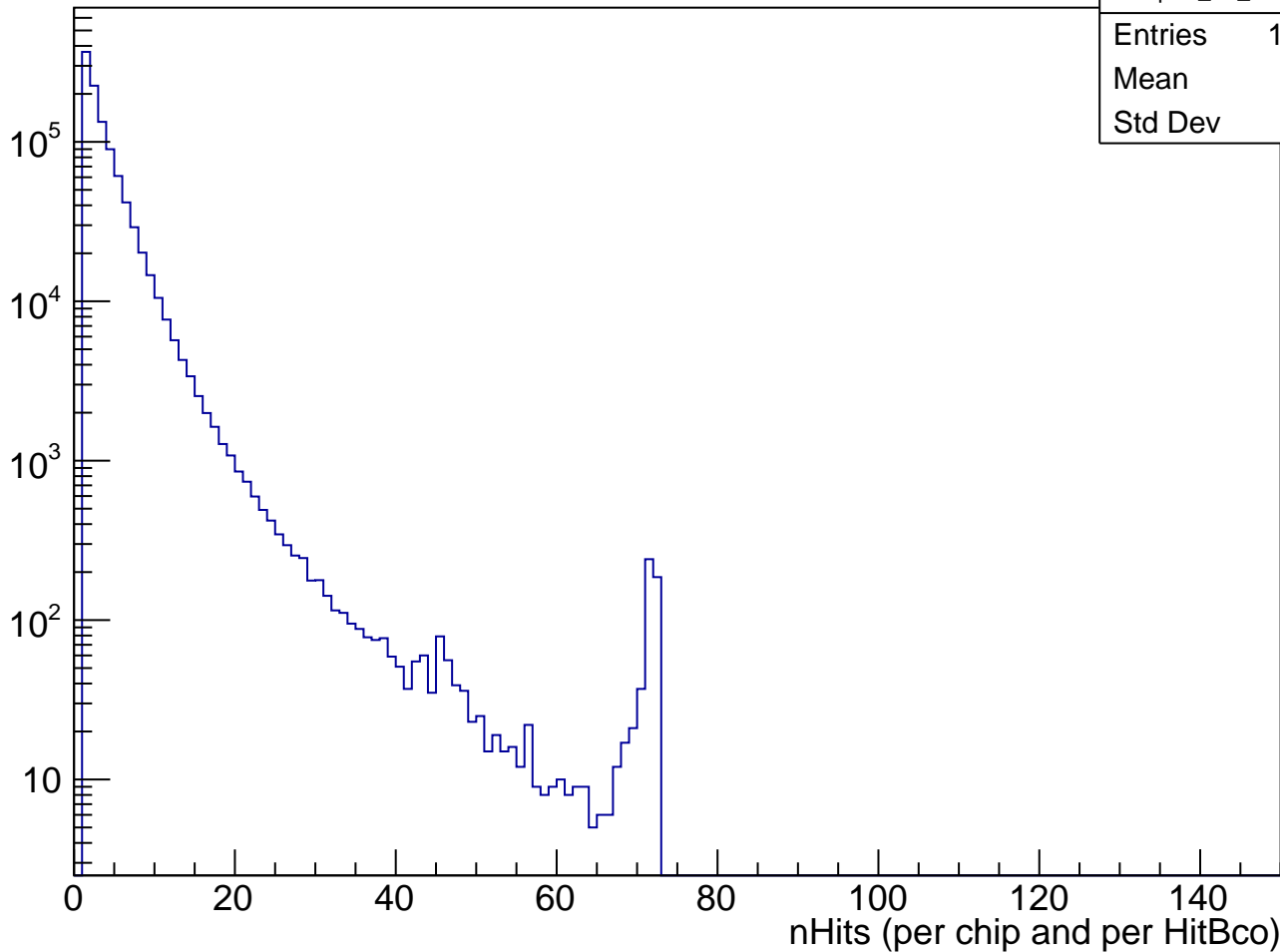
Entries



nChipHit_F5_Fch1_Chip22	
Entries	979006
Mean	3.353
Std Dev	3.783

nChipHit_F5_Fch1_Chip23

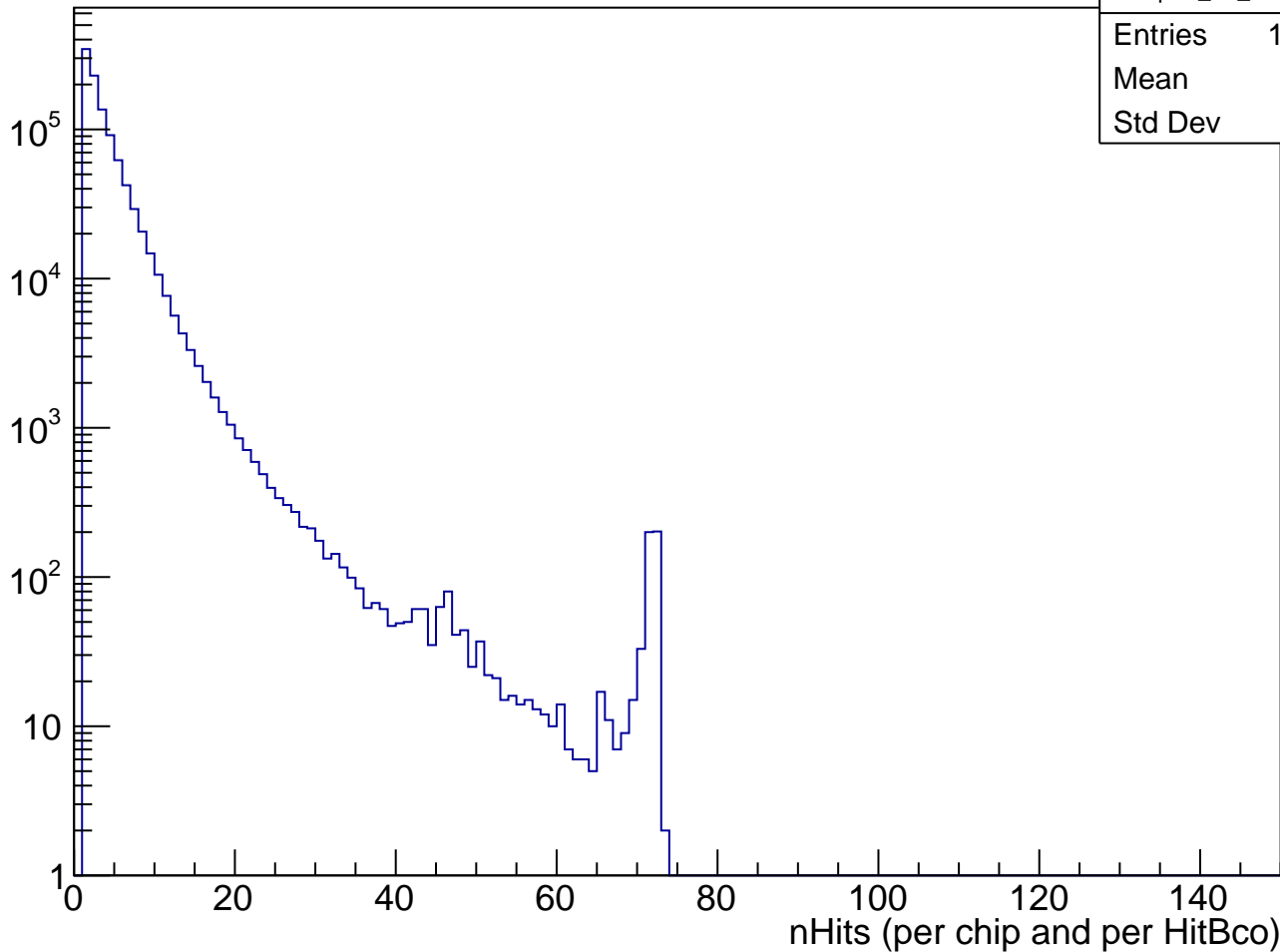
Entries



nChipHit_F5_Fch1_Chip23	
Entries	1028909
Mean	3.243
Std Dev	3.724

nChipHit_F5_Fch1_Chip24

Entries

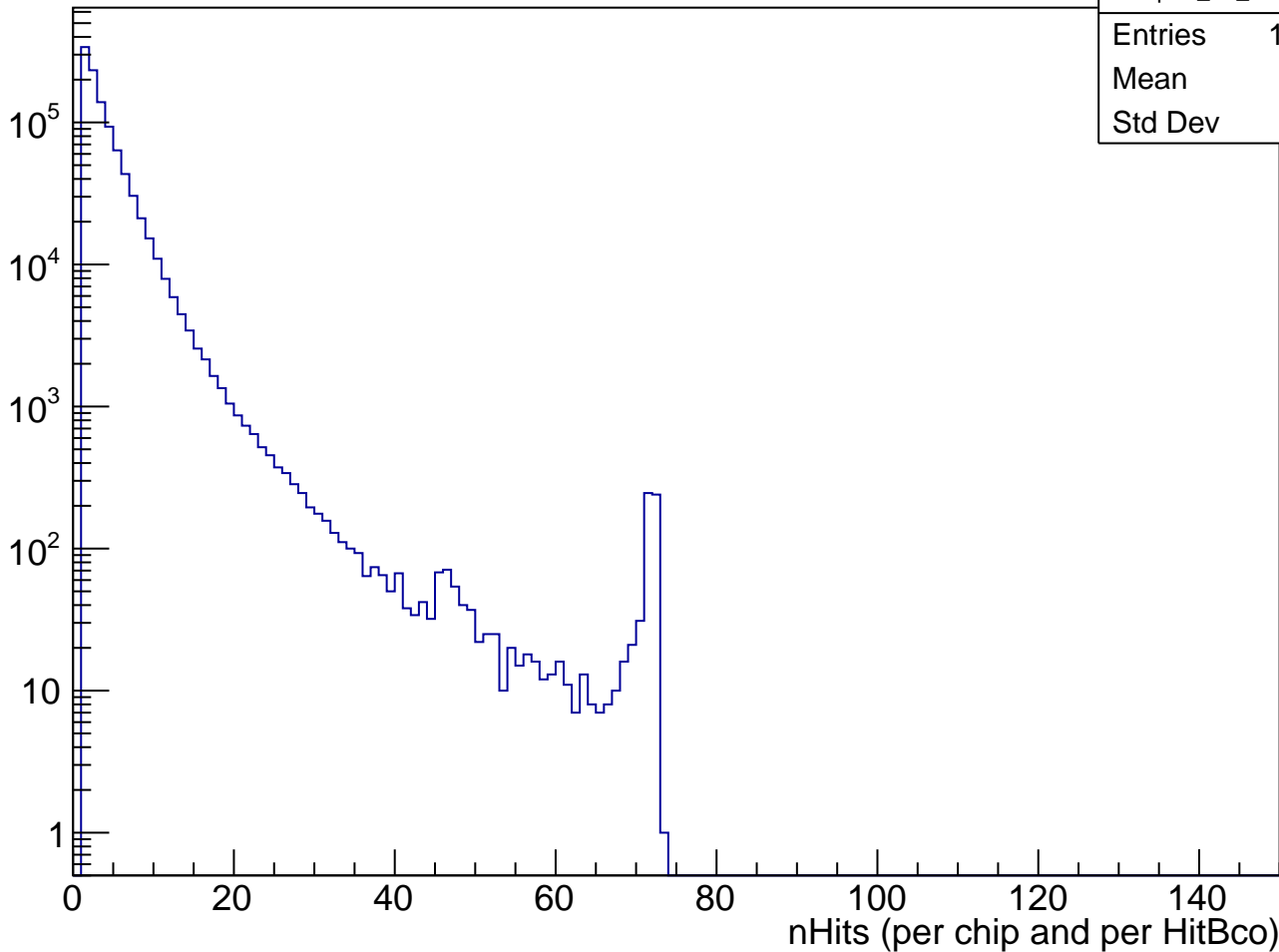


nChipHit_F5_Fch1_Chip24	
Entries	1018076
Mean	3.292
Std Dev	3.724

nHits (per chip and per HitBco)

nChipHit_F5_Fch1_Chip25

Entries

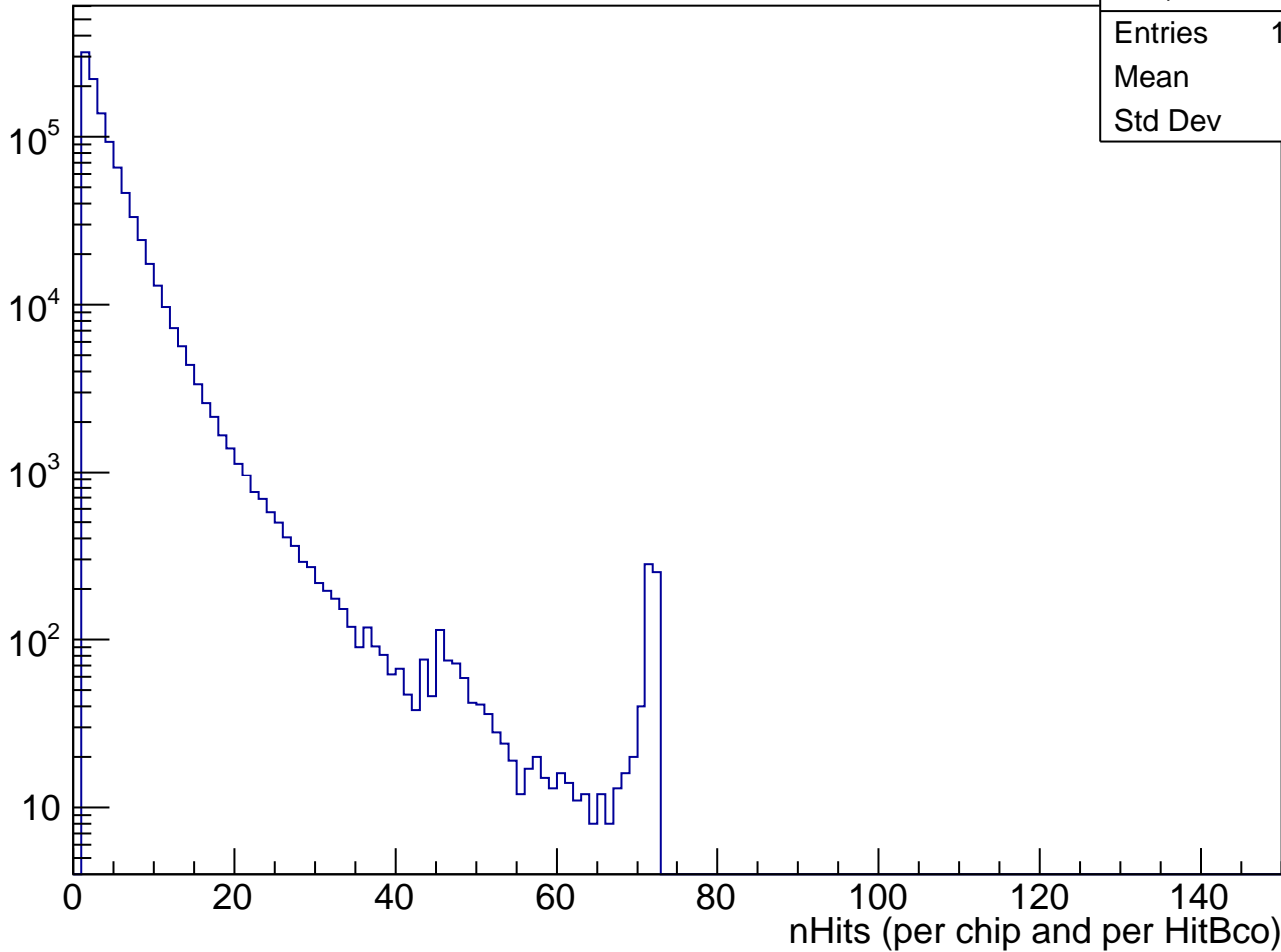


nChipHit_F5_Fch1_Chip25	
Entries	1026443
Mean	3.34
Std Dev	3.801

nHits (per chip and per HitBco)

nChipHit_F5_Fch1_Chip3

Entries



nChipHit_F5_Fch1_Chip3

Entries 1016770

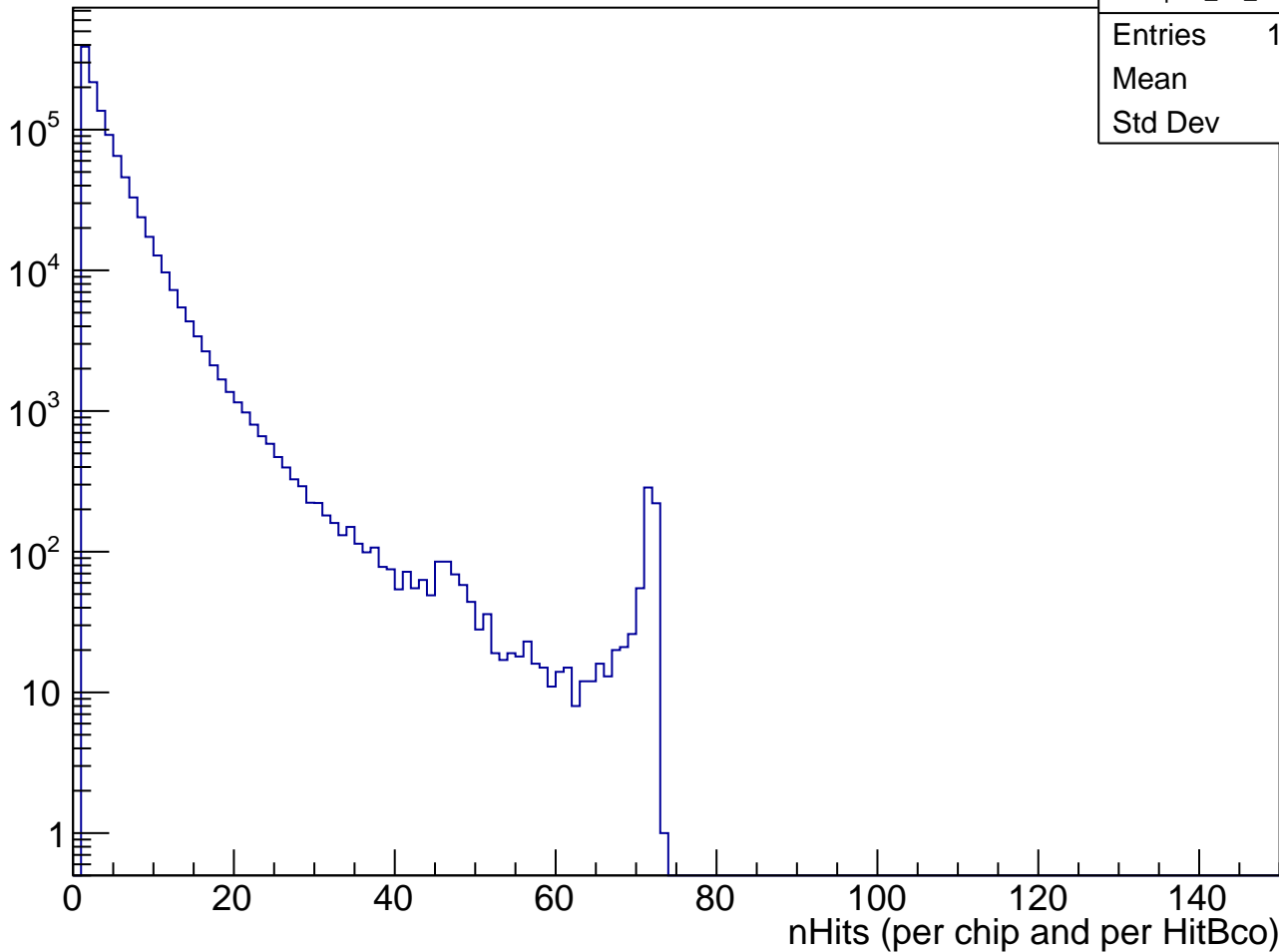
Mean 3.589

Std Dev 4.116

nHits (per chip and per HitBco)

nChipHit_F5_Fch1_Chip4

Entries



nChipHit_F5_Fch1_Chip4

Entries 1078355

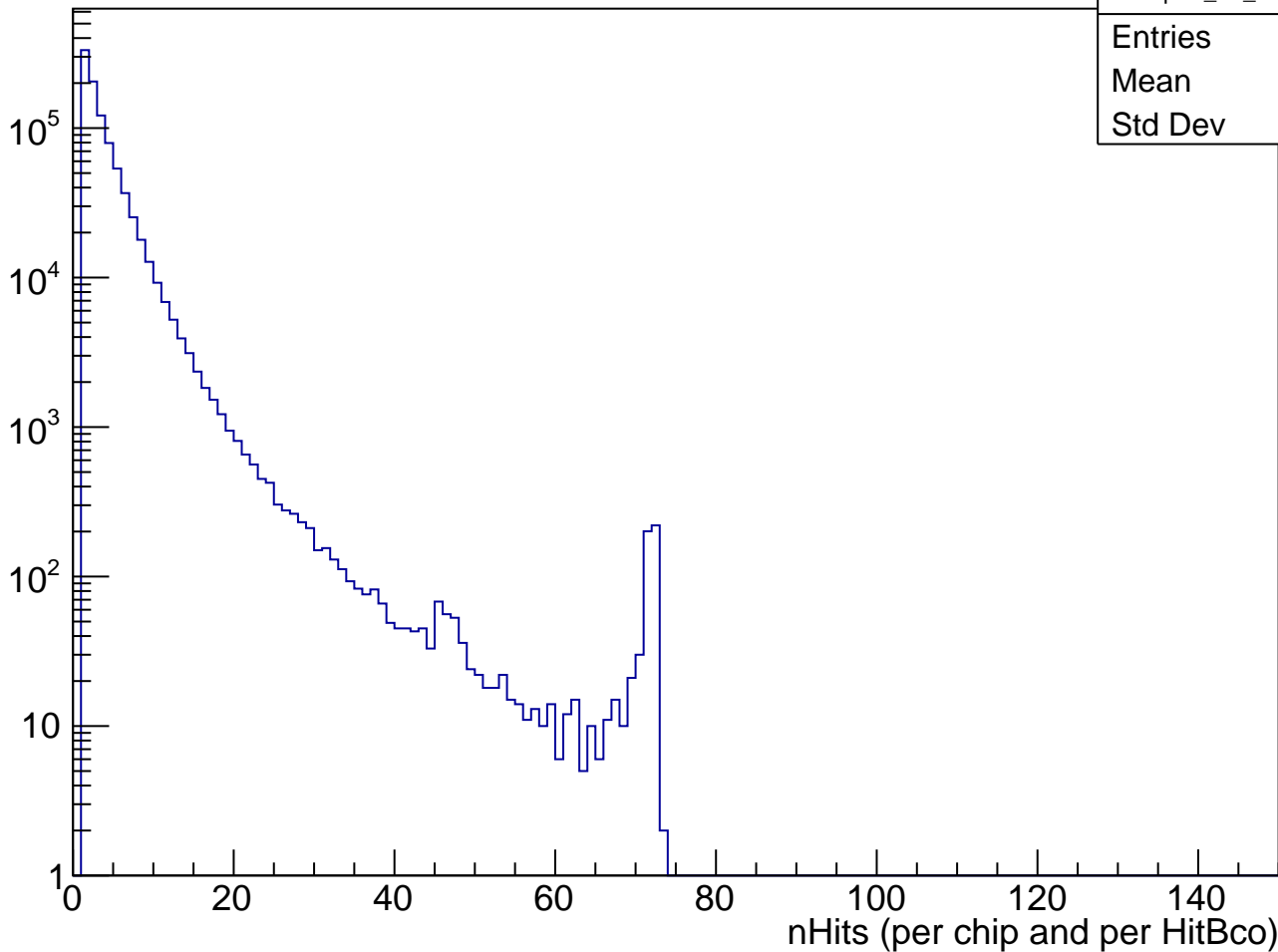
Mean 3.416

Std Dev 4.039

nHits (per chip and per HitBco)

nChipHit_F5_Fch1_Chip5

Entries



nChipHit_F5_Fch1_Chip5

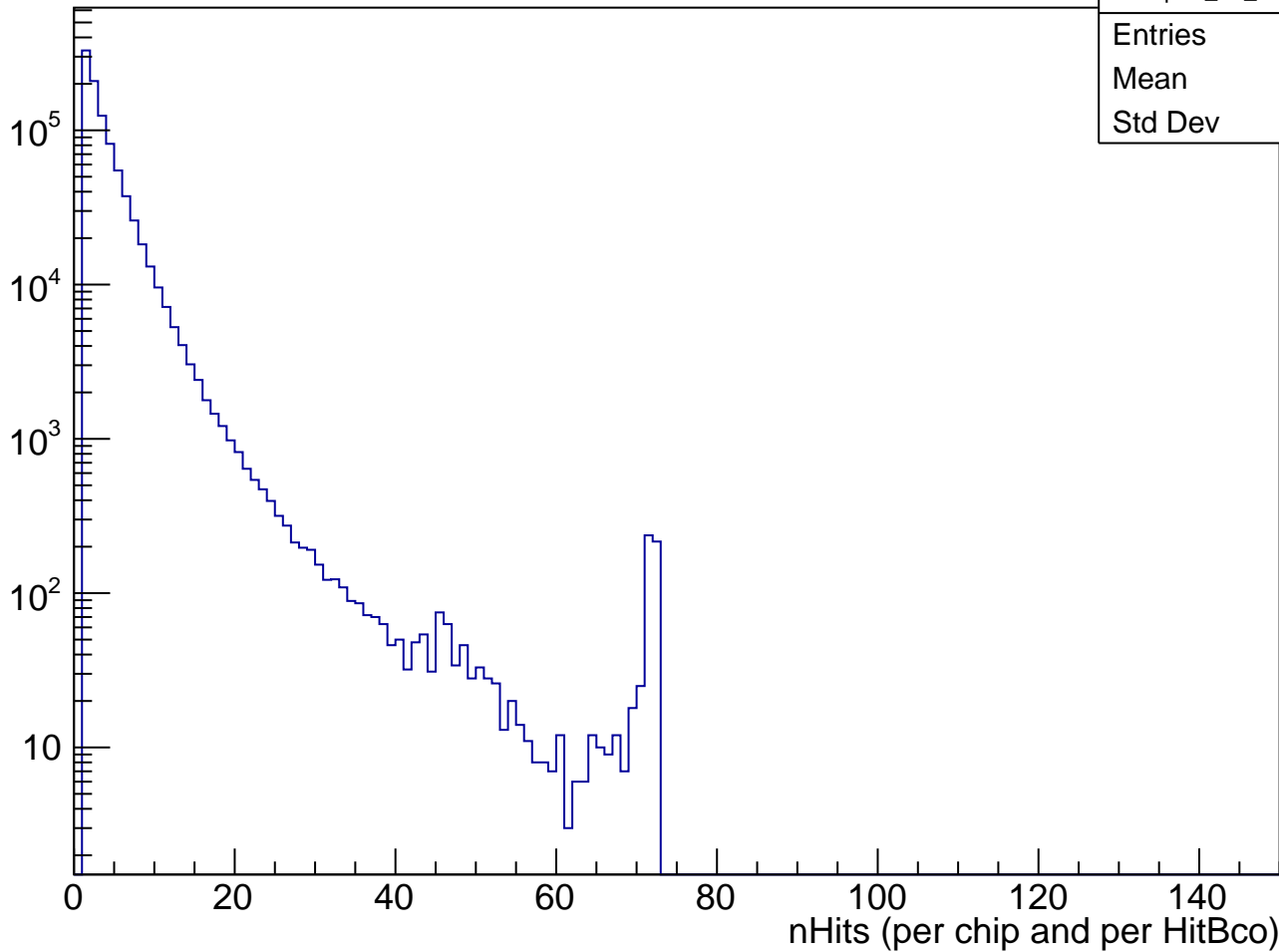
Entries 927051

Mean 3.238

Std Dev 3.799

nChipHit_F5_Fch1_Chip6

Entries



nChipHit_F5_Fch1_Chip6

Entries 936712

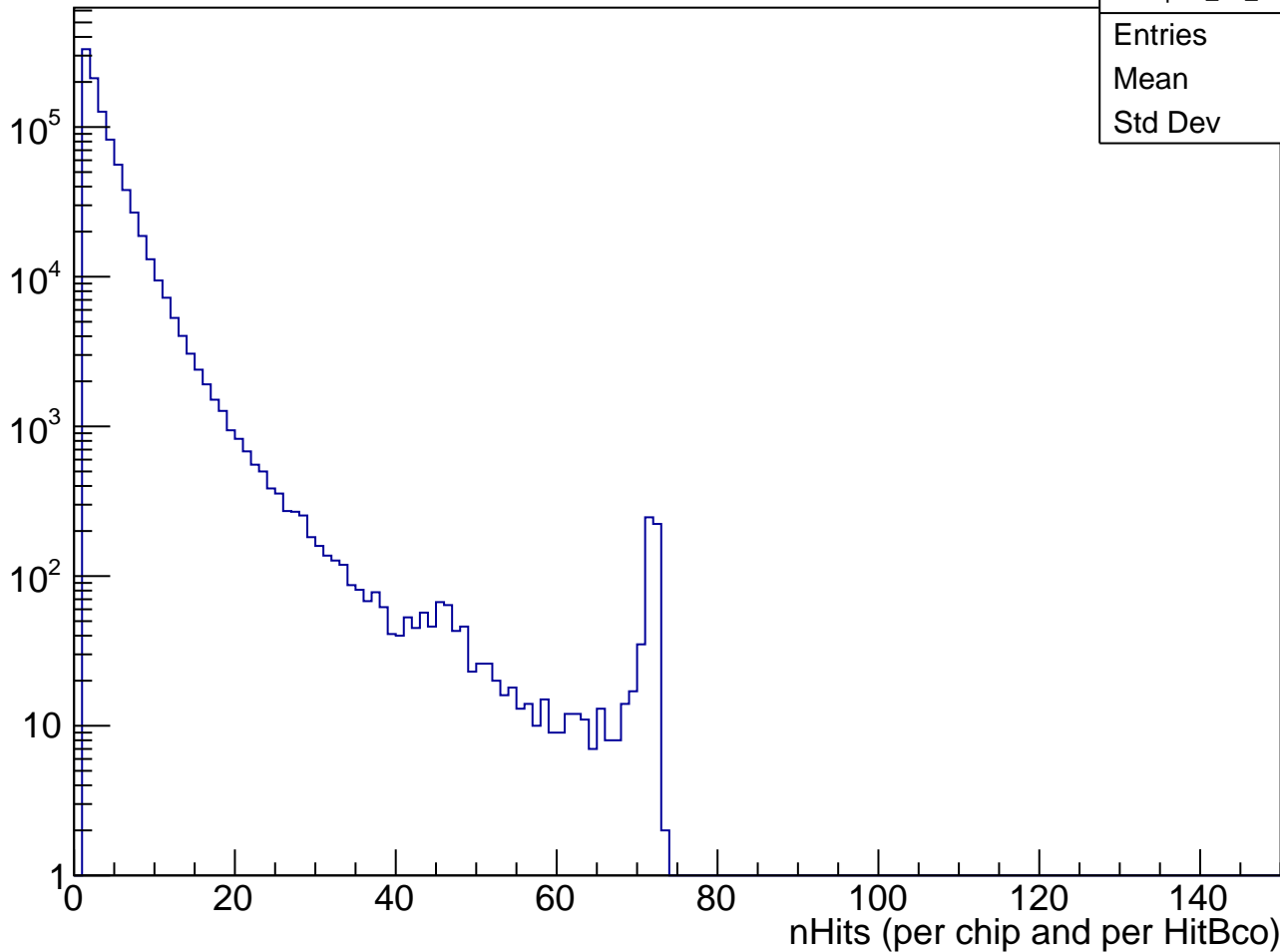
Mean 3.255

Std Dev 3.783

nHits (per chip and per HitBco)

nChipHit_F5_Fch1_Chip7

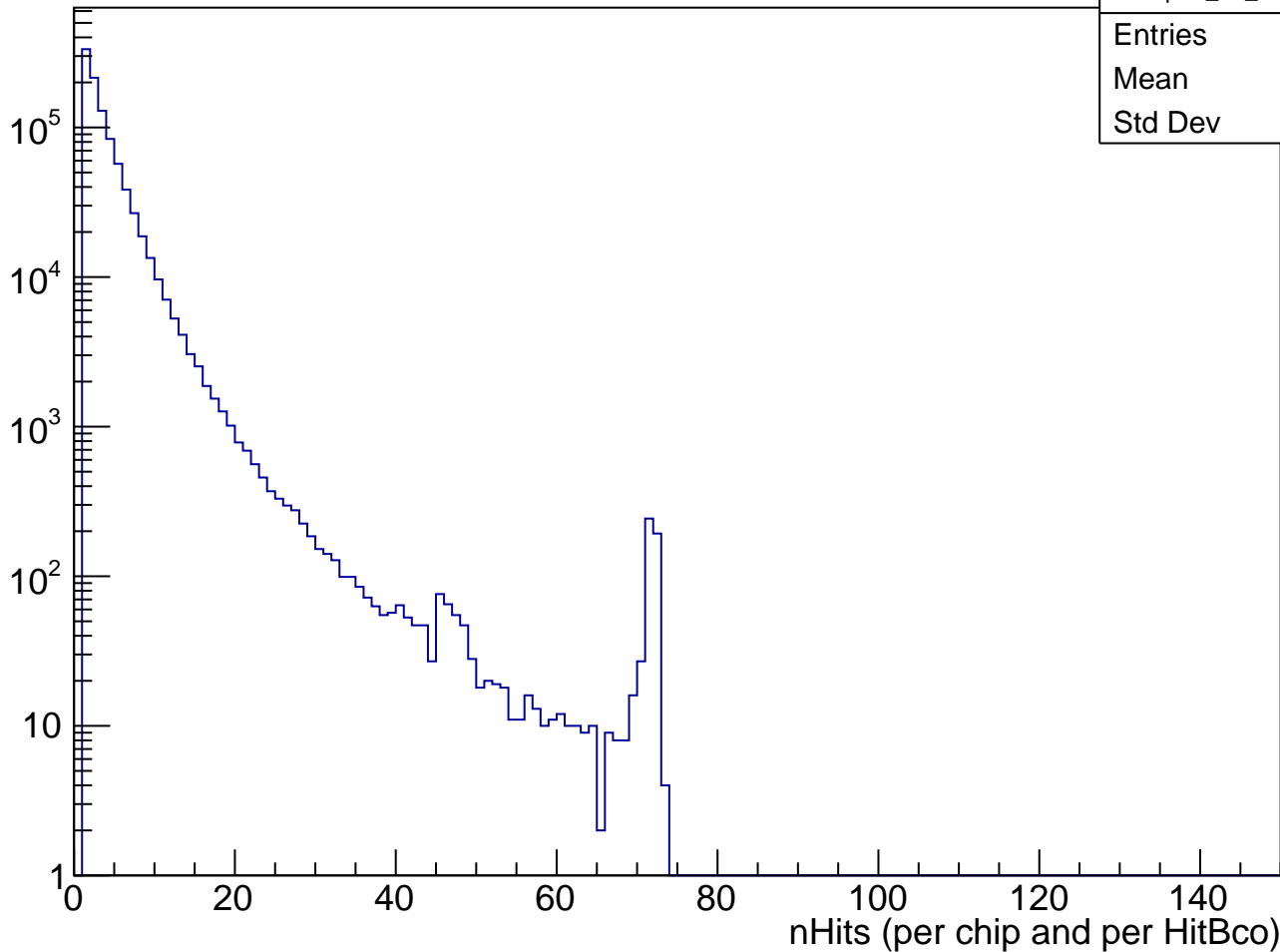
Entries



nChipHit_F5_Fch1_Chip7	
Entries	948189
Mean	3.267
Std Dev	3.82

nChipHit_F5_Fch1_Chip8

Entries



nChipHit_F5_Fch1_Chip8

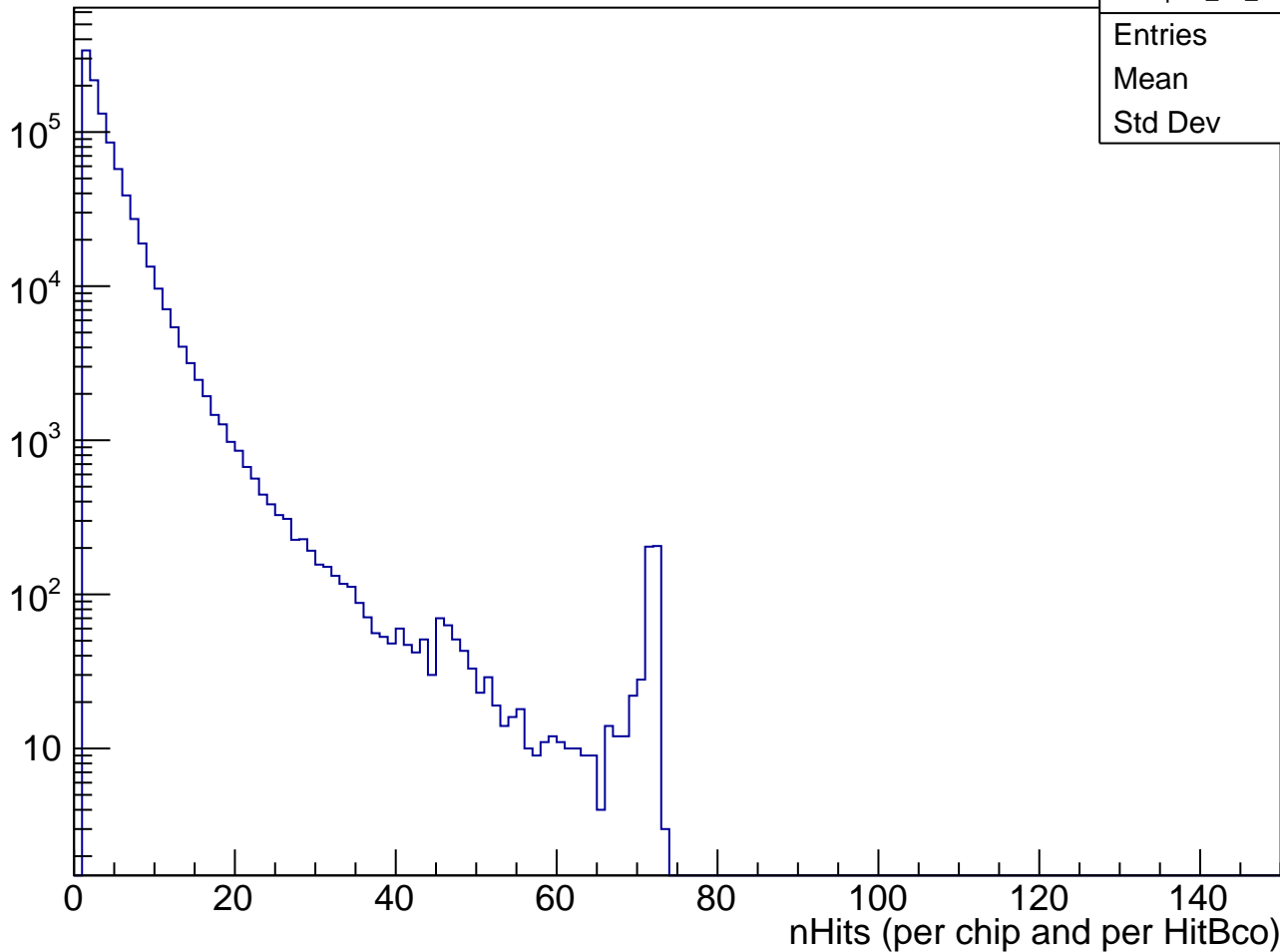
Entries 959788

Mean 3.26

Std Dev 3.765

nChipHit_F5_Fch1_Chip9

Entries



nChipHit_F5_Fch1_Chip9

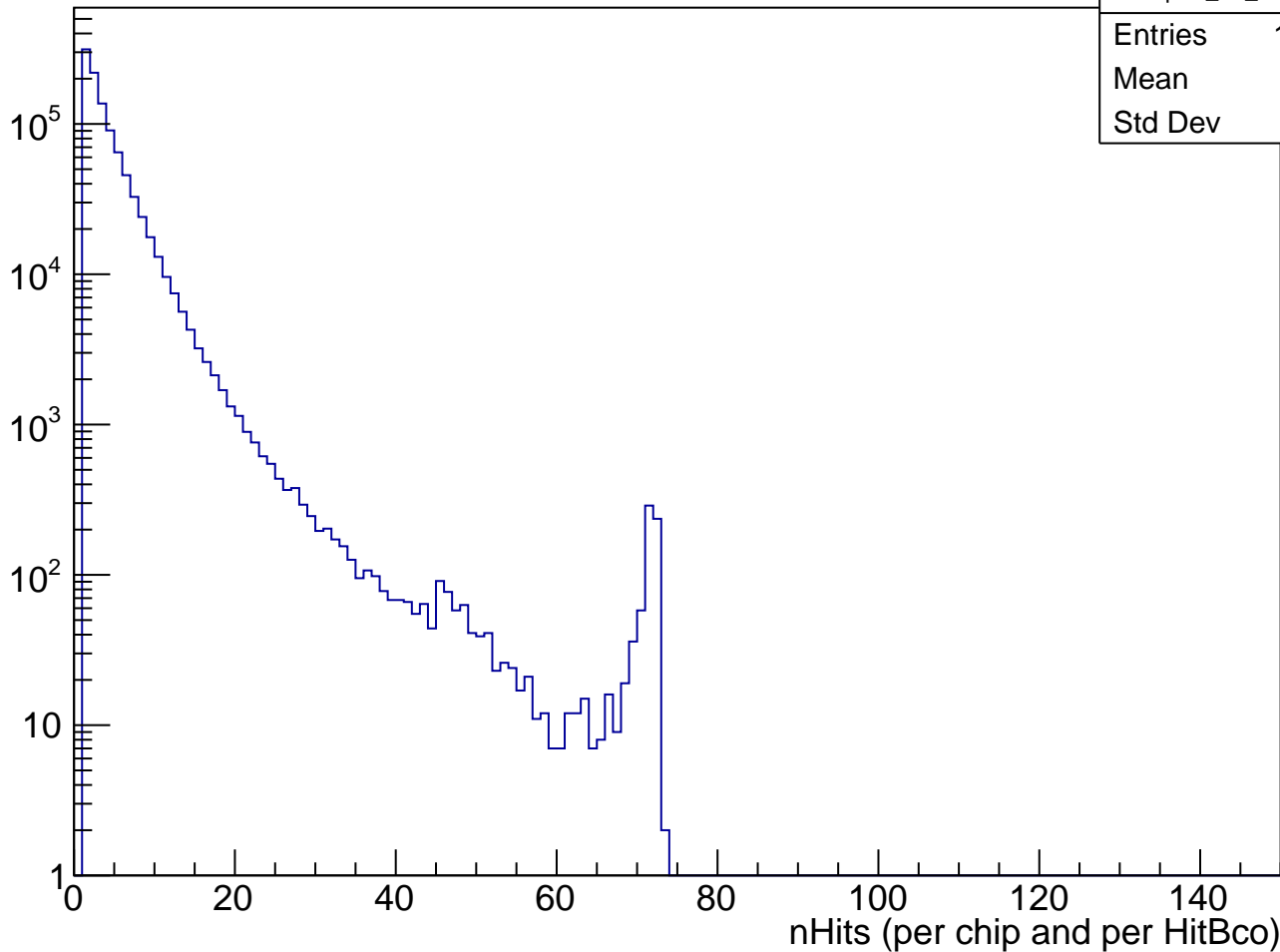
Entries 972560

Mean 3.252

Std Dev 3.744

nChipHit_F5_Fch2_Chip0

Entries



nChipHit_F5_Fch2_Chip0

Entries 1004311

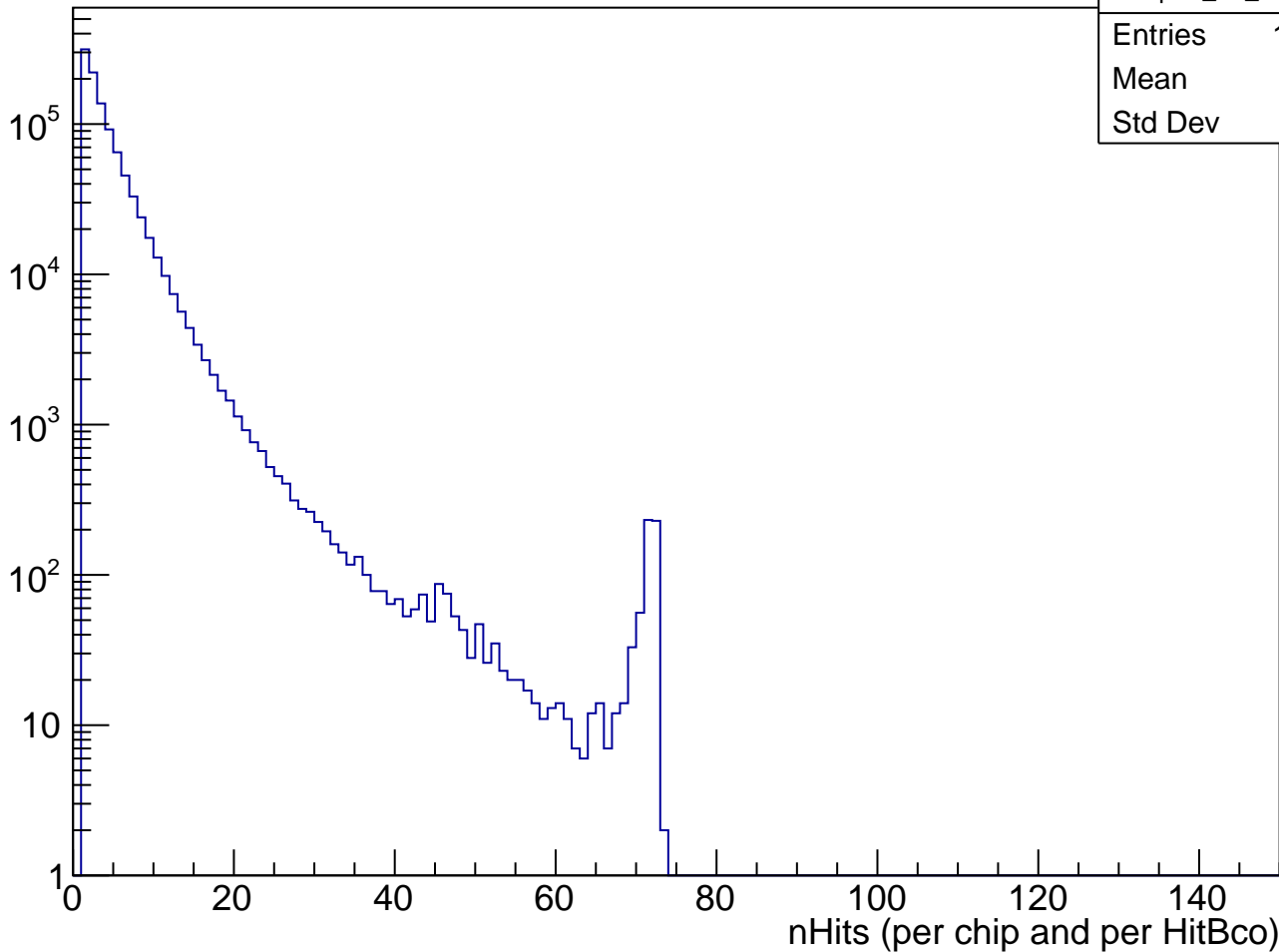
Mean 3.592

Std Dev 4.128

nHits (per chip and per HitBco)

nChipHit_F5_Fch2_Chip1

Entries

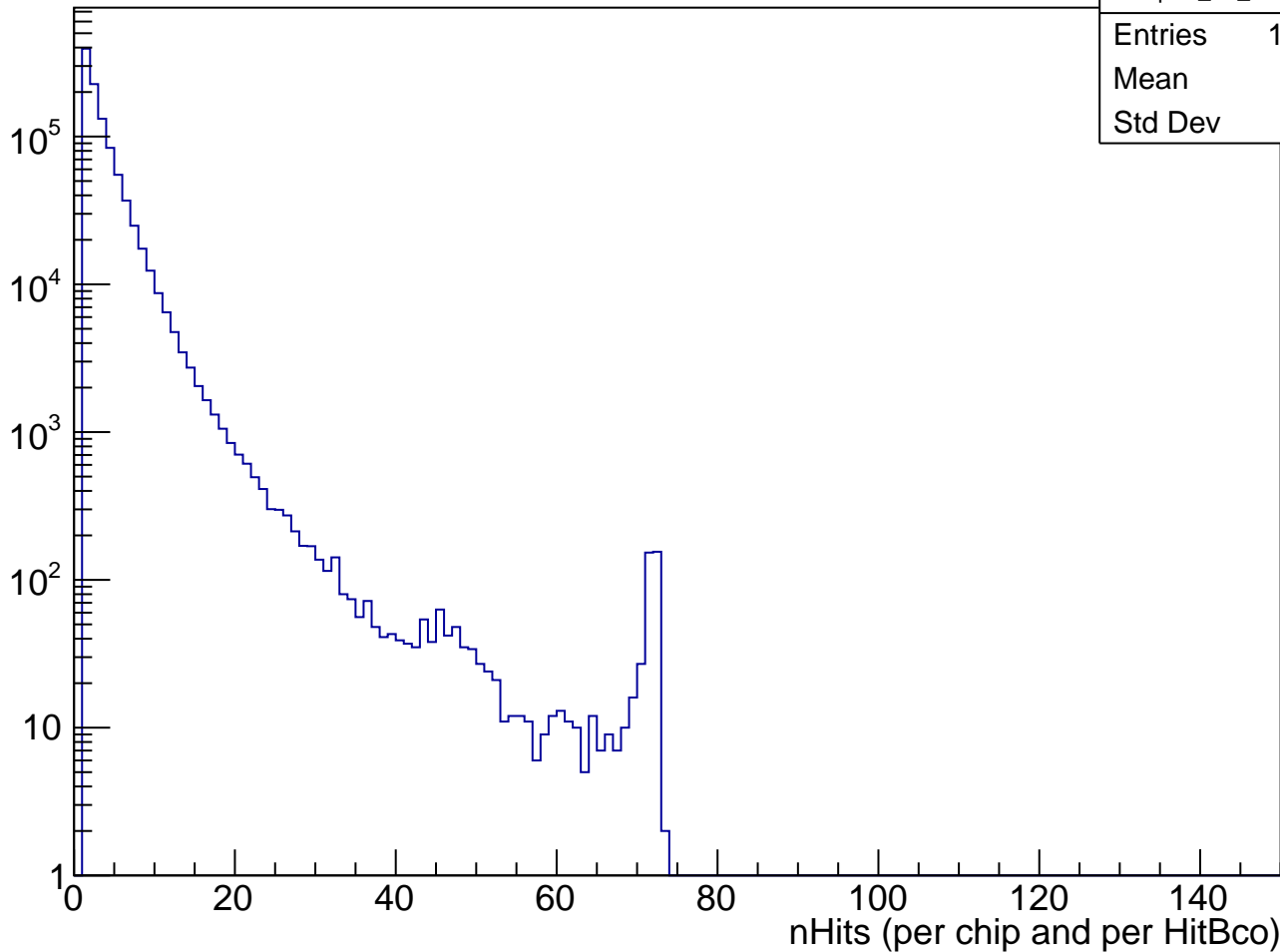


nChipHit_F5_Fch2_Chip1	
Entries	1008841
Mean	3.588
Std Dev	4.079

nHits (per chip and per HitBco)

nChipHit_F5_Fch2_Chip10

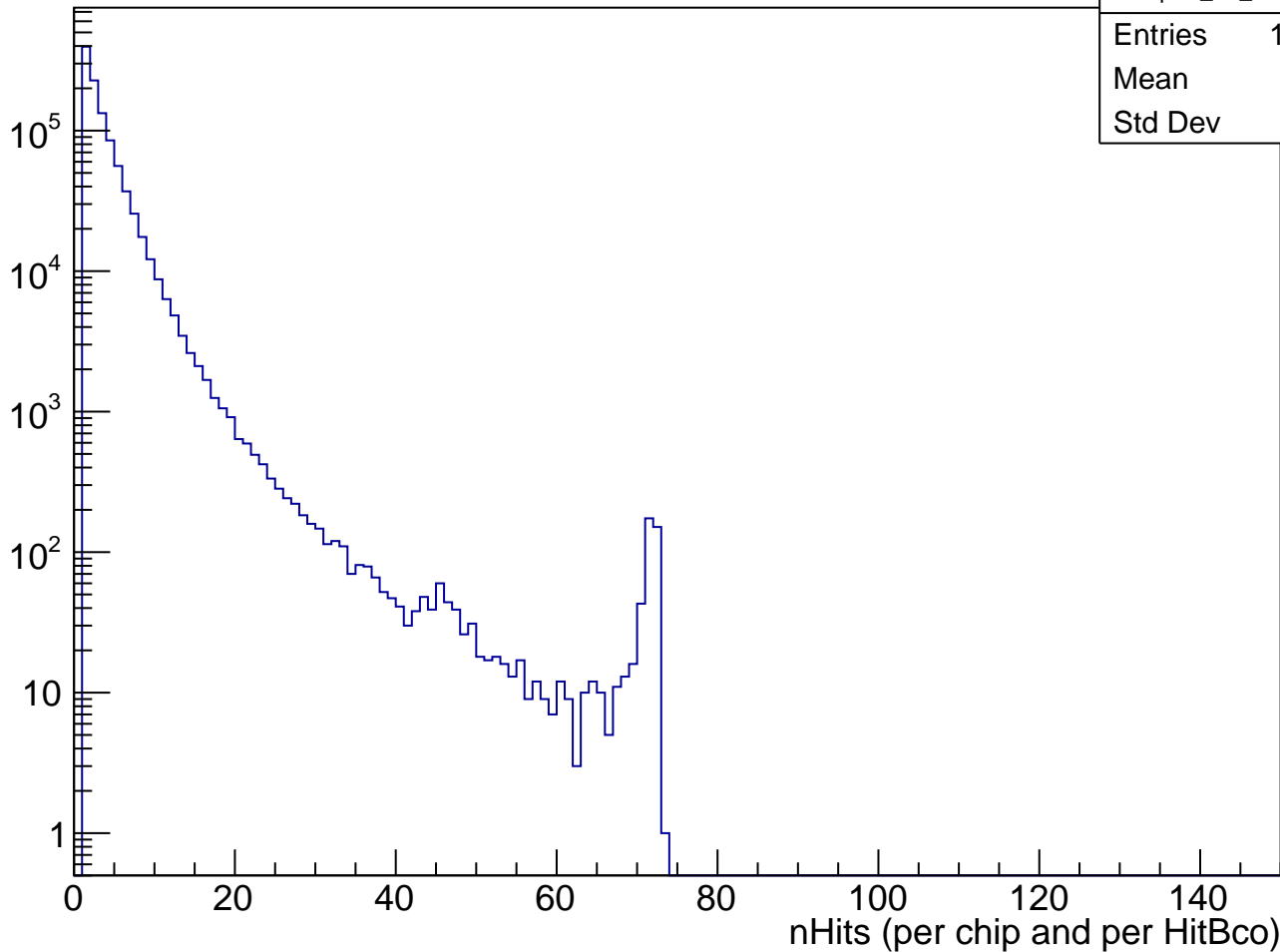
Entries



nChipHit_F5_Fch2_Chip10	
Entries	1020564
Mean	3.019
Std Dev	3.46

nChipHit_F5_Fch2_Chip11

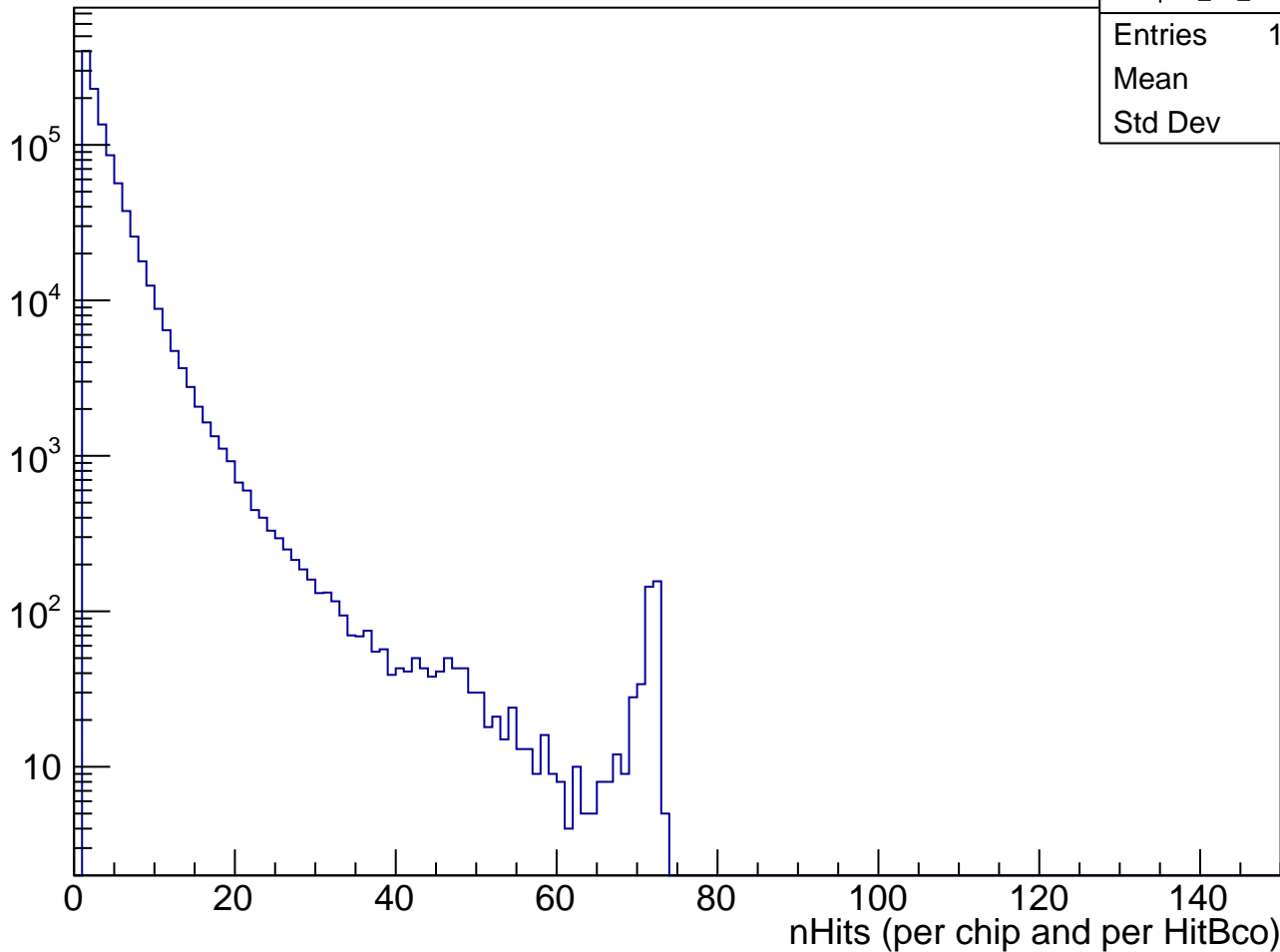
Entries



nChipHit_F5_Fch2_Chip11	
Entries	1028553
Mean	3.019
Std Dev	3.471

nChipHit_F5_Fch2_Chip12

Entries

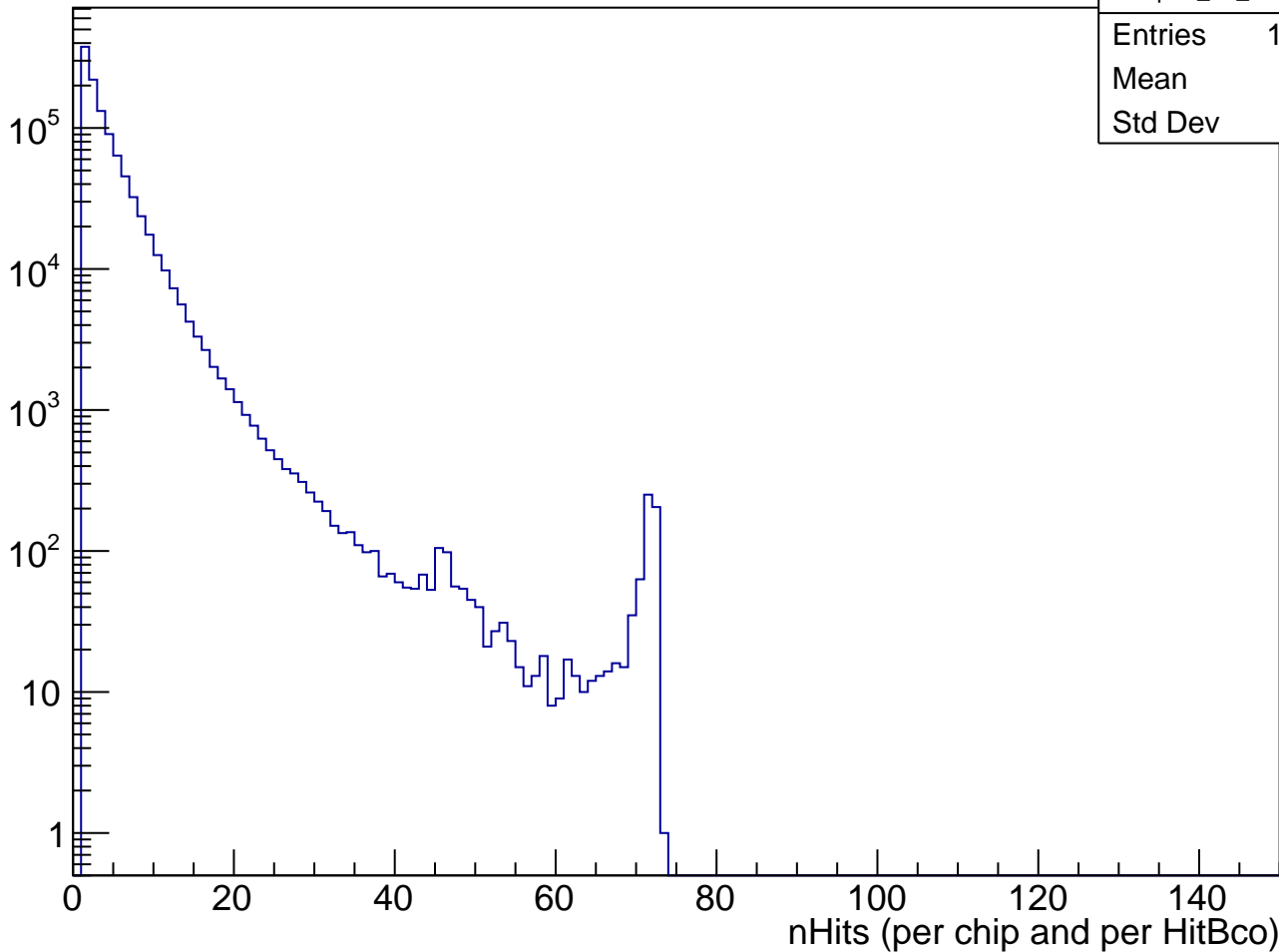


nChipHit_F5_Fch2_Chip12	
Entries	1042335
Mean	3.014
Std Dev	3.454

nHits (per chip and per HitBco)

nChipHit_F5_Fch2_Chip13

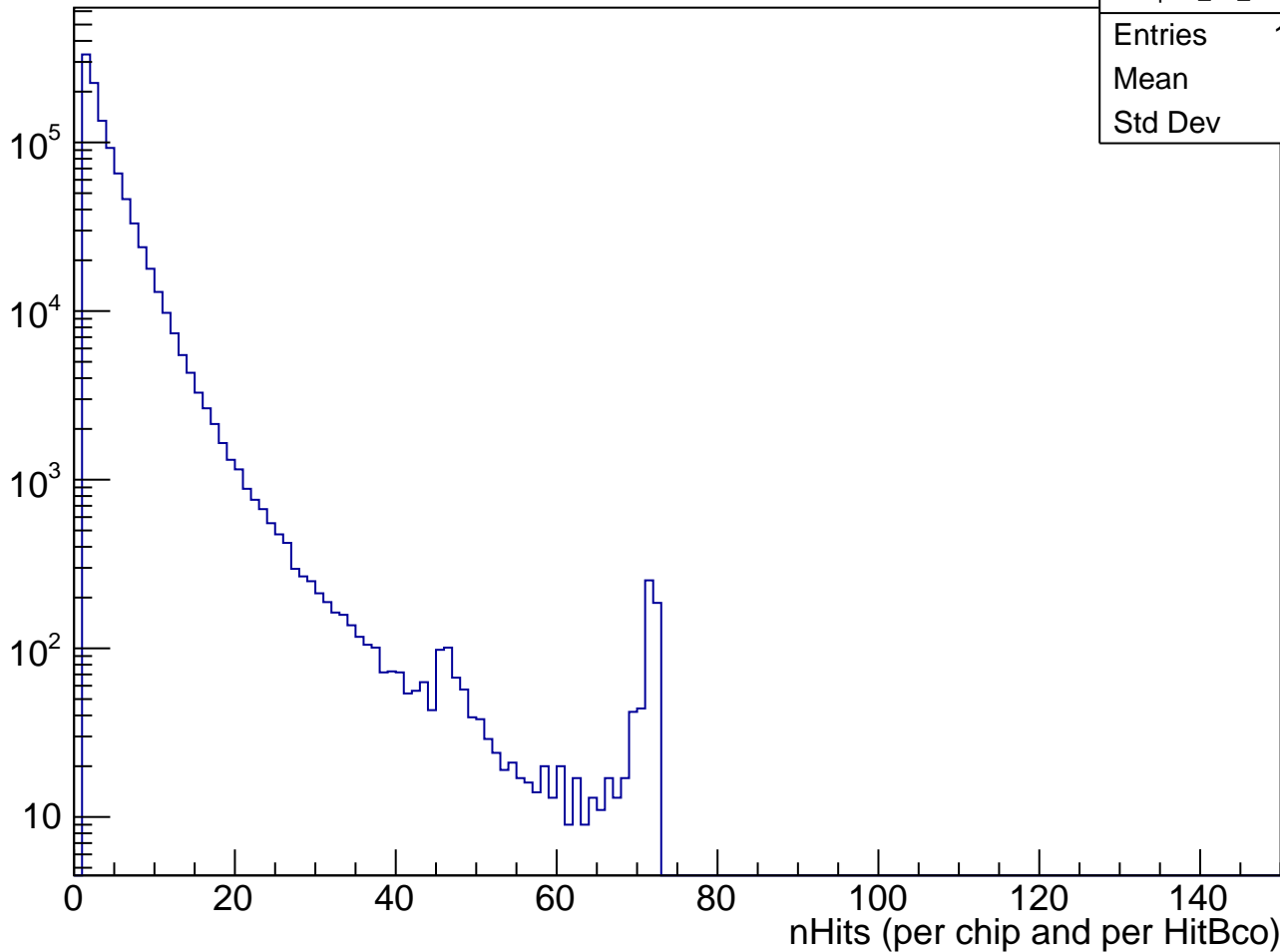
Entries



nChipHit_F5_Fch2_Chip13	
Entries	1060999
Mean	3.429
Std Dev	4.03

nChipHit_F5_Fch2_Chip14

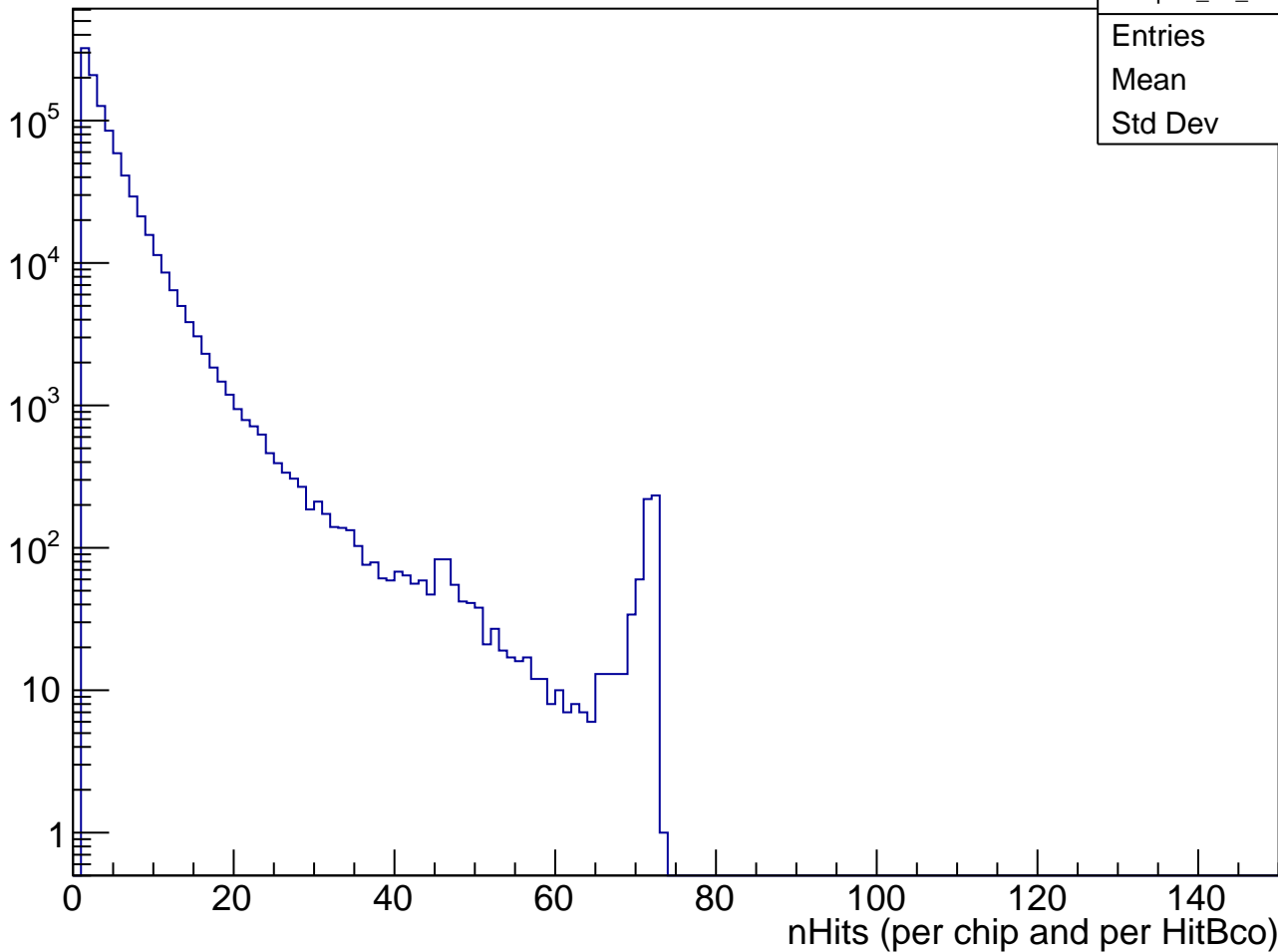
Entries



nChipHit_F5_Fch2_Chip14	
Entries	1030451
Mean	3.538
Std Dev	4.056

nChipHit_F5_Fch2_Chip15

Entries

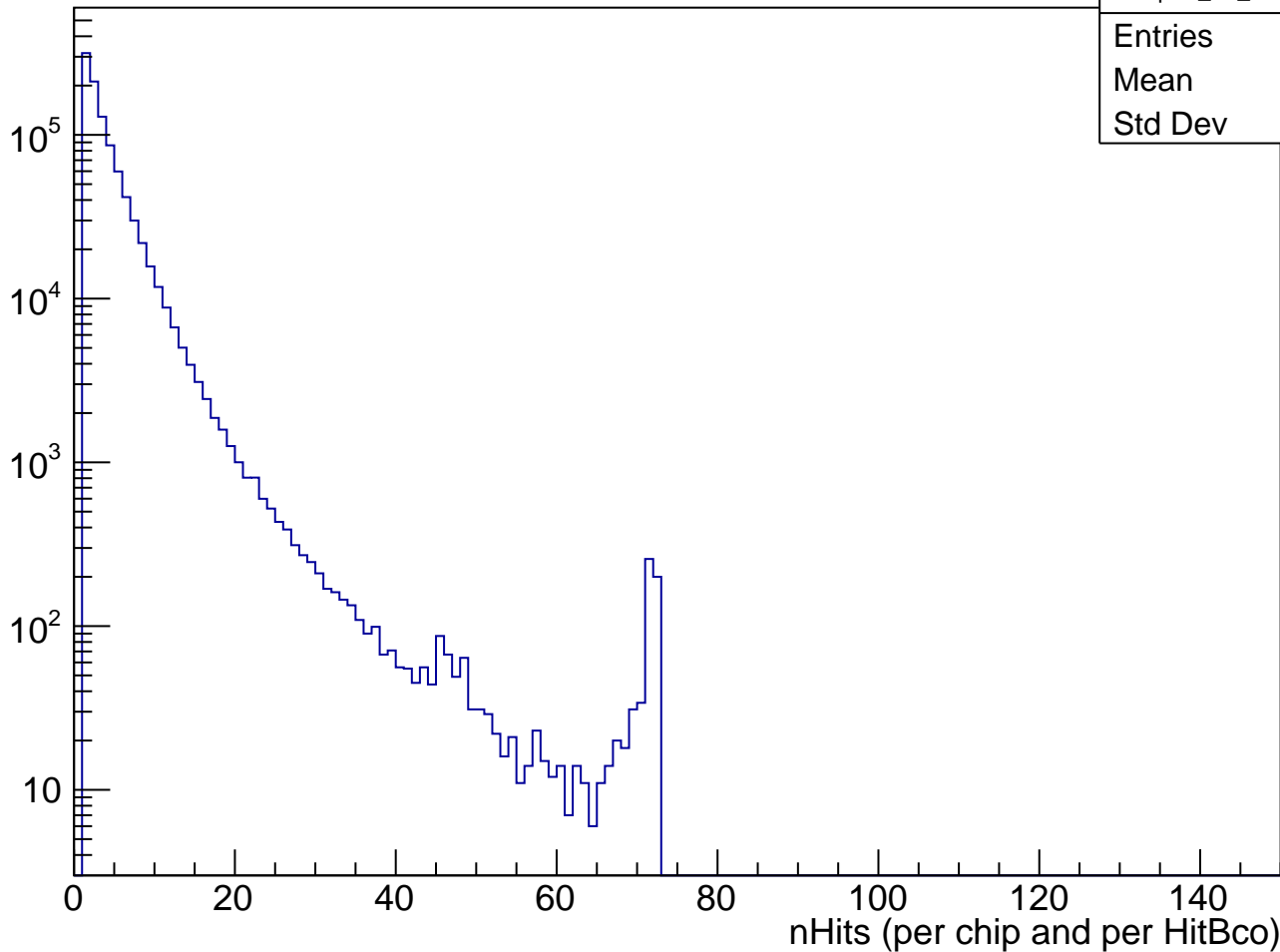


nChipHit_F5_Fch2_Chip15	
Entries	961347
Mean	3.454
Std Dev	4.019

nHits (per chip and per HitBco)

nChipHit_F5_Fch2_Chip16

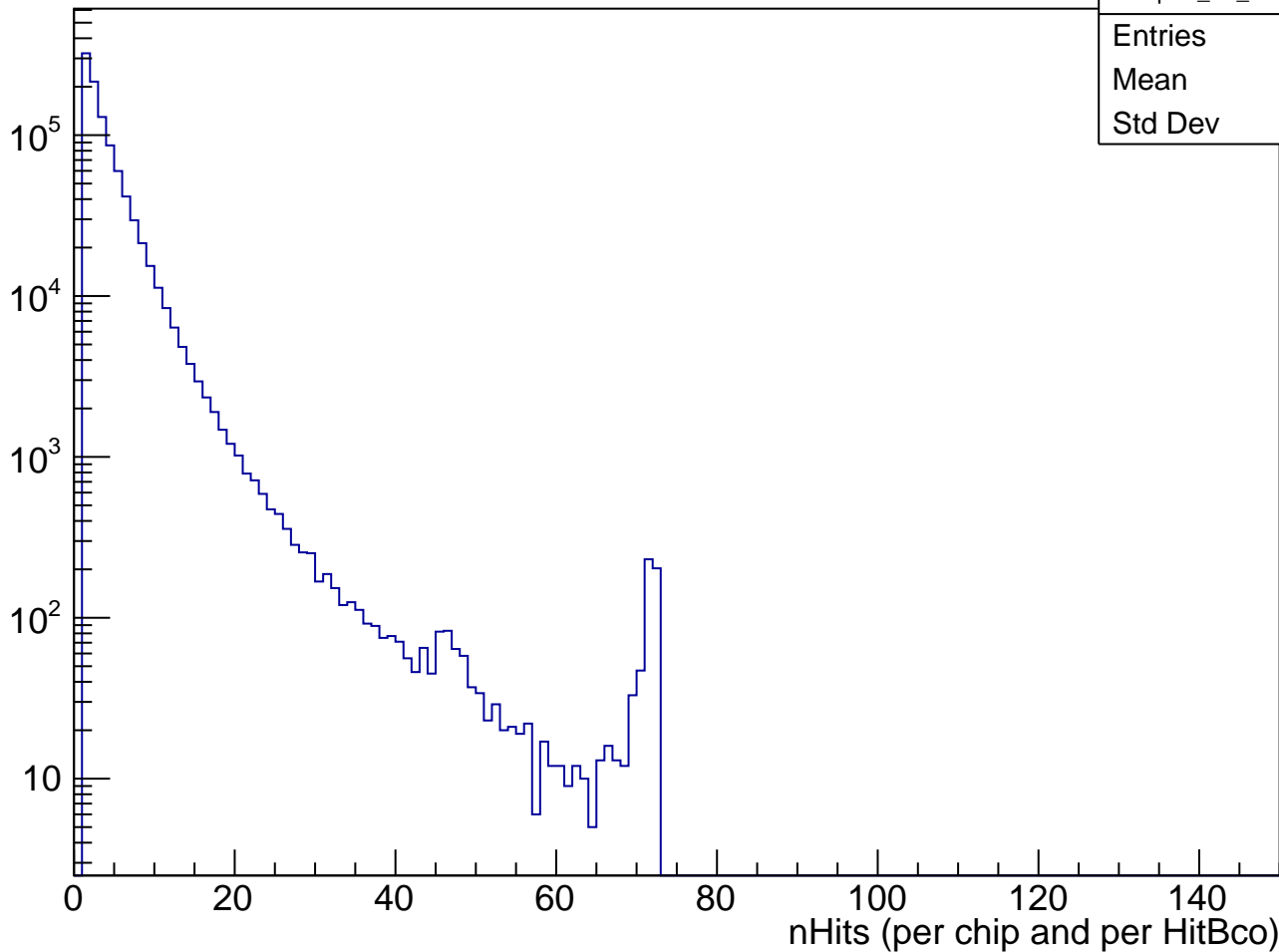
Entries



nChipHit_F5_Fch2_Chip16	
Entries	966371
Mean	3.496
Std Dev	4.048

nChipHit_F5_Fch2_Chip17

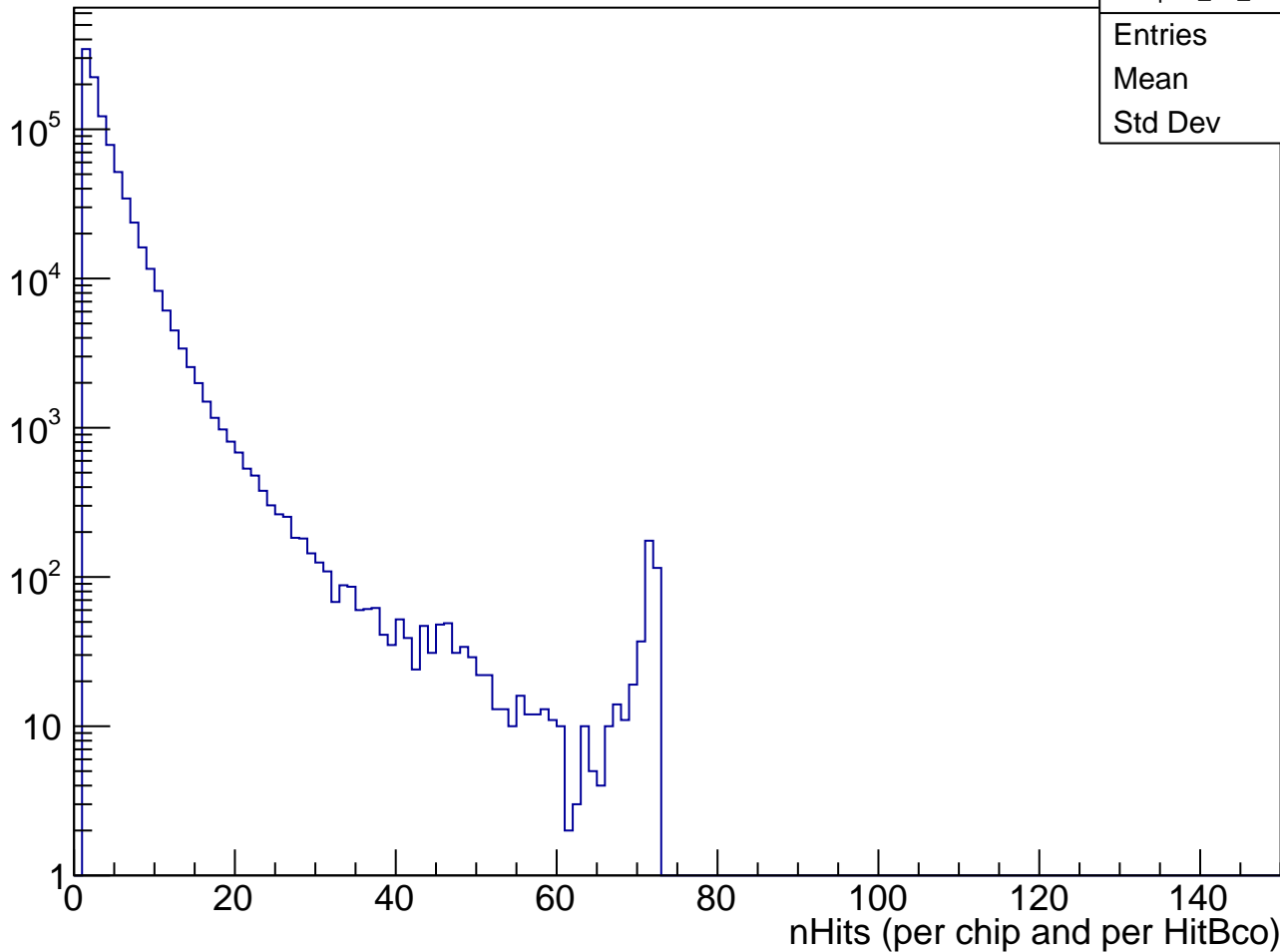
Entries



nChipHit_F5_Fch2_Chip17	
Entries	973662
Mean	3.444
Std Dev	4

nChipHit_F5_Fch2_Chip18

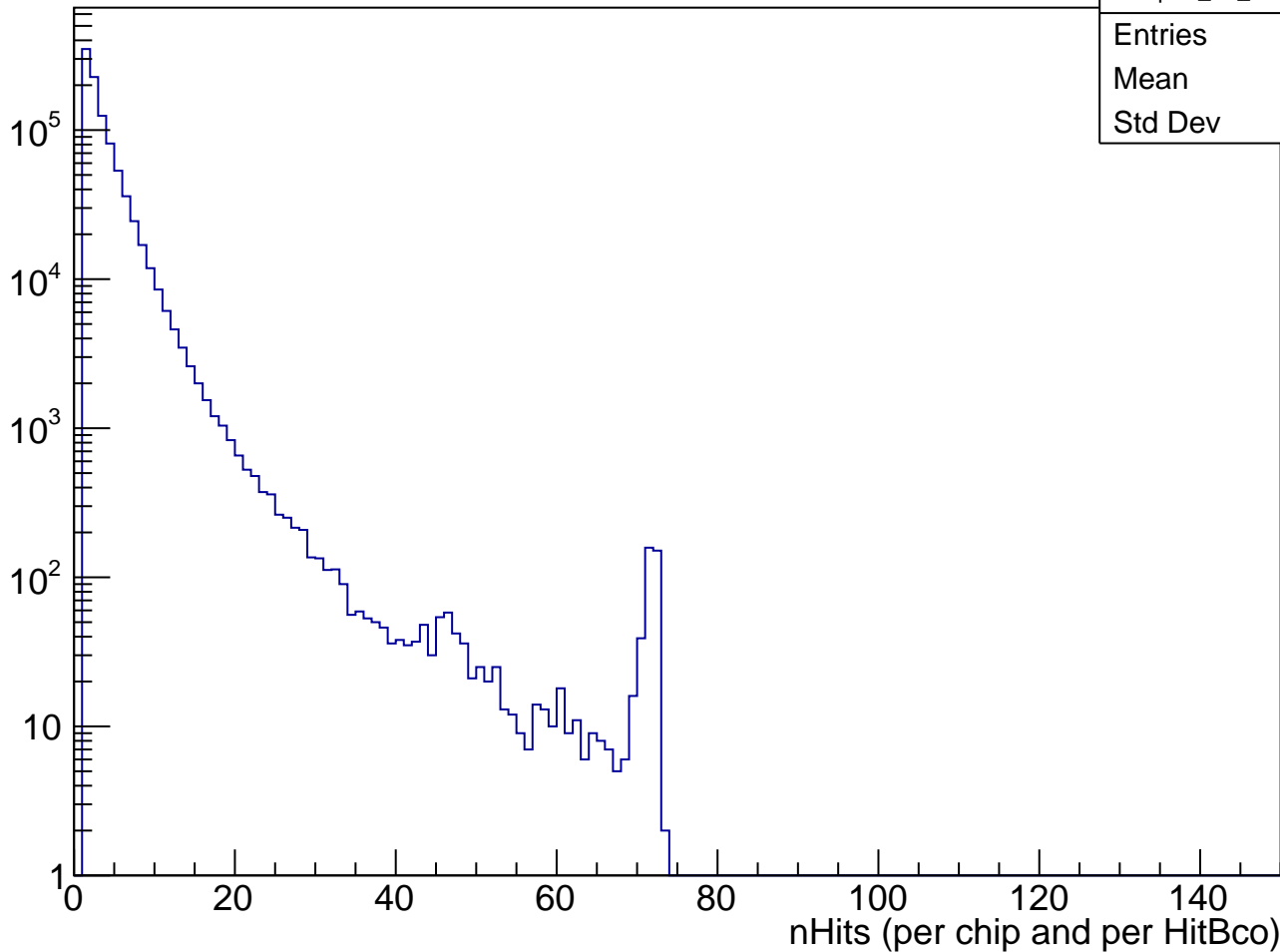
Entries



nChipHit_F5_Fch2_Chip18	
Entries	942742
Mean	3.059
Std Dev	3.476

nChipHit_F5_Fch2_Chip19

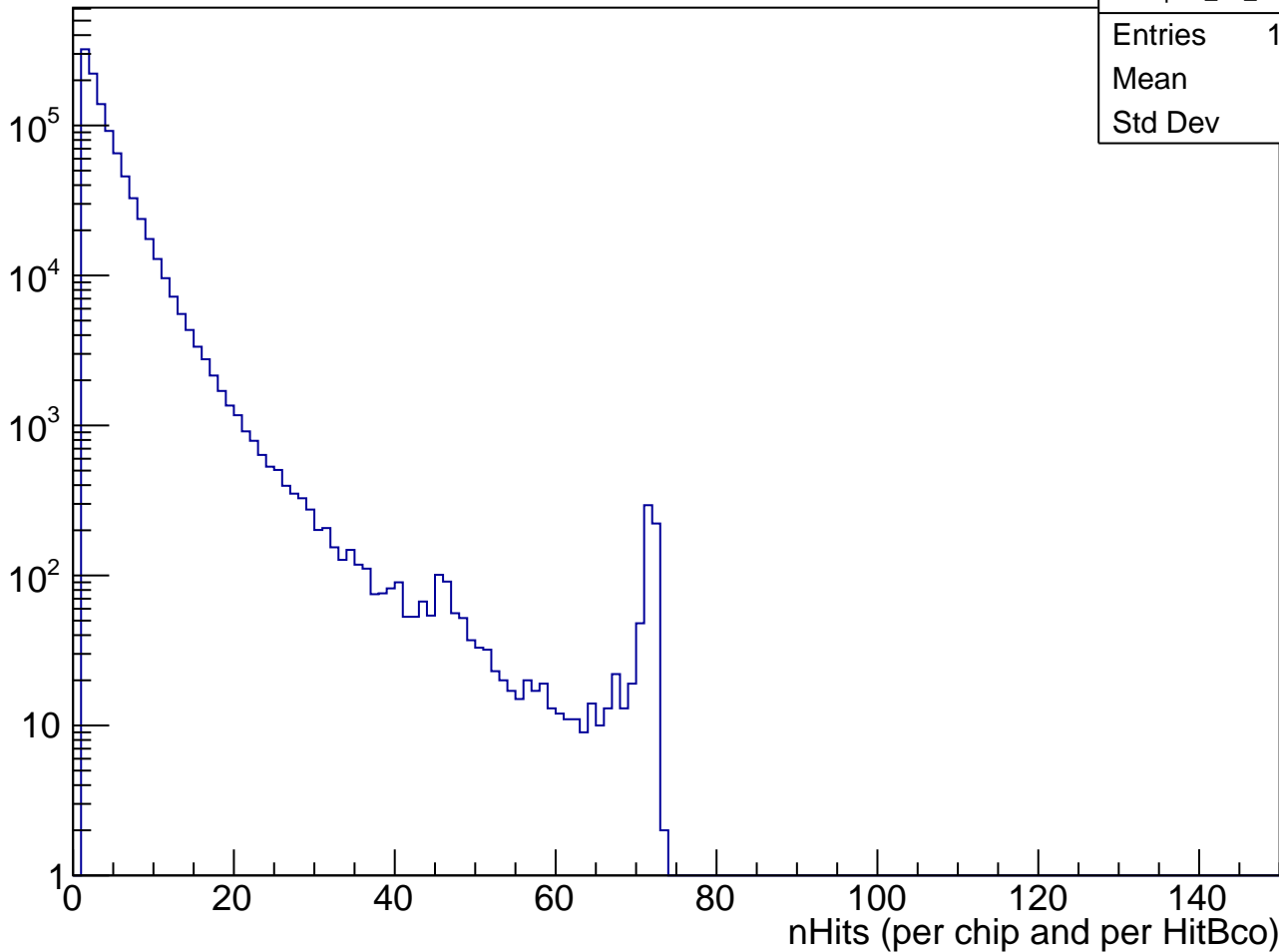
Entries



nChipHit_F5_Fch2_Chip19	
Entries	962338
Mean	3.076
Std Dev	3.495

nChipHit_F5_Fch2_Chip2

Entries



nChipHit_F5_Fch2_Chip2

Entries 1018896

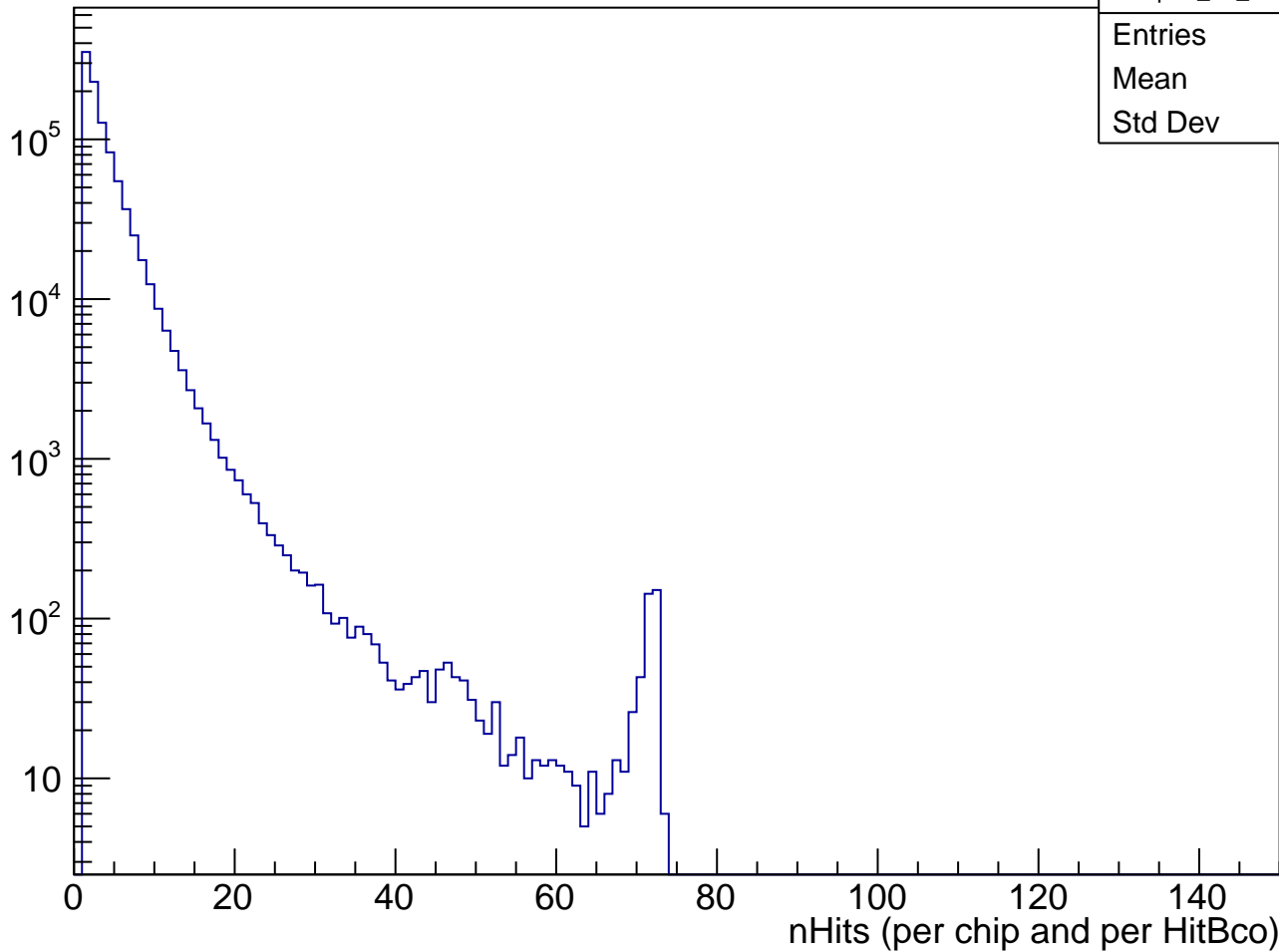
Mean 3.57

Std Dev 4.108

nHits (per chip and per HitBco)

nChipHit_F5_Fch2_Chip20

Entries

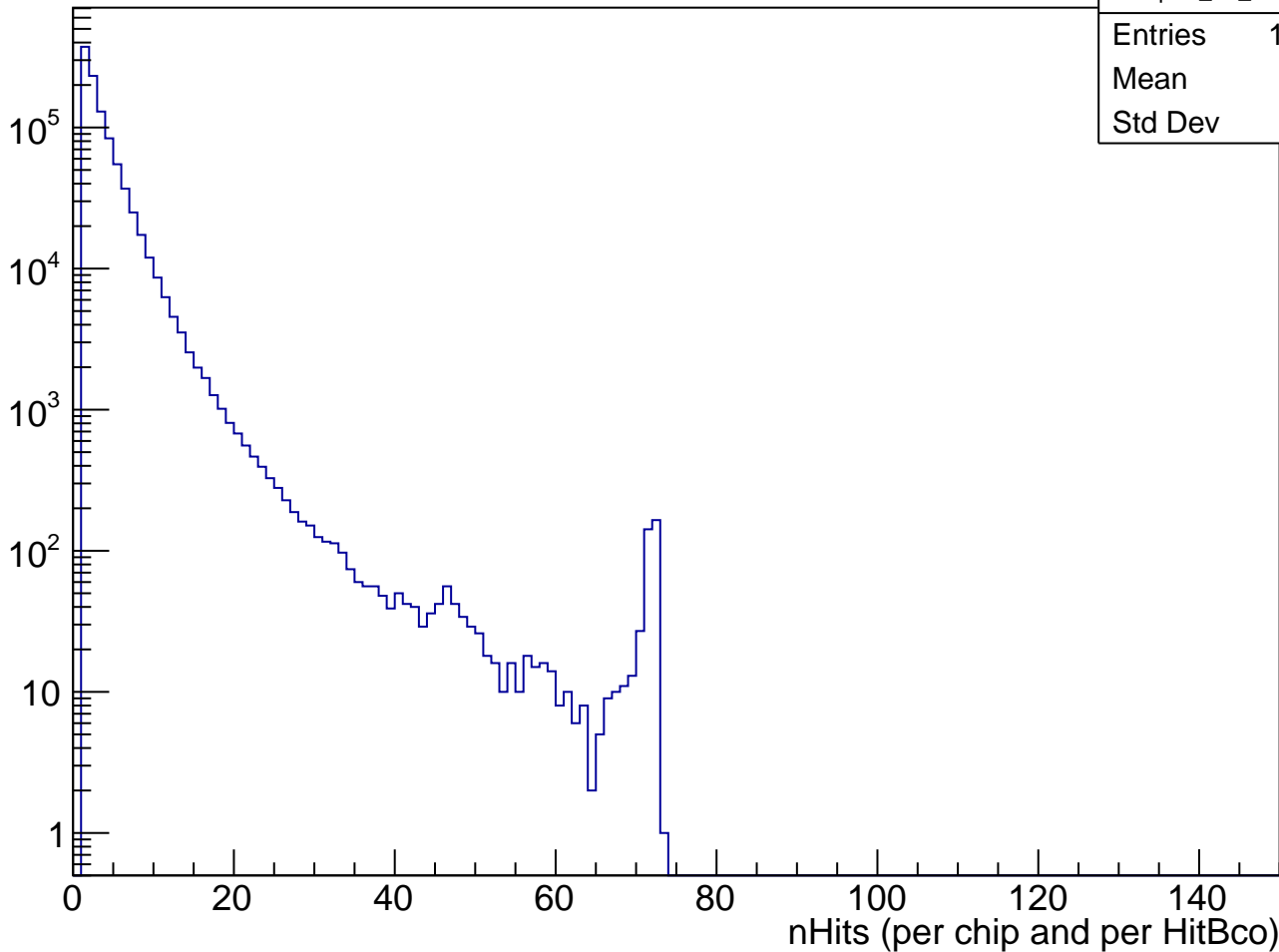


nChipHit_F5_Fch2_Chip20	
Entries	976148
Mean	3.103
Std Dev	3.541

nHits (per chip and per HitBco)

nChipHit_F5_Fch2_Chip21

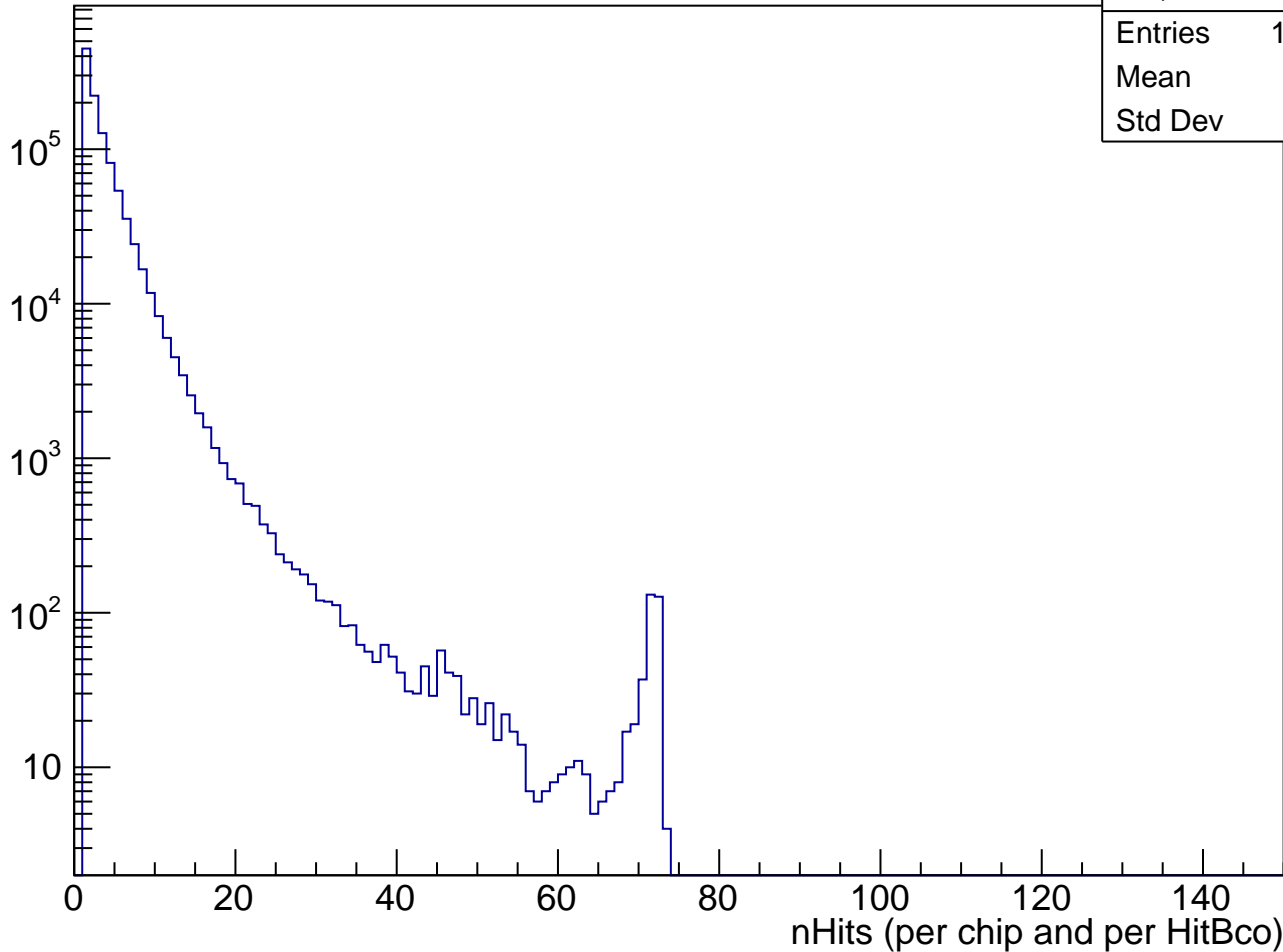
Entries



nChipHit_F5_Fch2_Chip21	
Entries	1002223
Mean	3.036
Std Dev	3.44

nChipHit_F5_Fch2_Chip22

Entries

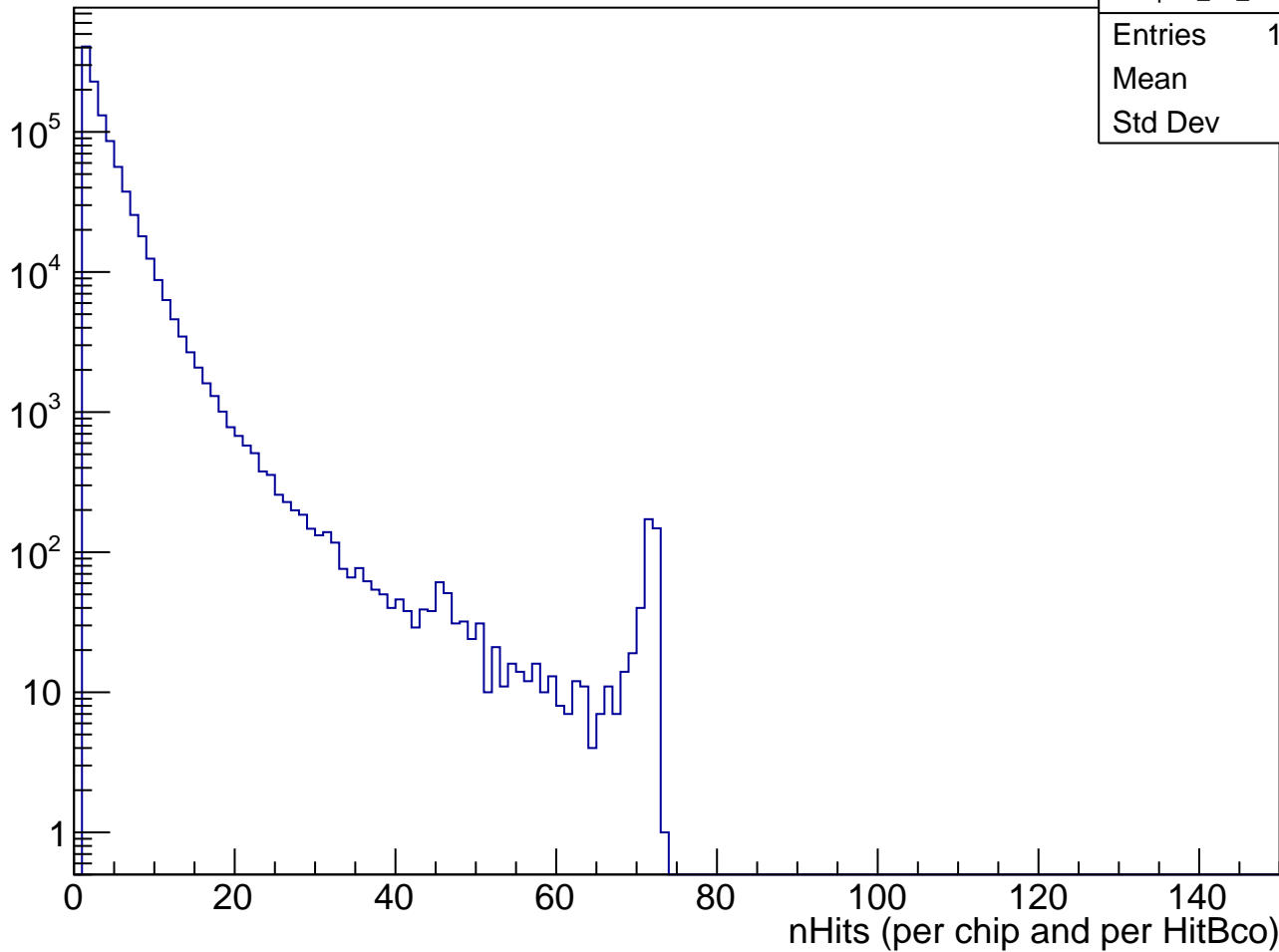


nChipHit_F5_Fch2_Chip22	
Entries	1056996
Mean	2.872
Std Dev	3.331

nHits (per chip and per HitBco)

nChipHit_F5_Fch2_Chip23

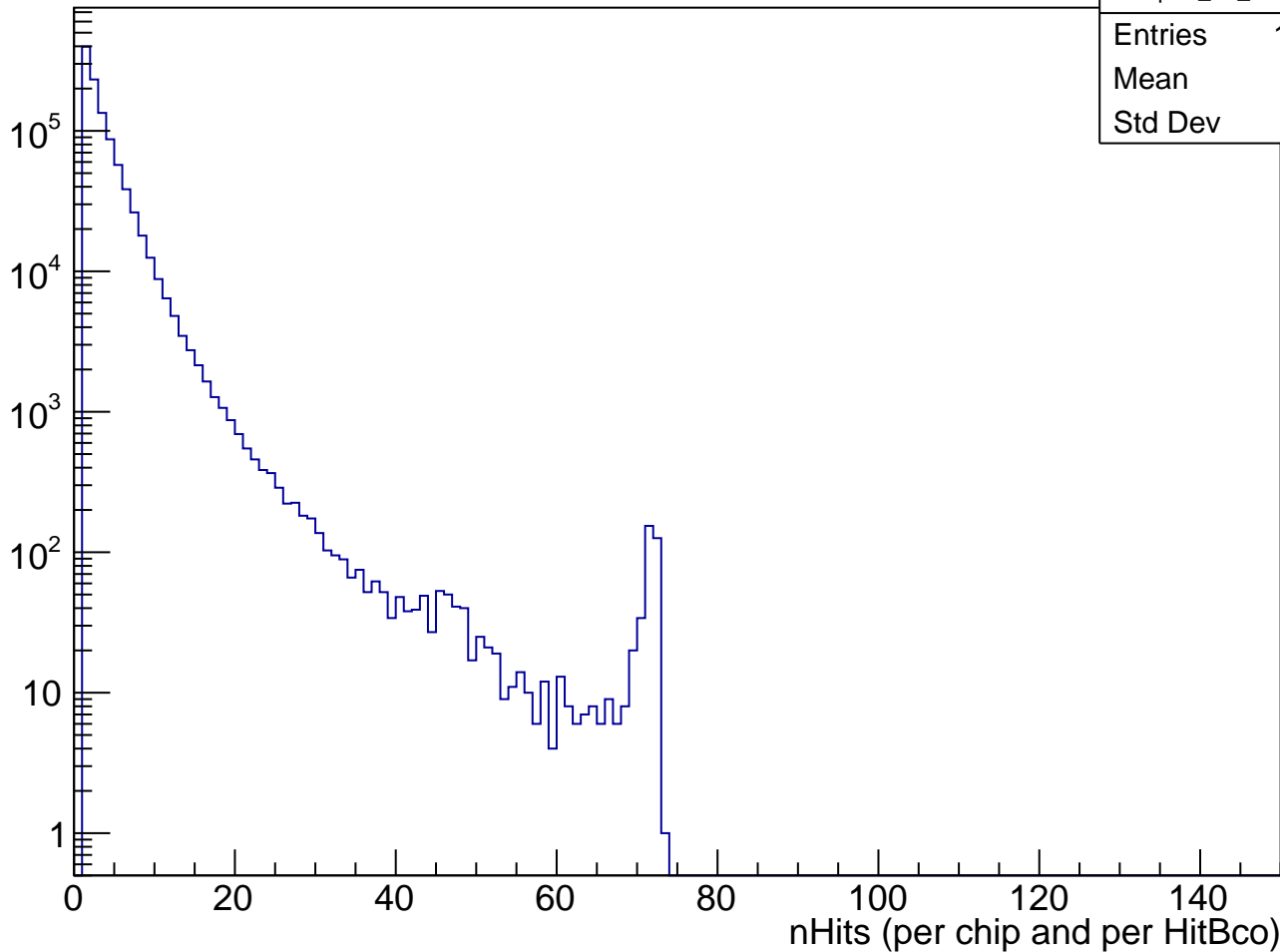
Entries



nChipHit_F5_Fch2_Chip23	
Entries	1040516
Mean	2.994
Std Dev	3.436

nChipHit_F5_Fch2_Chip24

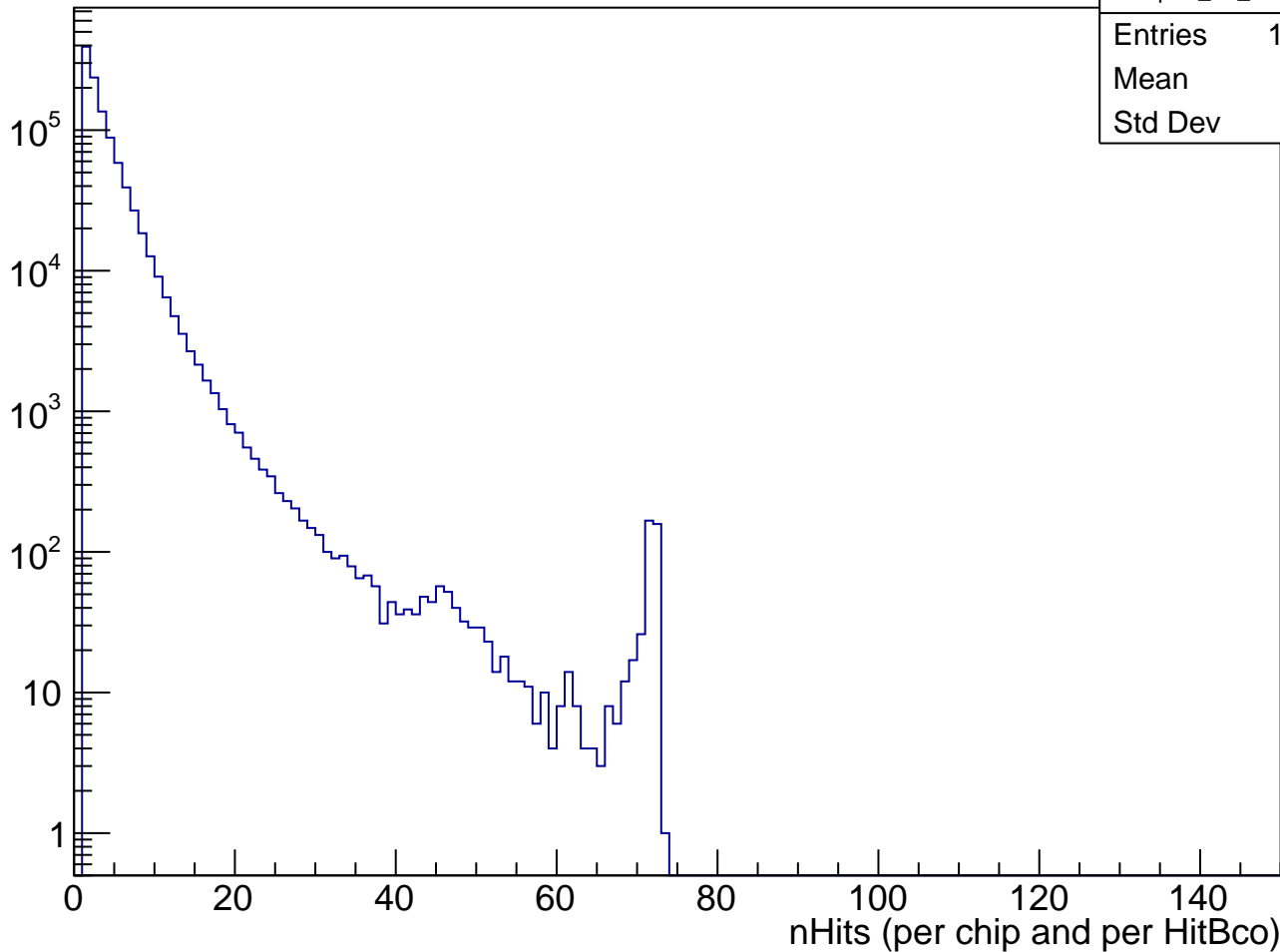
Entries



nChipHit_F5_Fch2_Chip24	
Entries	1042141
Mean	3.019
Std Dev	3.4

nChipHit_F5_Fch2_Chip25

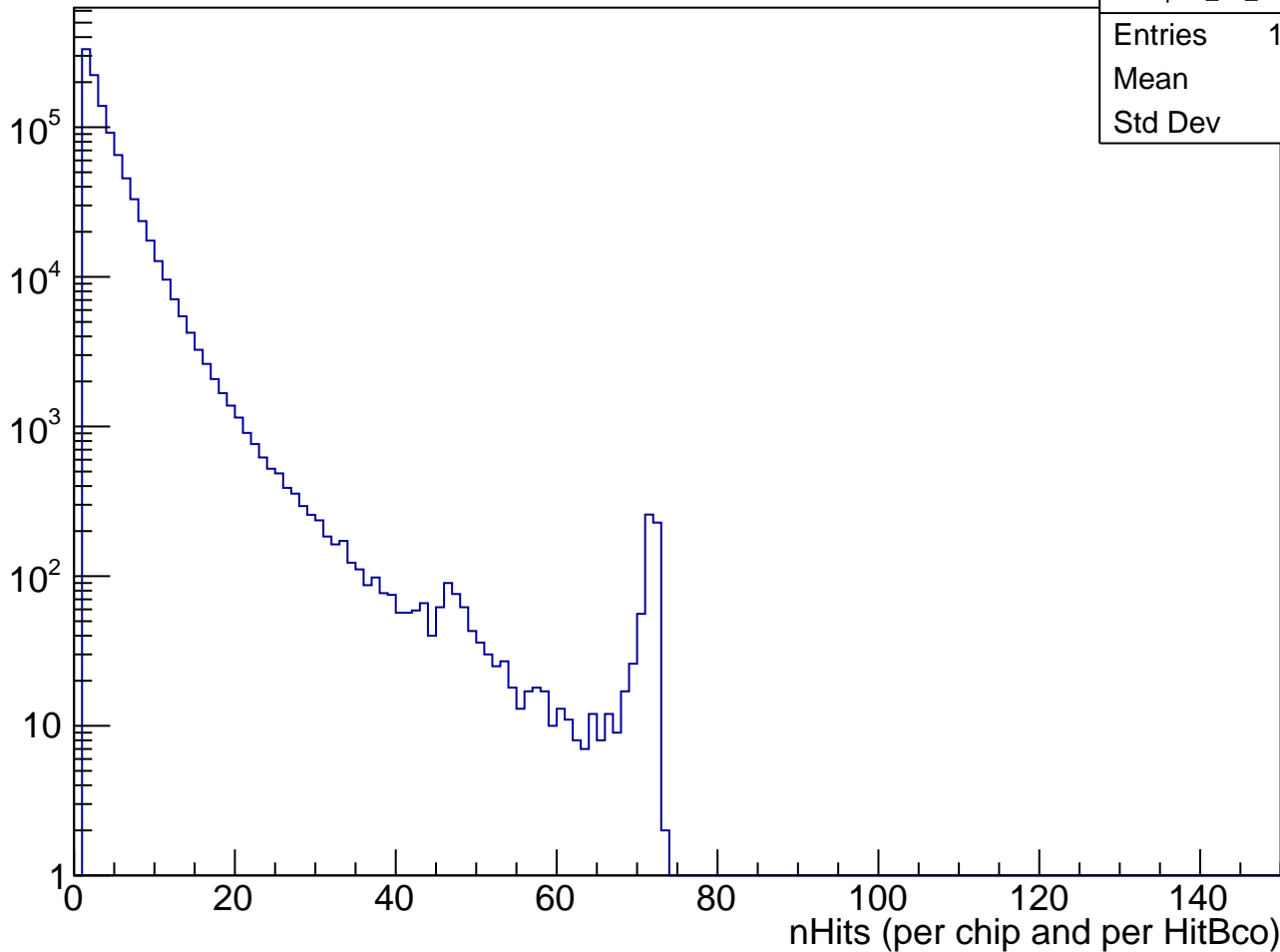
Entries



nChipHit_F5_Fch2_Chip25	
Entries	1047462
Mean	3.039
Std Dev	3.414

nChipHit_F5_Fch2_Chip3

Entries



nChipHit_F5_Fch2_Chip3

Entries 1029482

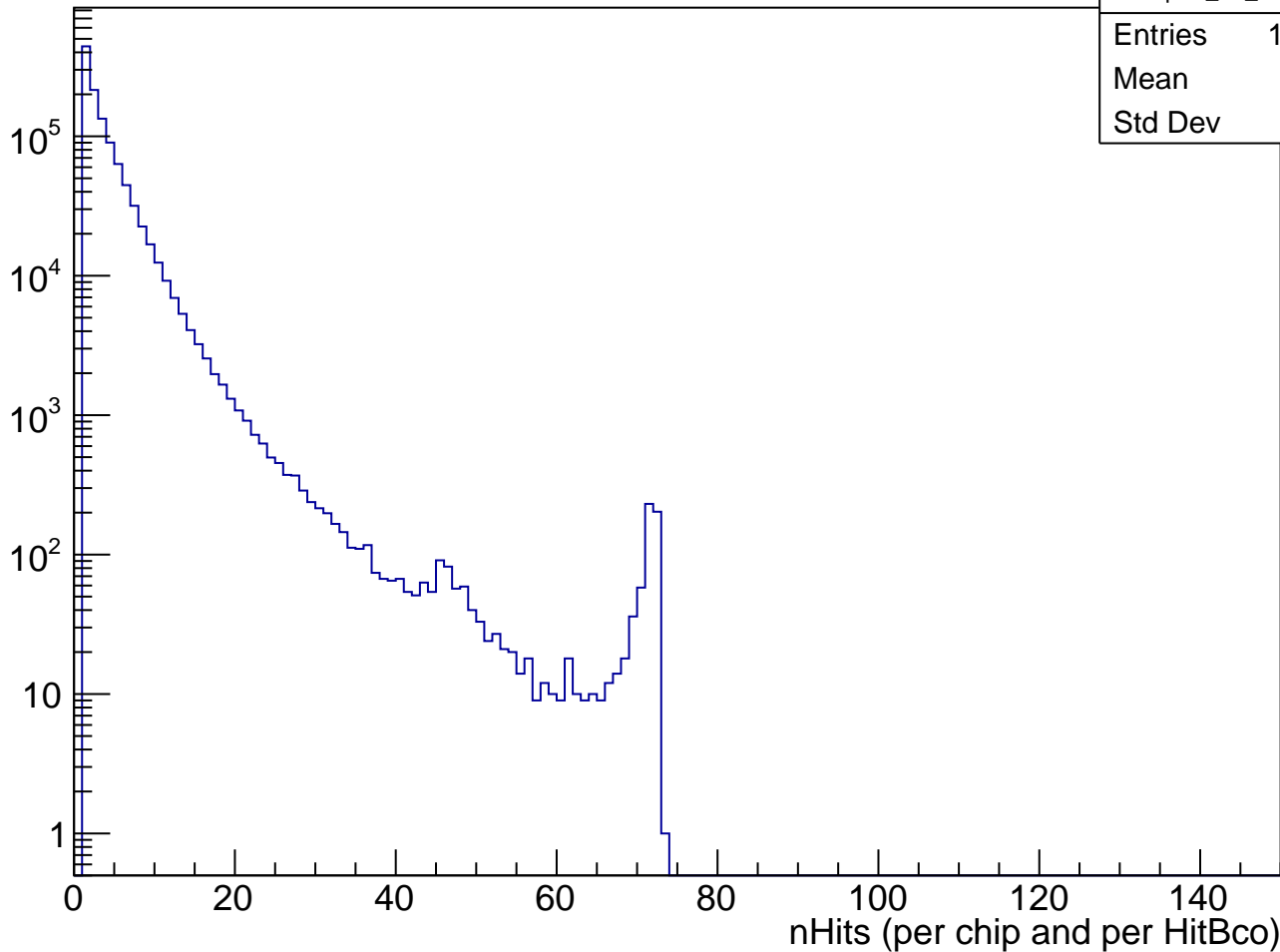
Mean 3.528

Std Dev 4.056

nHits (per chip and per HitBco)

nChipHit_F5_Fch2_Chip4

Entries



nChipHit_F5_Fch2_Chip4

Entries 1116514

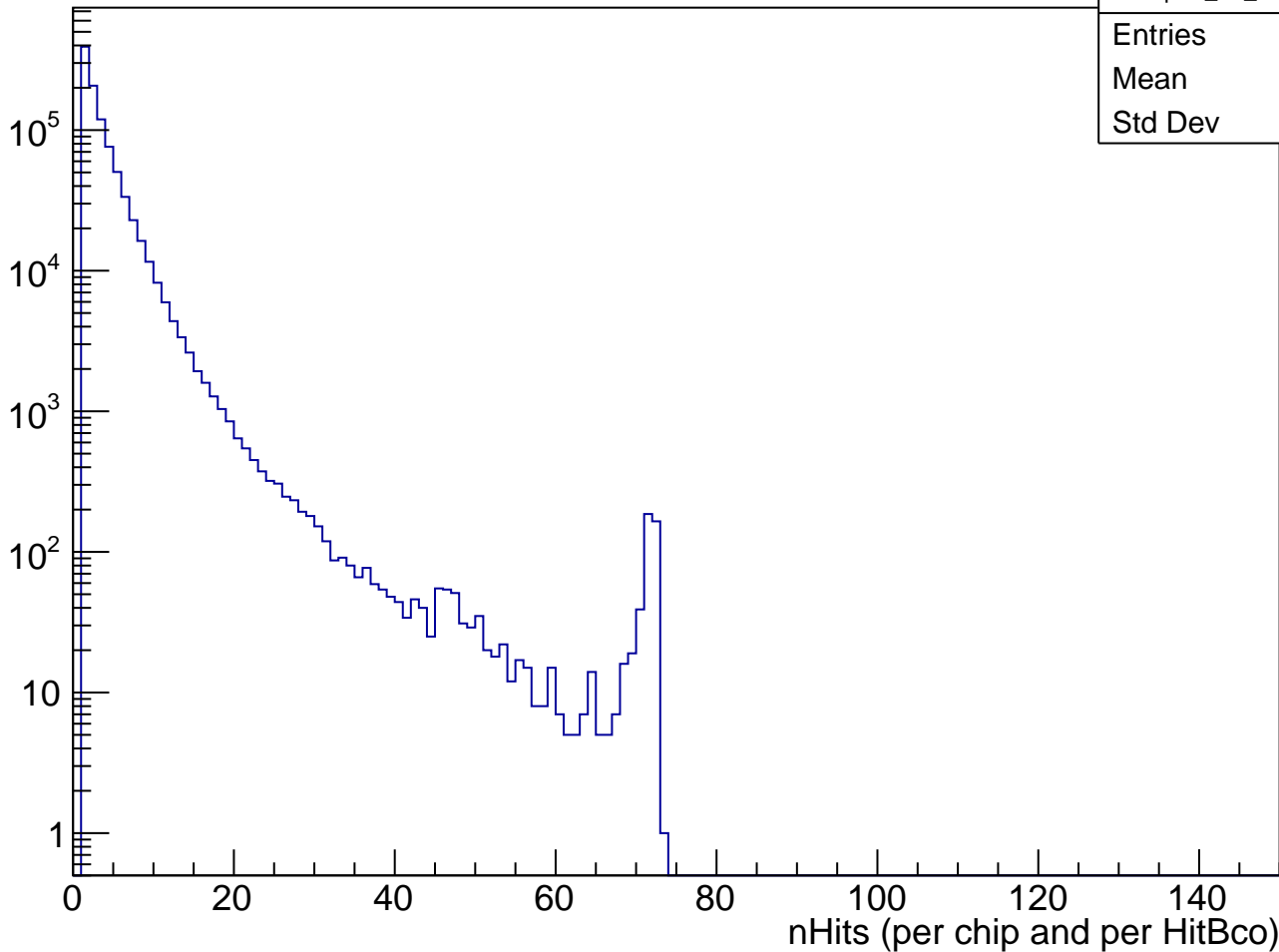
Mean 3.258

Std Dev 3.903

nHits (per chip and per HitBco)

nChipHit_F5_Fch2_Chip5

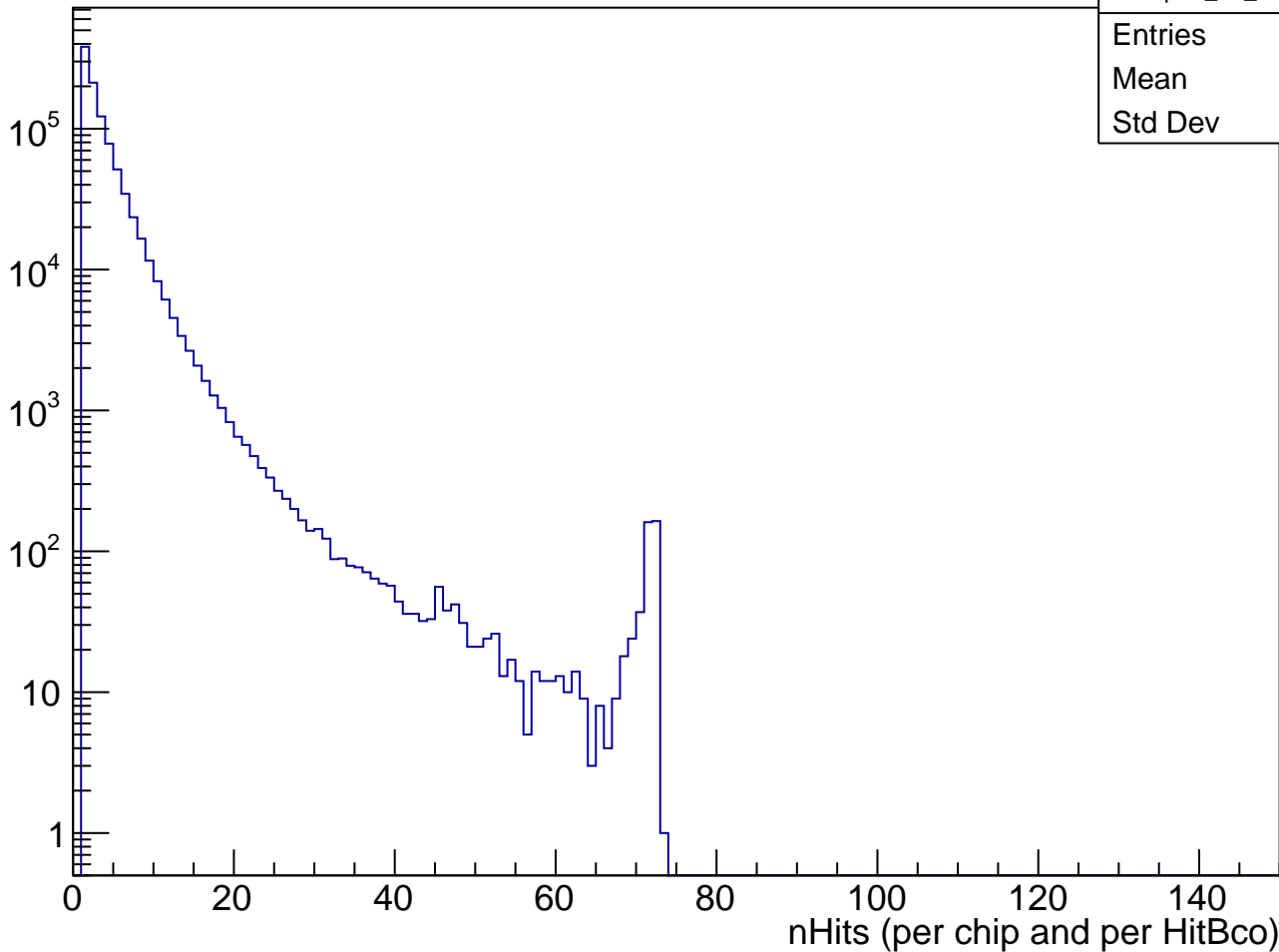
Entries



nChipHit_F5_Fch2_Chip5	
Entries	965428
Mean	2.98
Std Dev	3.548

nChipHit_F5_Fch2_Chip6

Entries



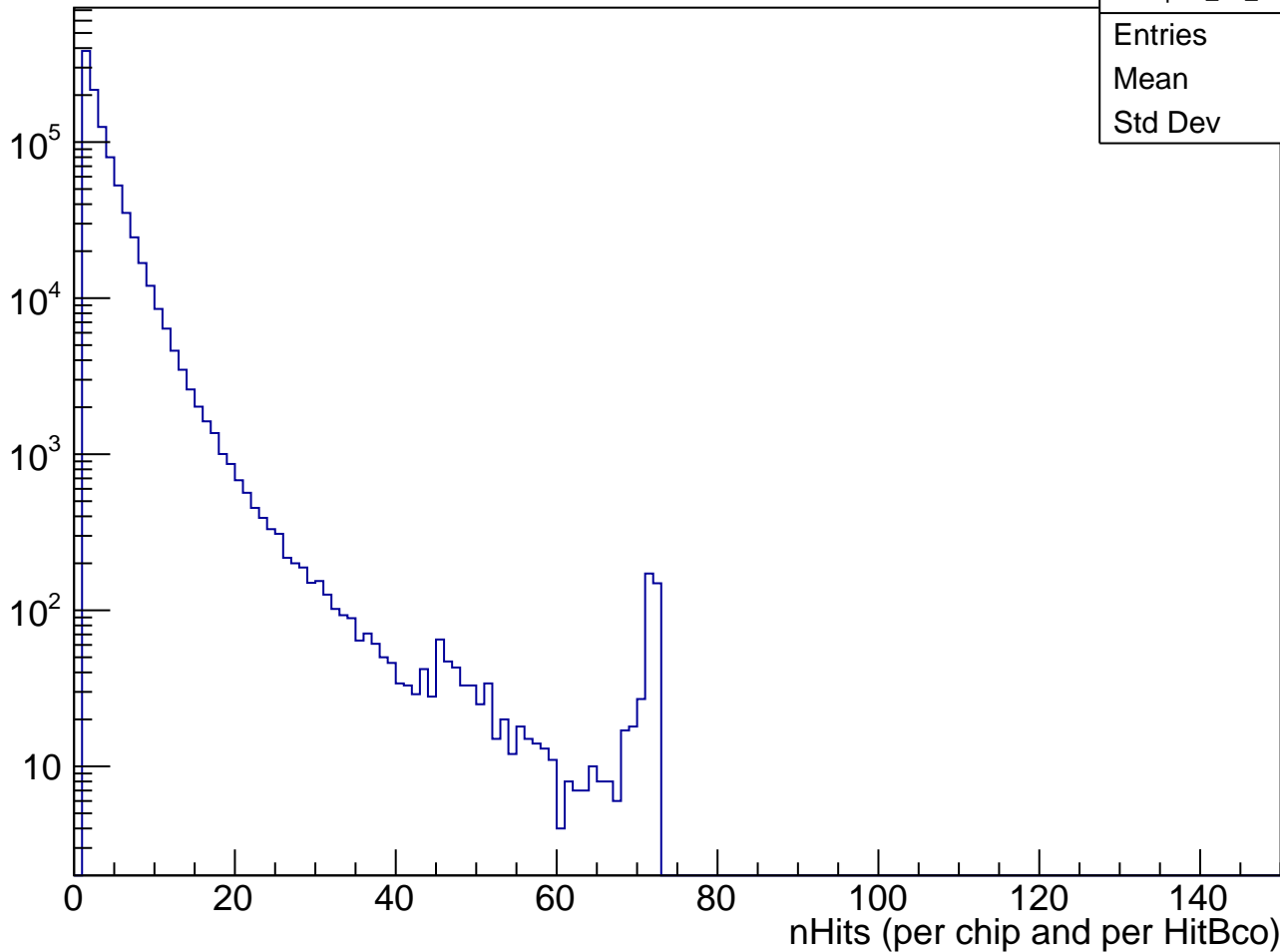
nChipHit_F5_Fch2_Chip6

Entries	969510
Mean	3.009
Std Dev	3.521

nHits (per chip and per HitBco)

nChipHit_F5_Fch2_Chip7

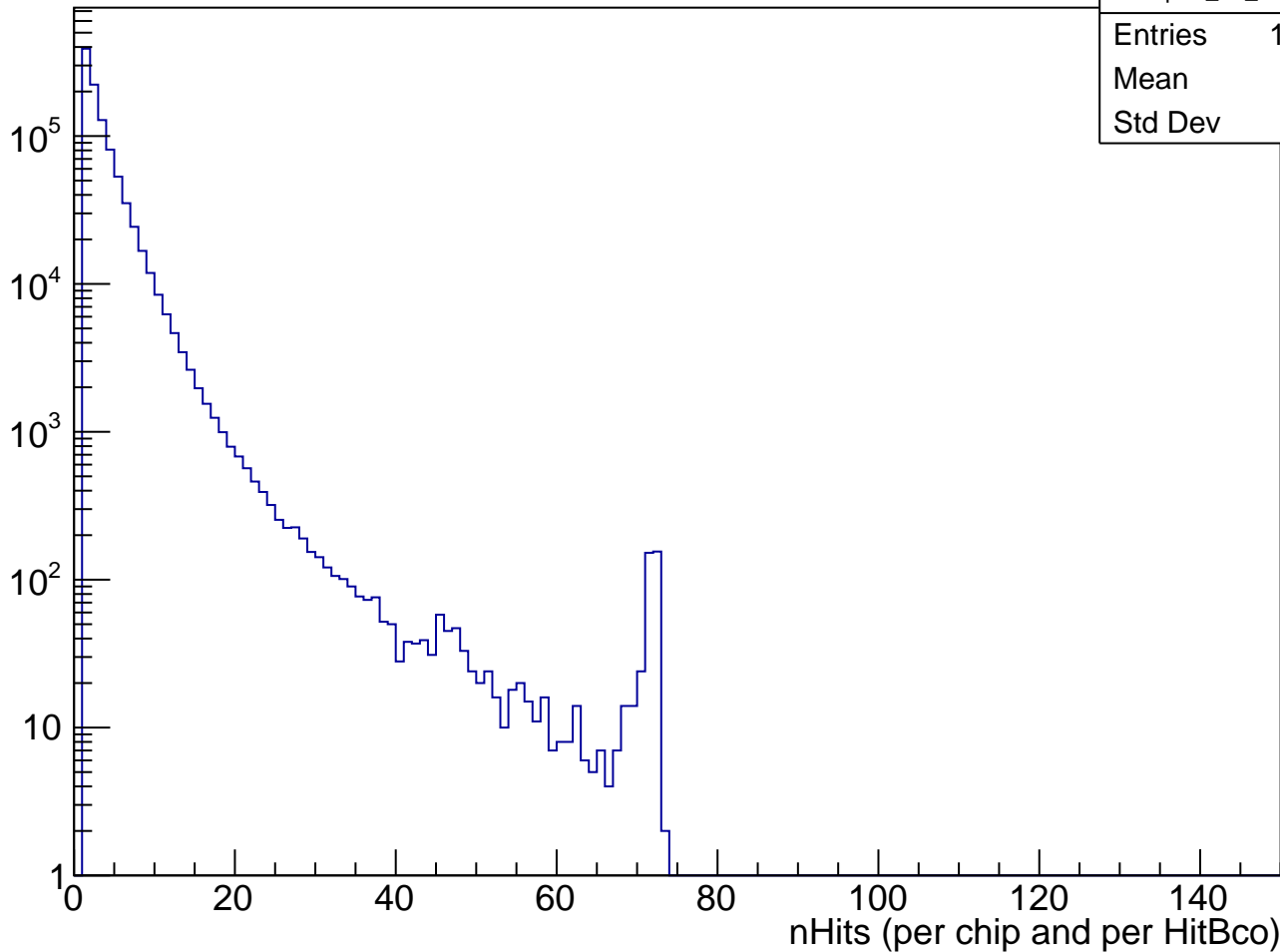
Entries



nChipHit_F5_Fch2_Chip7	
Entries	983618
Mean	3.022
Std Dev	3.508

nChipHit_F5_Fch2_Chip8

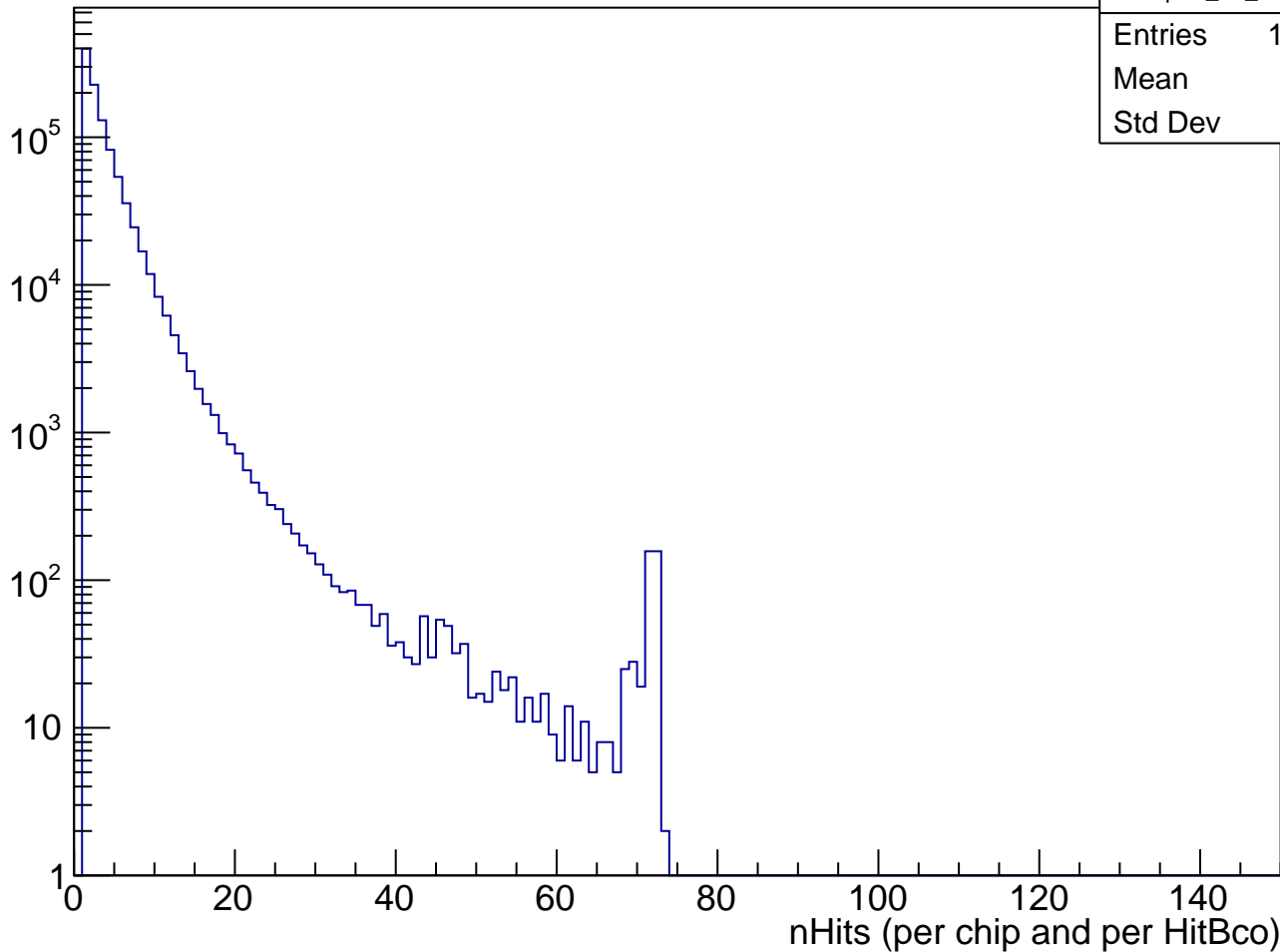
Entries



nChipHit_F5_Fch2_Chip8	
Entries	1000080
Mean	2.995
Std Dev	3.454

nChipHit_F5_Fch2_Chip9

Entries



nChipHit_F5_Fch2_Chip9

Entries 1017834

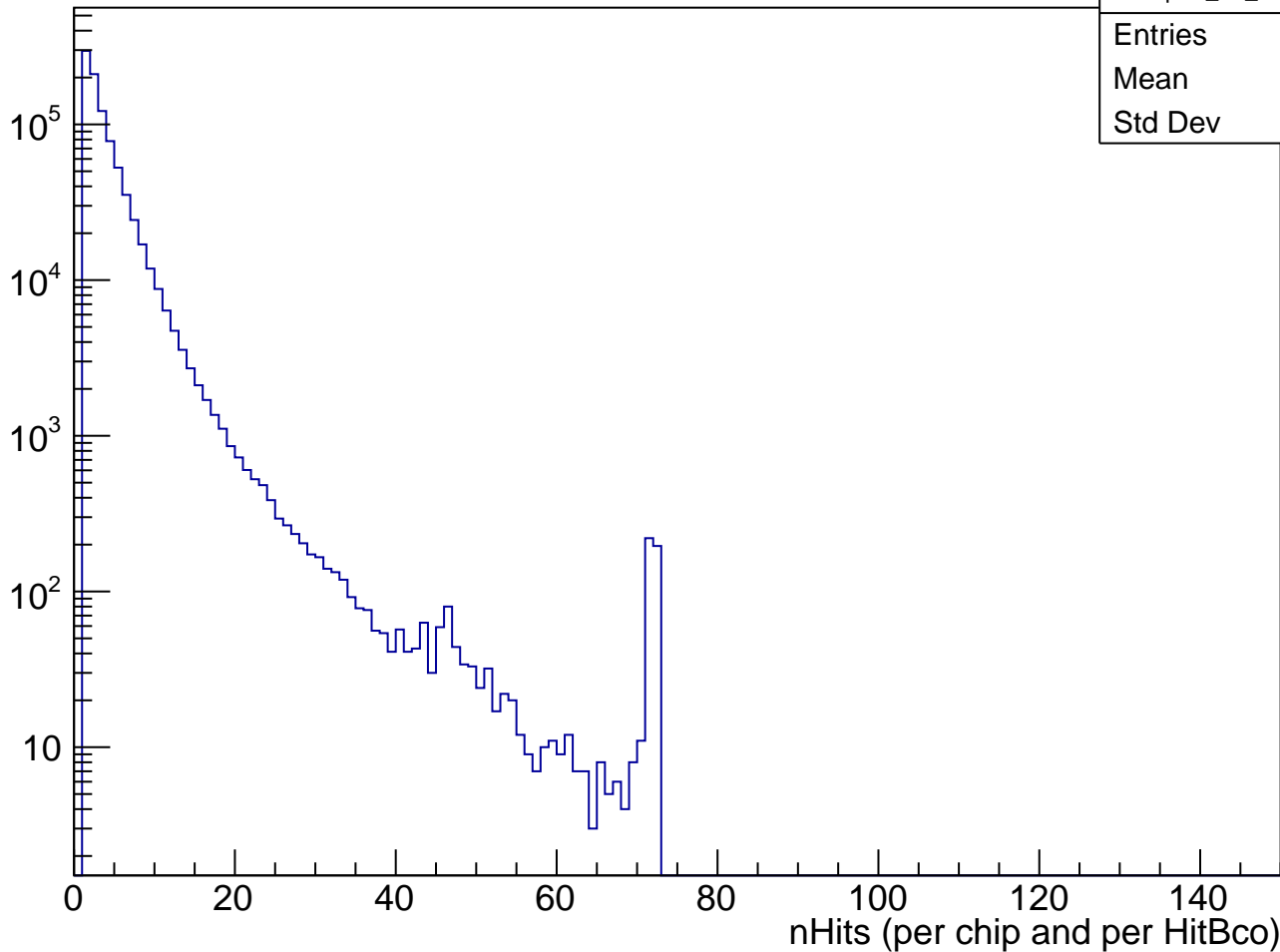
Mean 2.978

Std Dev 3.436

nHits (per chip and per HitBco)

nChipHit_F5_Fch3_Chip0

Entries



nChipHit_F5_Fch3_Chip0

Entries 887354

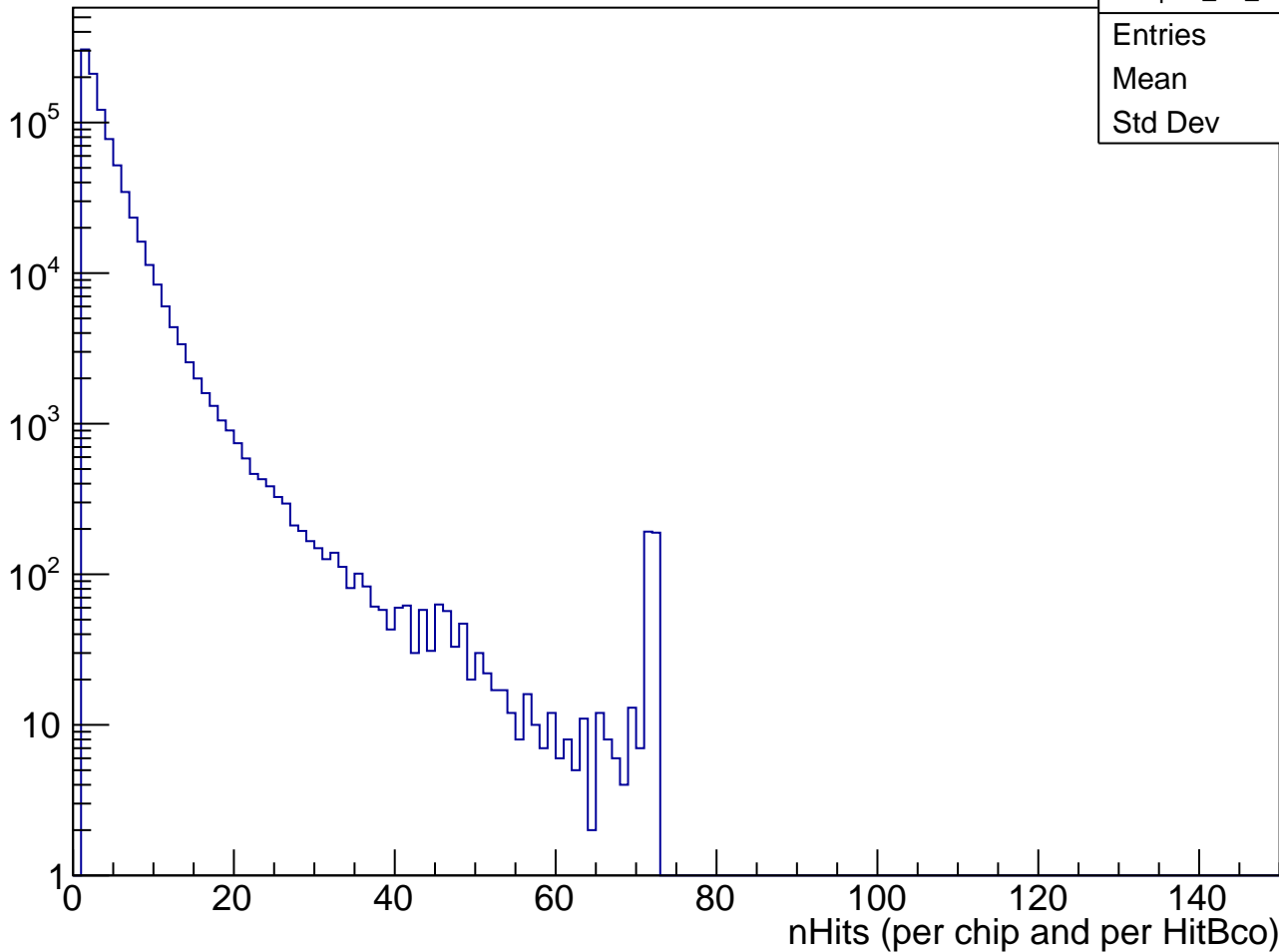
Mean 3.254

Std Dev 3.742

nHits (per chip and per HitBco)

nChipHit_F5_Fch3_Chip1

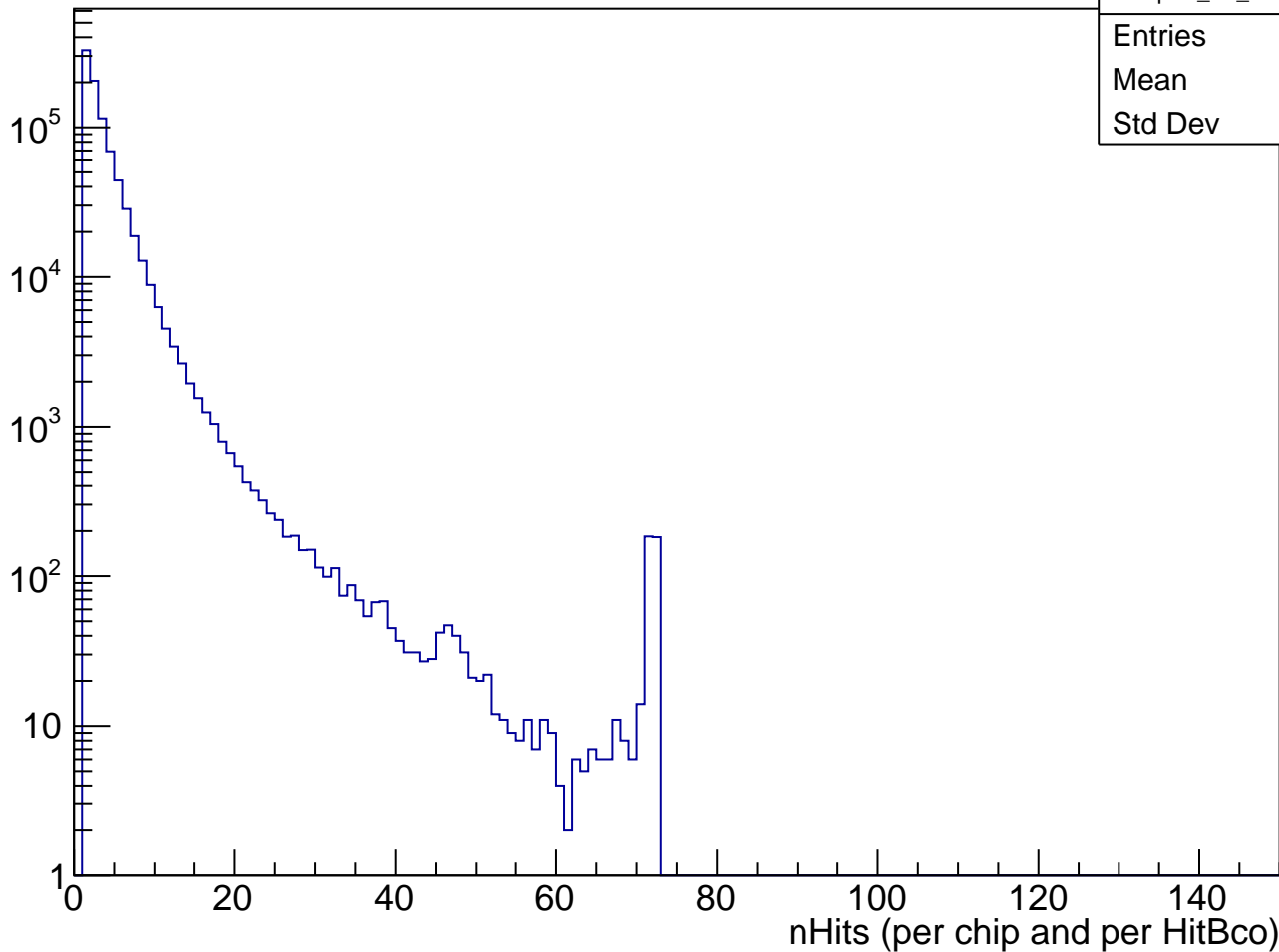
Entries



nChipHit_F5_Fch3_Chip1	
Entries	890460
Mean	3.191
Std Dev	3.673

nChipHit_F5_Fch3_Chip10

Entries

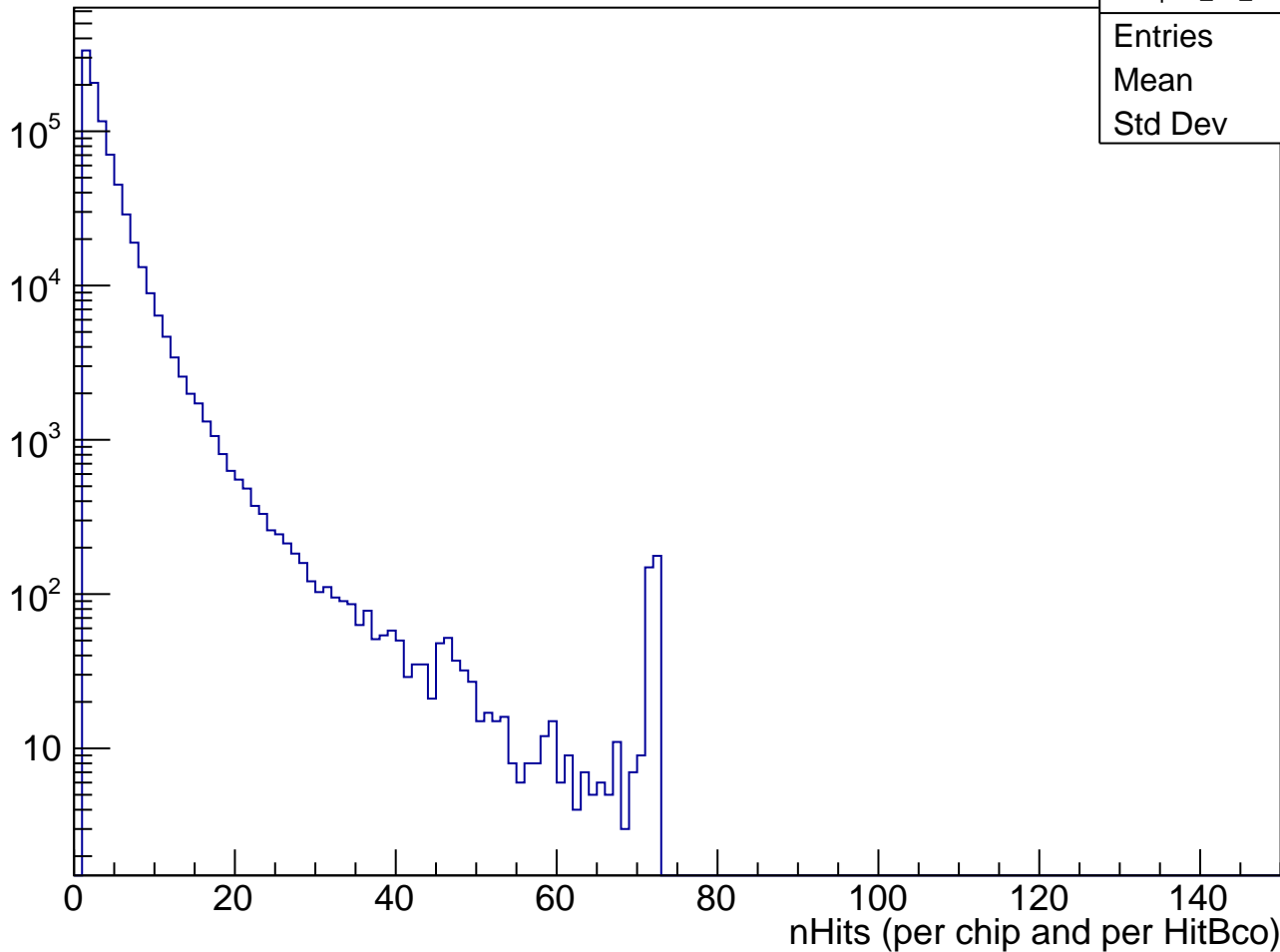


nChipHit_F5_Fch3_Chip10	
Entries	858218
Mean	2.927
Std Dev	3.441

nHits (per chip and per HitBco)

nChipHit_F5_Fch3_Chip11

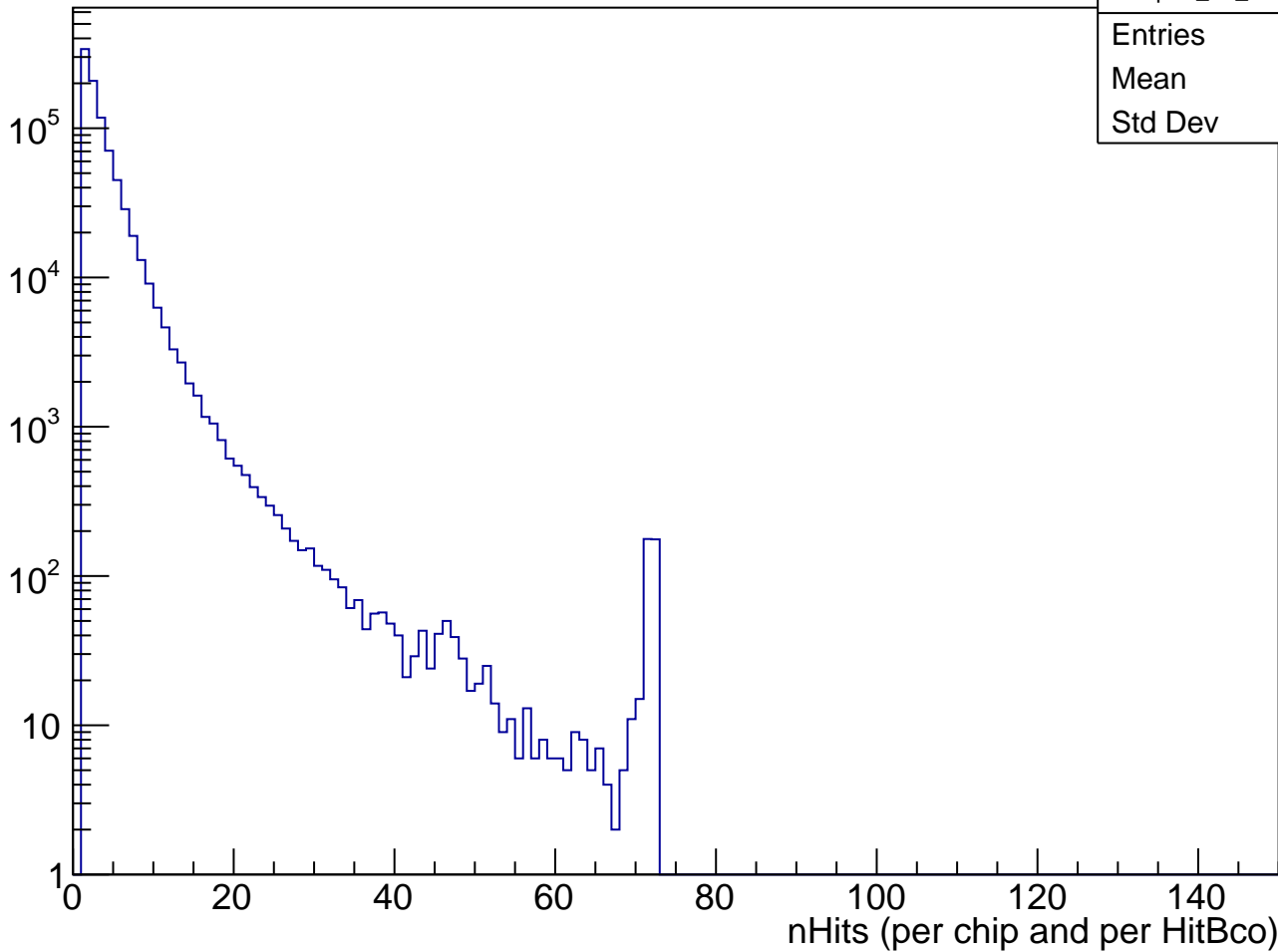
Entries



nChipHit_F5_Fch3_Chip11	
Entries	870911
Mean	2.925
Std Dev	3.411

nChipHit_F5_Fch3_Chip12

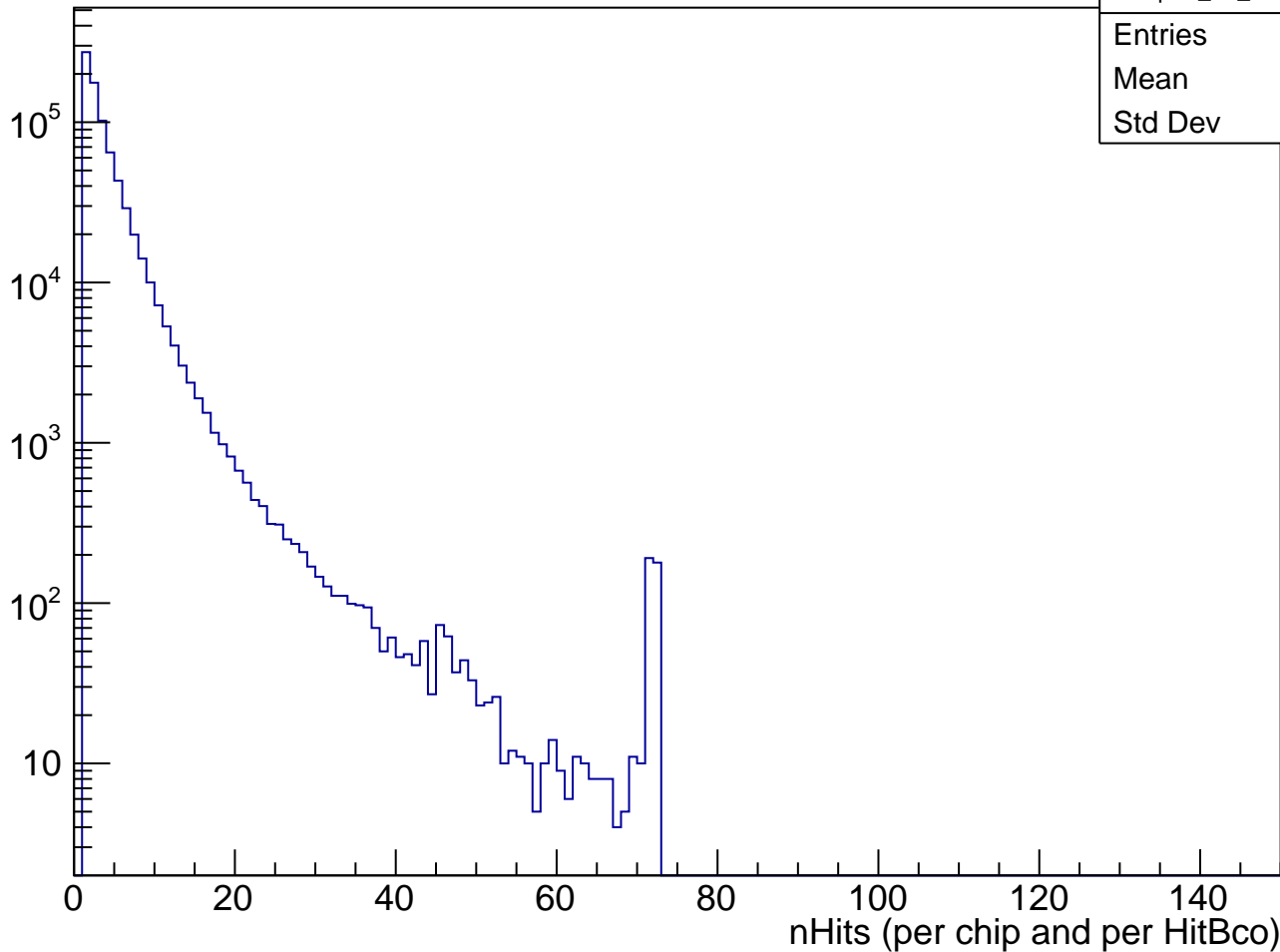
Entries



nChipHit_F5_Fch3_Chip12	
Entries	878886
Mean	2.908
Std Dev	3.399

nChipHit_F5_Fch3_Chip13

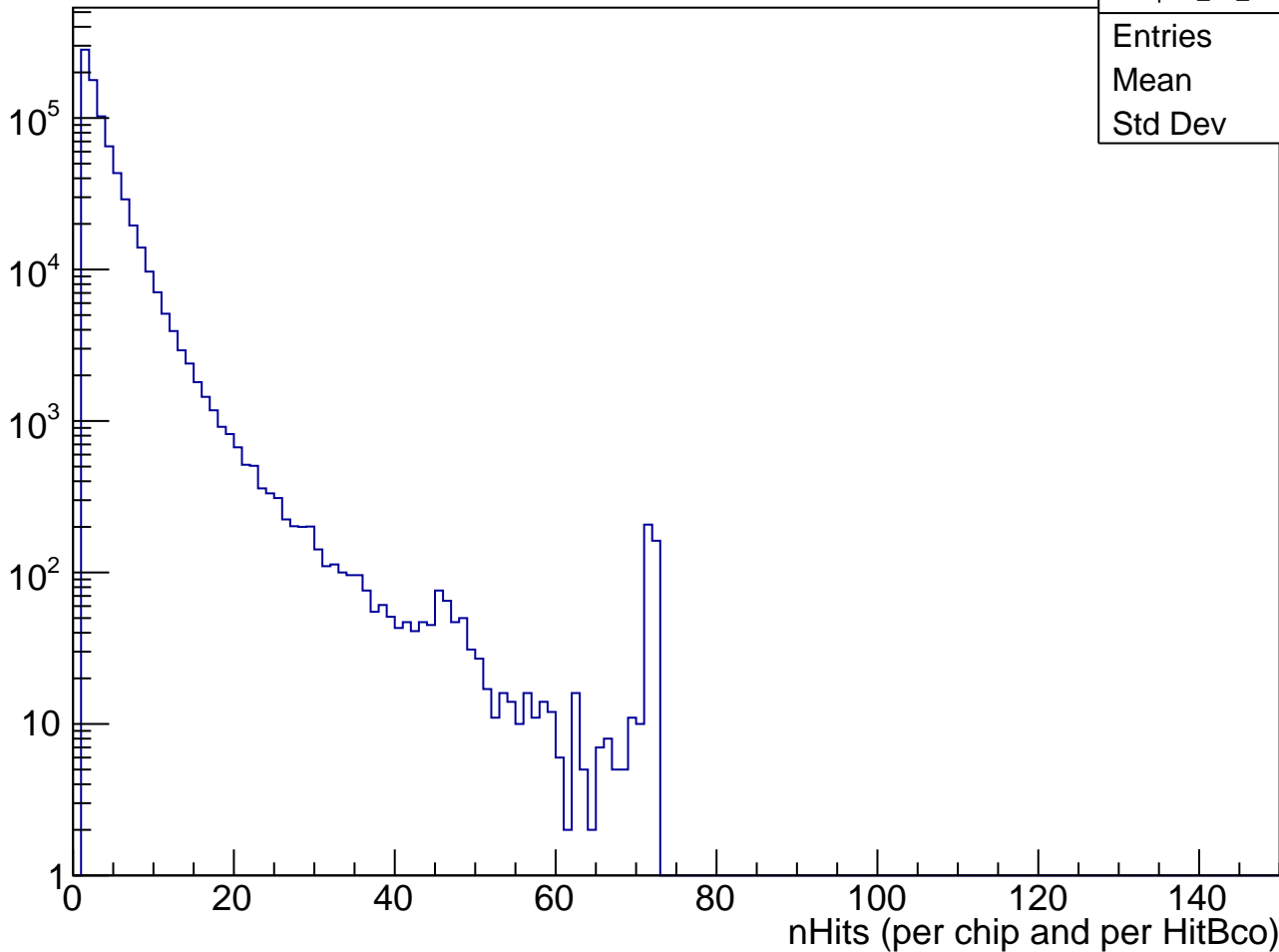
Entries



nChipHit_F5_Fch3_Chip13	
Entries	767173
Mean	3.205
Std Dev	3.828

nChipHit_F5_Fch3_Chip14

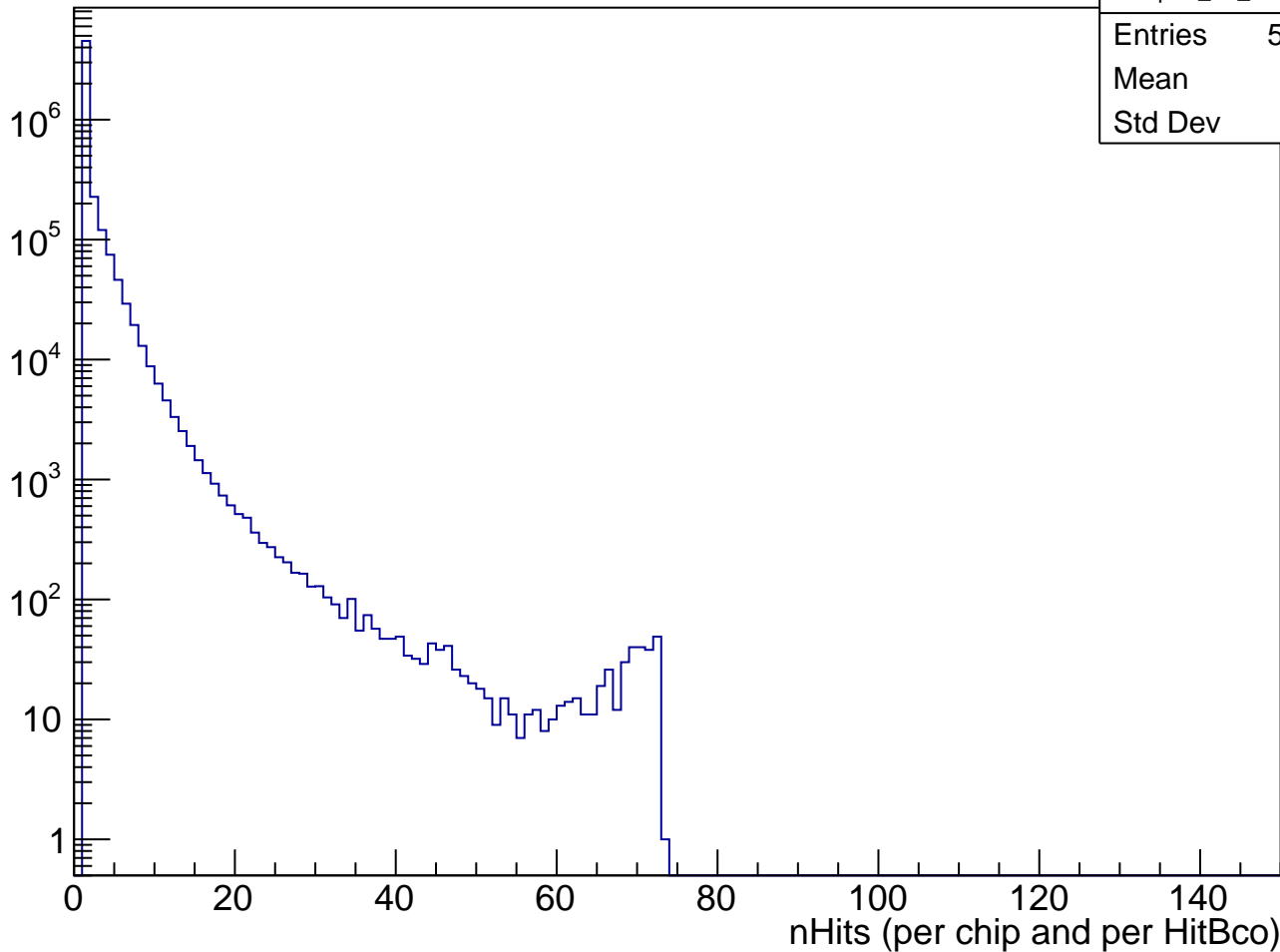
Entries



nChipHit_F5_Fch3_Chip14	
Entries	776671
Mean	3.16
Std Dev	3.784

nChipHit_F5_Fch3_Chip15

Entries

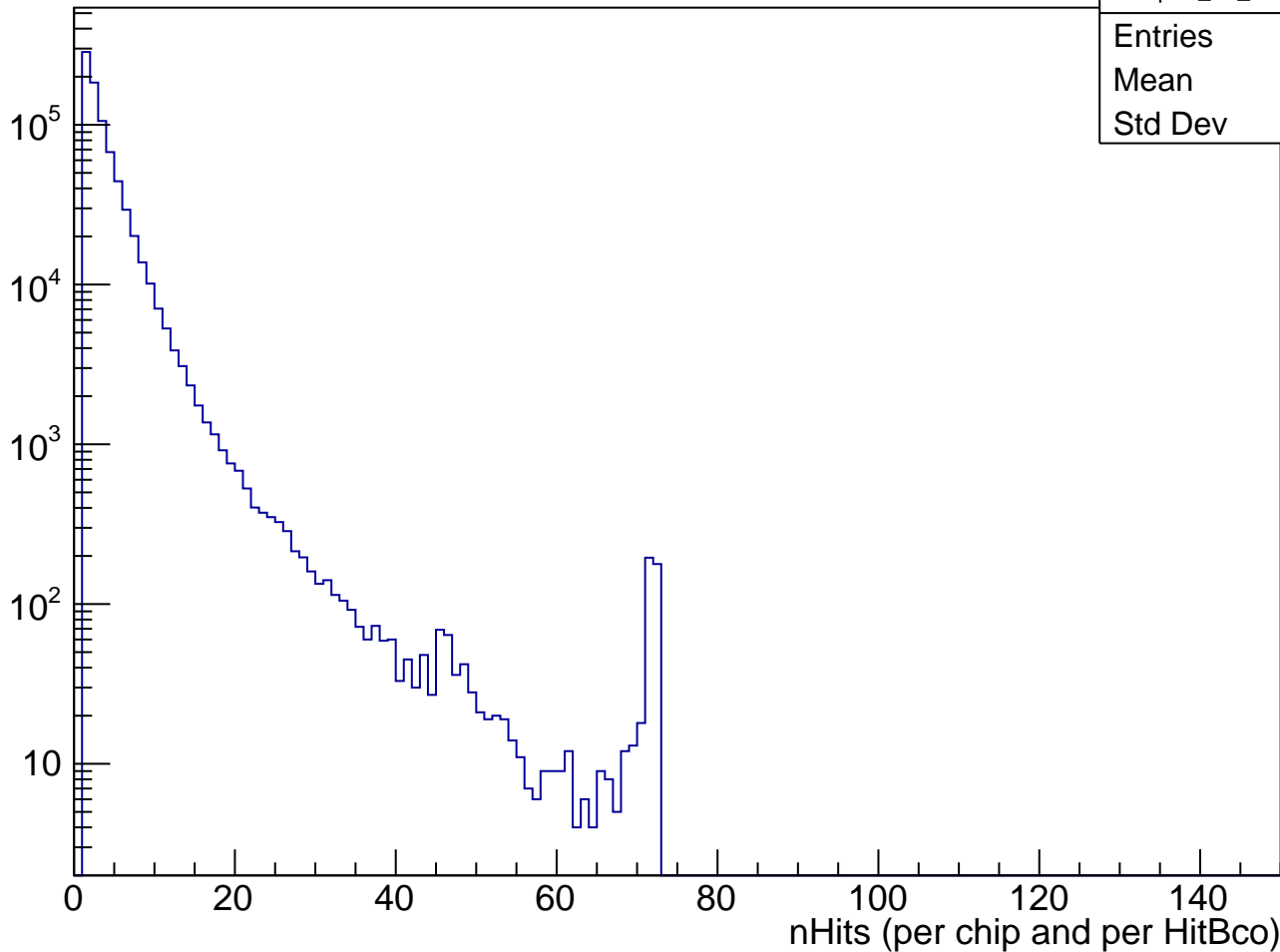


nChipHit_F5_Fch3_Chip15	
Entries	5099814
Mean	1.333
Std Dev	1.537

nHits (per chip and per HitBco)

nChipHit_F5_Fch3_Chip16

Entries

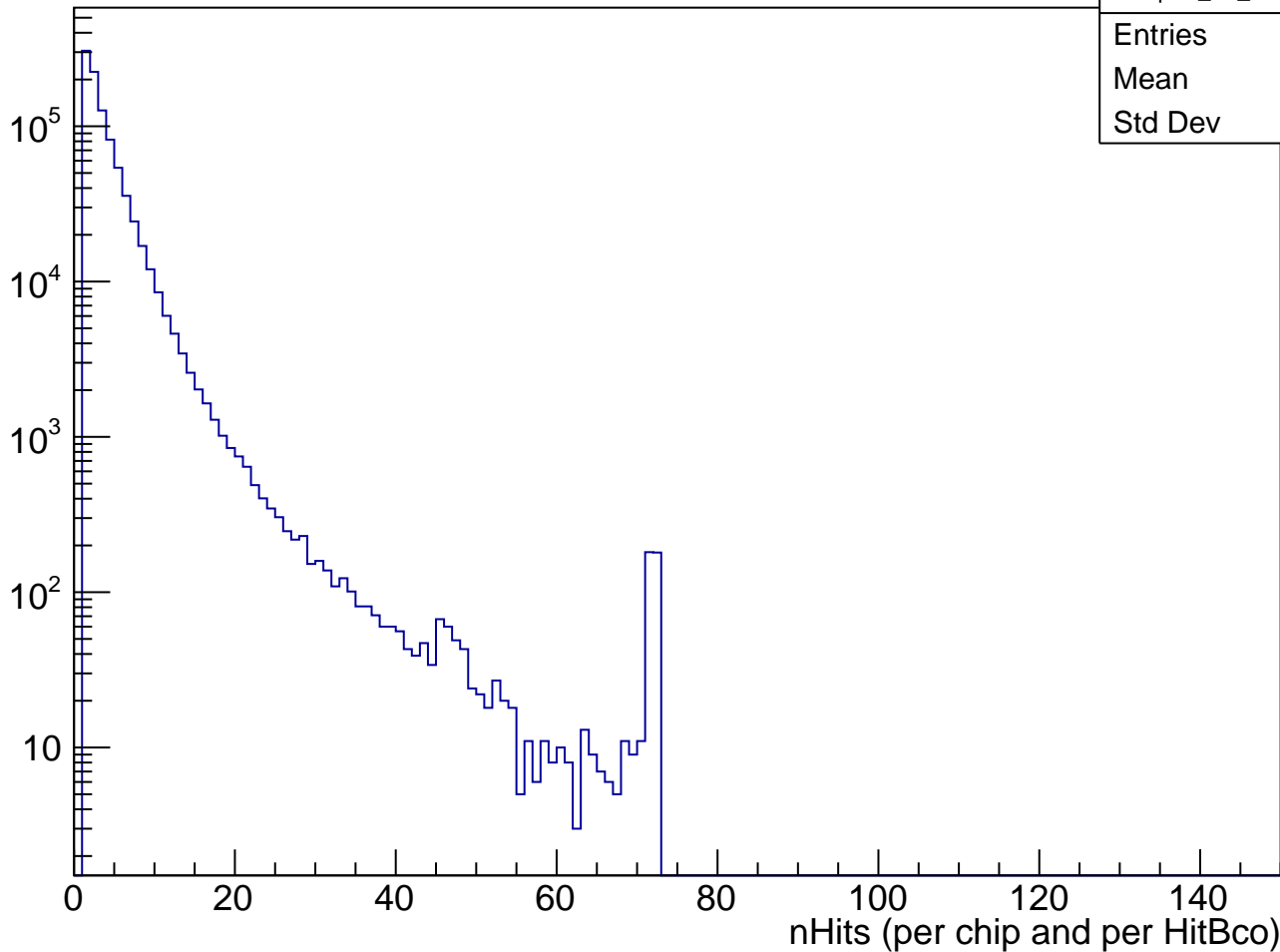


nChipHit_F5_Fch3_Chip16	
Entries	793461
Mean	3.15
Std Dev	3.742

nHits (per chip and per HitBco)

nChipHit_F5_Fch3_Chip17

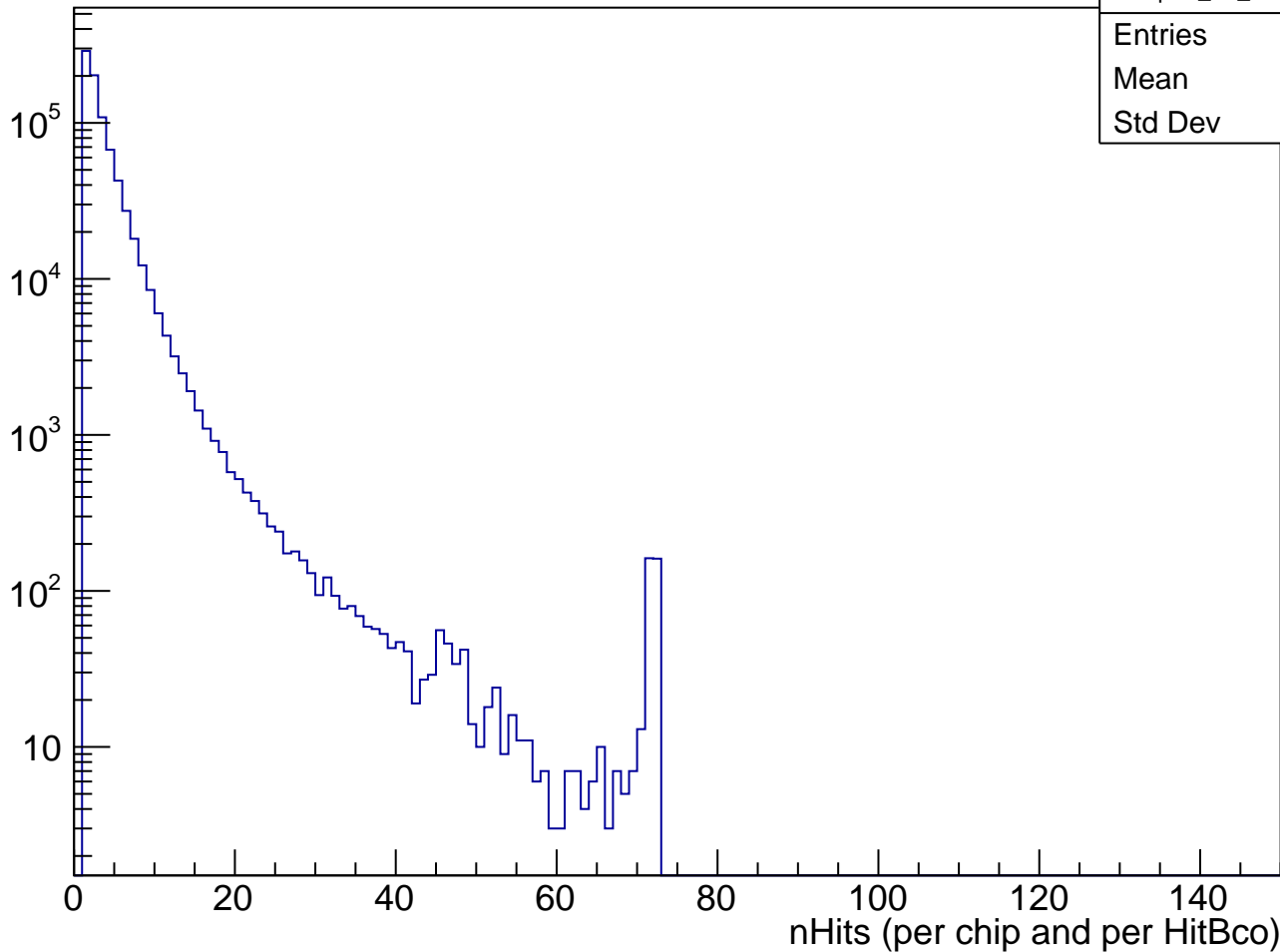
Entries



nChipHit_F5_Fch3_Chip17	
Entries	919706
Mean	3.199
Std Dev	3.625

nChipHit_F5_Fch3_Chip18

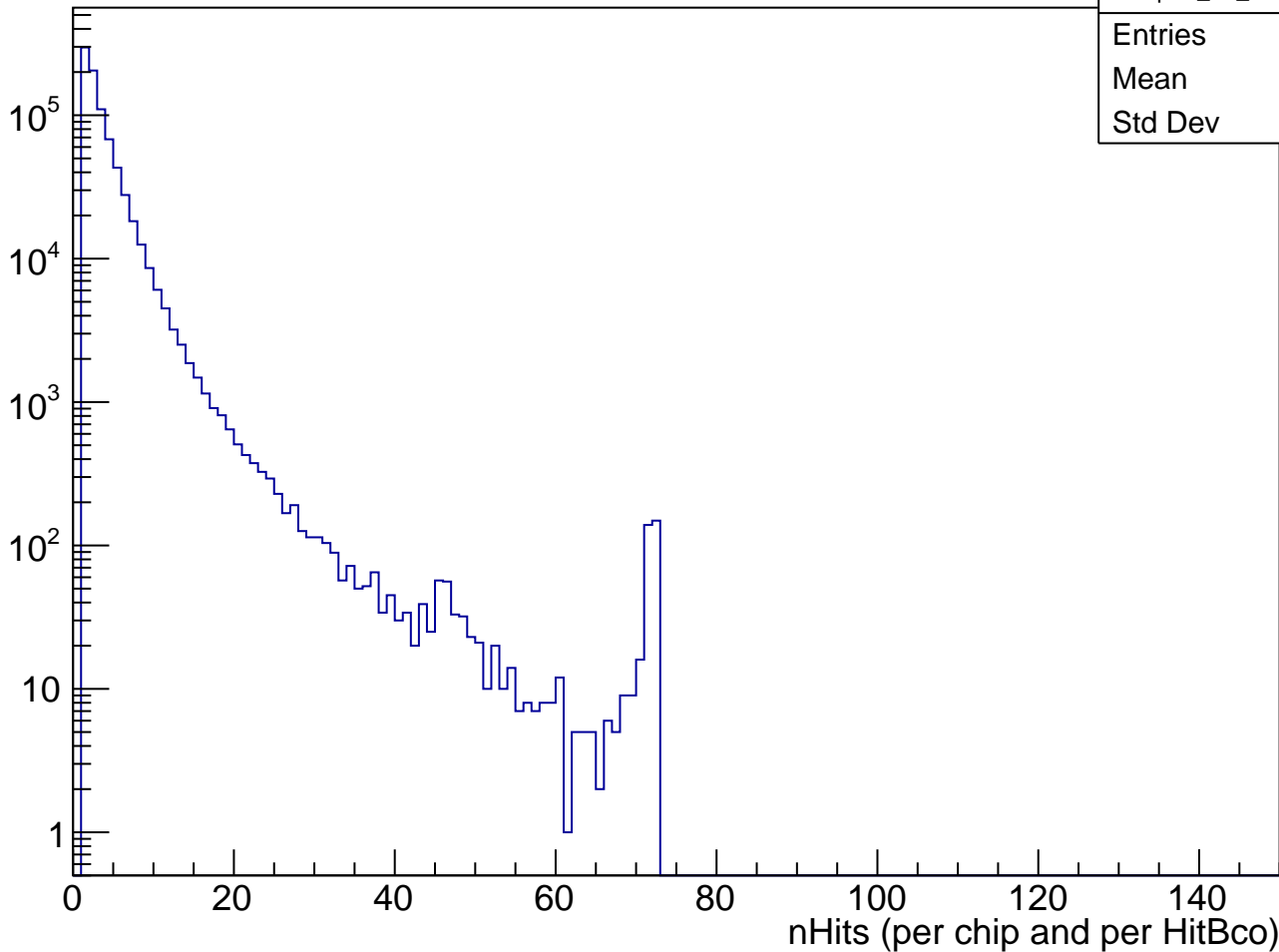
Entries



nChipHit_F5_Fch3_Chip18	
Entries	803156
Mean	2.977
Std Dev	3.443

nChipHit_F5_Fch3_Chip19

Entries

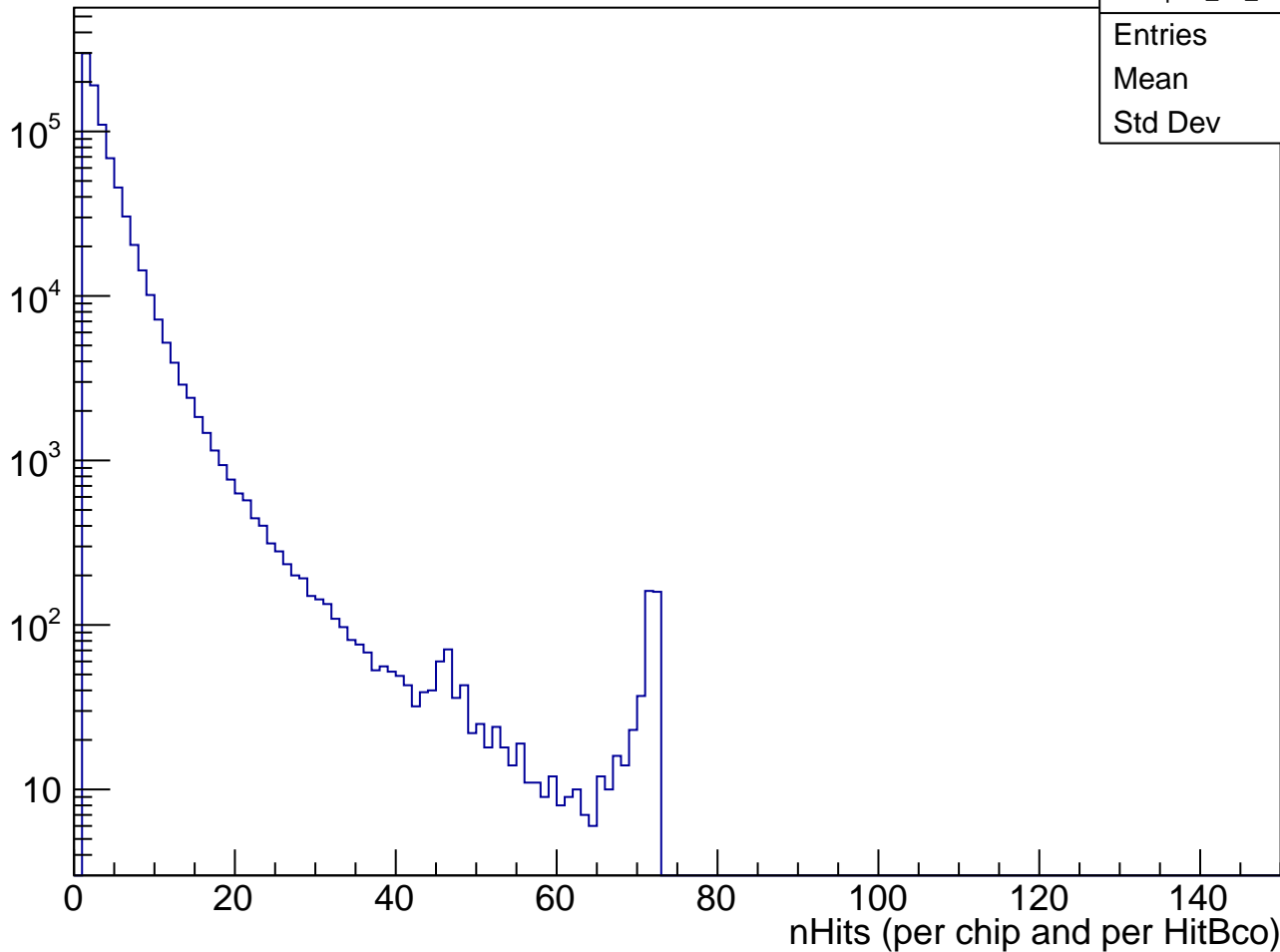


nChipHit_F5_Fch3_Chip19	
Entries	817672
Mean	2.961
Std Dev	3.388

nHits (per chip and per HitBco)

nChipHit_F5_Fch3_Chip2

Entries

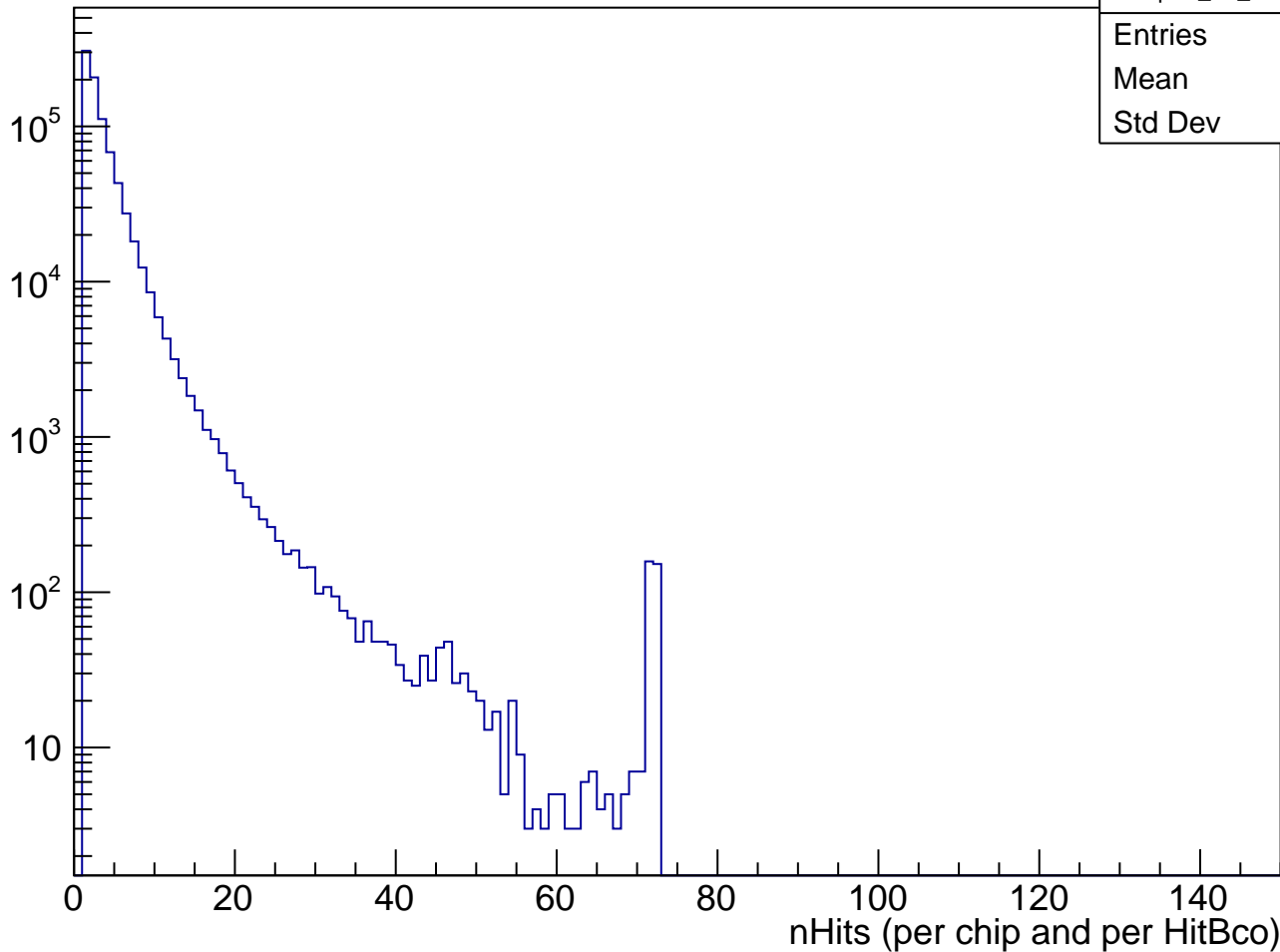


nChipHit_F5_Fch3_Chip2	
Entries	821720
Mean	3.118
Std Dev	3.69

nHits (per chip and per HitBco)

nChipHit_F5_Fch3_Chip20

Entries

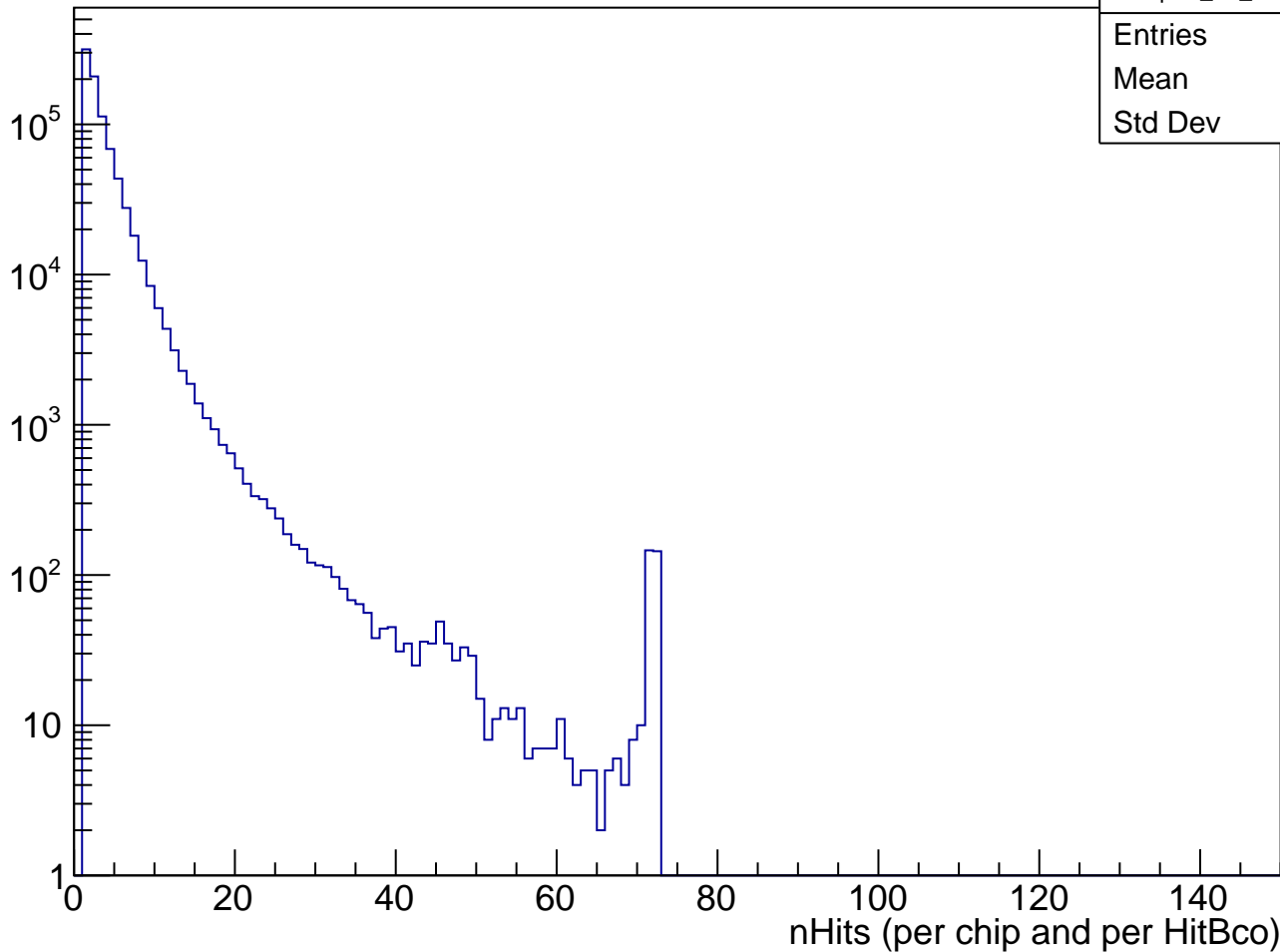


nChipHit_F5_Fch3_Chip20	
Entries	829944
Mean	2.924
Std Dev	3.348

nHits (per chip and per HitBco)

nChipHit_F5_Fch3_Chip21

Entries

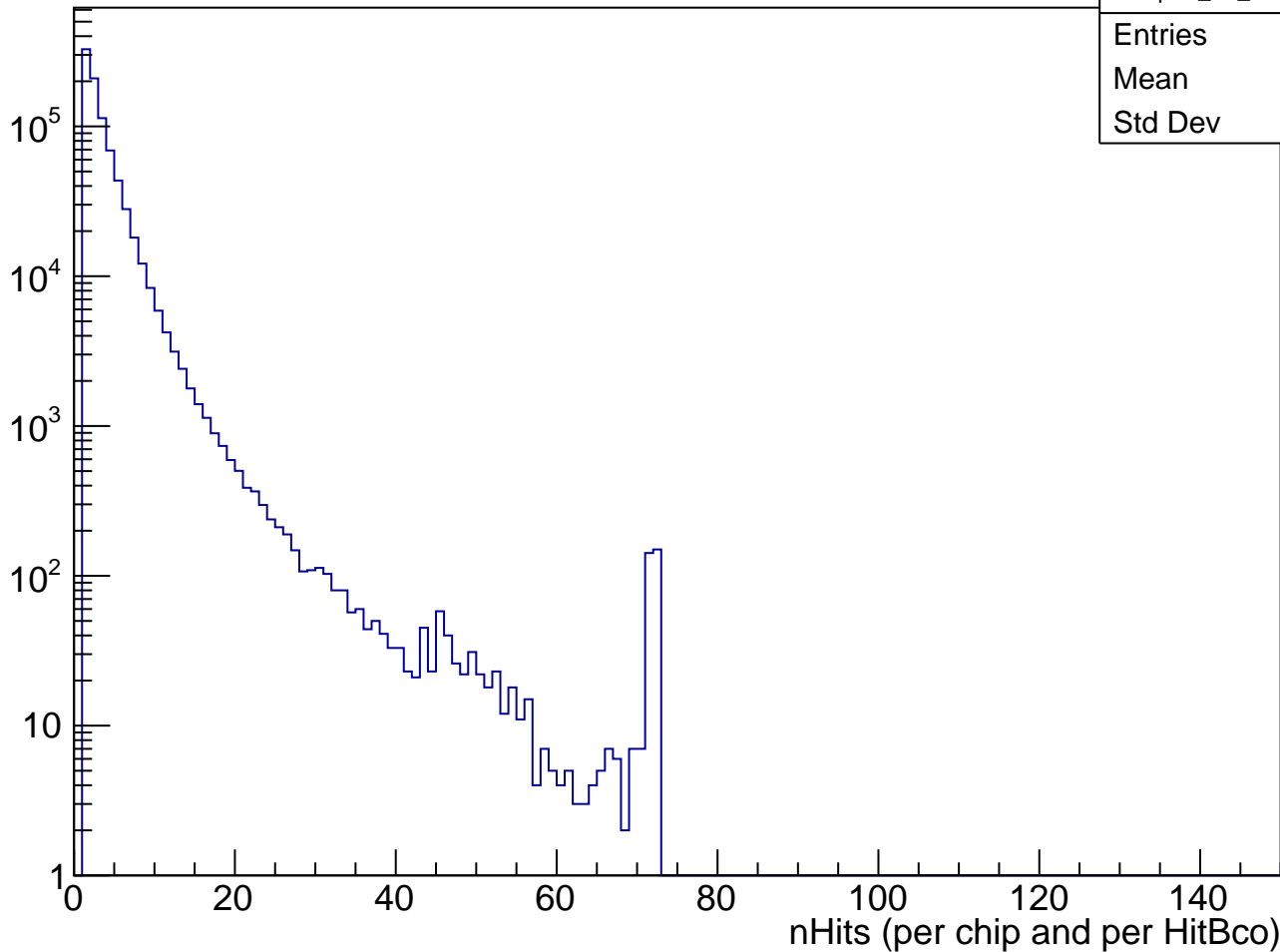


nChipHit_F5_Fch3_Chip21	
Entries	842540
Mean	2.902
Std Dev	3.322

nHits (per chip and per HitBco)

nChipHit_F5_Fch3_Chip22

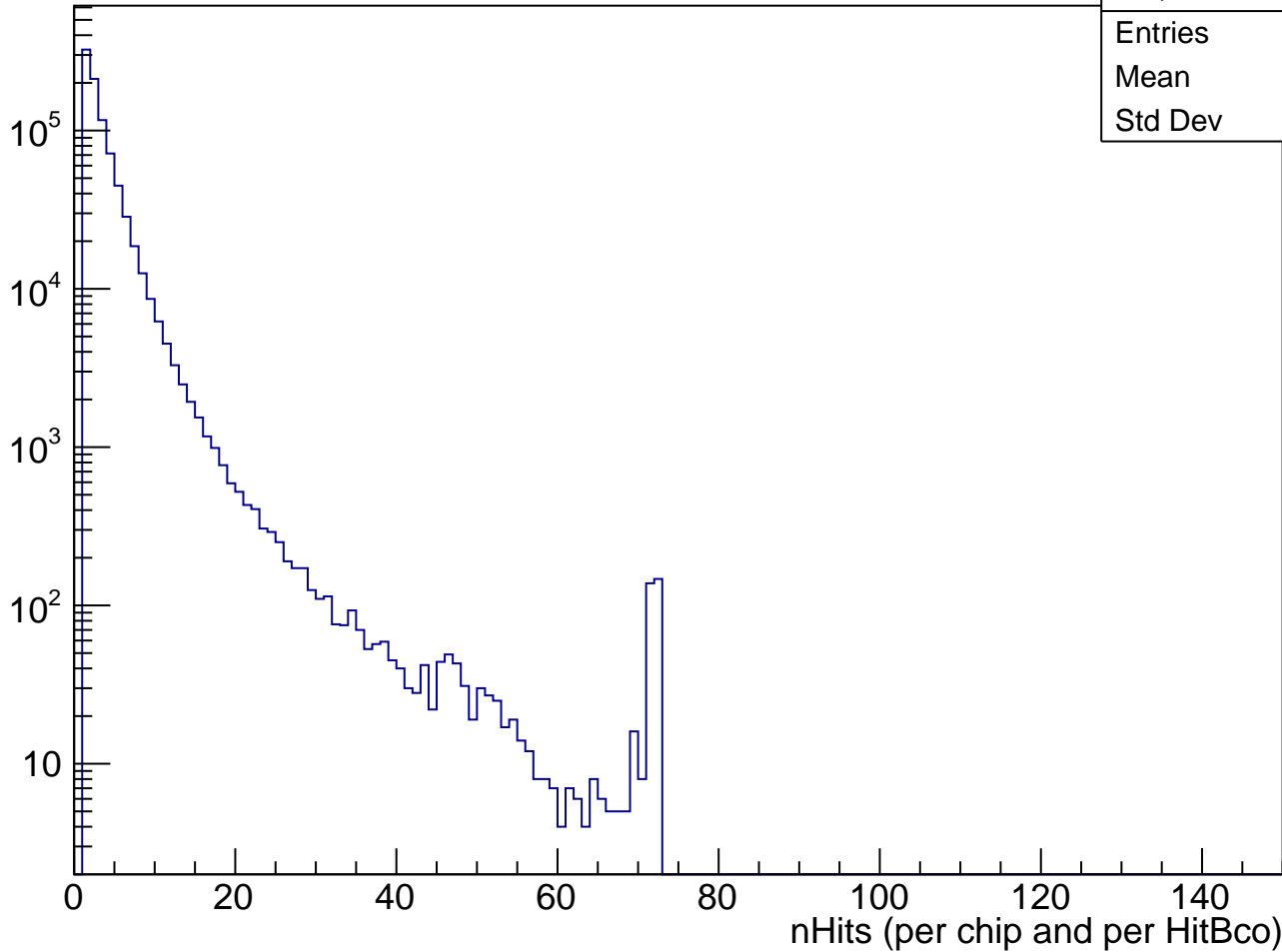
Entries



nChipHit_F5_Fch3_Chip22	
Entries	855415
Mean	2.867
Std Dev	3.279

nChipHit_F5_Fch3_Chip23

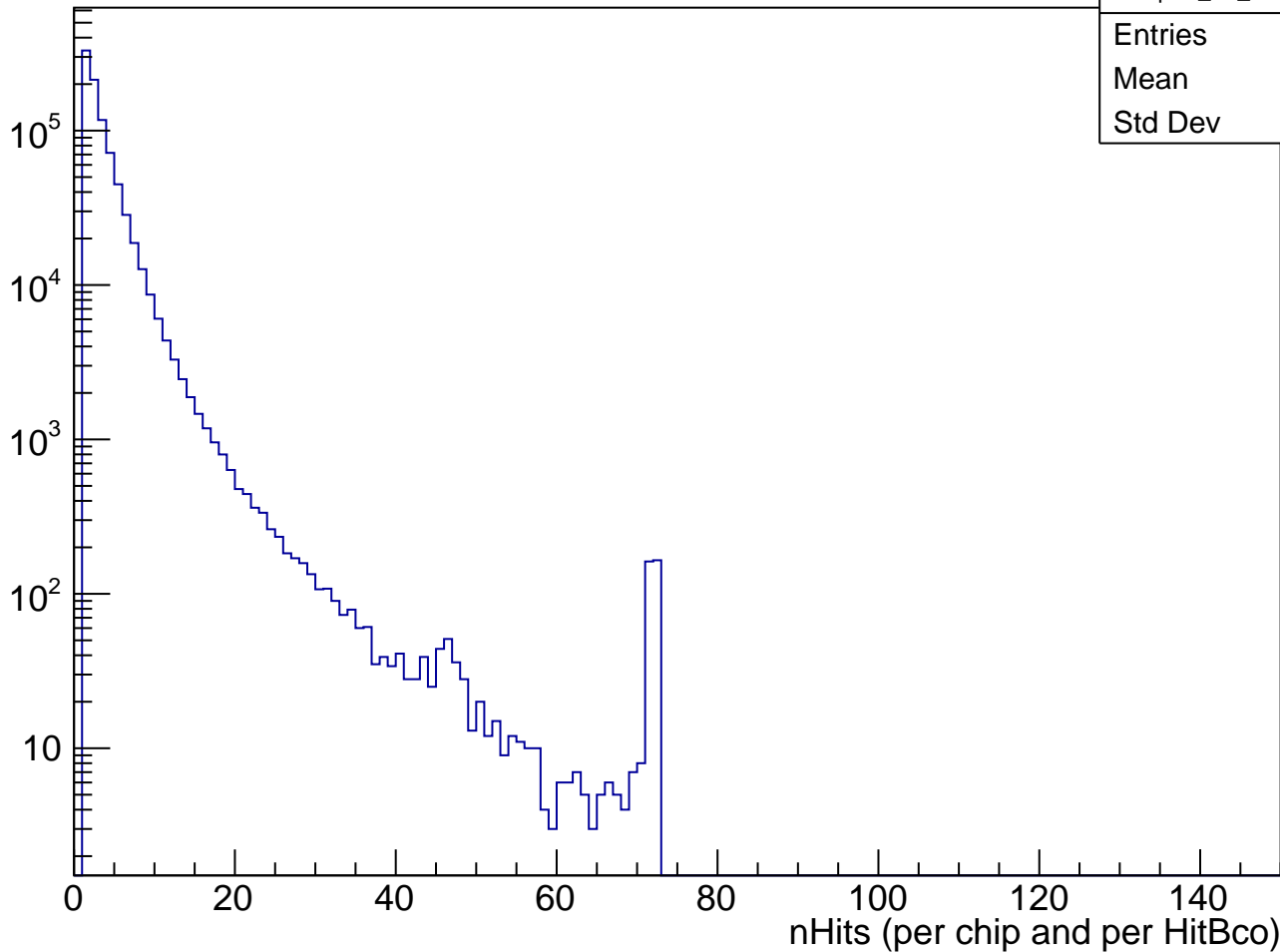
Entries



nChipHit_F5_Fch3_Chip23	
Entries	865659
Mean	2.916
Std Dev	3.361

nChipHit_F5_Fch3_Chip24

Entries

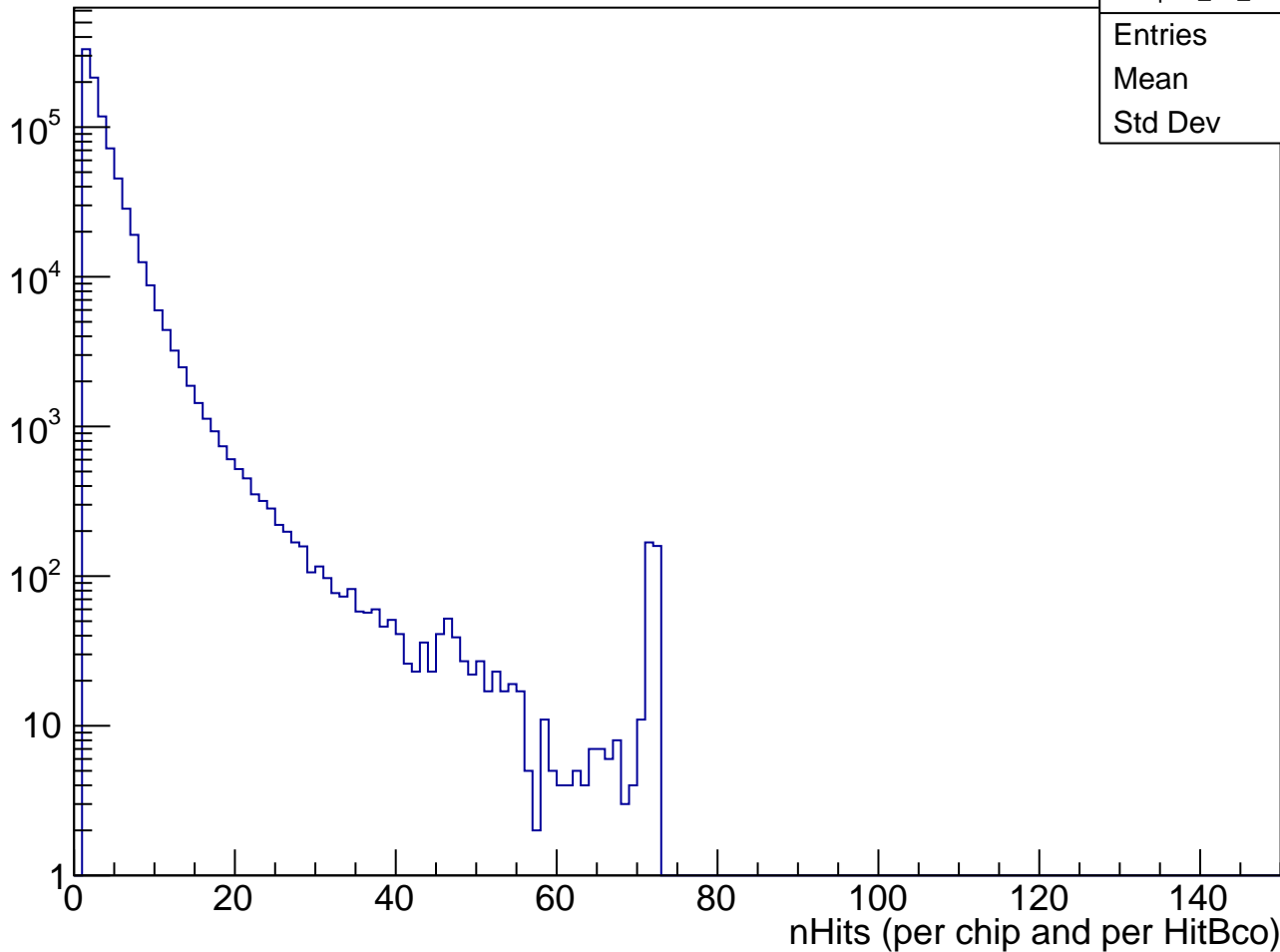


nChipHit_F5_Fch3_Chip24	
Entries	873303
Mean	2.892
Std Dev	3.32

nHits (per chip and per HitBco)

nChipHit_F5_Fch3_Chip25

Entries

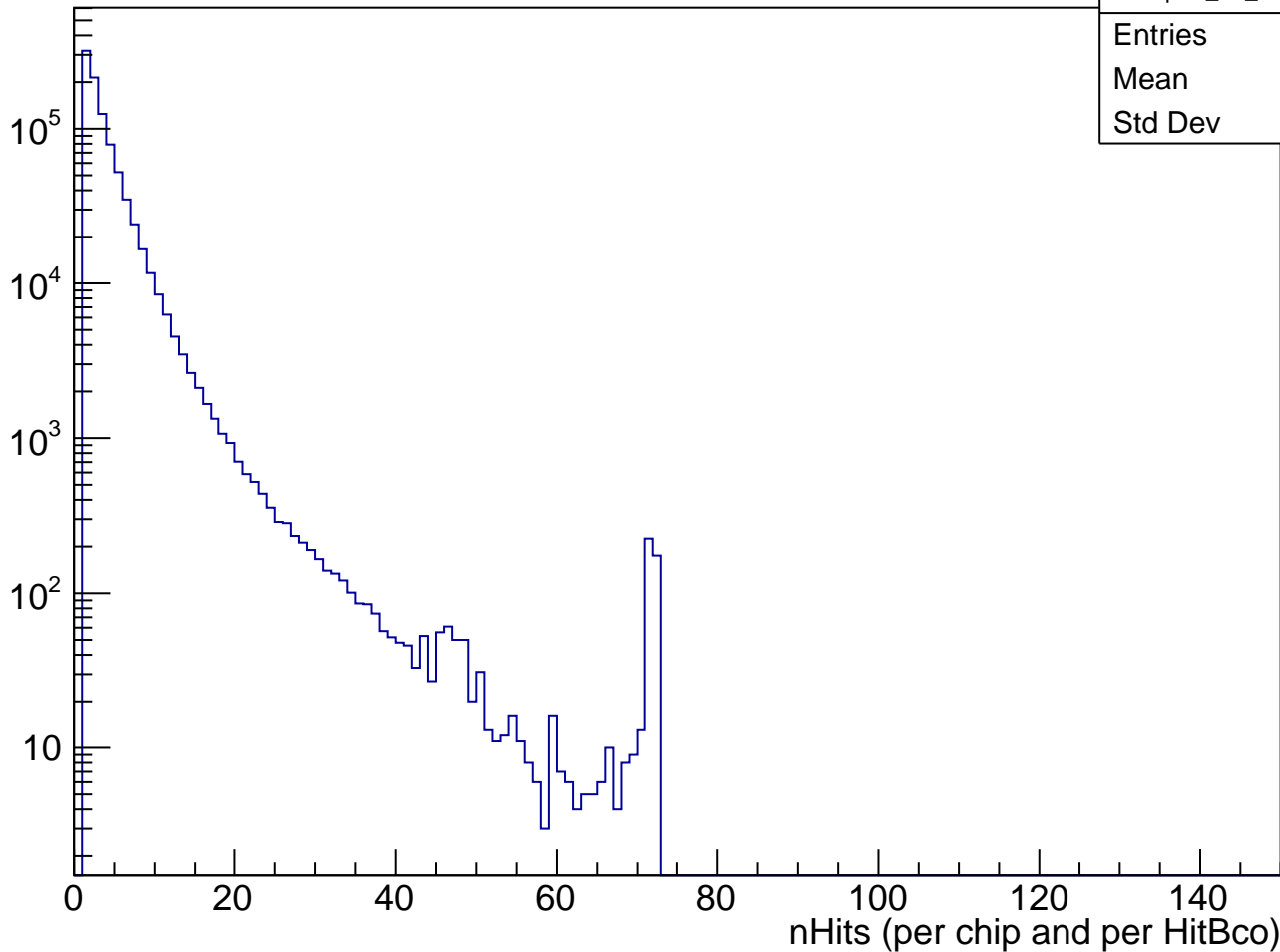


nChipHit_F5_Fch3_Chip25	
Entries	877154
Mean	2.891
Std Dev	3.331

nHits (per chip and per HitBco)

nChipHit_F5_Fch3_Chip3

Entries



nChipHit_F5_Fch3_Chip3

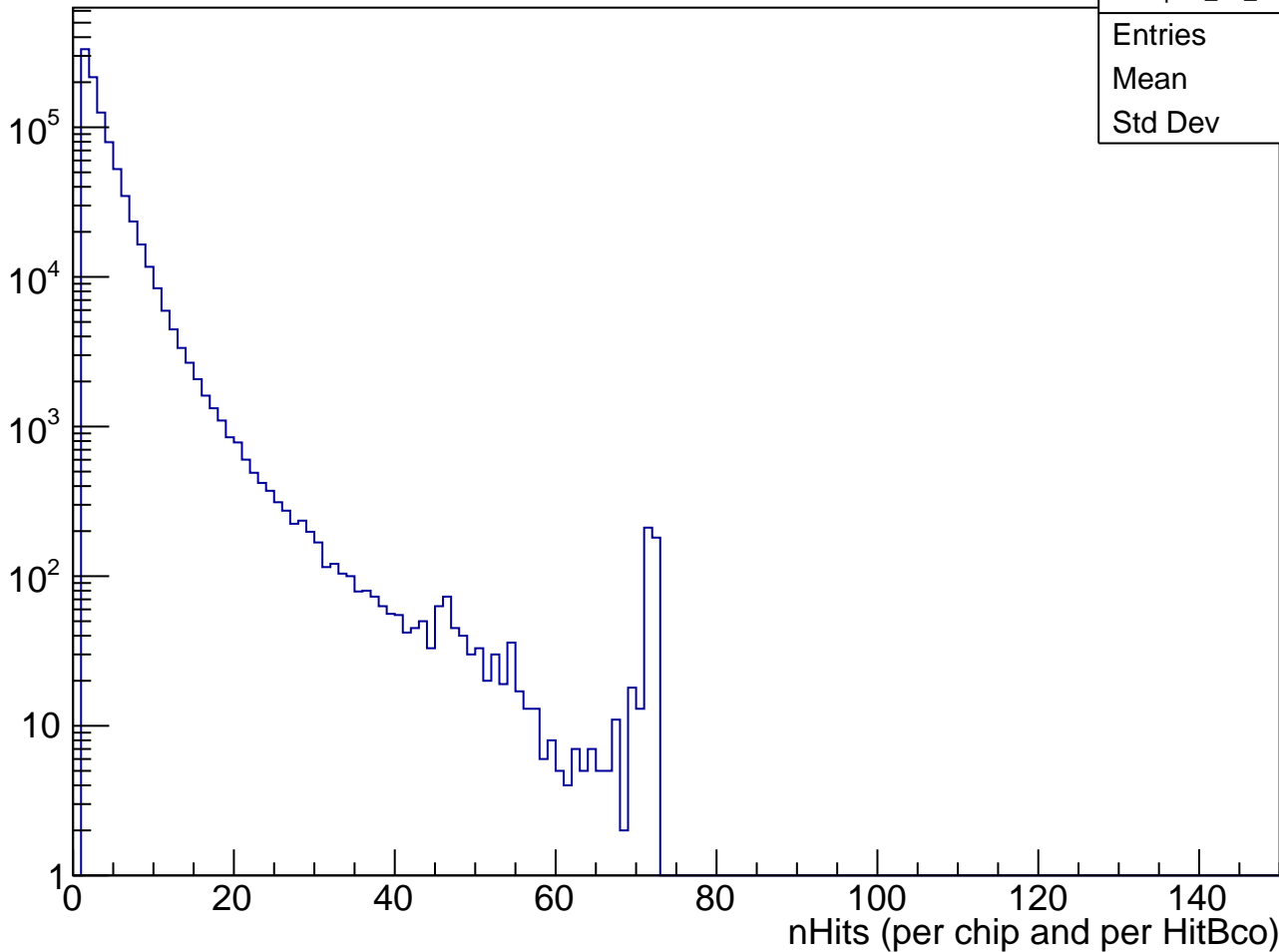
Entries 914452

Mean 3.177

Std Dev 3.664

nChipHit_F5_Fch3_Chip4

Entries



nChipHit_F5_Fch3_Chip4

Entries 930339

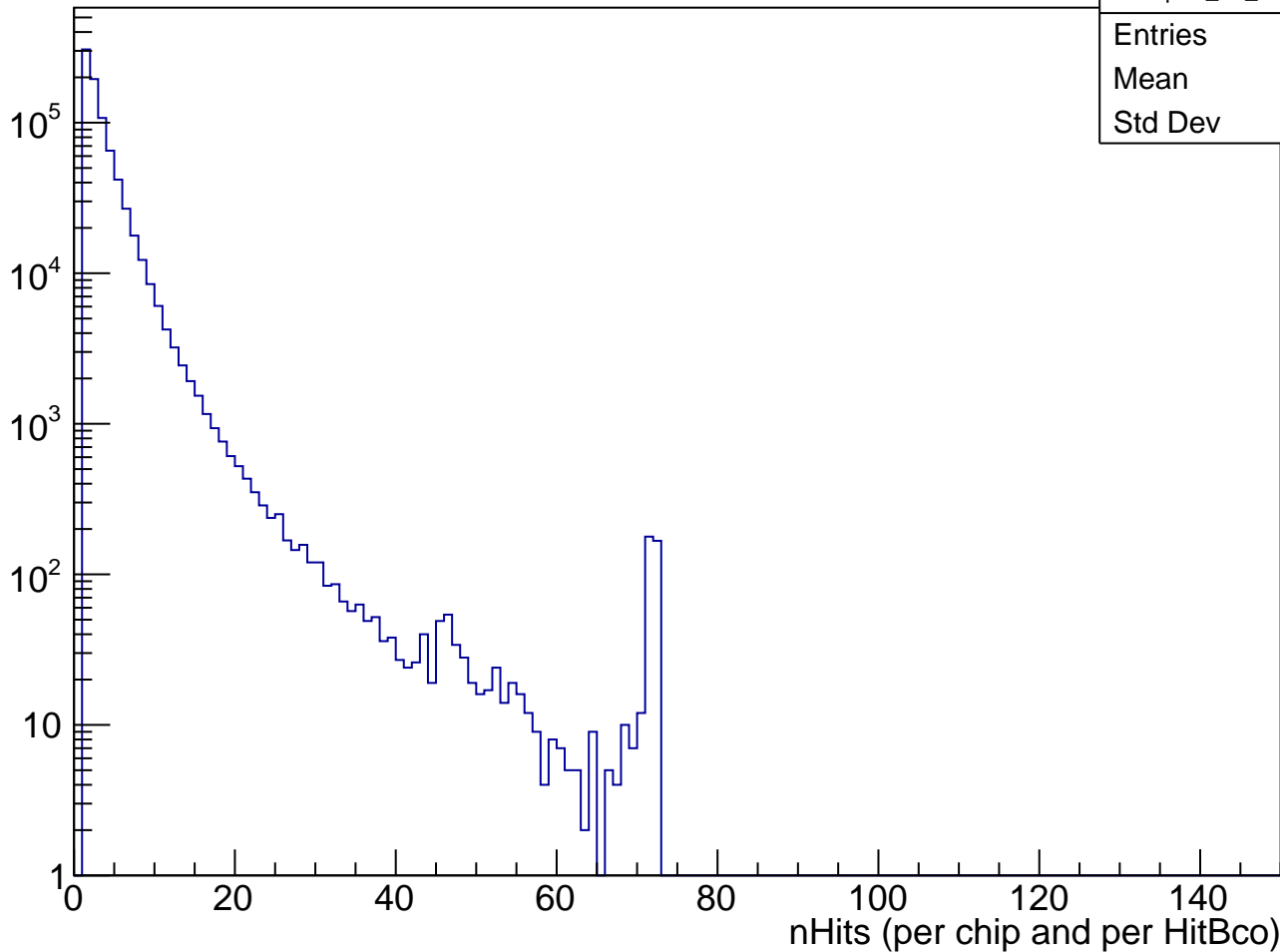
Mean 3.137

Std Dev 3.66

nHits (per chip and per HitBco)

nChipHit_F5_Fch3_Chip5

Entries



nChipHit_F5_Fch3_Chip5

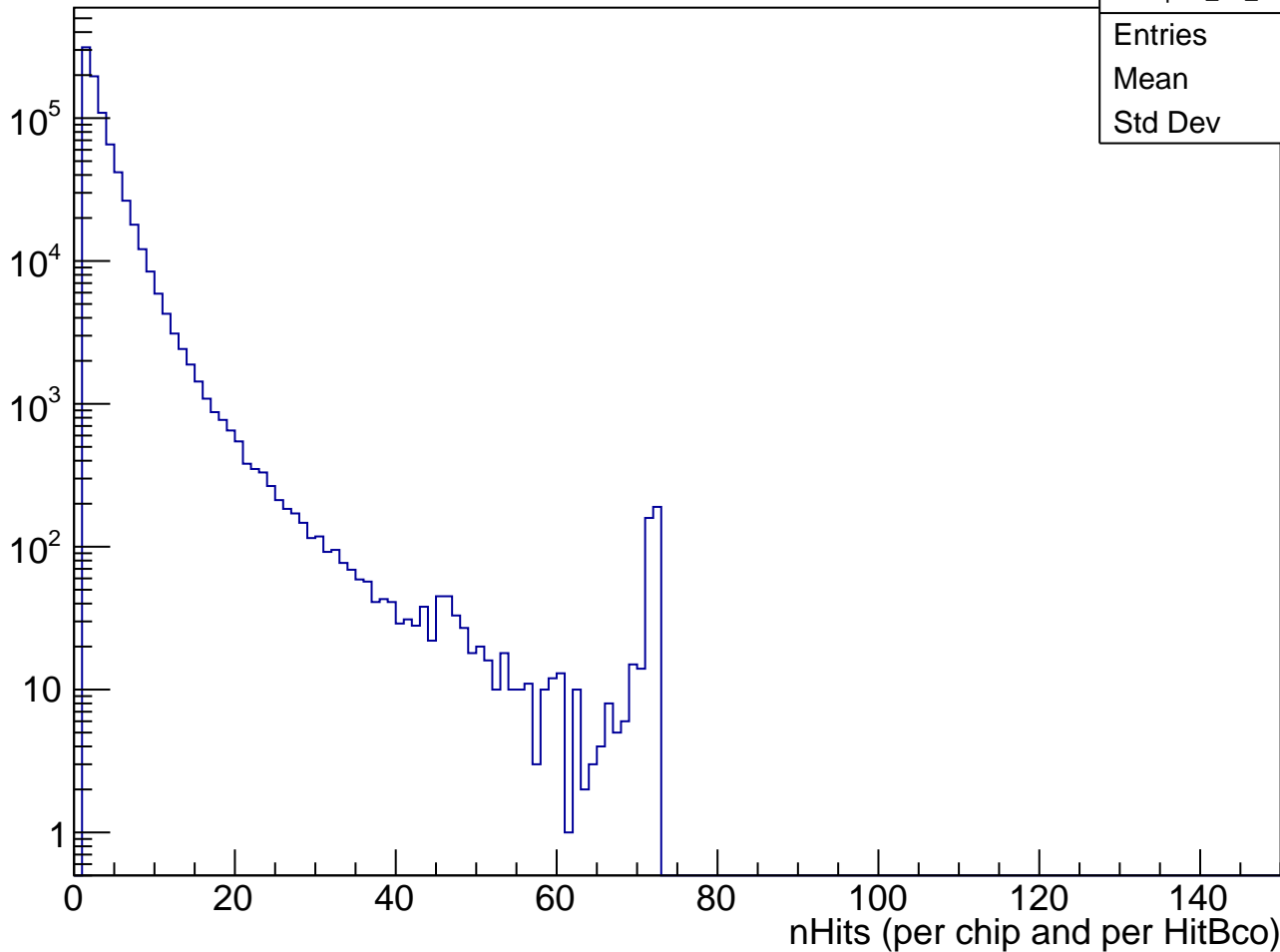
Entries 808269

Mean 2.932

Std Dev 3.434

nChipHit_F5_Fch3_Chip6

Entries



nChipHit_F5_Fch3_Chip6

Entries 817112

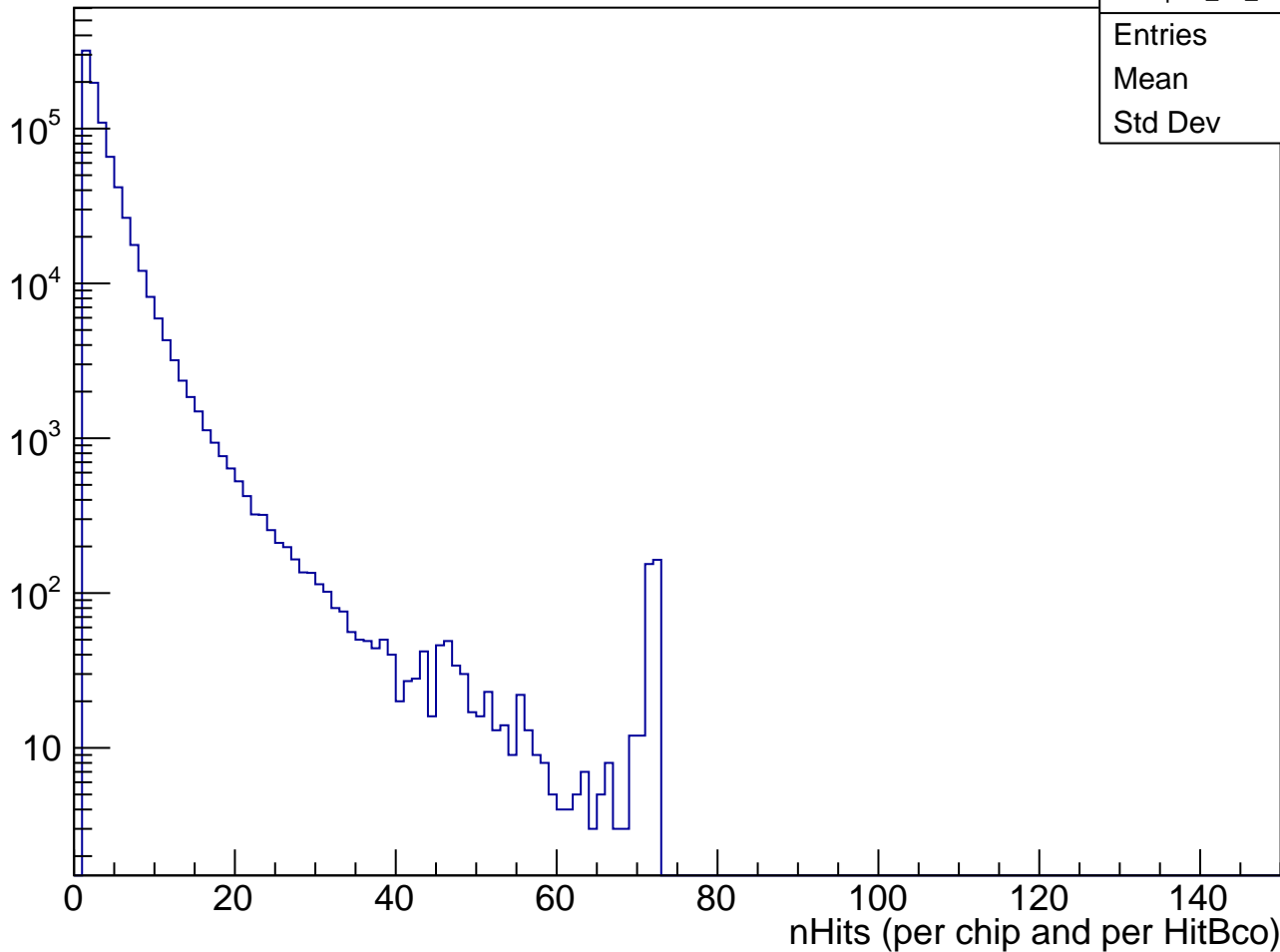
Mean 2.91

Std Dev 3.424

nHits (per chip and per HitBco)

nChipHit_F5_Fch3_Chip7

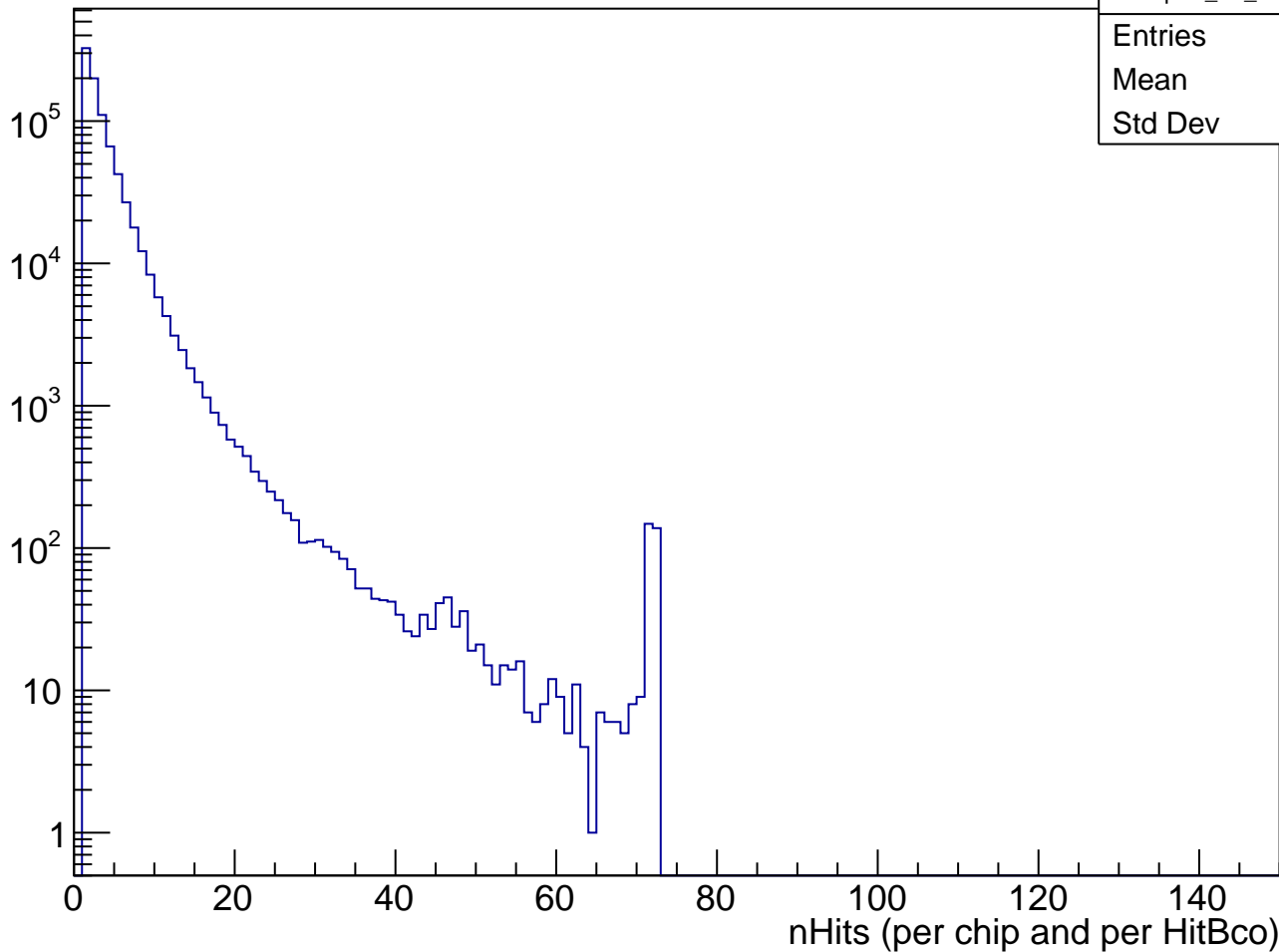
Entries



nChipHit_F5_Fch3_Chip7	
Entries	824570
Mean	2.89
Std Dev	3.376

nChipHit_F5_Fch3_Chip8

Entries



nChipHit_F5_Fch3_Chip8

Entries 835972

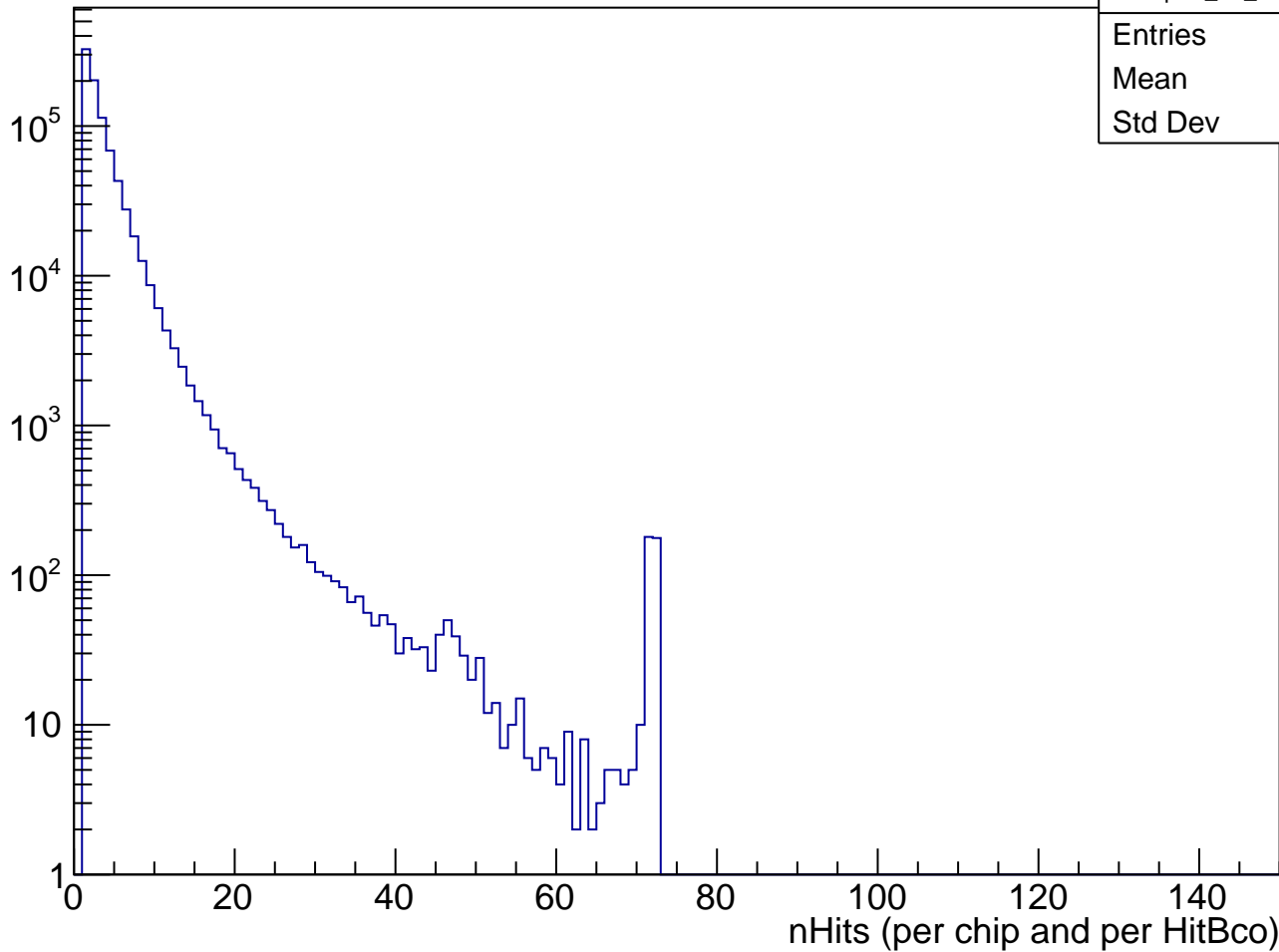
Mean 2.872

Std Dev 3.324

nHits (per chip and per HitBco)

nChipHit_F5_Fch3_Chip9

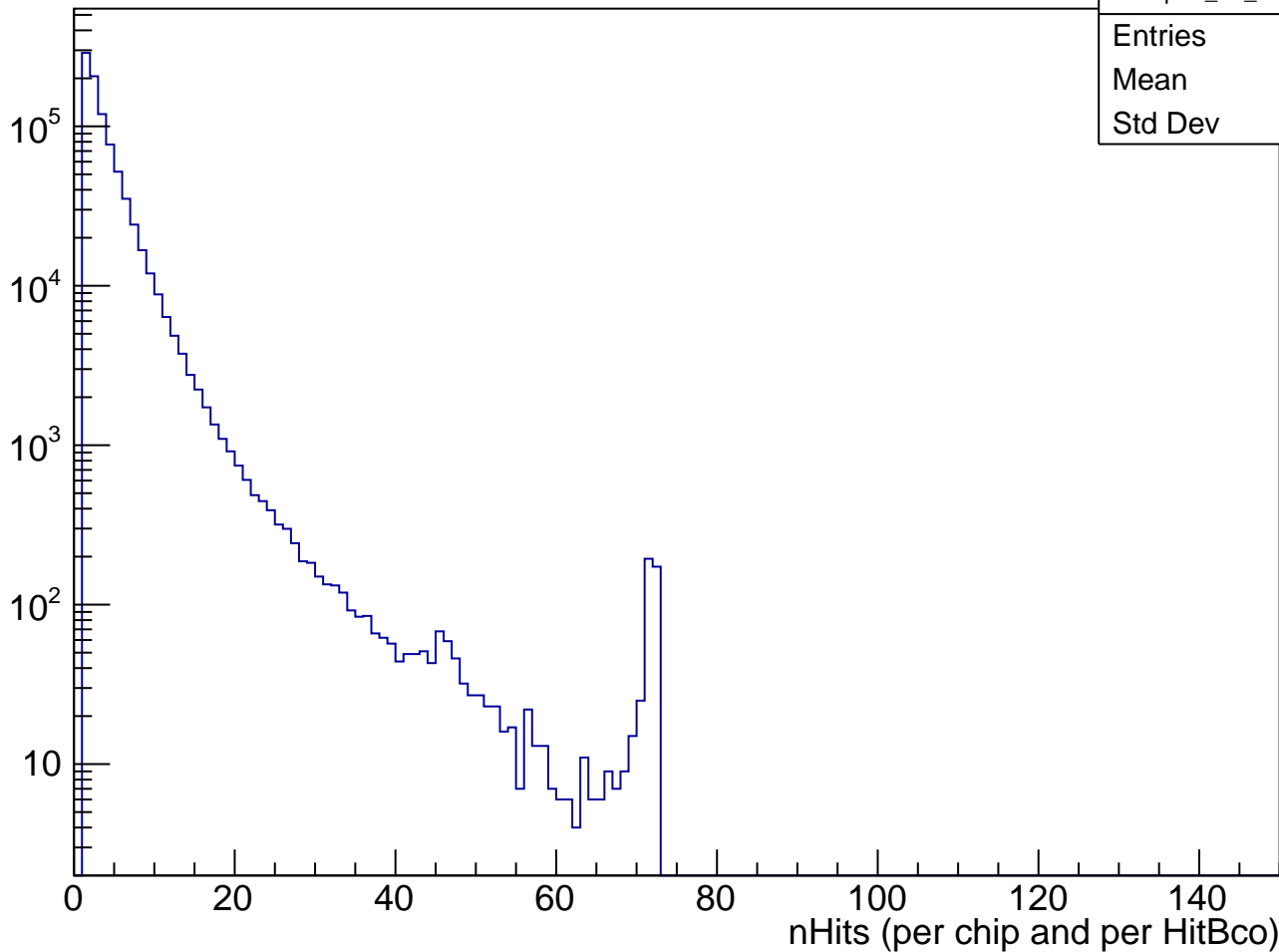
Entries



nChipHit_F5_Fch3_Chip9	
Entries	847743
Mean	2.899
Std Dev	3.384

nChipHit_F5_Fch4_Chip0

Entries



nChipHit_F5_Fch4_Chip0

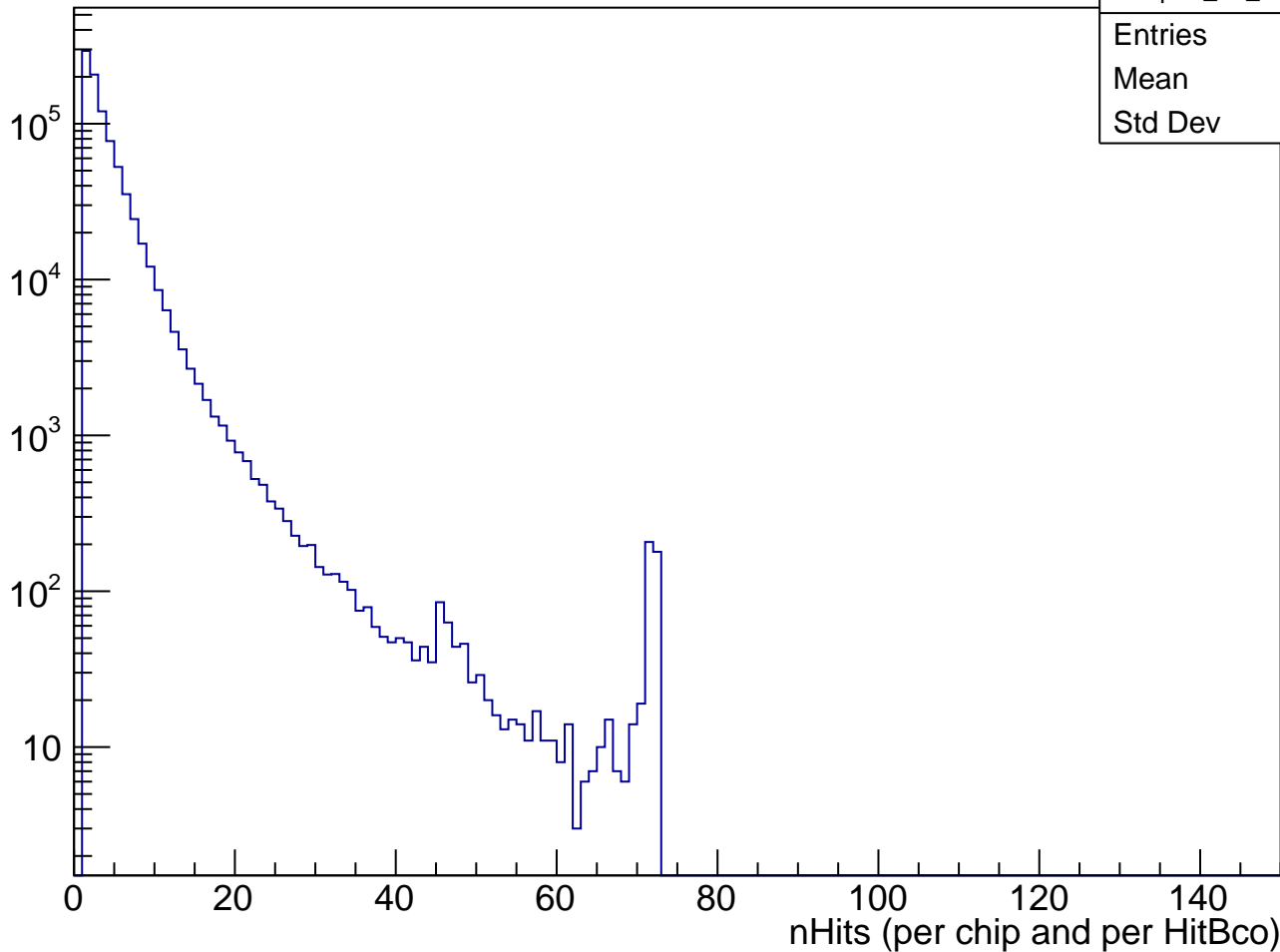
Entries 870952

Mean 3.283

Std Dev 3.767

nChipHit_F5_Fch4_Chip1

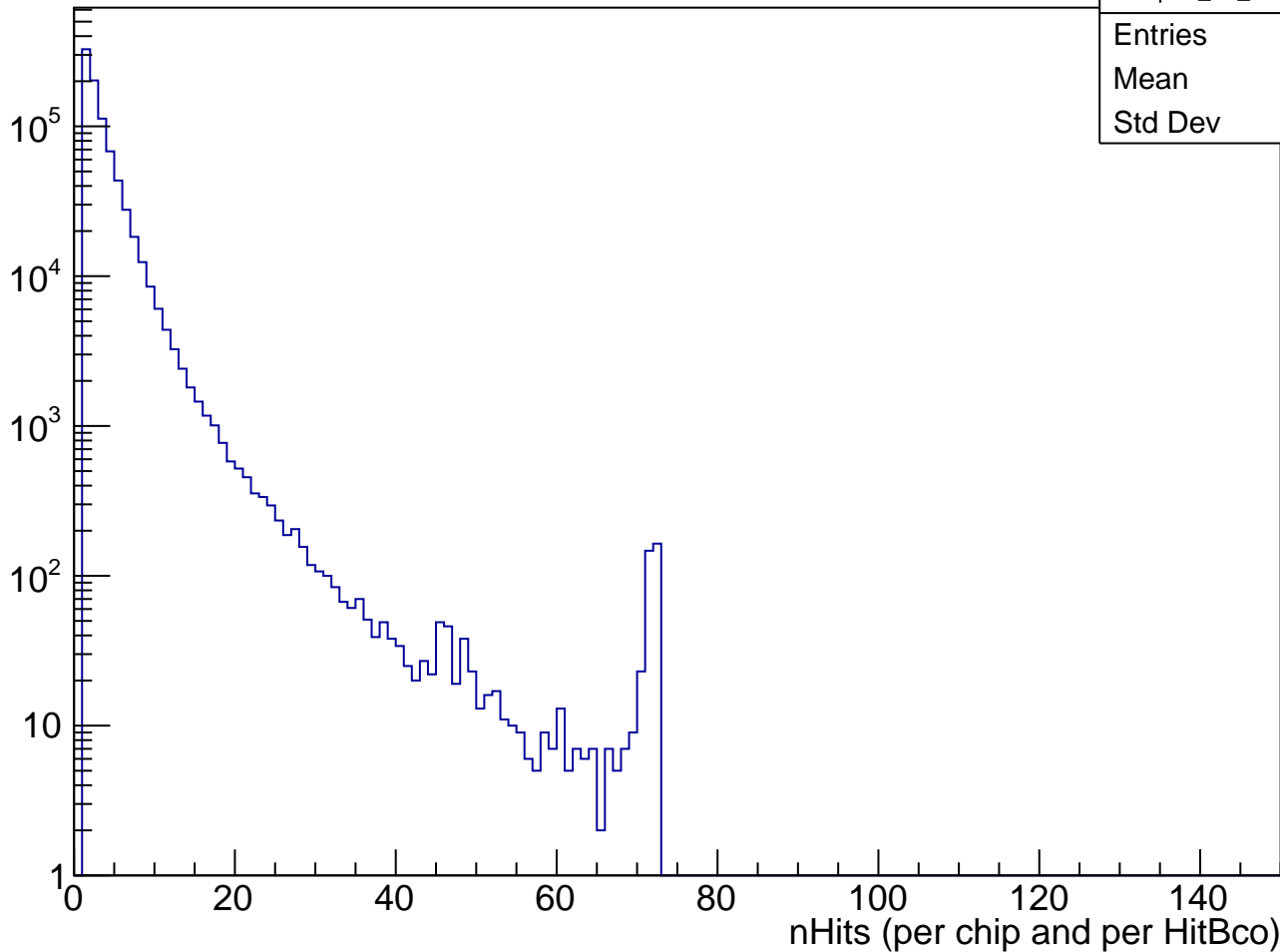
Entries



nChipHit_F5_Fch4_Chip1	
Entries	878454
Mean	3.271
Std Dev	3.762

nChipHit_F5_Fch4_Chip10

Entries

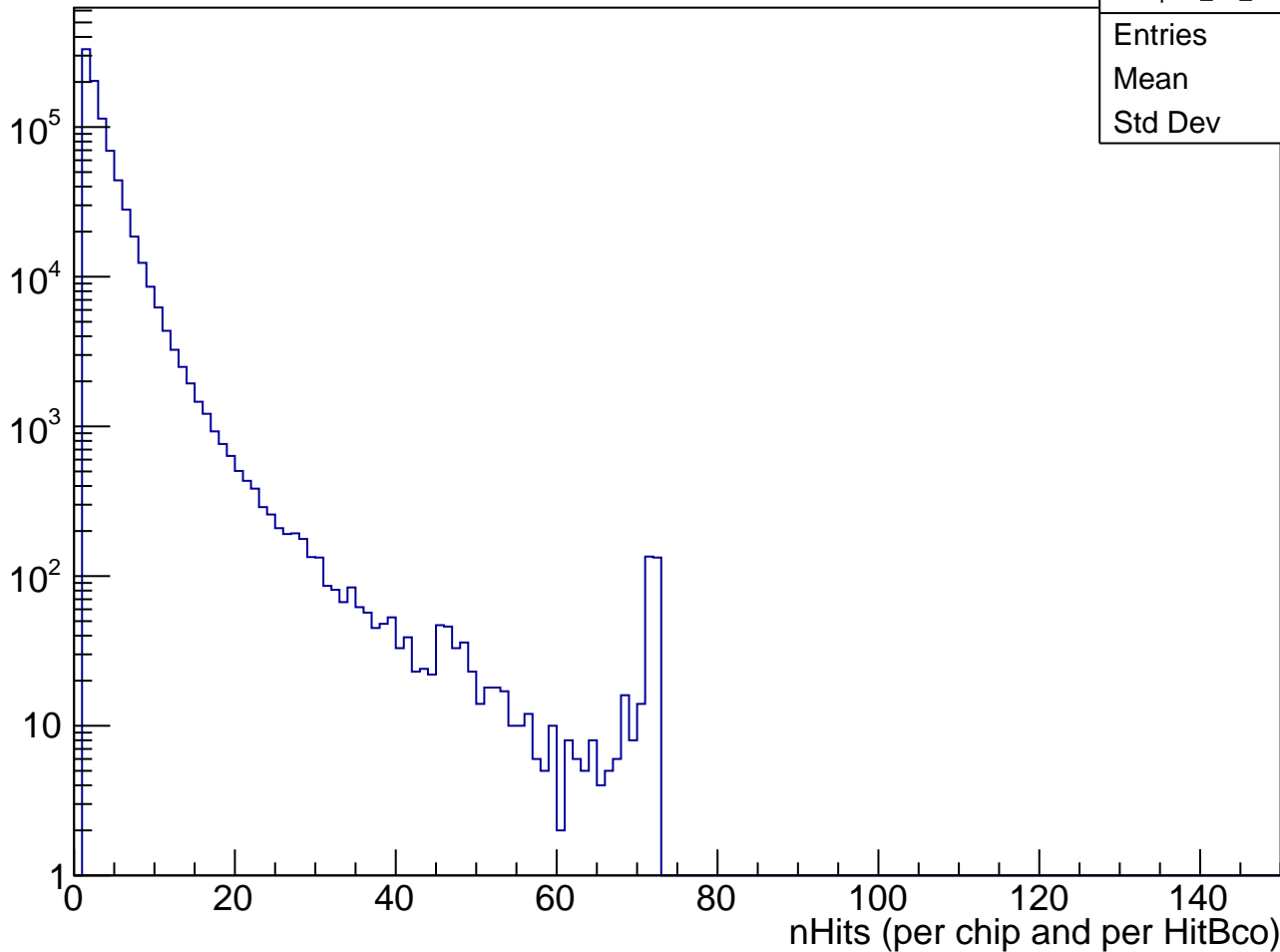


nChipHit_F5_Fch4_Chip10	
Entries	847818
Mean	2.893
Std Dev	3.359

nHits (per chip and per HitBco)

nChipHit_F5_Fch4_Chip11

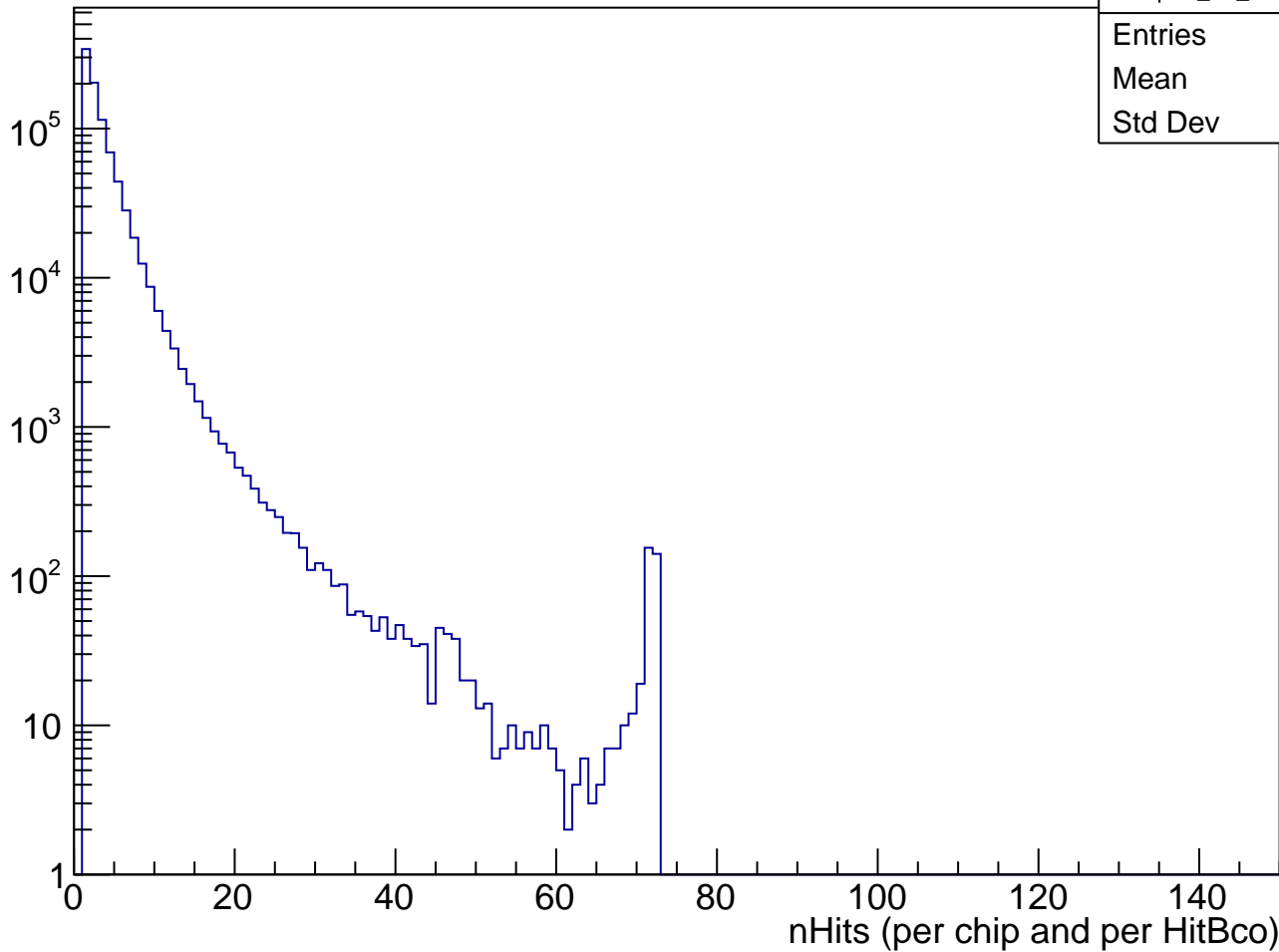
Entries



nChipHit_F5_Fch4_Chip11	
Entries	856947
Mean	2.891
Std Dev	3.329

nChipHit_F5_Fch4_Chip12

Entries

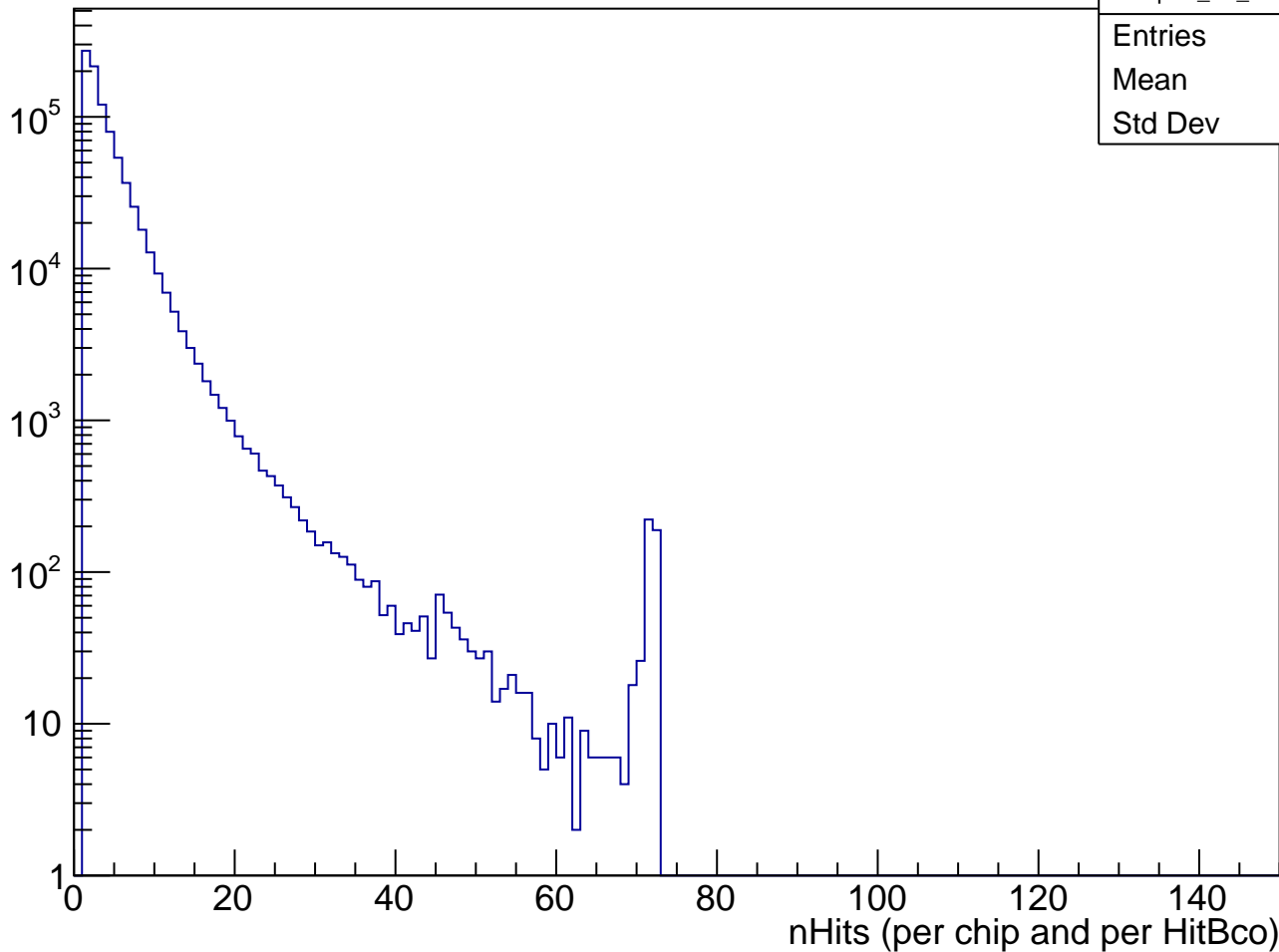


nChipHit_F5_Fch4_Chip12	
Entries	867676
Mean	2.873
Std Dev	3.328

nHits (per chip and per HitBco)

nChipHit_F5_Fch4_Chip13

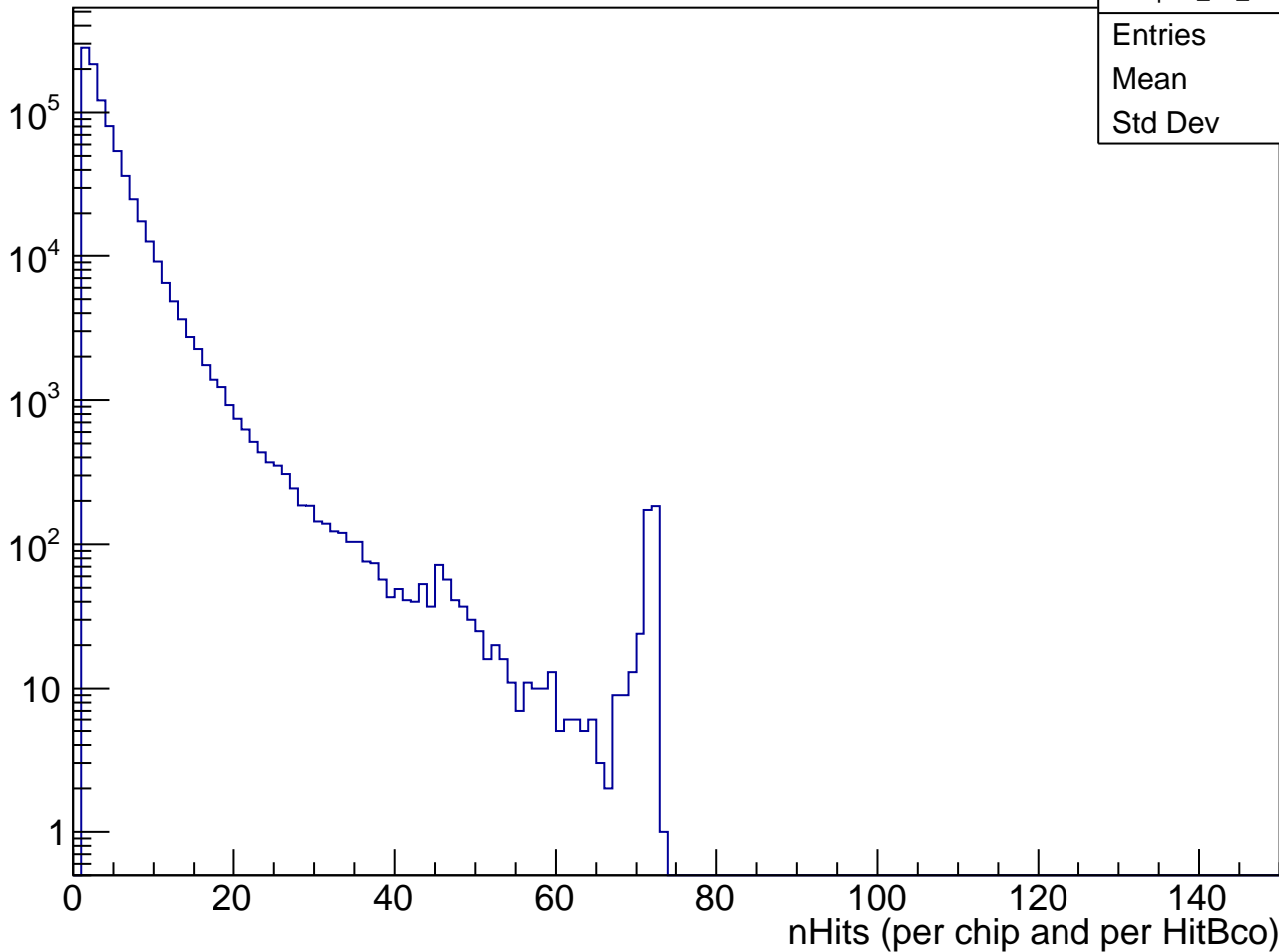
Entries



nChipHit_F5_Fch4_Chip13	
Entries	877718
Mean	3.377
Std Dev	3.839

nChipHit_F5_Fch4_Chip14

Entries

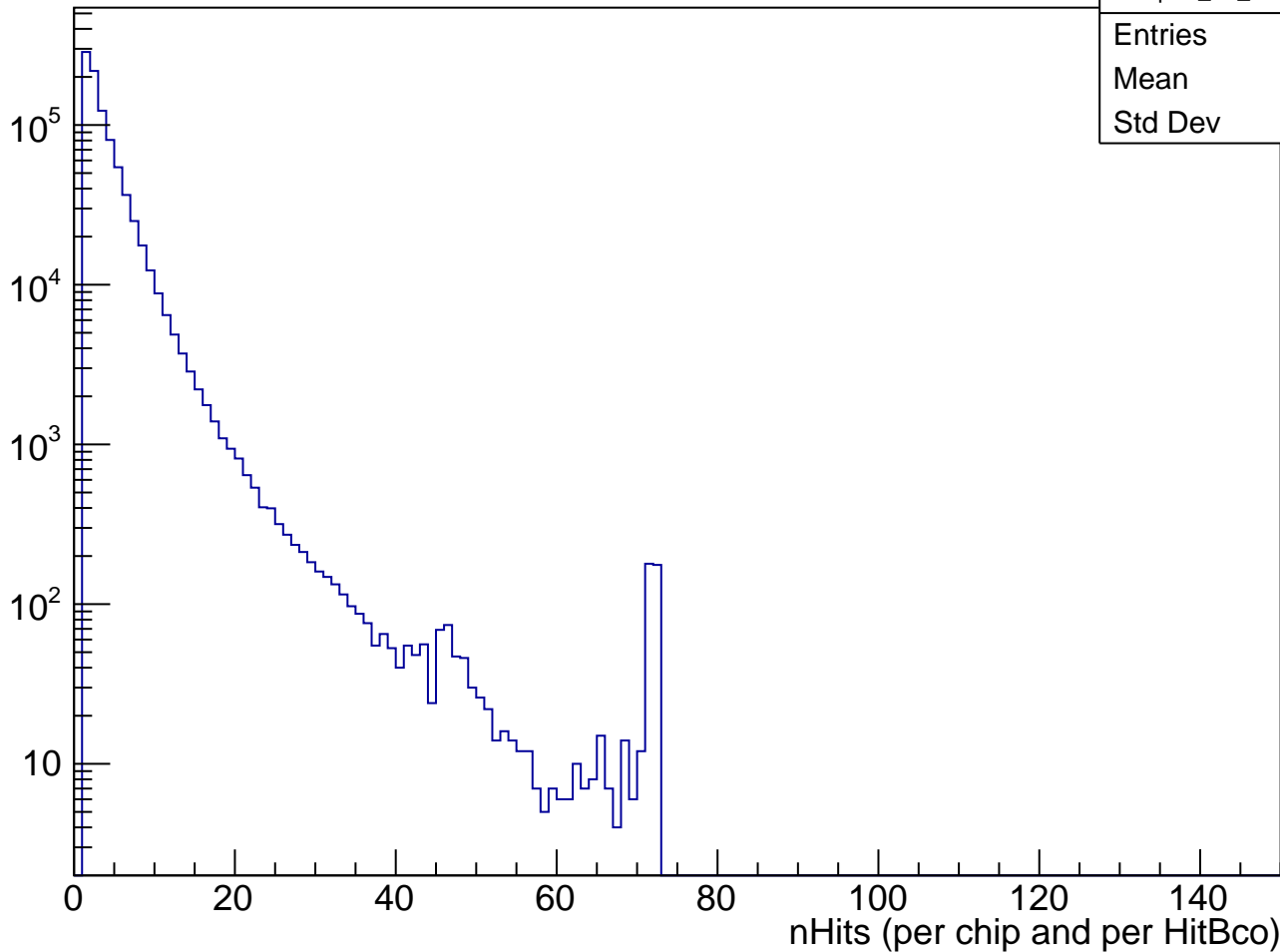


nChipHit_F5_Fch4_Chip14	
Entries	885228
Mean	3.313
Std Dev	3.725

nHits (per chip and per HitBco)

nChipHit_F5_Fch4_Chip15

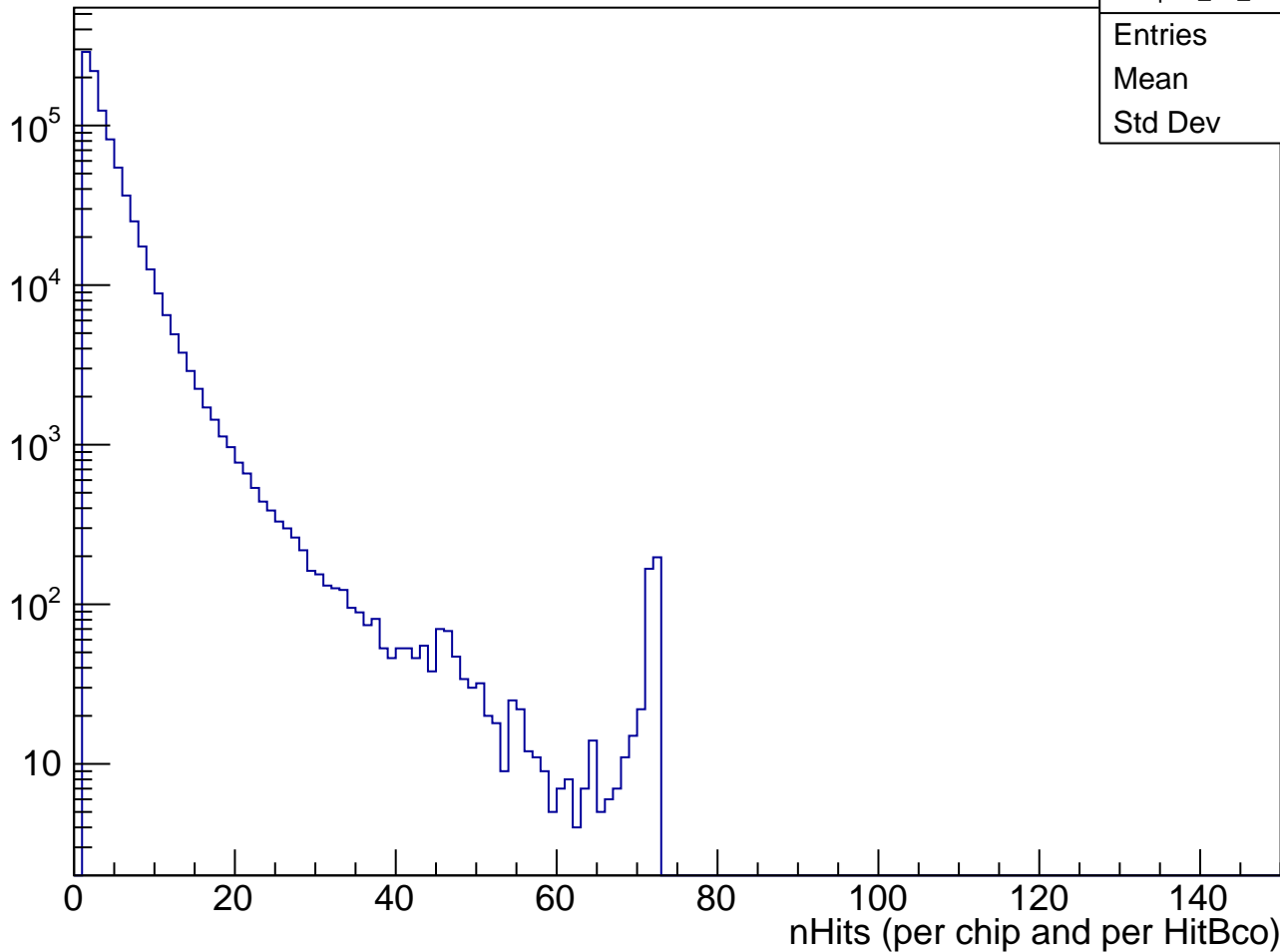
Entries



nChipHit_F5_Fch4_Chip15	
Entries	894288
Mean	3.295
Std Dev	3.715

nChipHit_F5_Fch4_Chip16

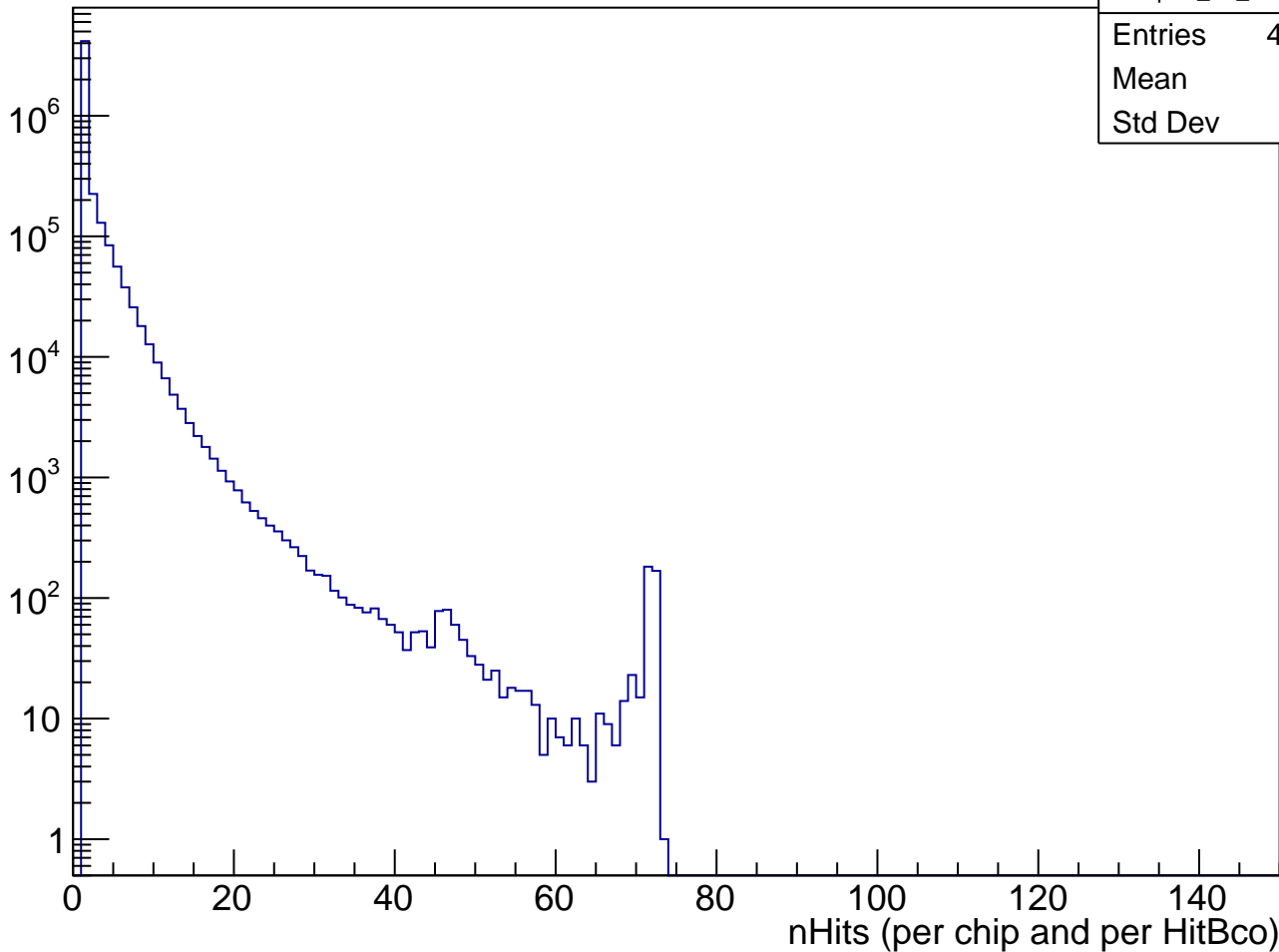
Entries



nChipHit_F5_Fch4_Chip16	
Entries	900703
Mean	3.296
Std Dev	3.737

nChipHit_F5_Fch4_Chip17

Entries

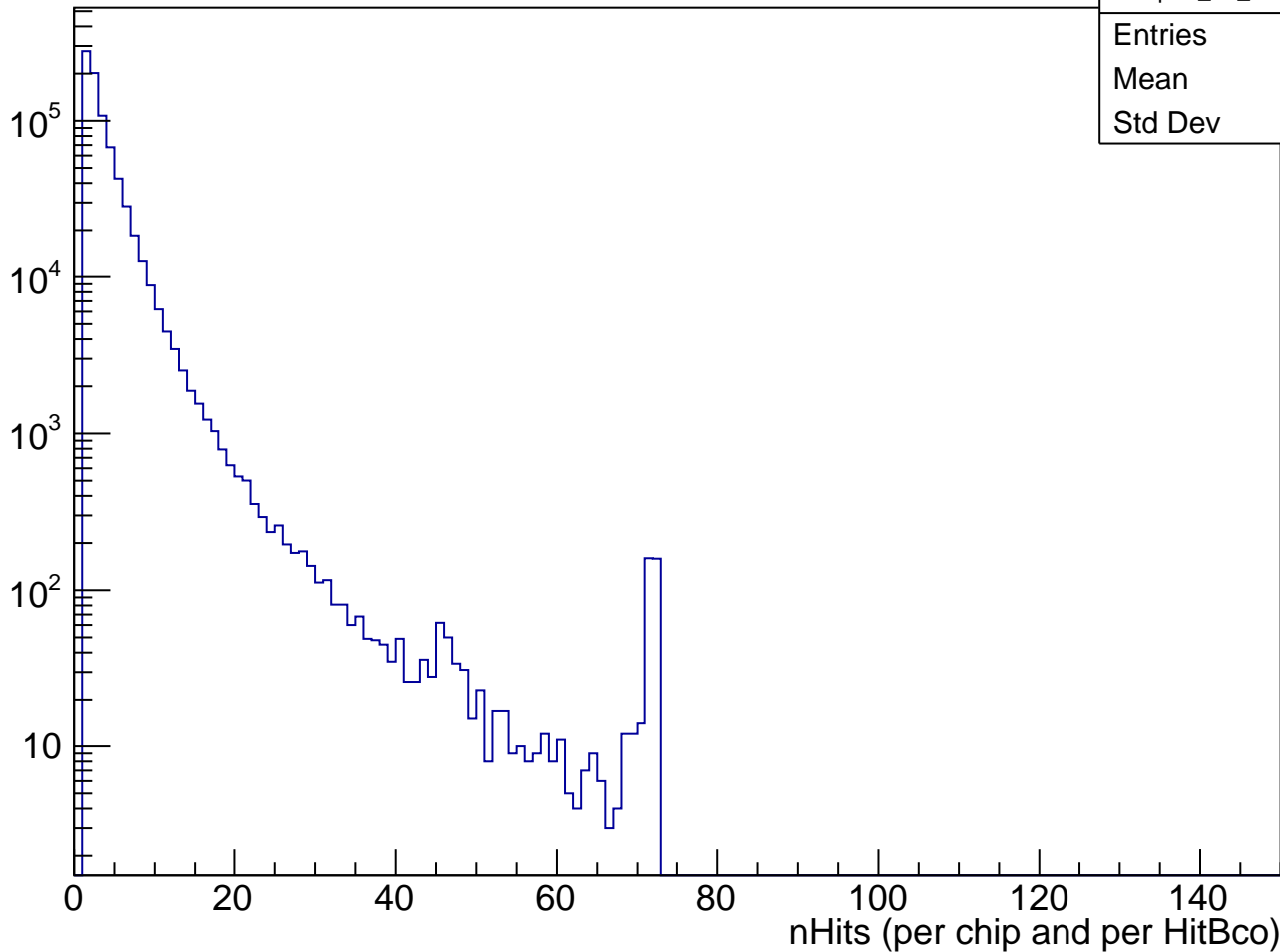


nChipHit_F5_Fch4_Chip17	
Entries	4791012
Mean	1.442
Std Dev	1.865

nHits (per chip and per HitBco)

nChipHit_F5_Fch4_Chip18

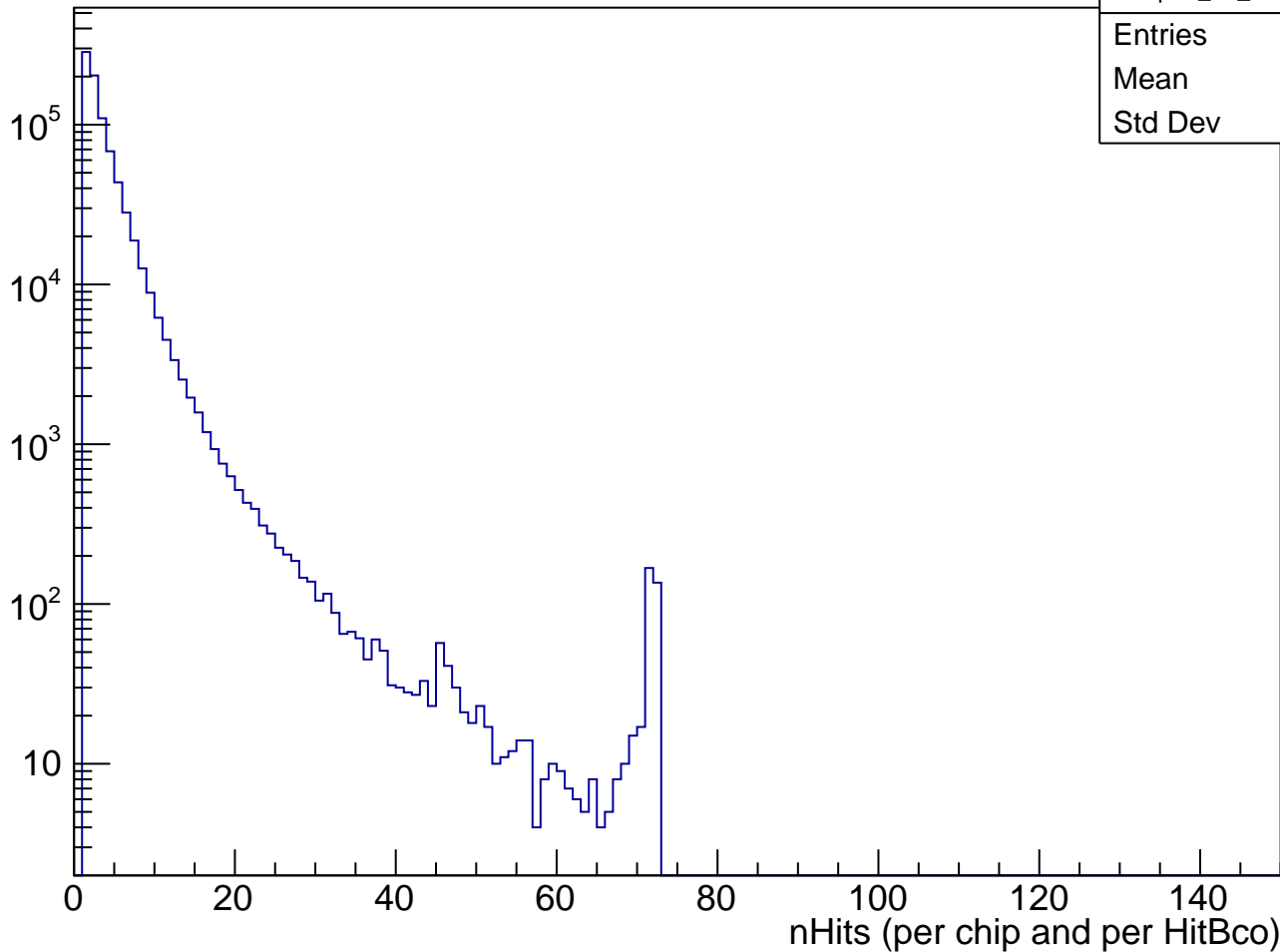
Entries



nChipHit_F5_Fch4_Chip18	
Entries	794354
Mean	3.032
Std Dev	3.489

nChipHit_F5_Fch4_Chip19

Entries

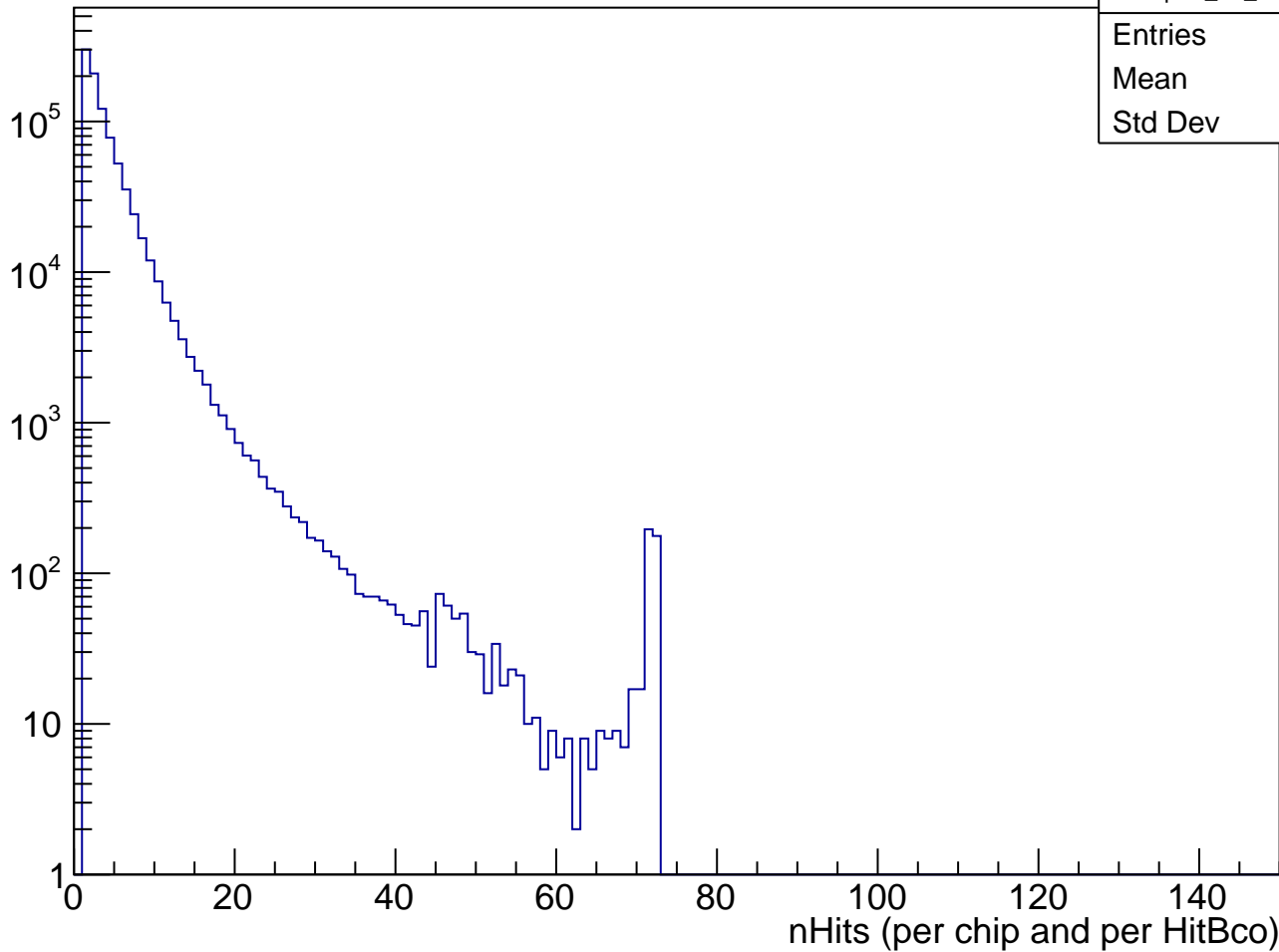


nChipHit_F5_Fch4_Chip19	
Entries	806119
Mean	3.01
Std Dev	3.442

nHits (per chip and per HitBco)

nChipHit_F5_Fch4_Chip2

Entries



nChipHit_F5_Fch4_Chip2

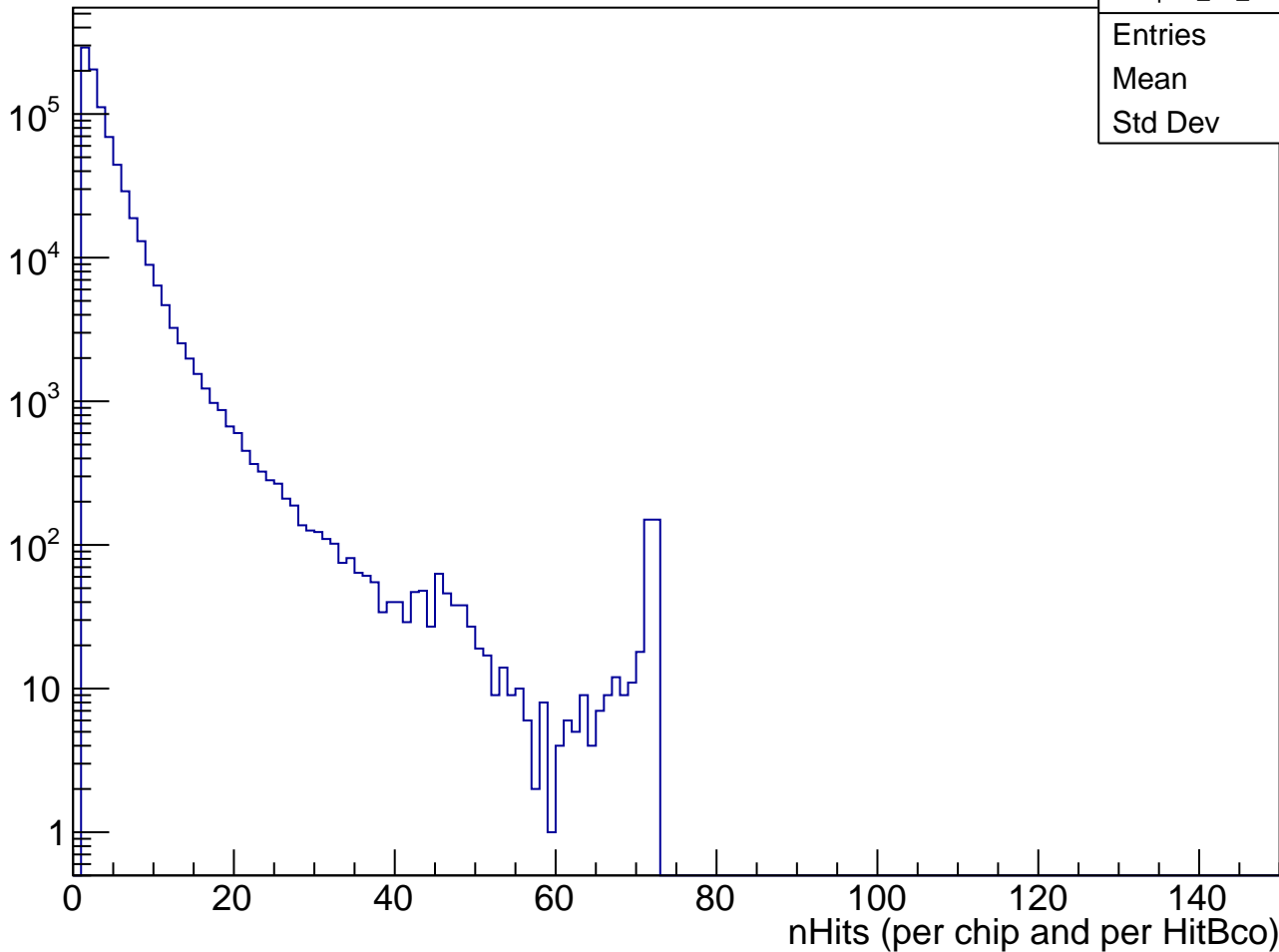
Entries 889057

Mean 3.249

Std Dev 3.744

nChipHit_F5_Fch4_Chip20

Entries

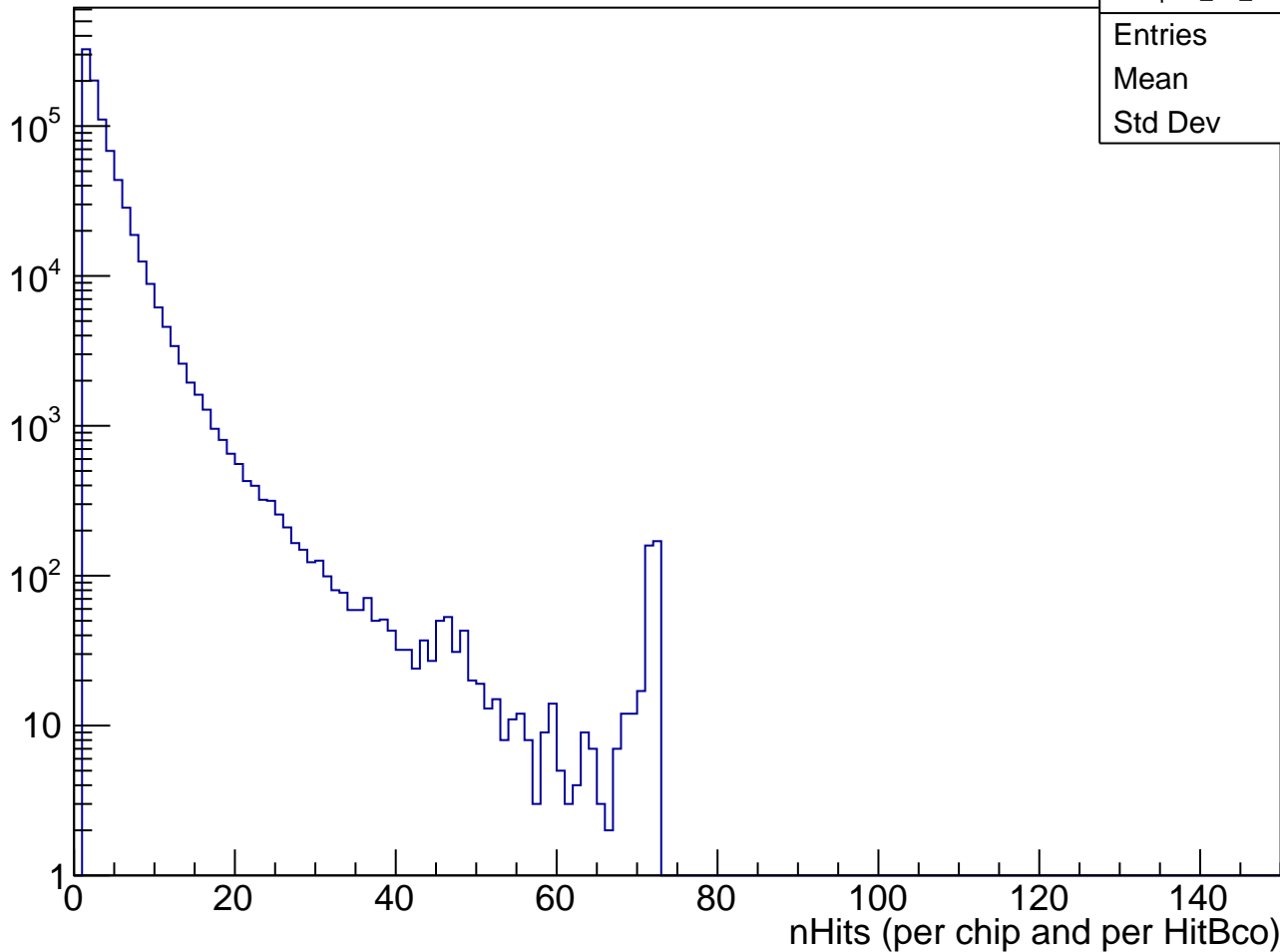


nChipHit_F5_Fch4_Chip20	
Entries	818270
Mean	3.017
Std Dev	3.463

nHits (per chip and per HitBco)

nChipHit_F5_Fch4_Chip21

Entries

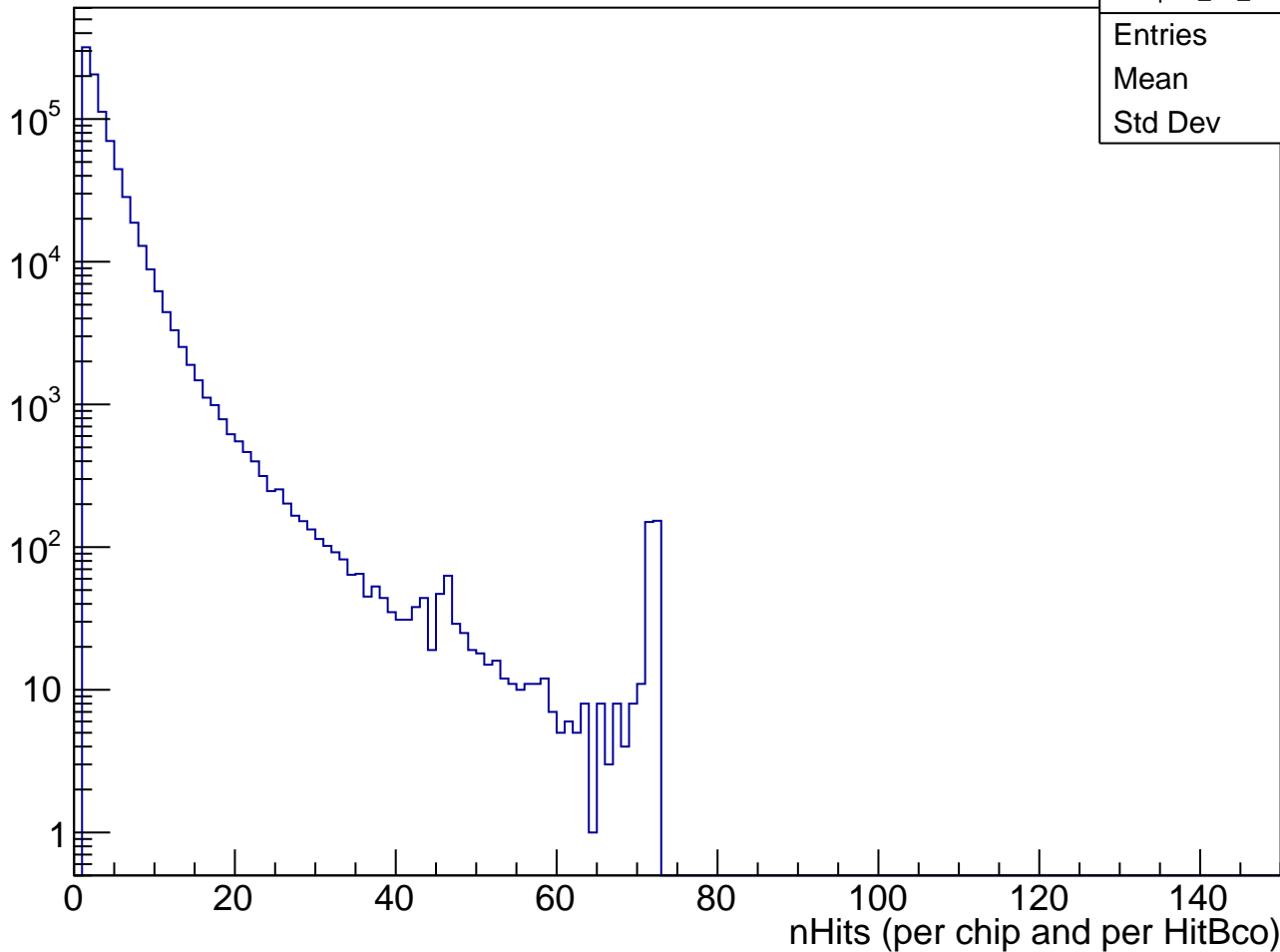


nChipHit_F5_Fch4_Chip21	
Entries	846326
Mean	2.927
Std Dev	3.427

nHits (per chip and per HitBco)

nChipHit_F5_Fch4_Chip22

Entries

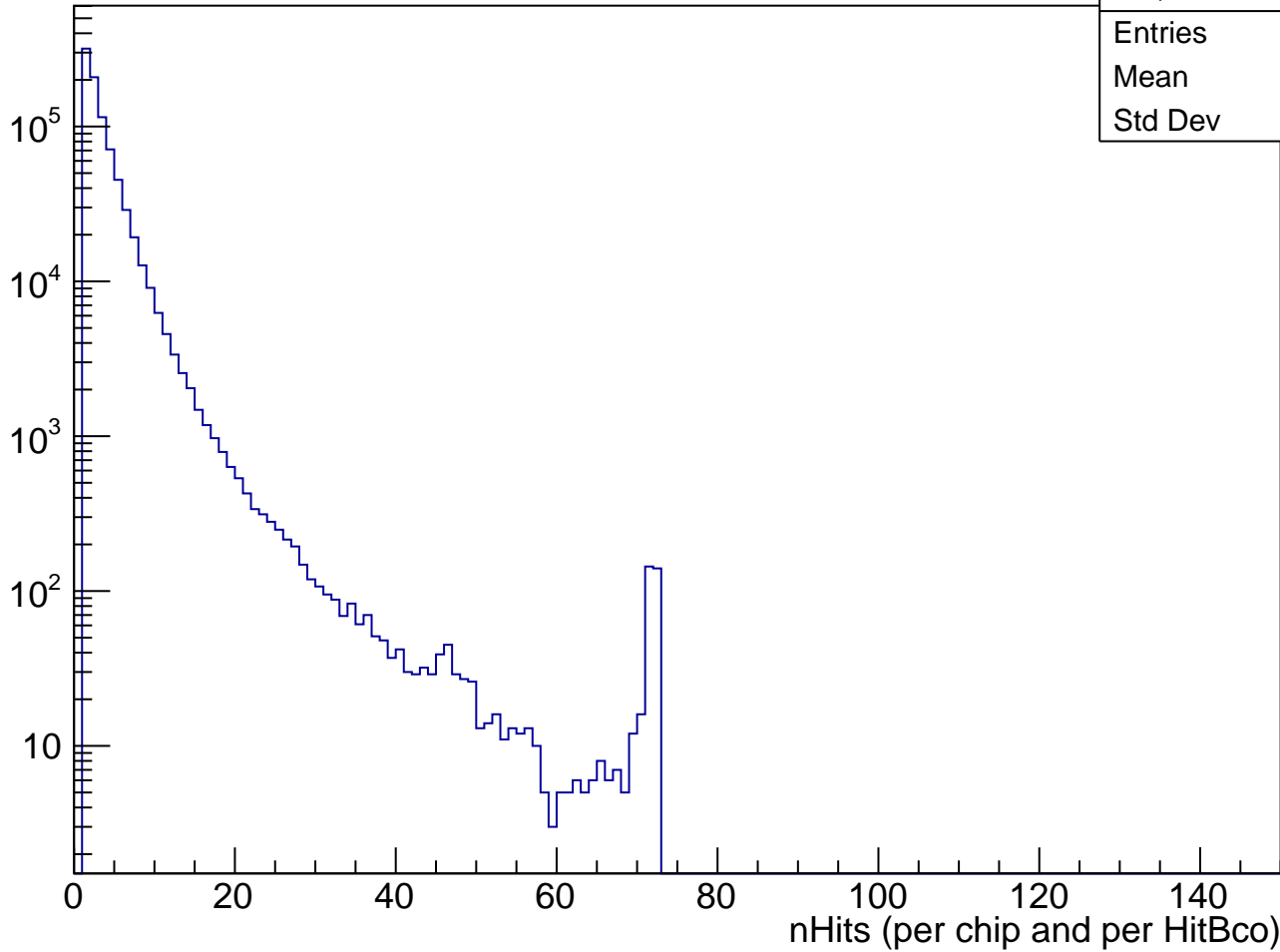


nChipHit_F5_Fch4_Chip22	
Entries	847855
Mean	2.932
Std Dev	3.372

nHits (per chip and per HitBco)

nChipHit_F5_Fch4_Chip23

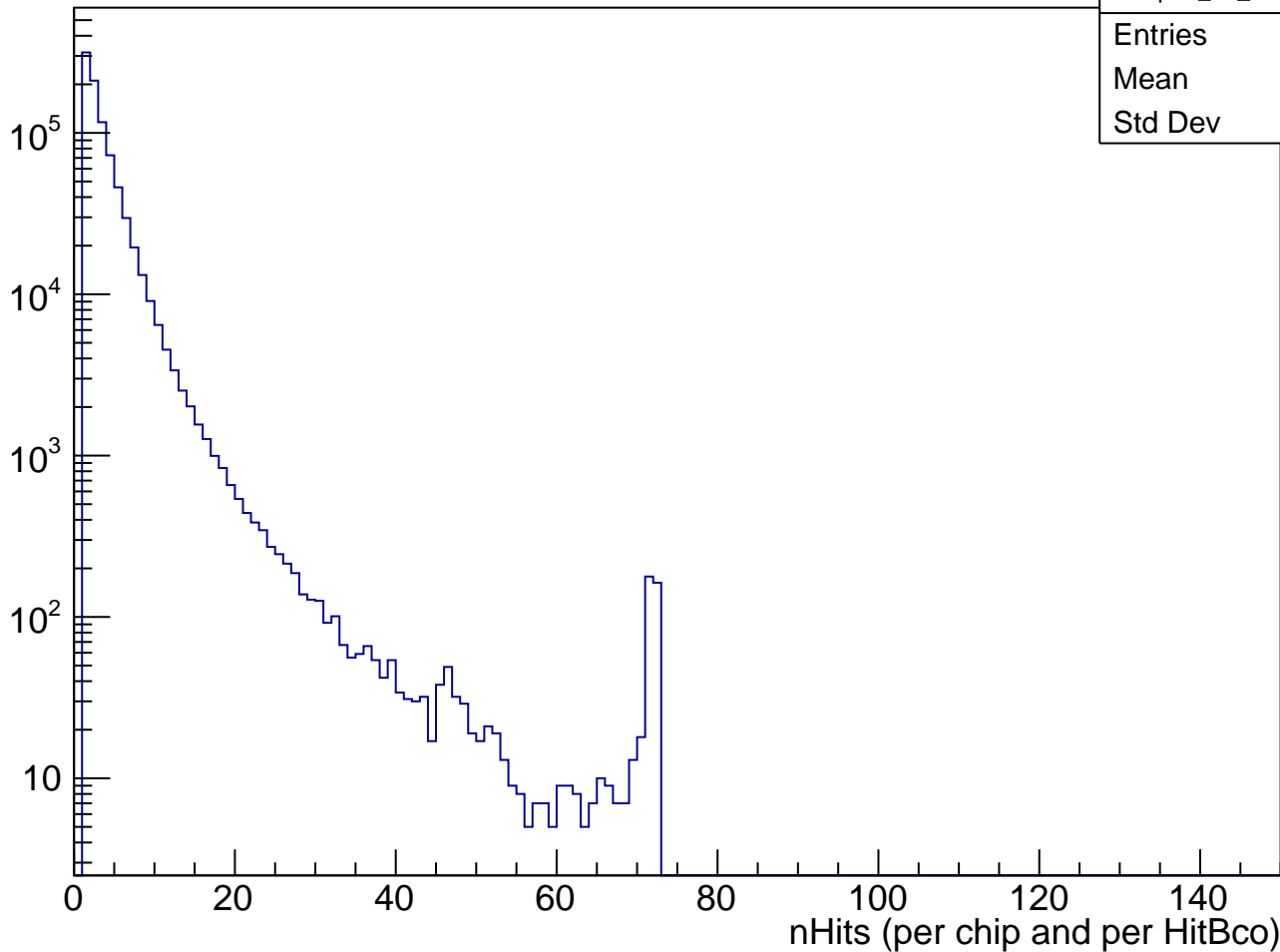
Entries



nChipHit_F5_Fch4_Chip23	
Entries	855849
Mean	2.94
Std Dev	3.352

nChipHit_F5_Fch4_Chip24

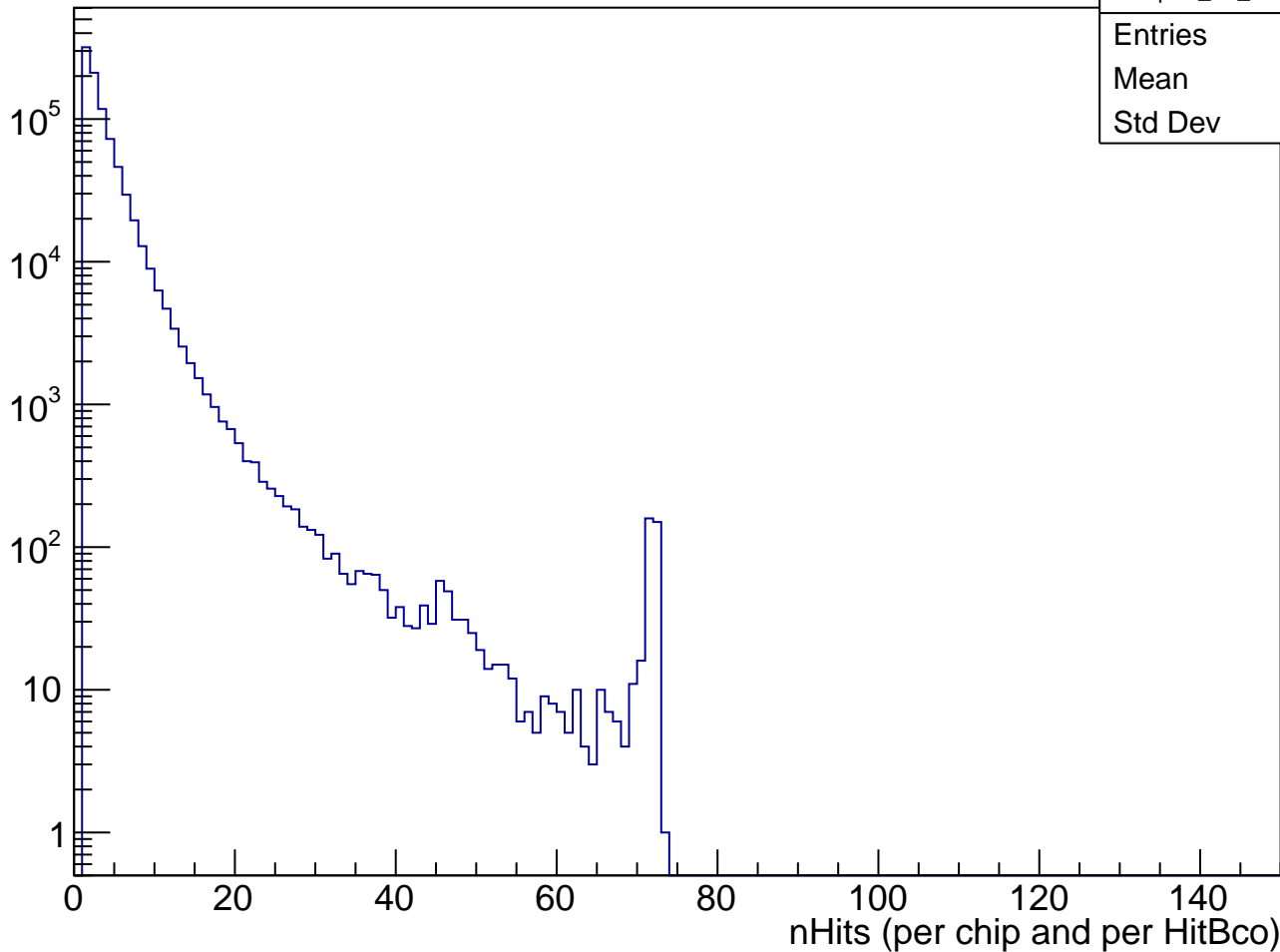
Entries



nChipHit_F5_Fch4_Chip24	
Entries	861578
Mean	2.966
Std Dev	3.413

nChipHit_F5_Fch4_Chip25

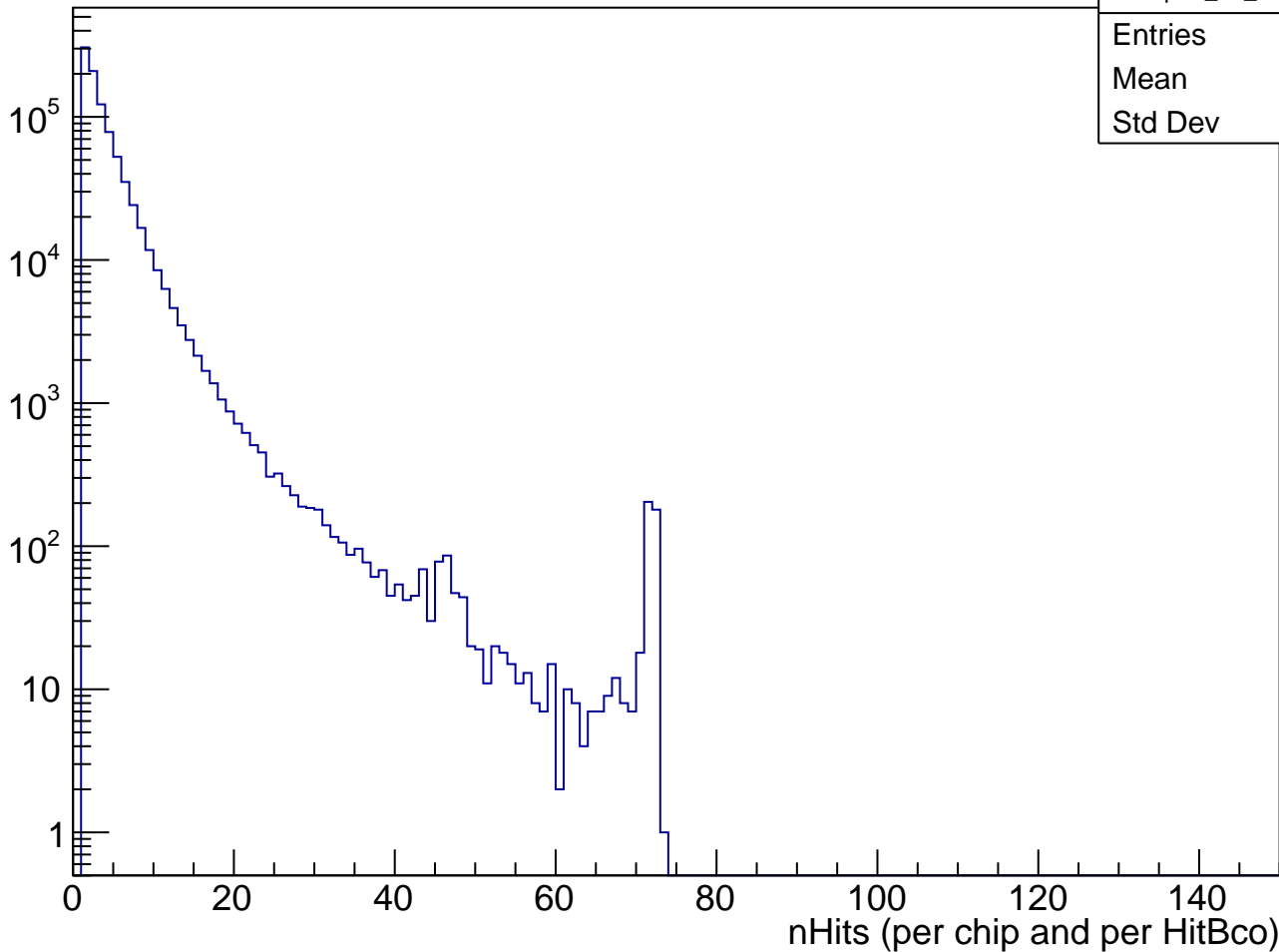
Entries



nChipHit_F5_Fch4_Chip25	
Entries	865000
Mean	2.946
Std Dev	3.36

nChipHit_F5_Fch4_Chip3

Entries



nChipHit_F5_Fch4_Chip3

Entries 894964

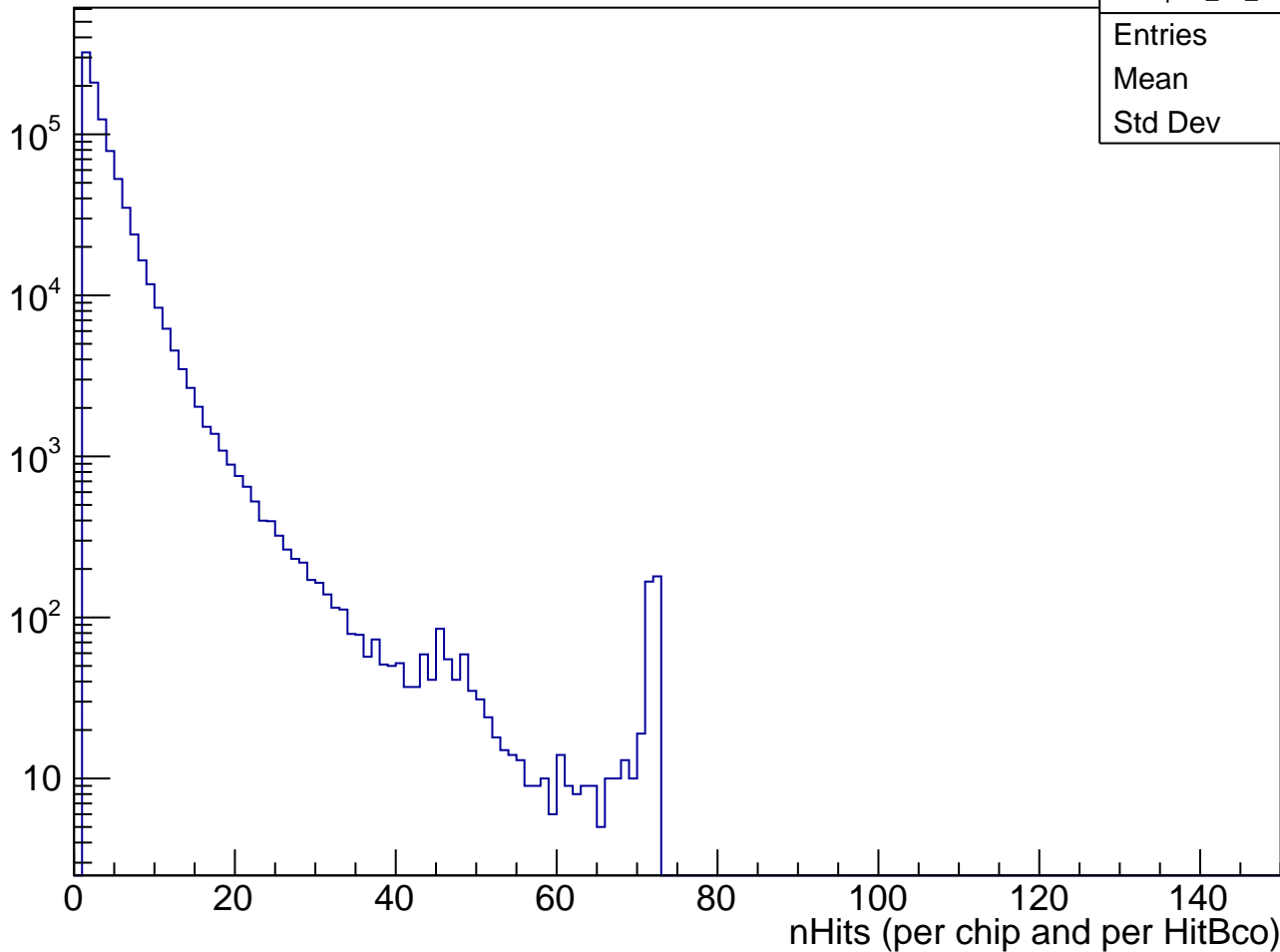
Mean 3.222

Std Dev 3.71

nHits (per chip and per HitBco)

nChipHit_F5_Fch4_Chip4

Entries



nChipHit_F5_Fch4_Chip4

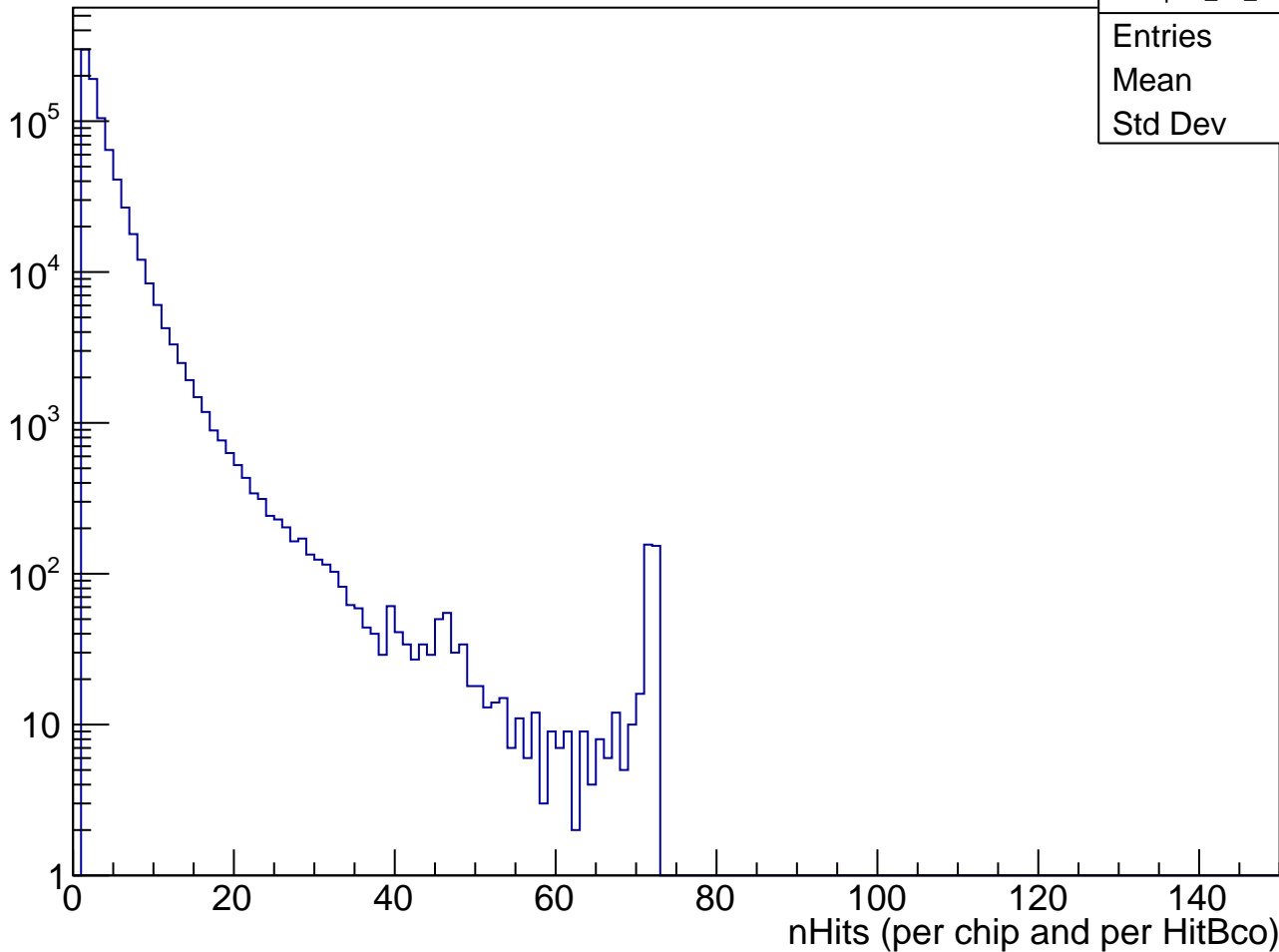
Entries 913043

Mean 3.172

Std Dev 3.663

nChipHit_F5_Fch4_Chip5

Entries



nChipHit_F5_Fch4_Chip5

Entries 791501

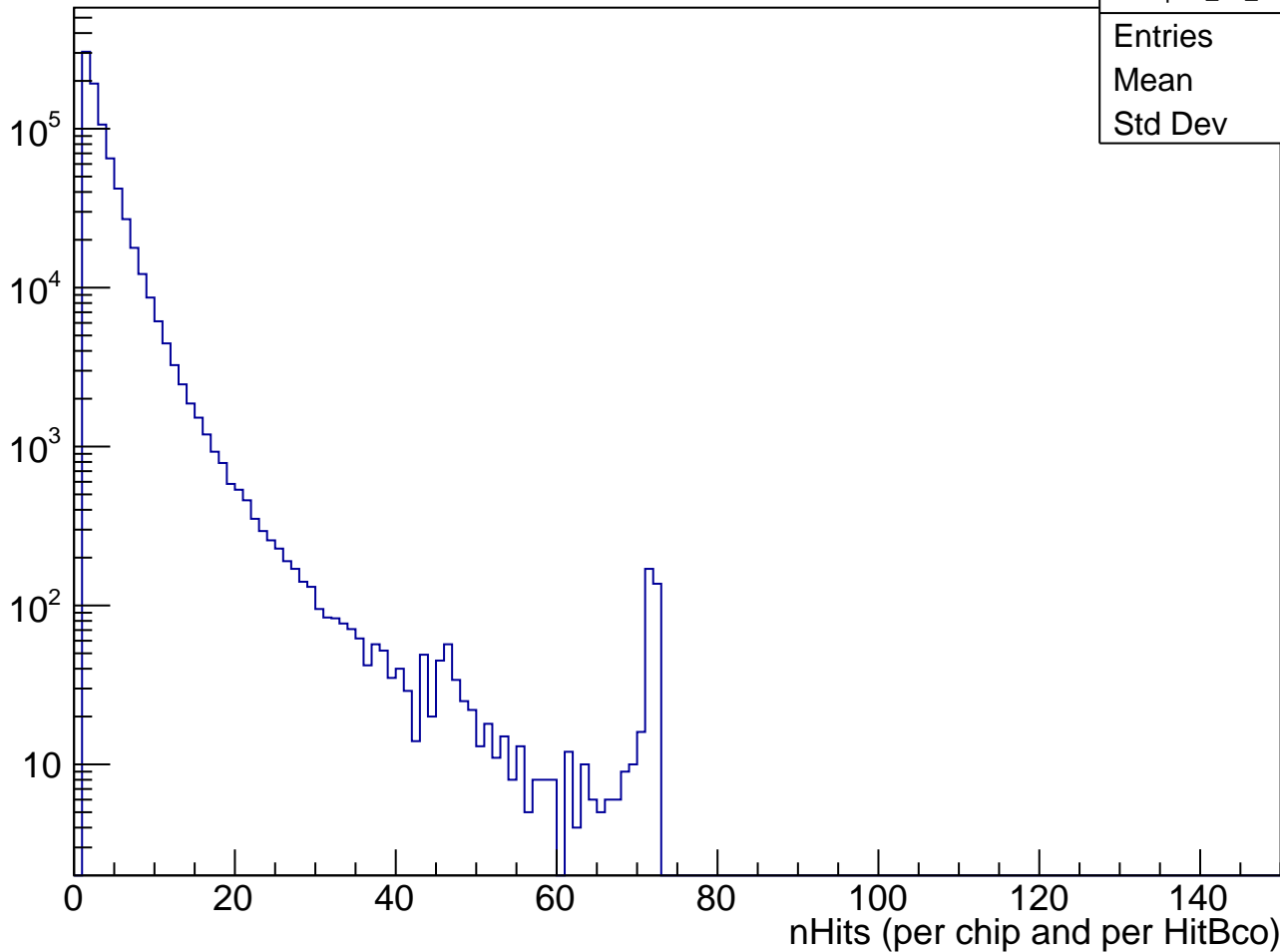
Mean 2.956

Std Dev 3.462

nHits (per chip and per HitBco)

nChipHit_F5_Fch4_Chip6

Entries



nChipHit_F5_Fch4_Chip6

Entries 803931

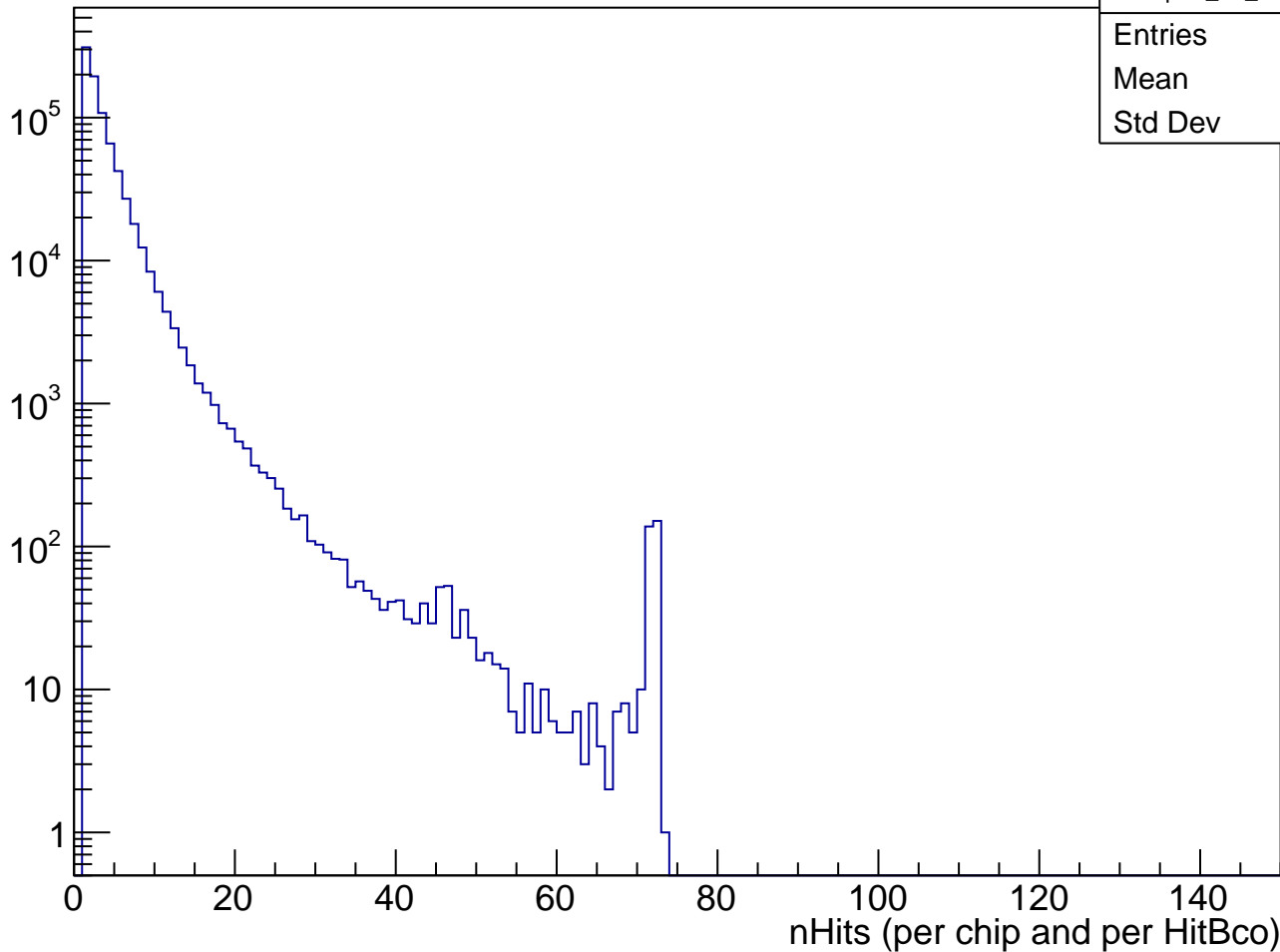
Mean 2.943

Std Dev 3.426

nHits (per chip and per HitBco)

nChipHit_F5_Fch4_Chip7

Entries

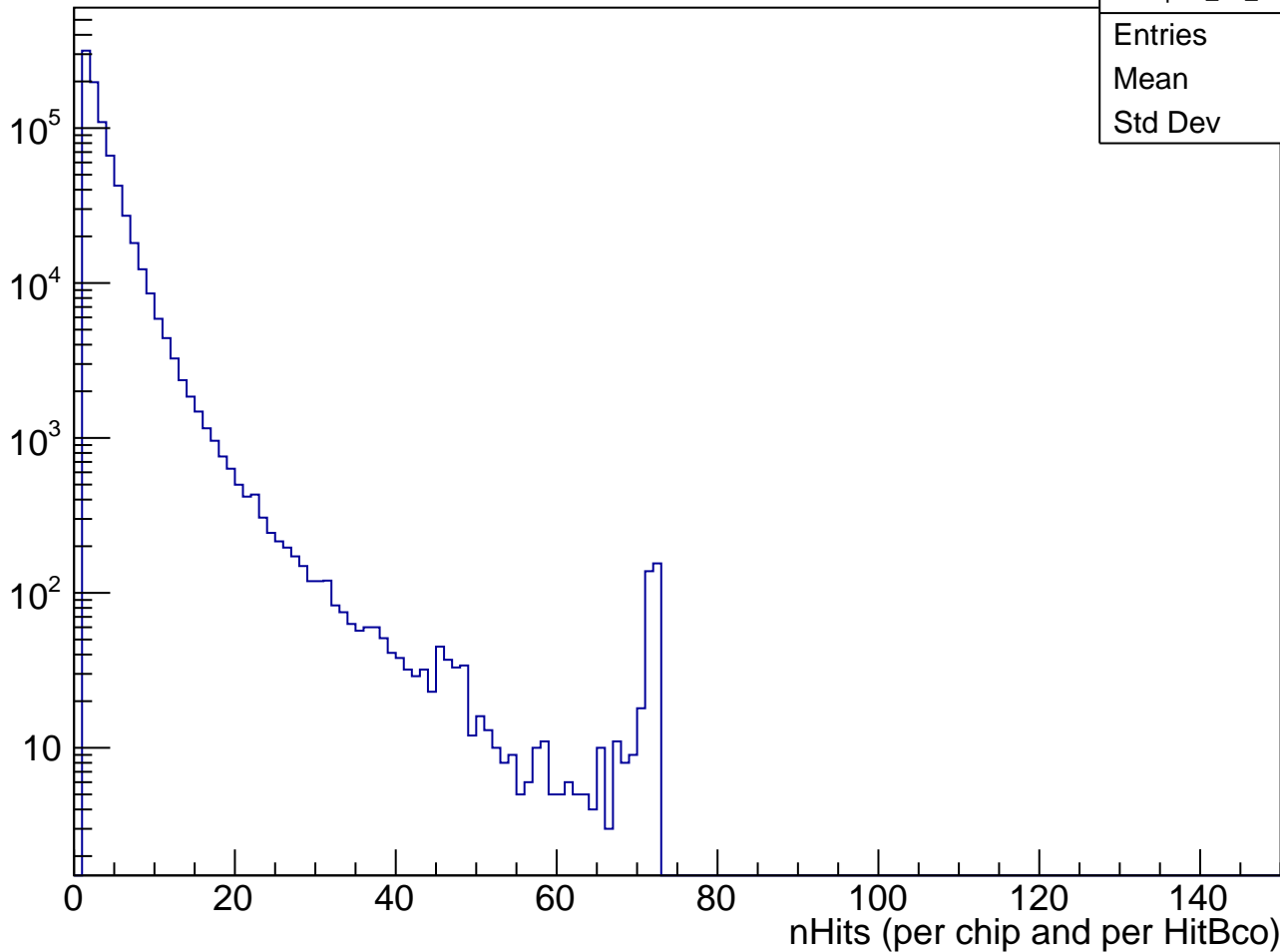


nChipHit_F5_Fch4_Chip7	
Entries	813843
Mean	2.93
Std Dev	3.39

nHits (per chip and per HitBco)

nChipHit_F5_Fch4_Chip8

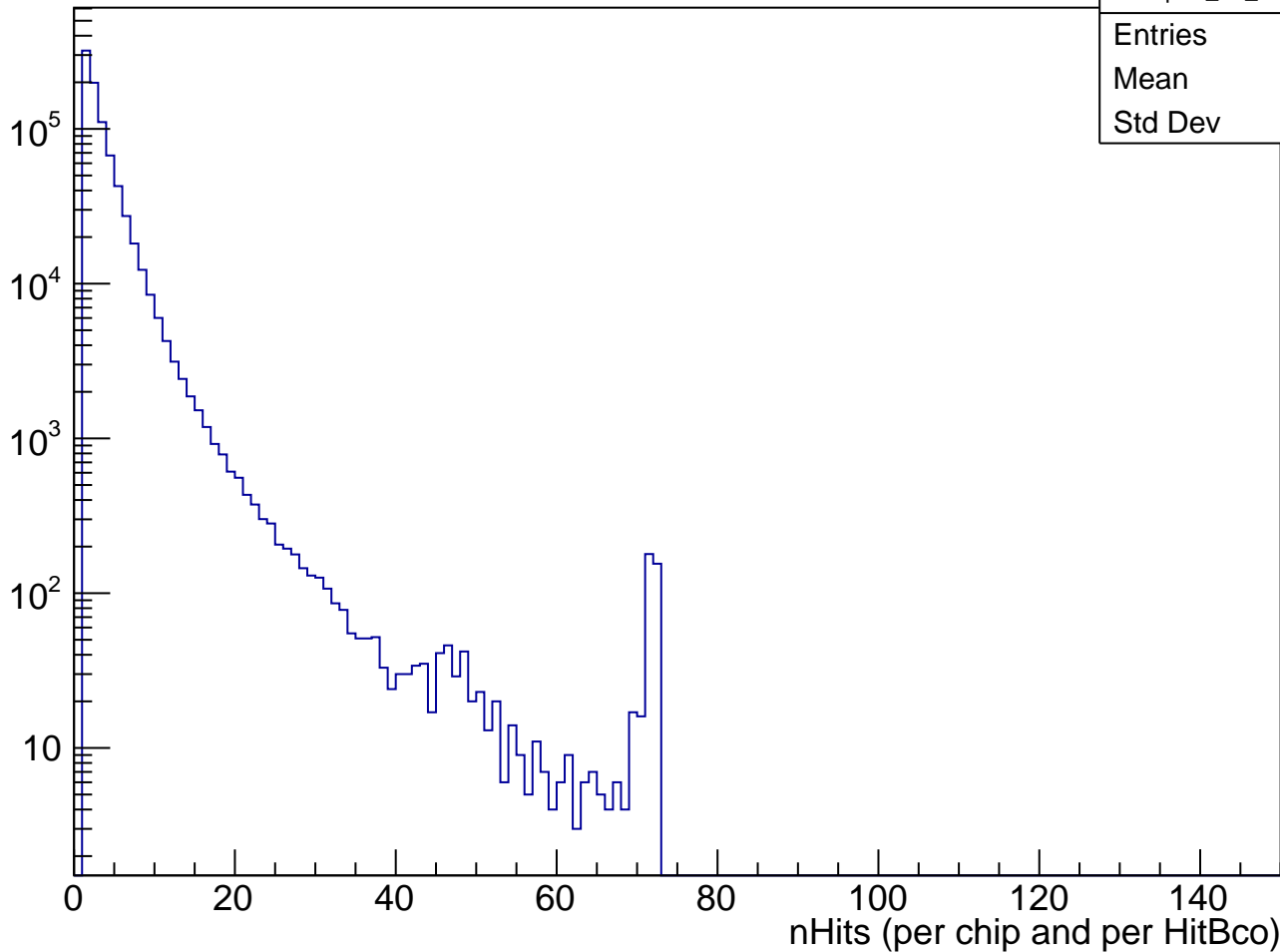
Entries



nChipHit_F5_Fch4_Chip8	
Entries	825304
Mean	2.911
Std Dev	3.373

nChipHit_F5_Fch4_Chip9

Entries



nChipHit_F5_Fch4_Chip9

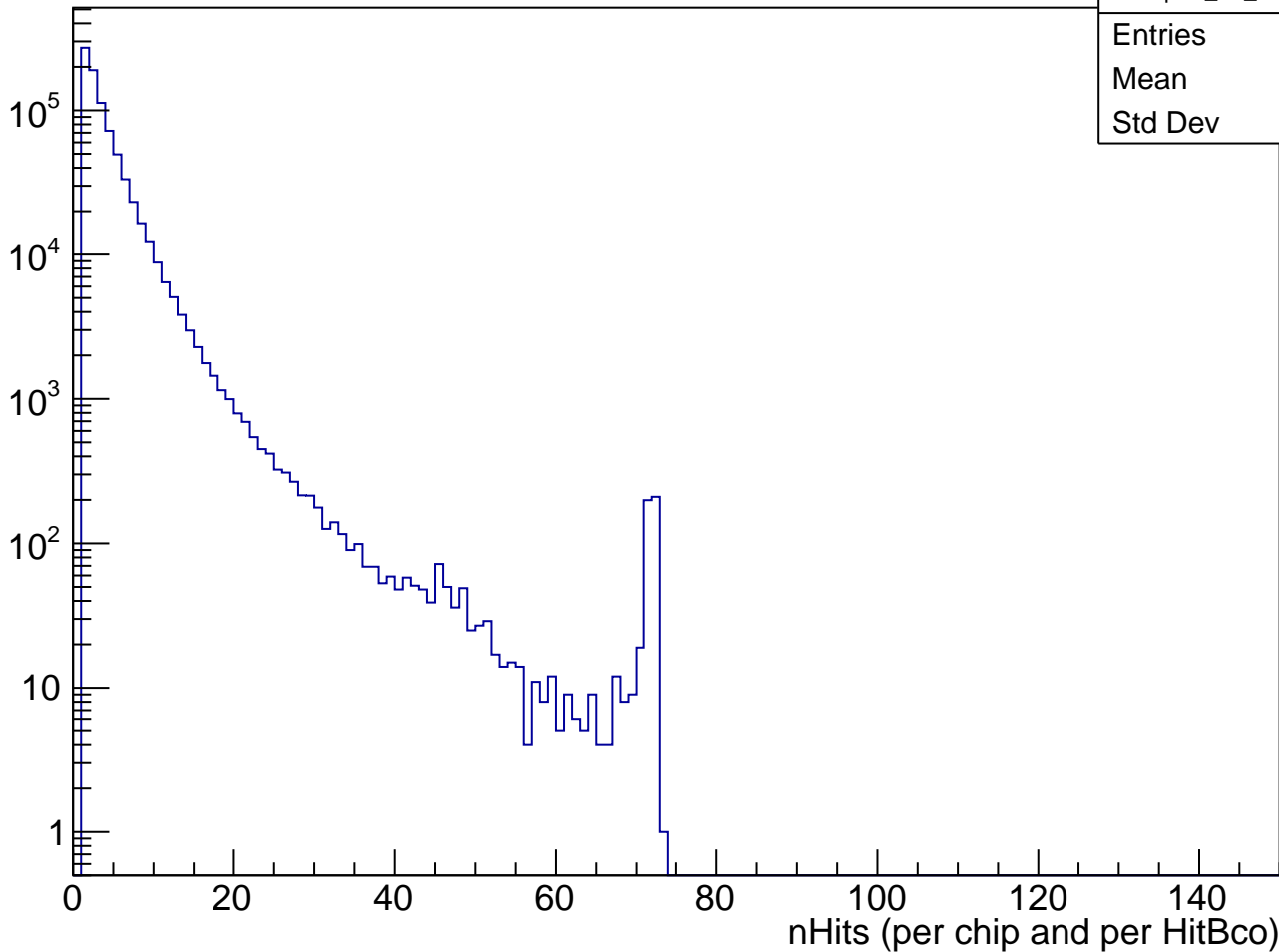
Entries 833223

Mean 2.905

Std Dev 3.394

nChipHit_F5_Fch5_Chip0

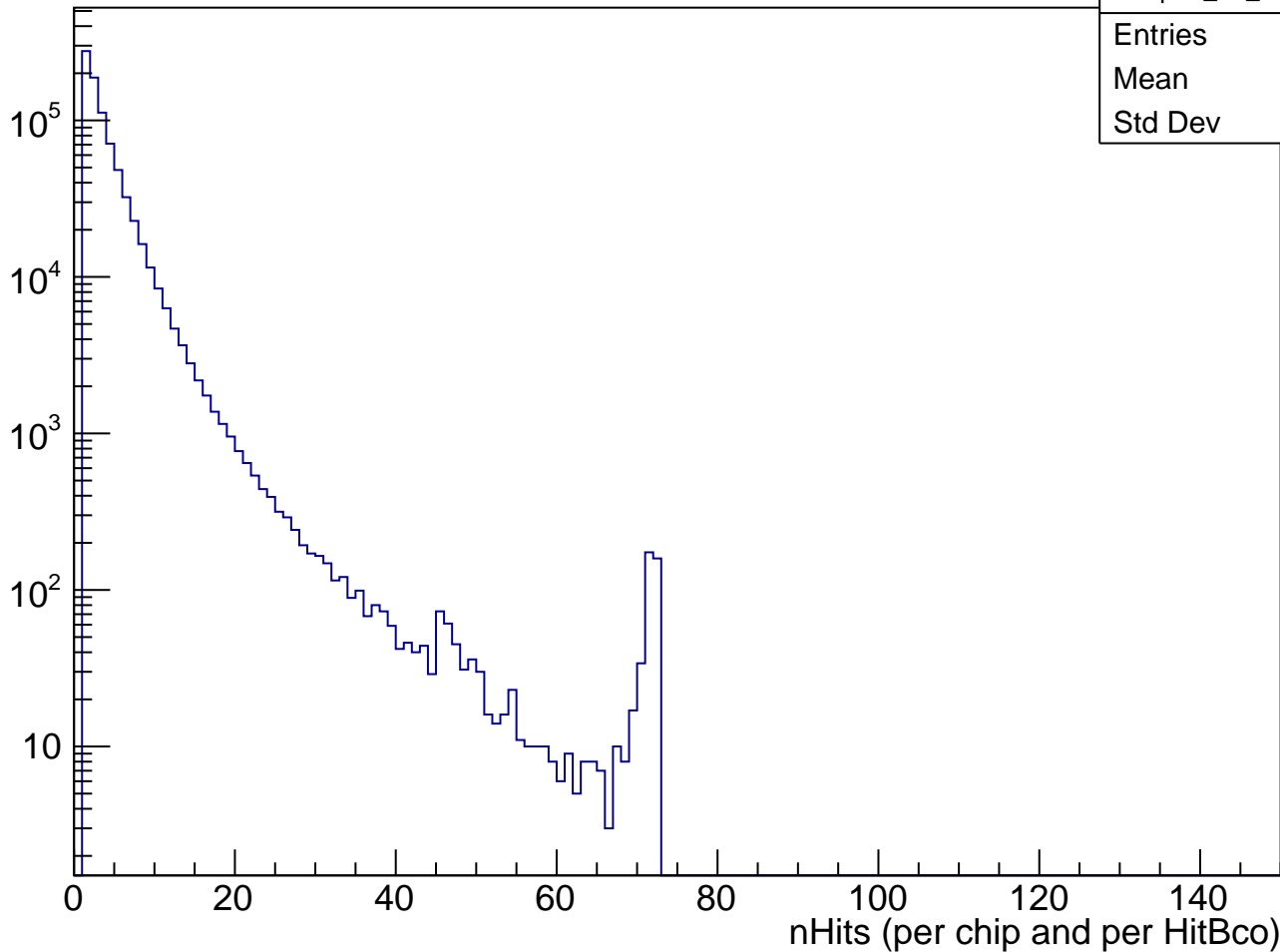
Entries



nChipHit_F5_Fch5_Chip0	
Entries	821442
Mean	3.363
Std Dev	3.909

nChipHit_F5_Fch5_Chip1

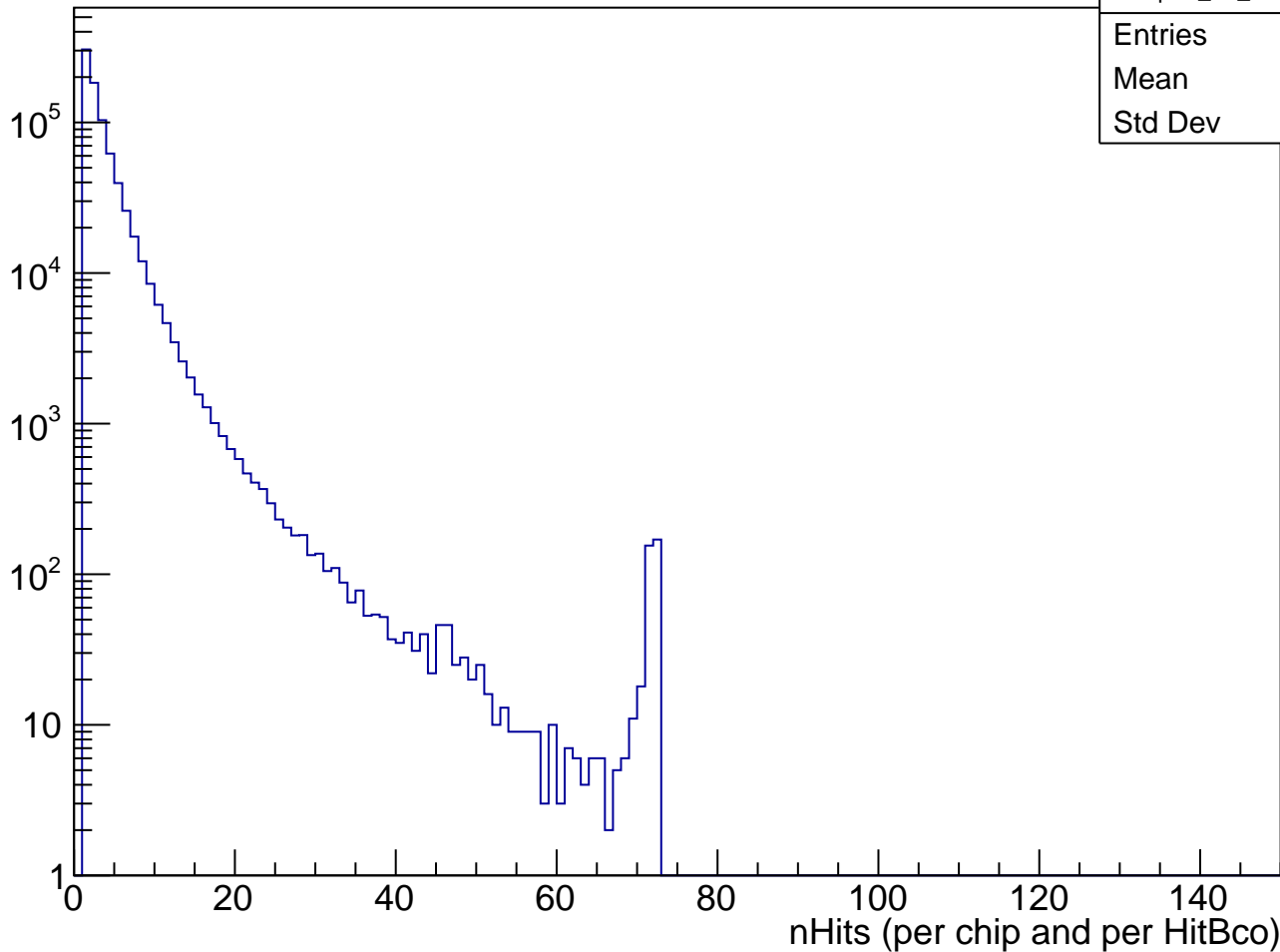
Entries



nChipHit_F5_Fch5_Chip1	
Entries	818167
Mean	3.307
Std Dev	3.836

nChipHit_F5_Fch5_Chip10

Entries

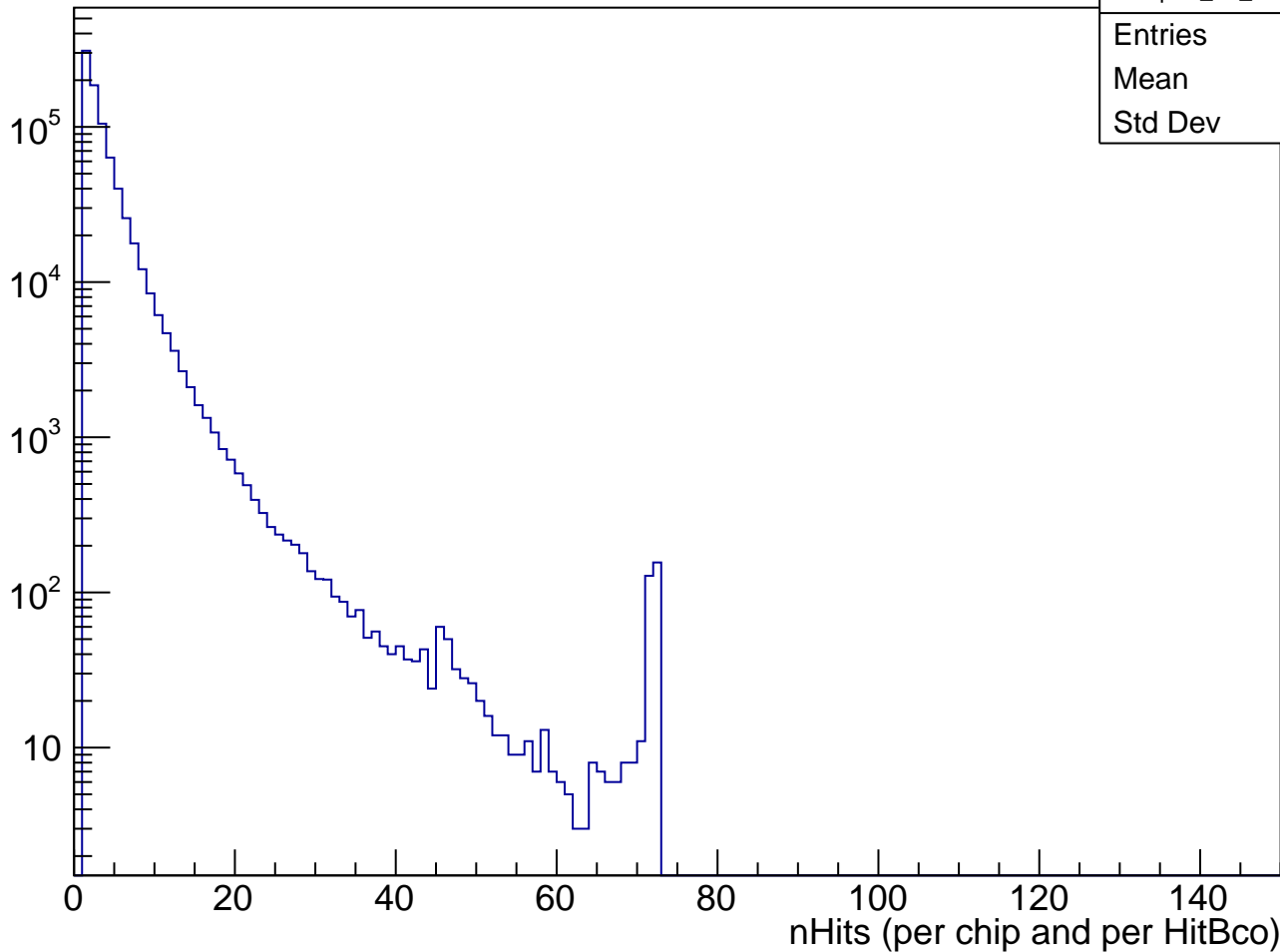


nChipHit_F5_Fch5_Chip10	
Entries	786614
Mean	2.962
Std Dev	3.53

nHits (per chip and per HitBco)

nChipHit_F5_Fch5_Chip11

Entries

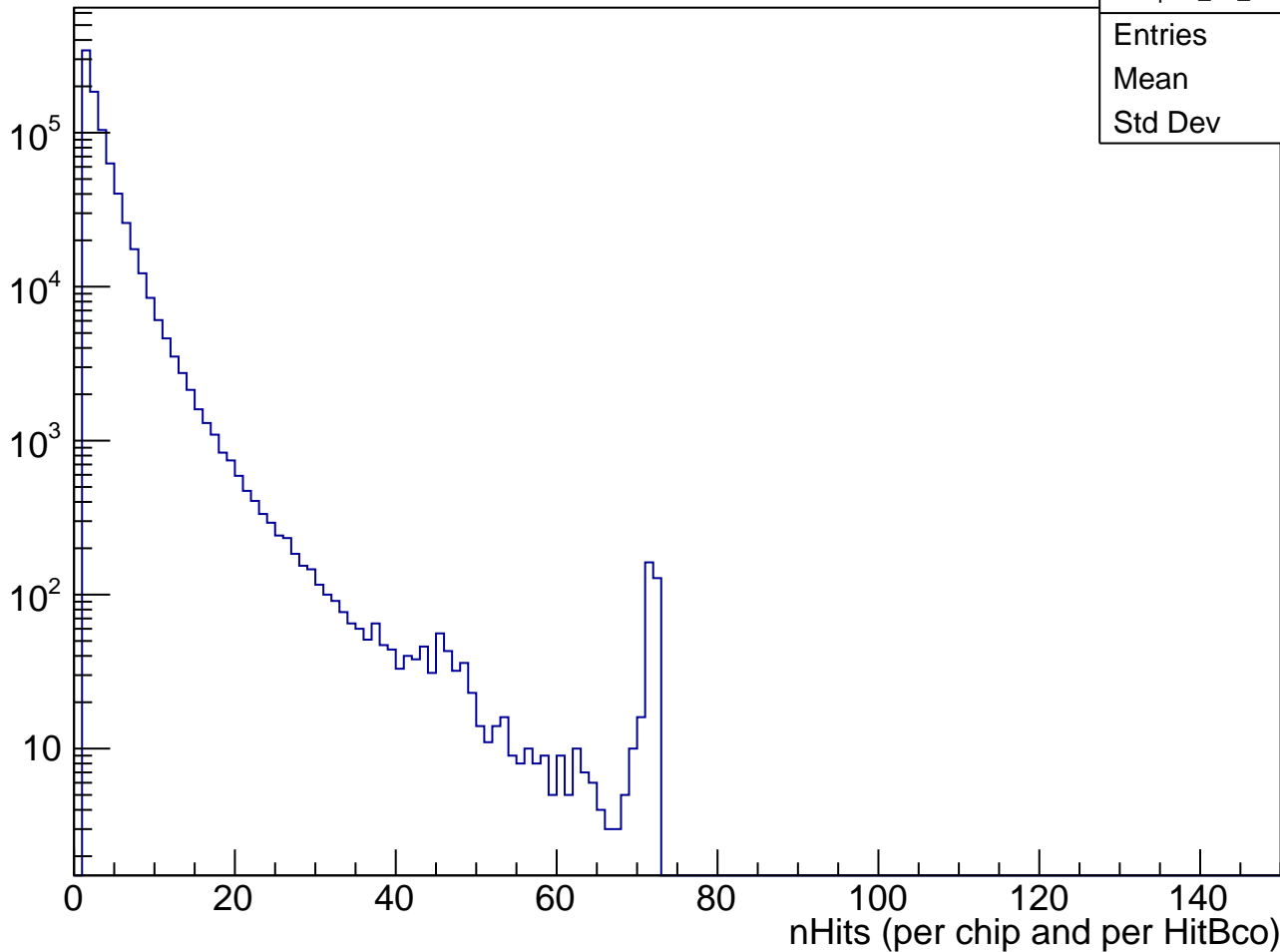


nChipHit_F5_Fch5_Chip11	
Entries	797199
Mean	2.958
Std Dev	3.501

nHits (per chip and per HitBco)

nChipHit_F5_Fch5_Chip12

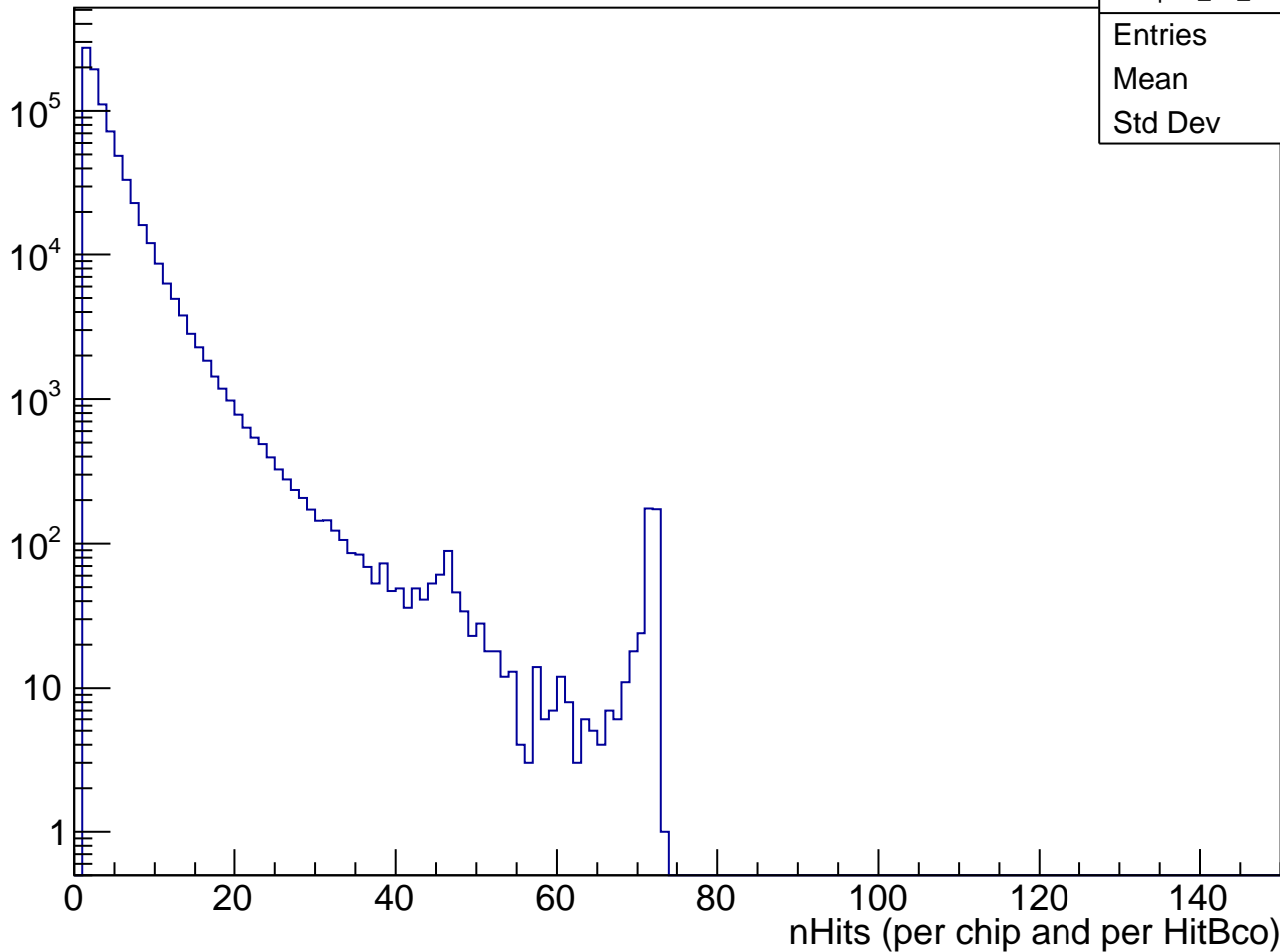
Entries



nChipHit_F5_Fch5_Chip12	
Entries	827906
Mean	2.88
Std Dev	3.454

nChipHit_F5_Fch5_Chip13

Entries

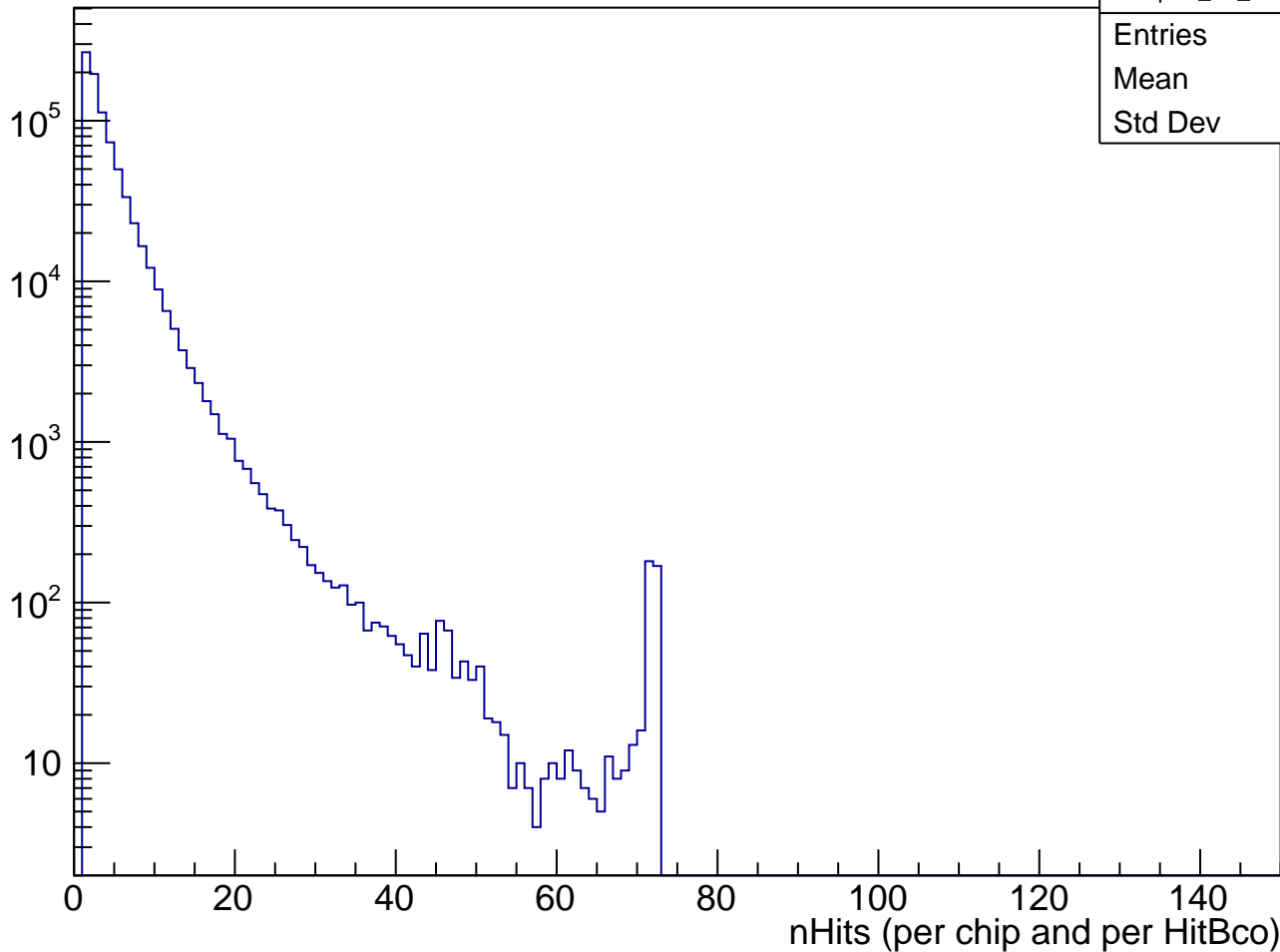


nChipHit_F5_Fch5_Chip13	
Entries	824104
Mean	3.328
Std Dev	3.832

nHits (per chip and per HitBco)

nChipHit_F5_Fch5_Chip14

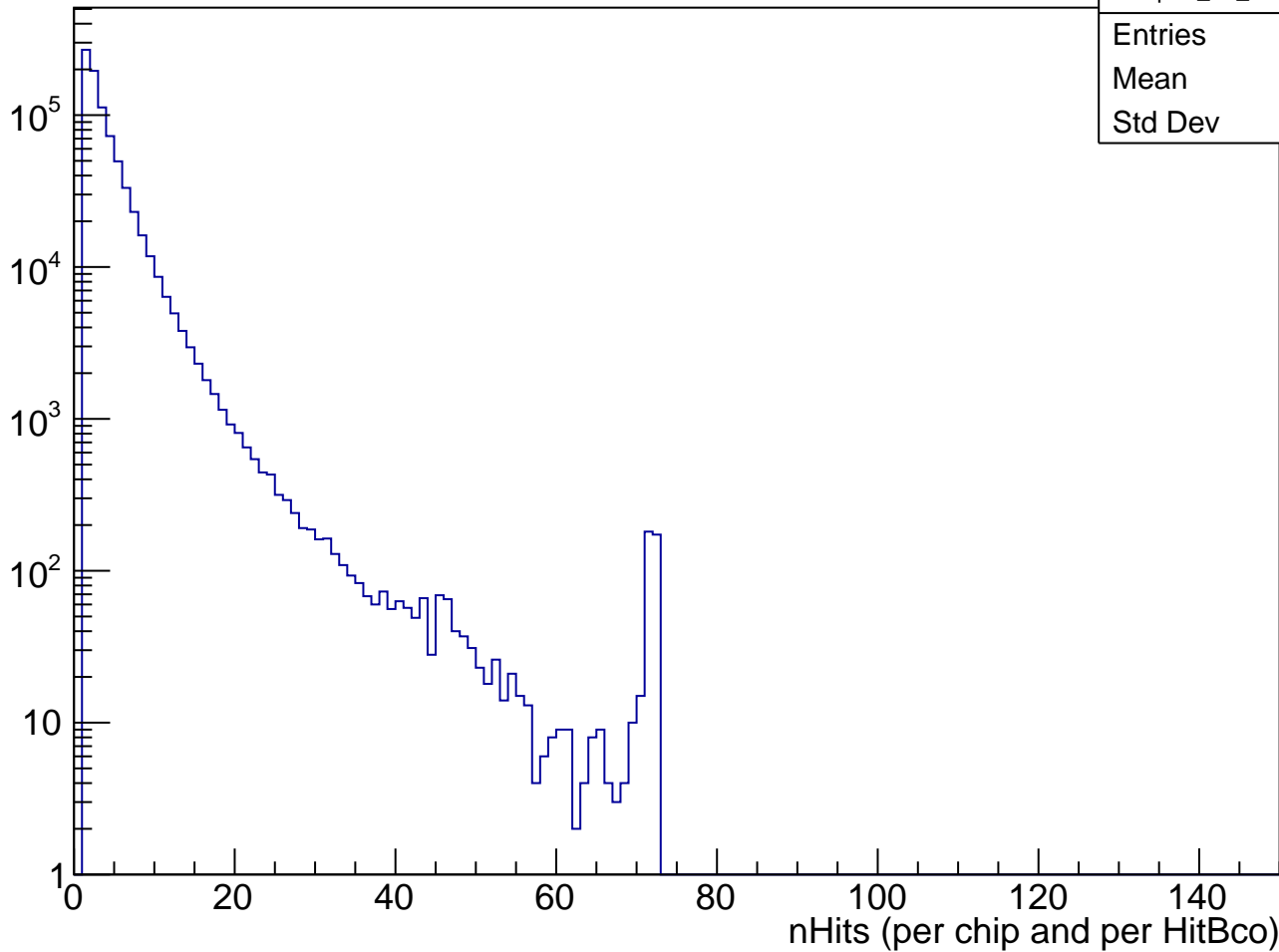
Entries



nChipHit_F5_Fch5_Chip14	
Entries	824712
Mean	3.364
Std Dev	3.866

nChipHit_F5_Fch5_Chip15

Entries

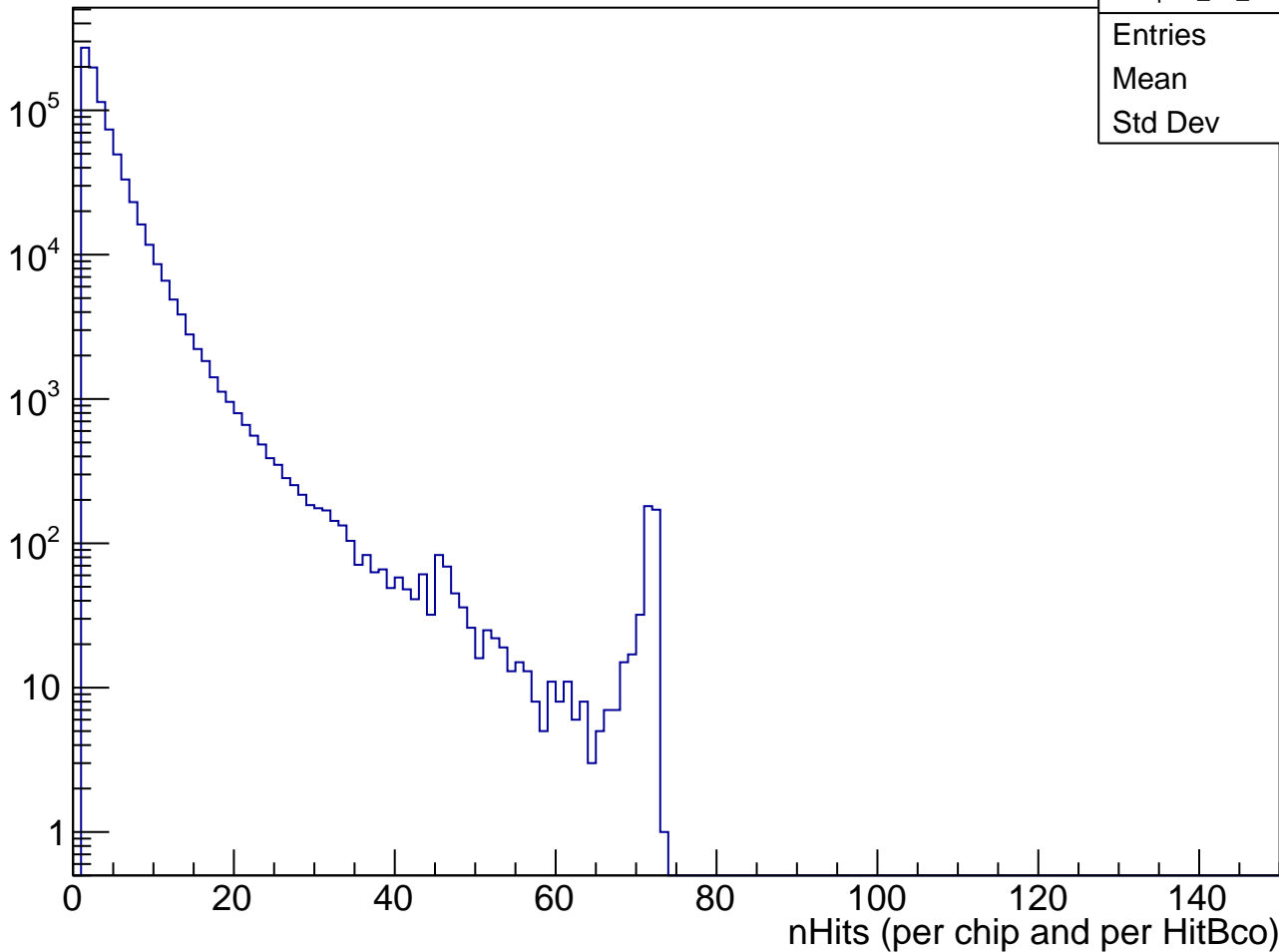


nChipHit_F5_Fch5_Chip15	
Entries	824357
Mean	3.341
Std Dev	3.839

nHits (per chip and per HitBco)

nChipHit_F5_Fch5_Chip16

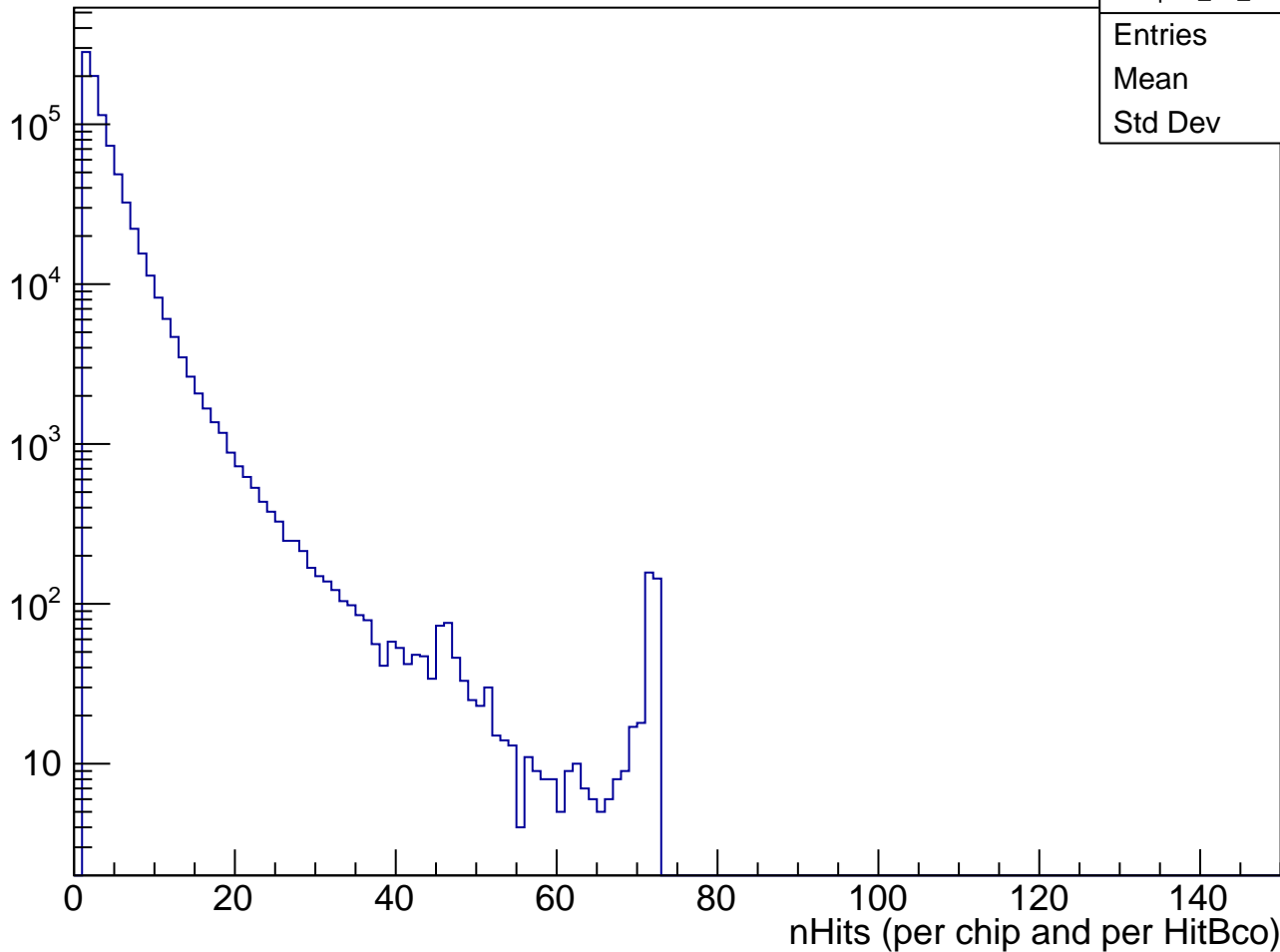
Entries



nChipHit_F5_Fch5_Chip16	
Entries	831617
Mean	3.336
Std Dev	3.862

nChipHit_F5_Fch5_Chip17

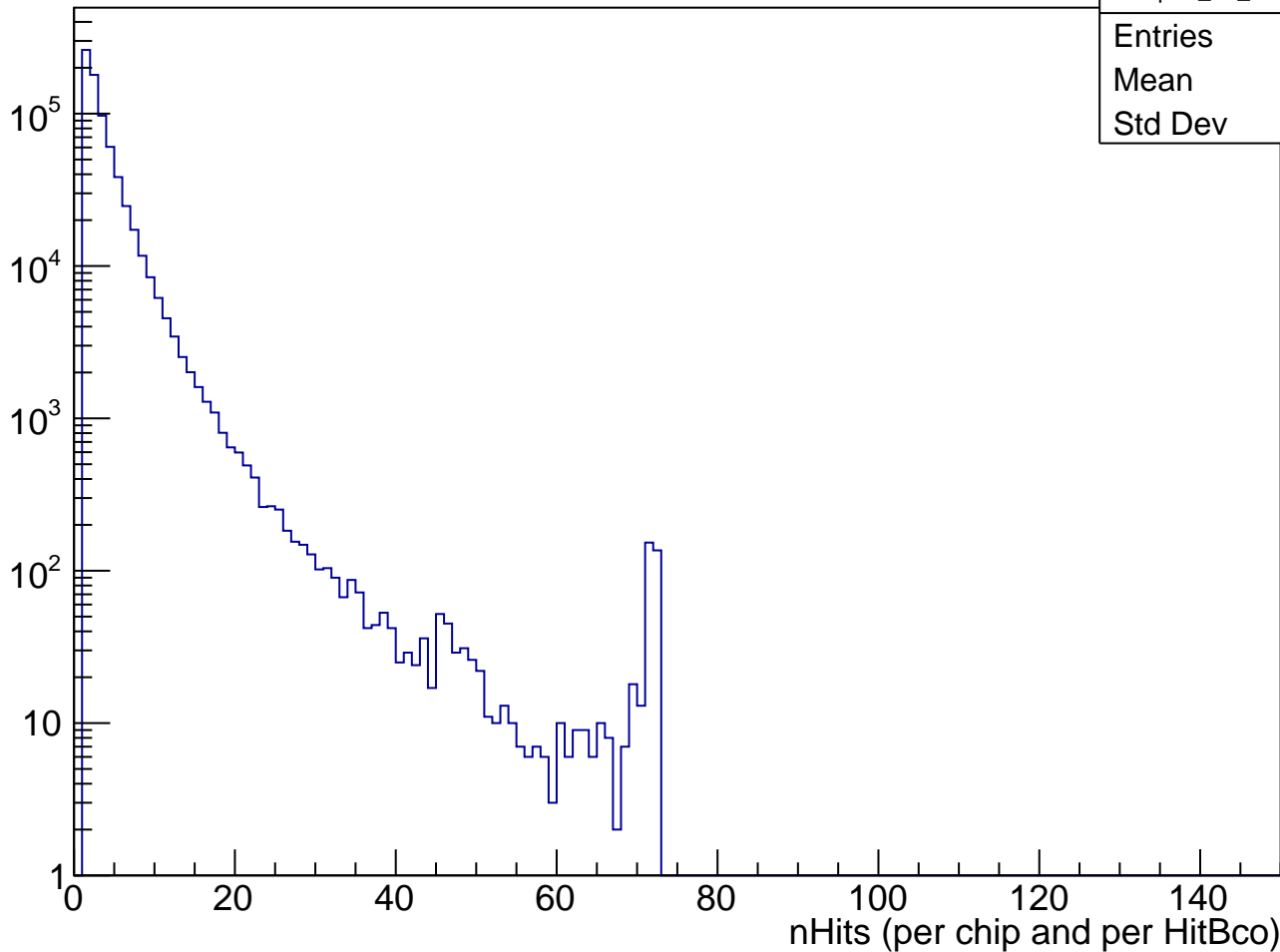
Entries



nChipHit_F5_Fch5_Chip17	
Entries	839253
Mean	3.245
Std Dev	3.722

nChipHit_F5_Fch5_Chip18

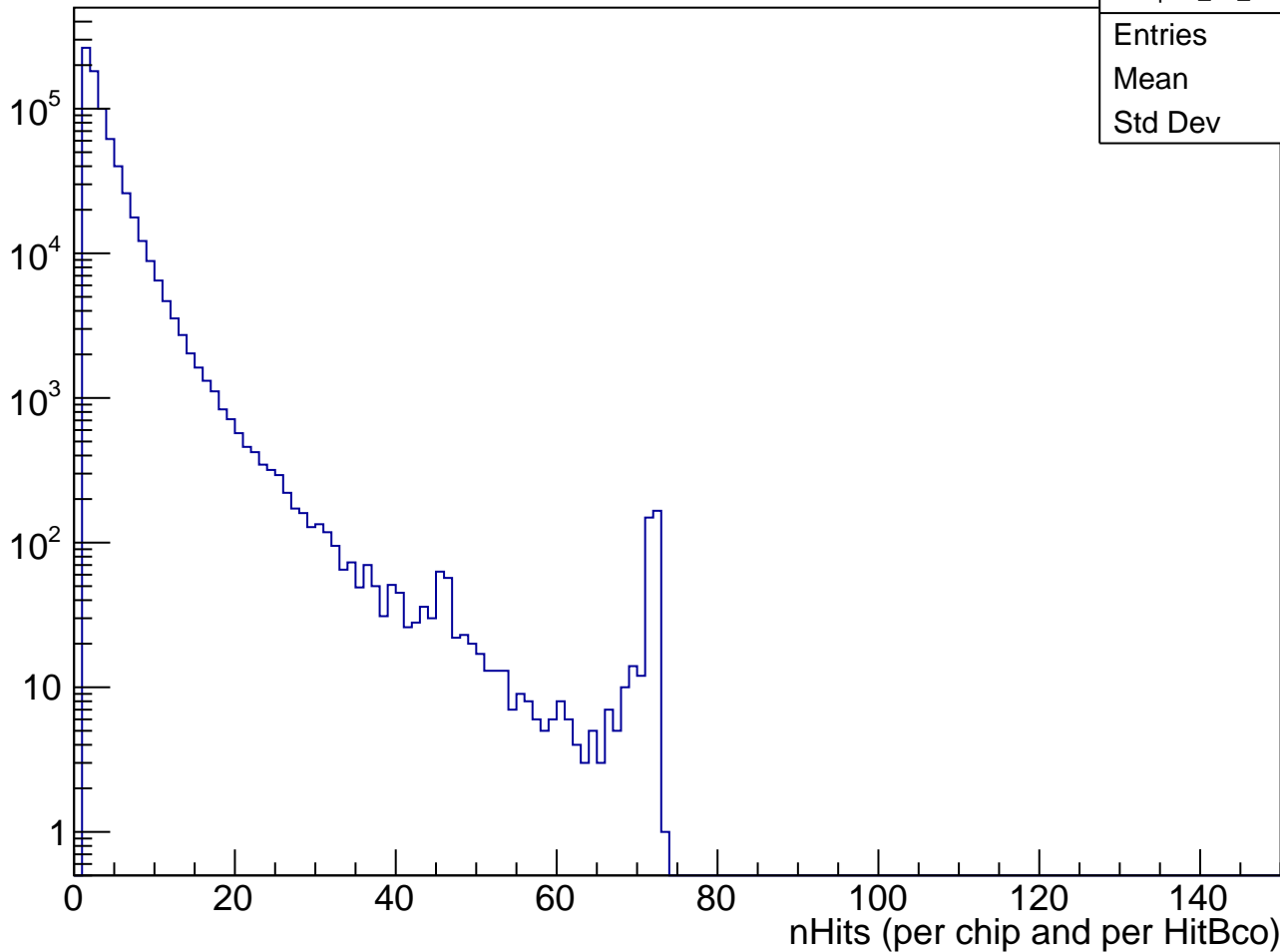
Entries



nChipHit_F5_Fch5_Chip18	
Entries	728041
Mean	3.056
Std Dev	3.572

nChipHit_F5_Fch5_Chip19

Entries

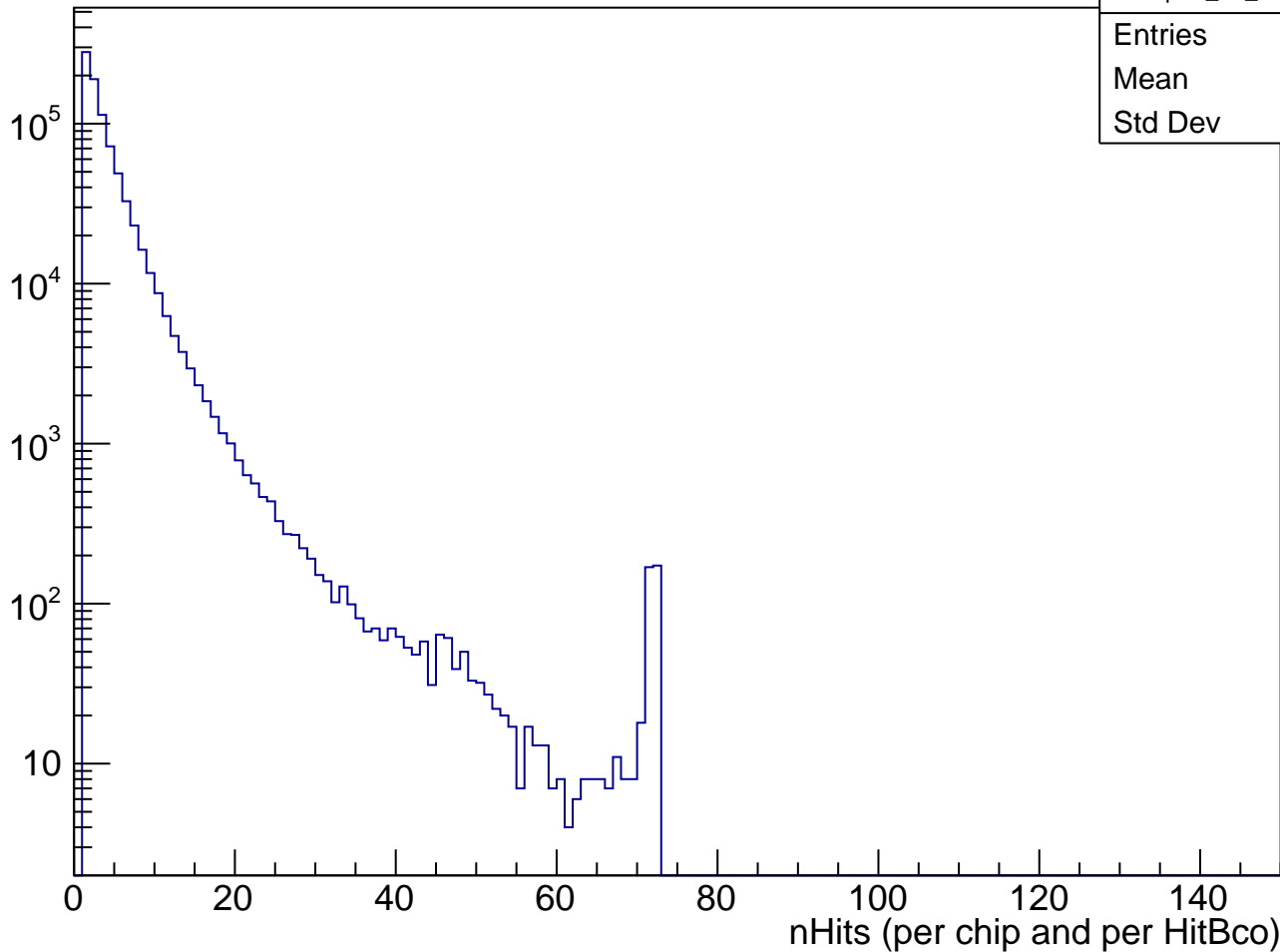


nChipHit_F5_Fch5_Chip19	
Entries	741052
Mean	3.09
Std Dev	3.612

nHits (per chip and per HitBco)

nChipHit_F5_Fch5_Chip2

Entries



nChipHit_F5_Fch5_Chip2

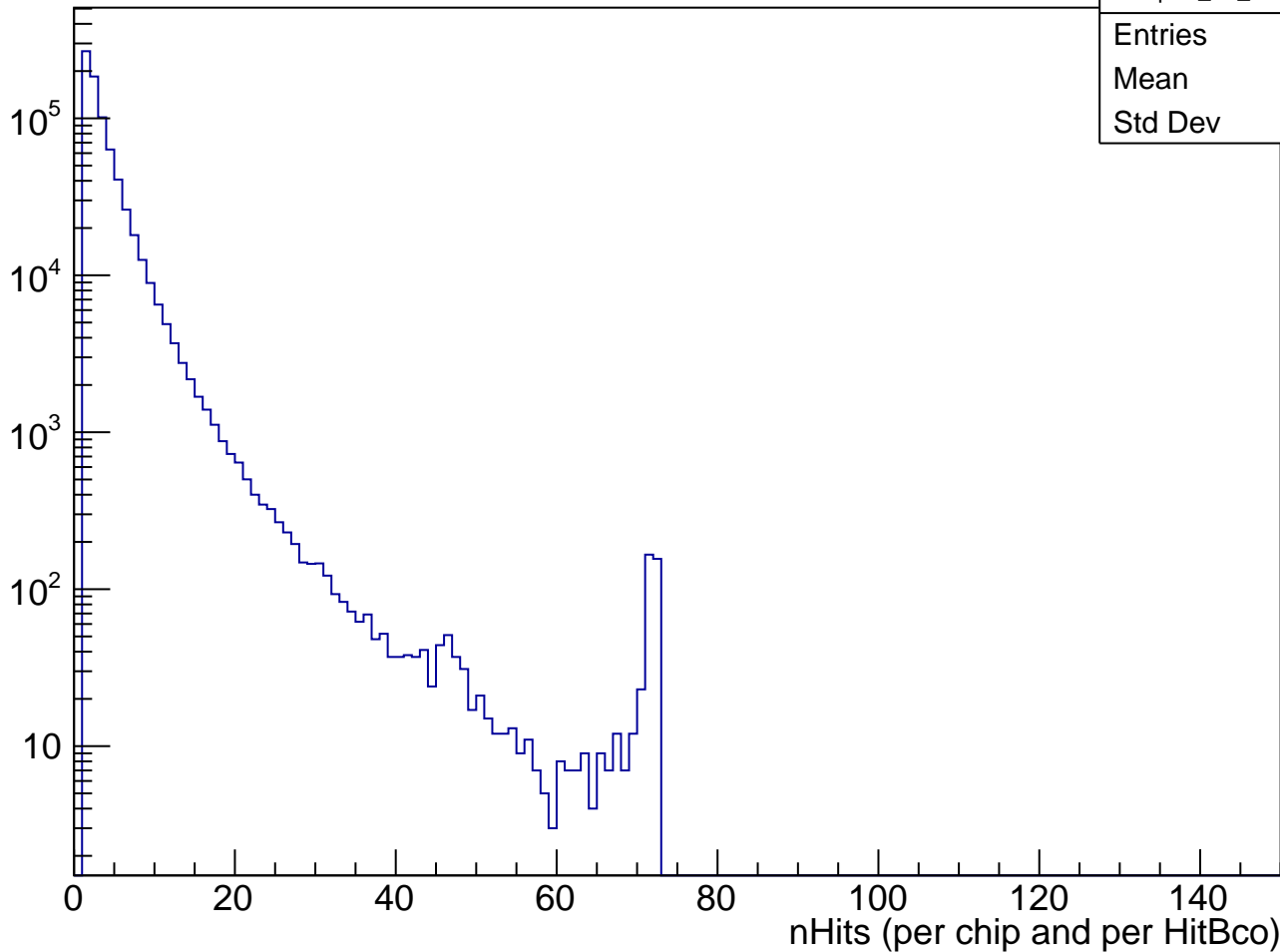
Entries 828847

Mean 3.318

Std Dev 3.853

nChipHit_F5_Fch5_Chip20

Entries



nChipHit_F5_Fch5_Chip20

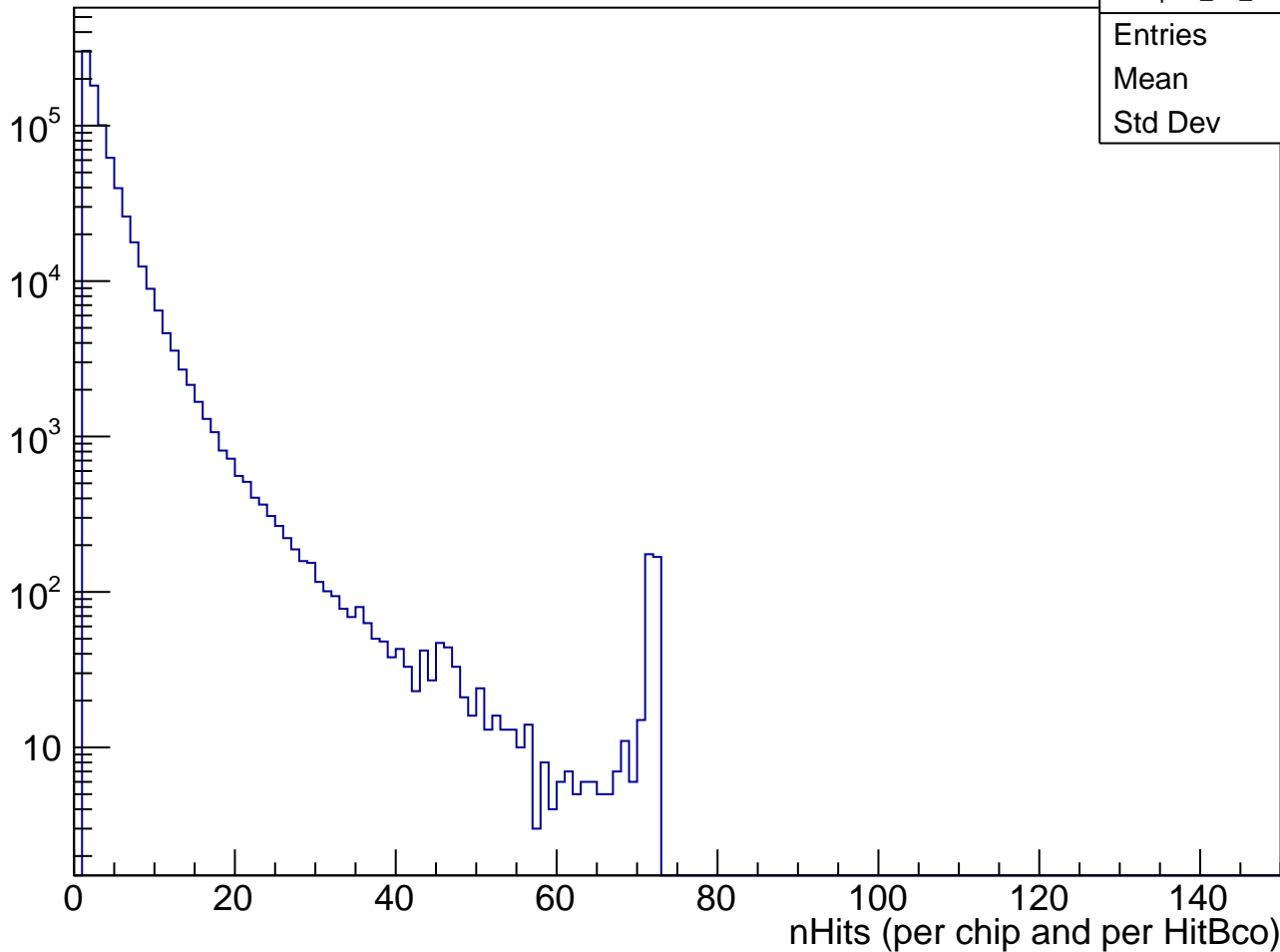
Entries 754284

Mean 3.102

Std Dev 3.65

nChipHit_F5_Fch5_Chip21

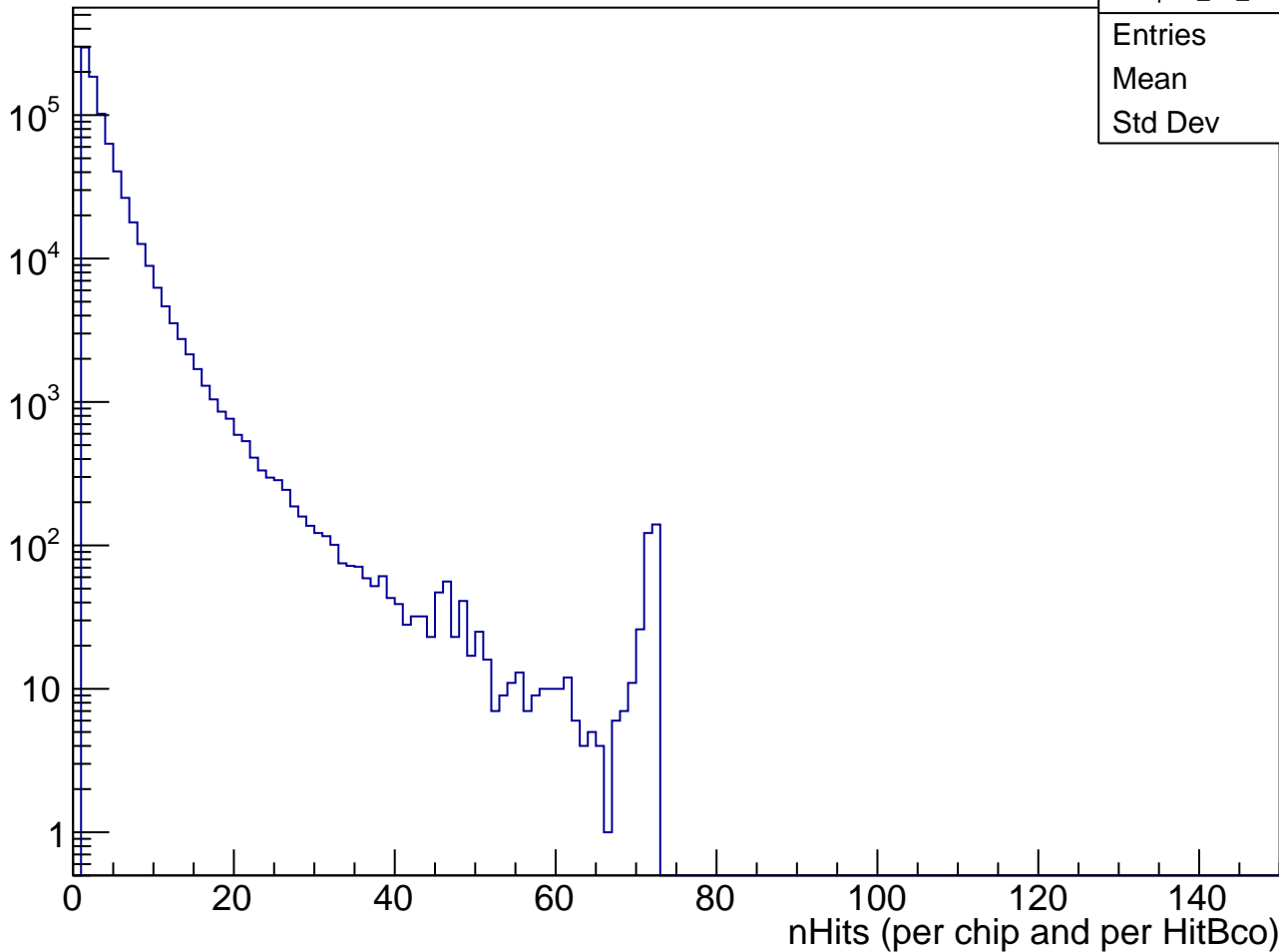
Entries



nChipHit_F5_Fch5_Chip21	
Entries	781469
Mean	2.991
Std Dev	3.58

nChipHit_F5_Fch5_Chip22

Entries

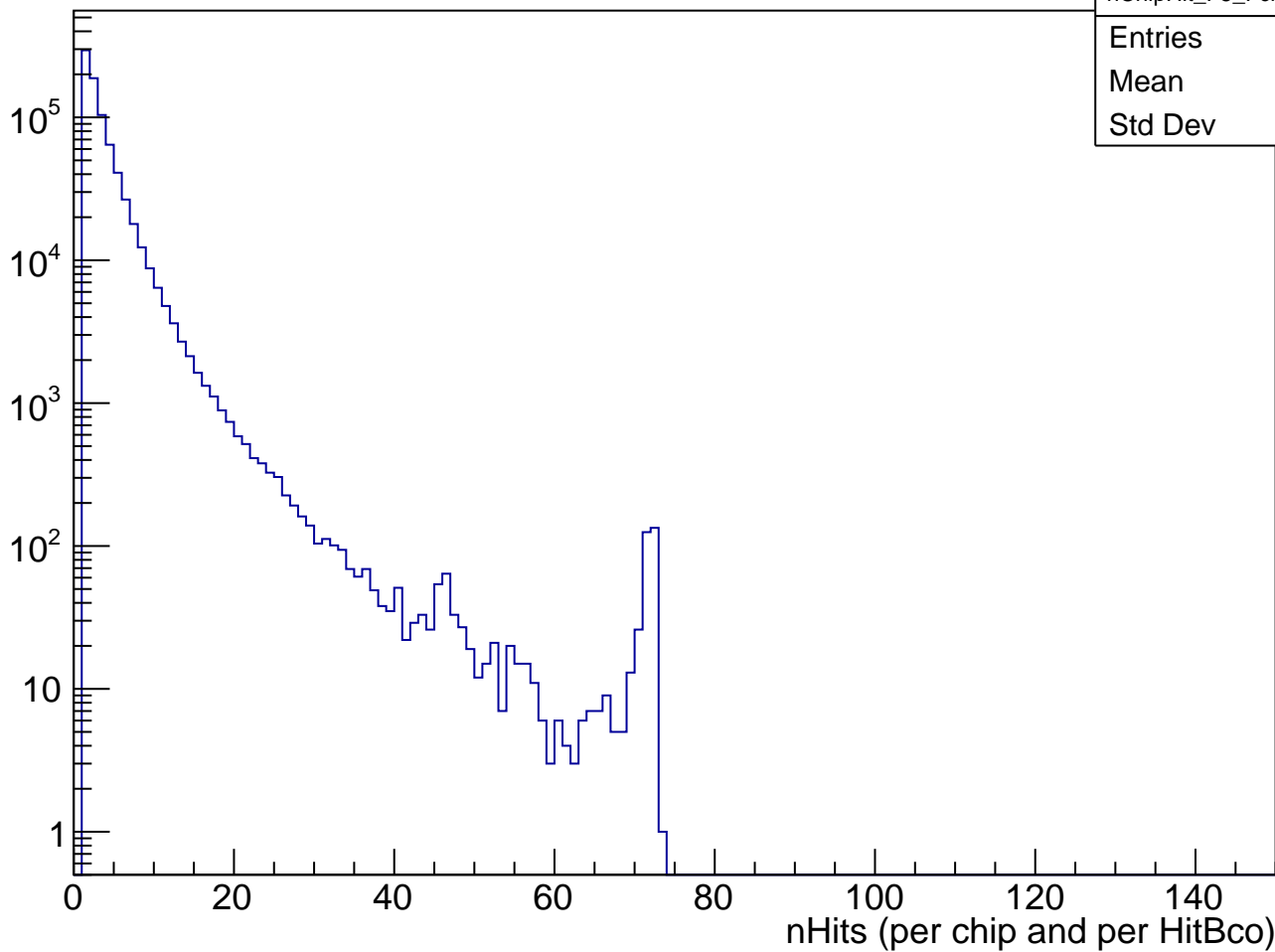


nChipHit_F5_Fch5_Chip22	
Entries	782958
Mean	3.008
Std Dev	3.532

nHits (per chip and per HitBco)

nChipHit_F5_Fch5_Chip23

Entries



nChipHit_F5_Fch5_Chip23

Entries 787635

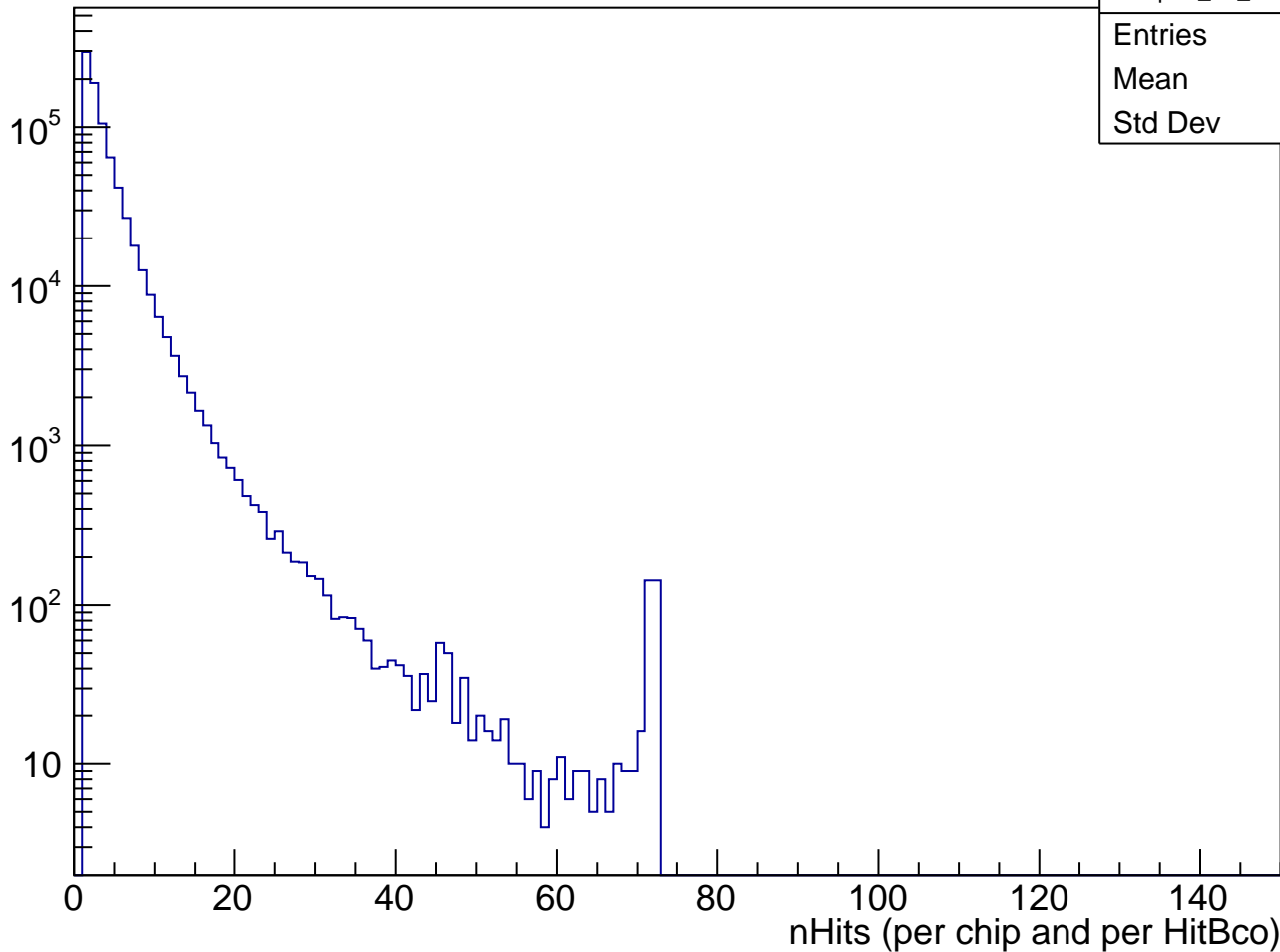
Mean 3.013

Std Dev 3.53

nHits (per chip and per HitBco)

nChipHit_F5_Fch5_Chip24

Entries

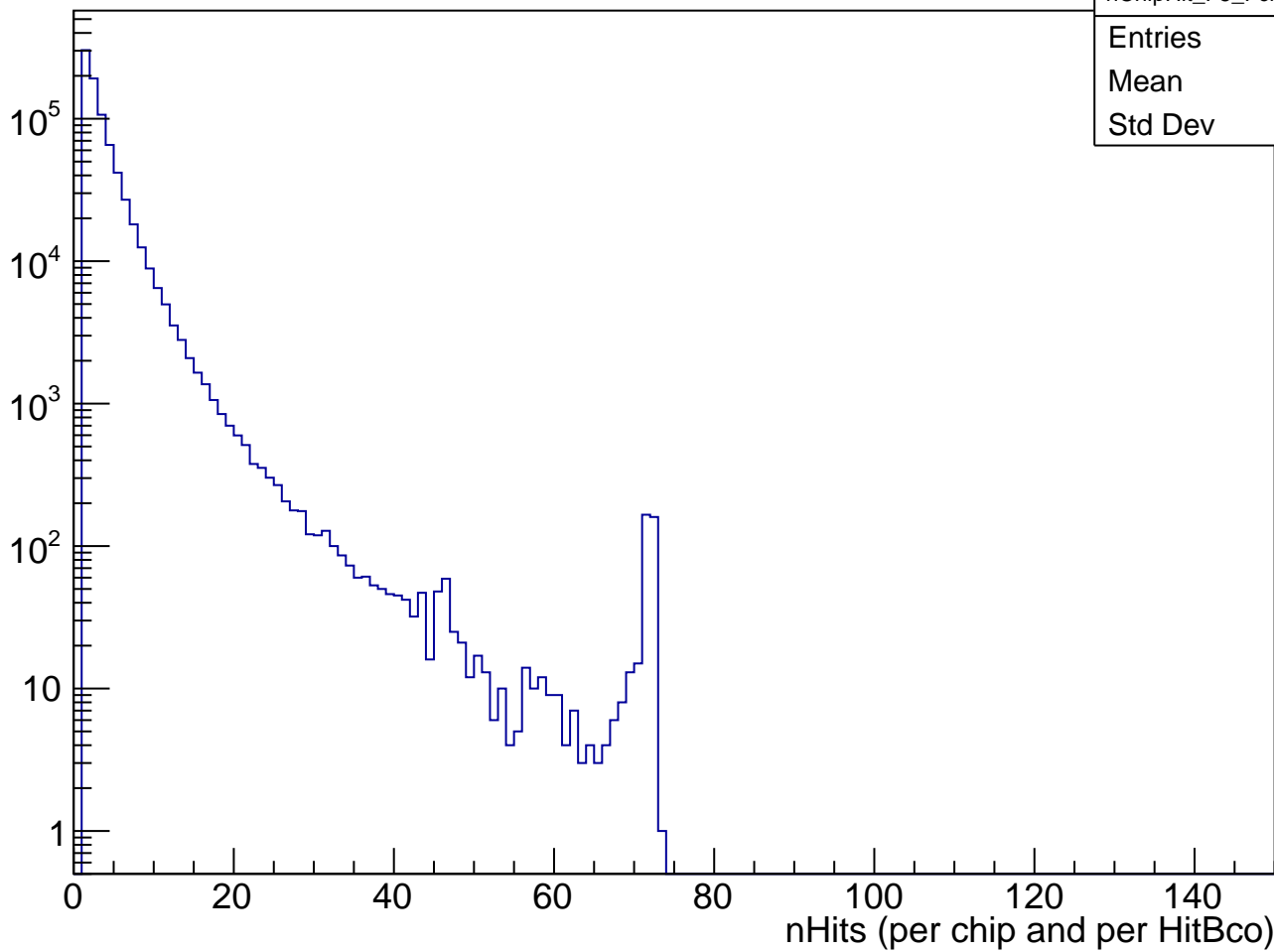


nChipHit_F5_Fch5_Chip24	
Entries	792809
Mean	3.013
Std Dev	3.531

nHits (per chip and per HitBco)

nChipHit_F5_Fch5_Chip25

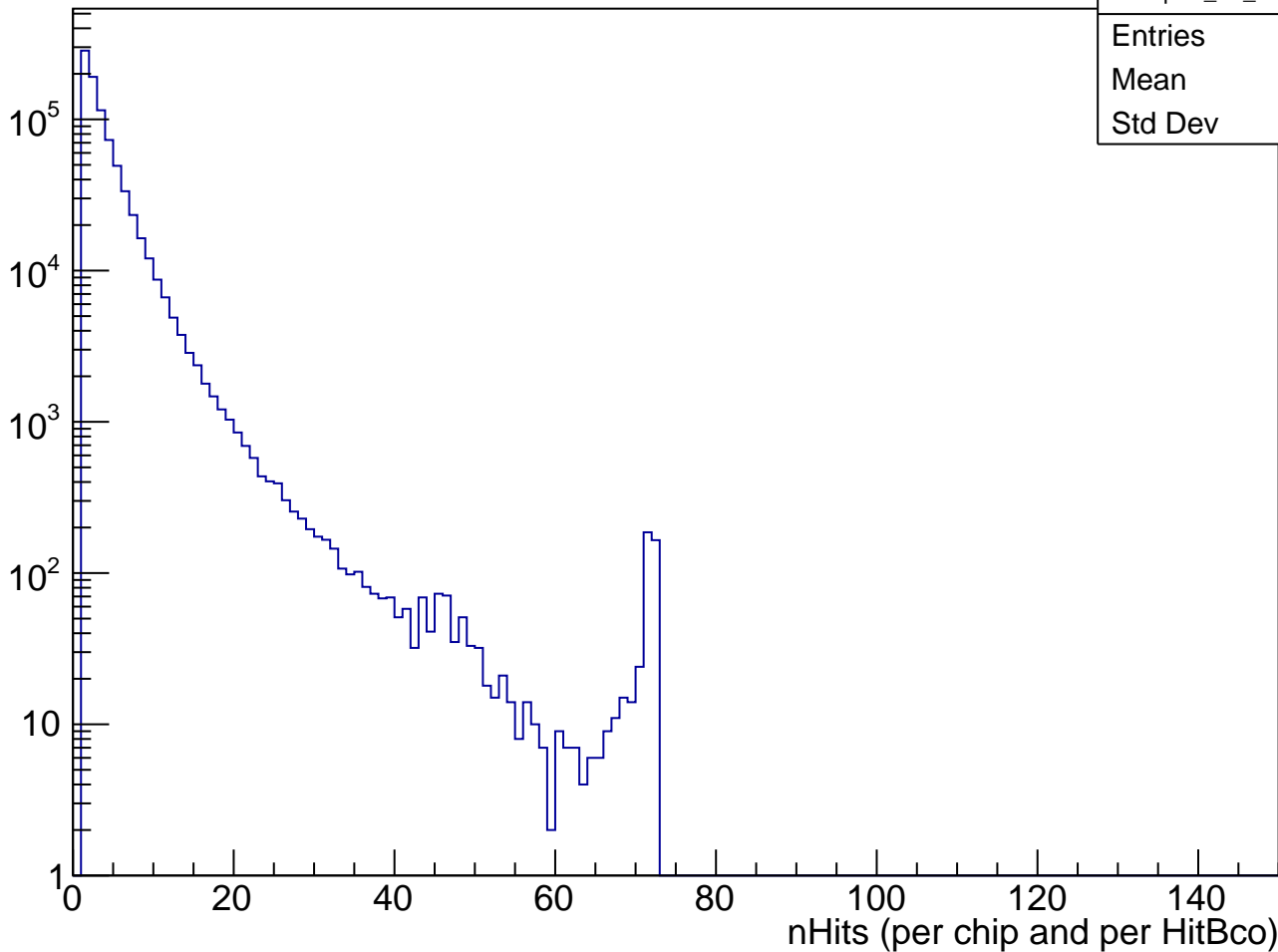
Entries



nChipHit_F5_Fch5_Chip25	
Entries	805370
Mean	2.997
Std Dev	3.523

nChipHit_F5_Fch5_Chip3

Entries



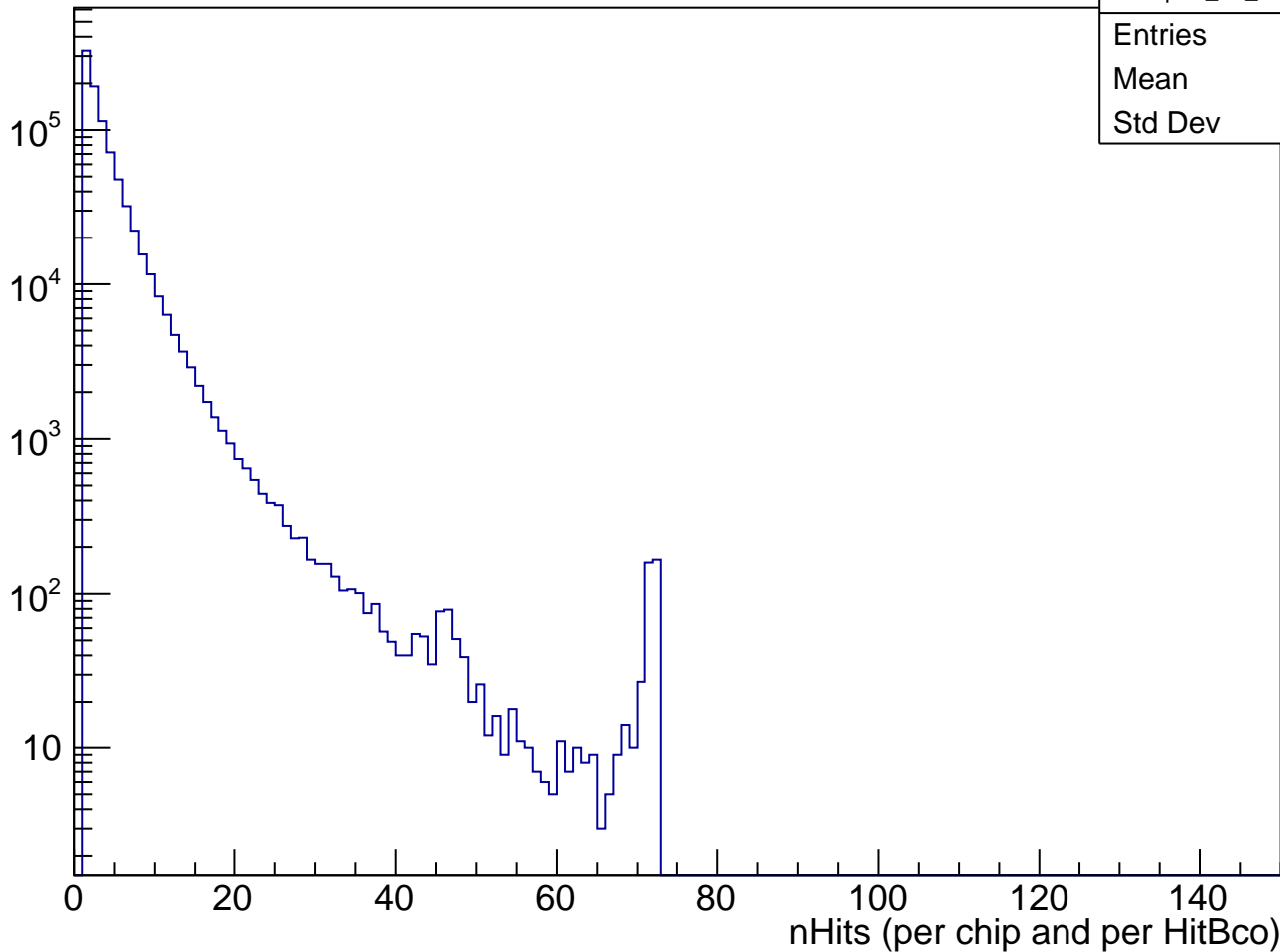
nChipHit_F5_Fch5_Chip3

Entries	839388
Mean	3.325
Std Dev	3.878

nHits (per chip and per HitBco)

nChipHit_F5_Fch5_Chip4

Entries



nChipHit_F5_Fch5_Chip4

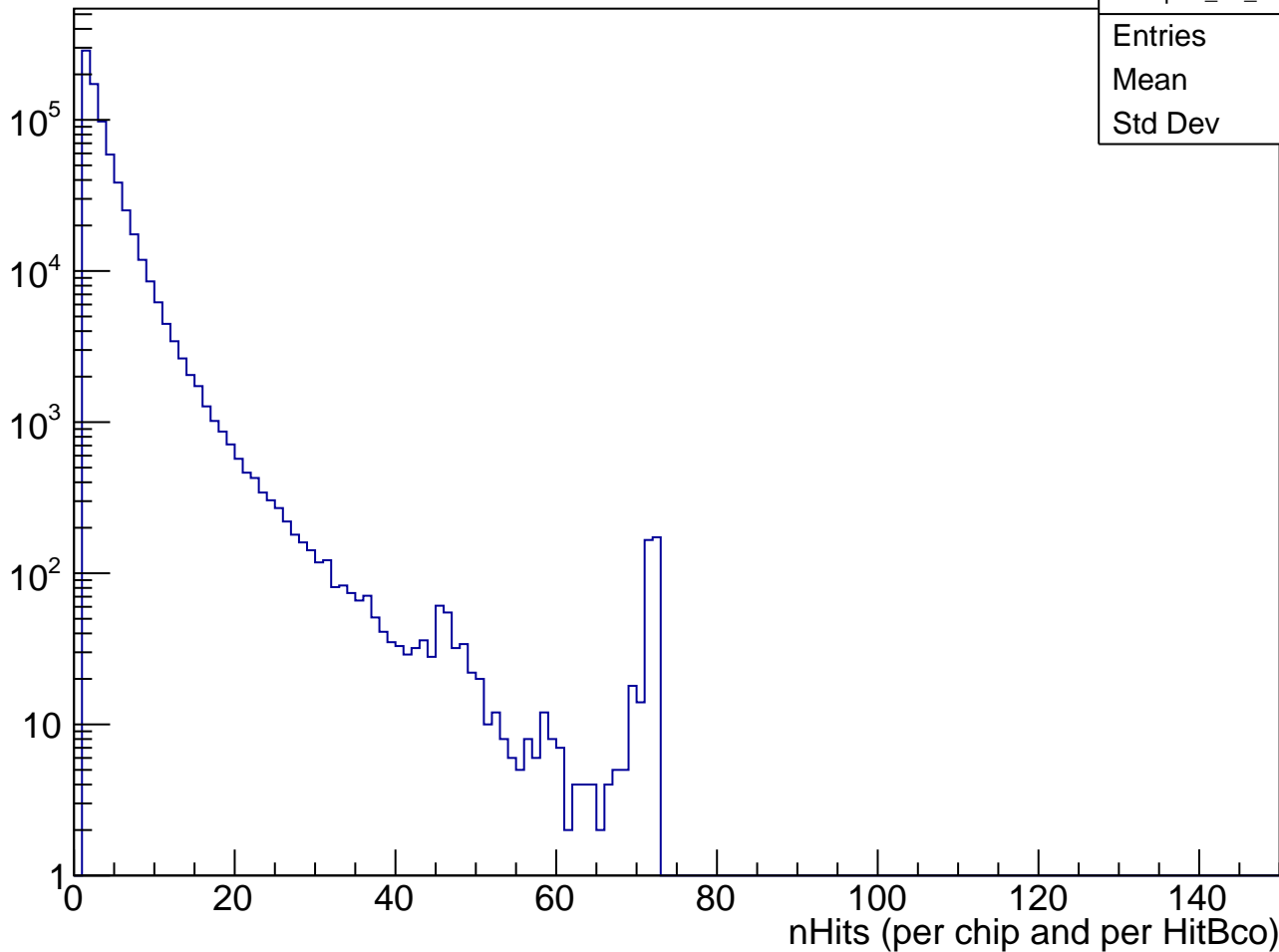
Entries 871319

Mean 3.168

Std Dev 3.745

nChipHit_F5_Fch5_Chip5

Entries



nChipHit_F5_Fch5_Chip5

Entries 746988

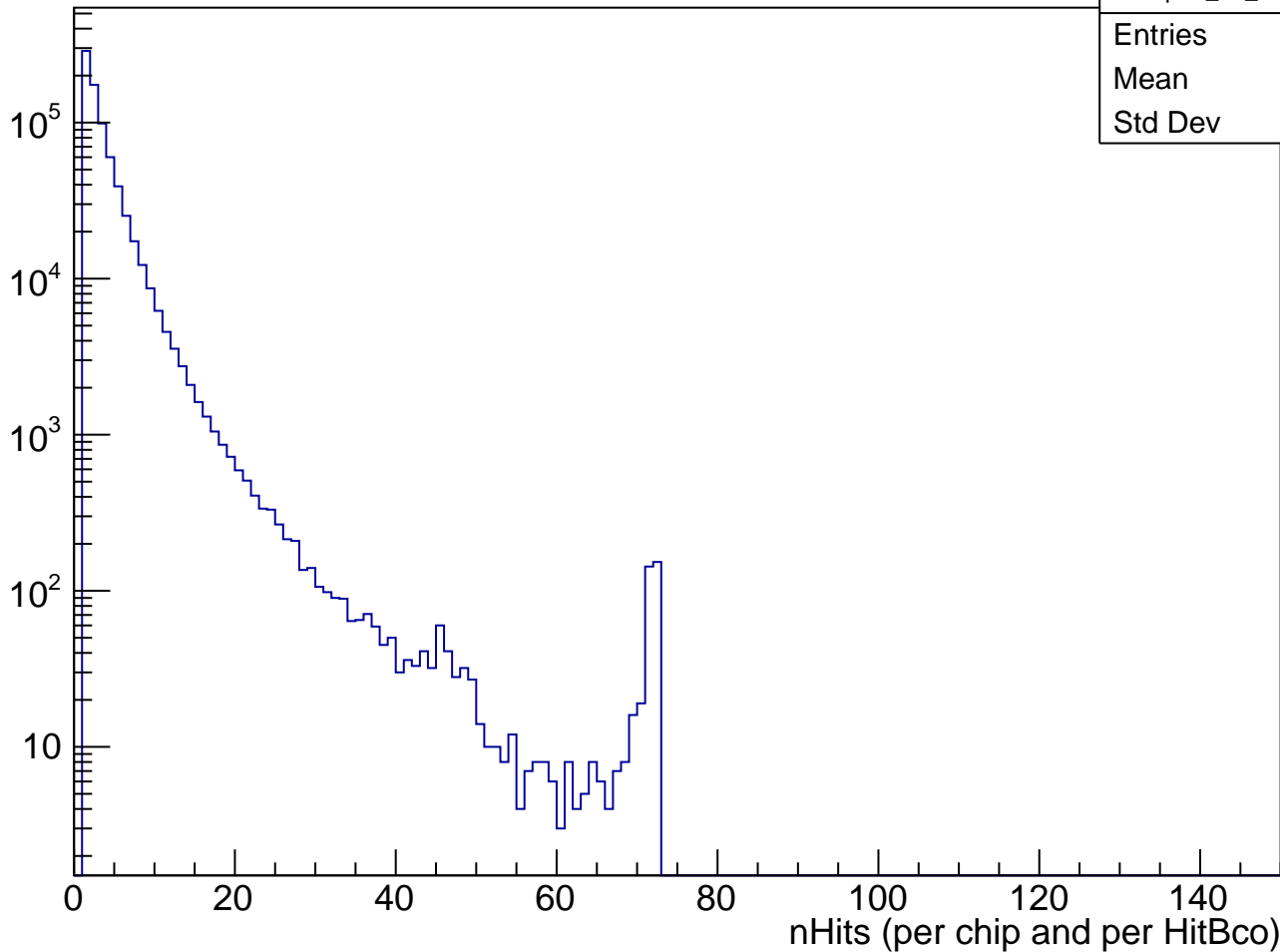
Mean 3.016

Std Dev 3.62

nHits (per chip and per HitBco)

nChipHit_F5_Fch5_Chip6

Entries



nChipHit_F5_Fch5_Chip6

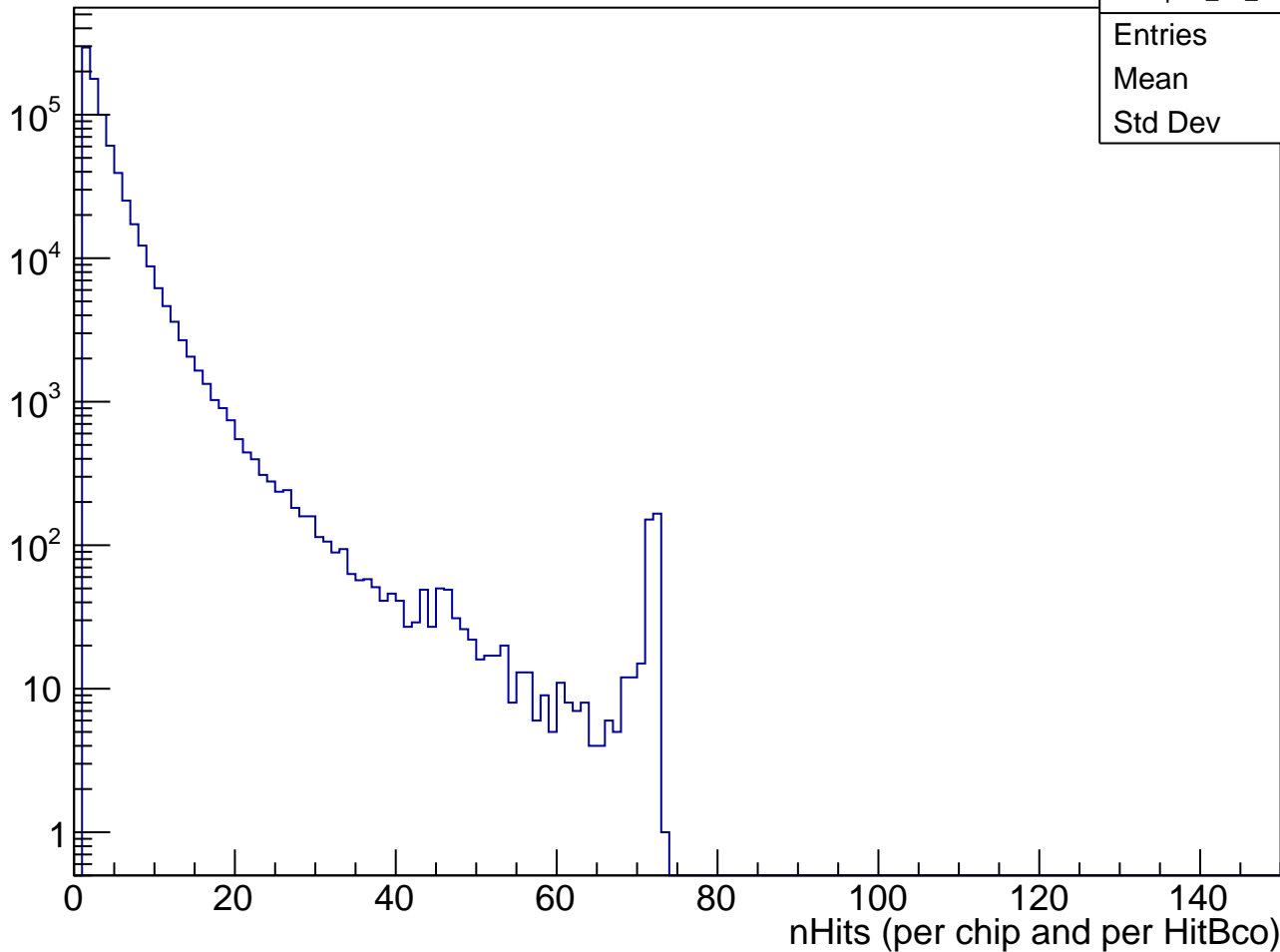
Entries 753028

Mean 3.021

Std Dev 3.591

nChipHit_F5_Fch5_Chip7

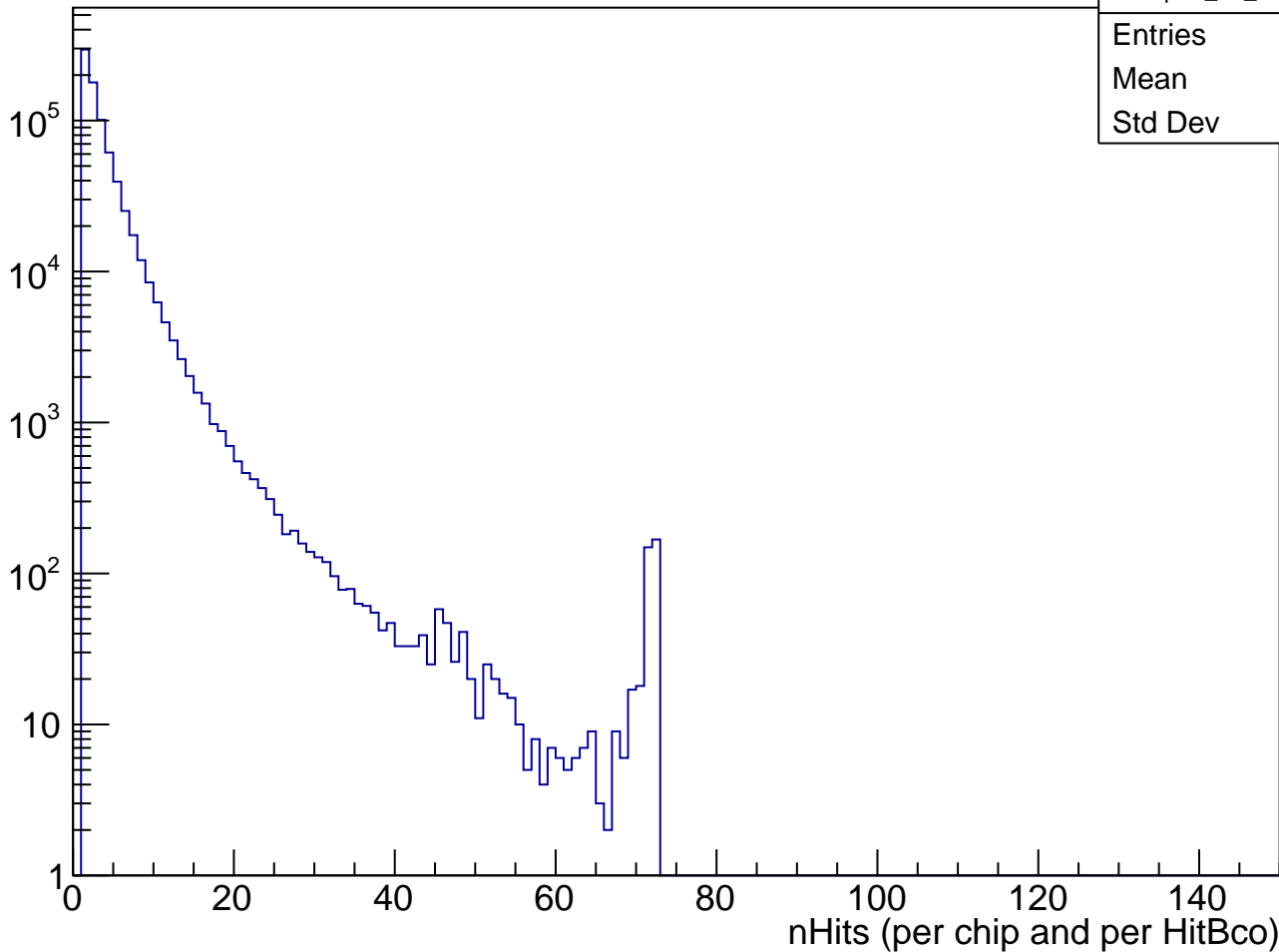
Entries



nChipHit_F5_Fch5_Chip7	
Entries	764143
Mean	3.002
Std Dev	3.591

nChipHit_F5_Fch5_Chip8

Entries



nChipHit_F5_Fch5_Chip8

Entries 768144

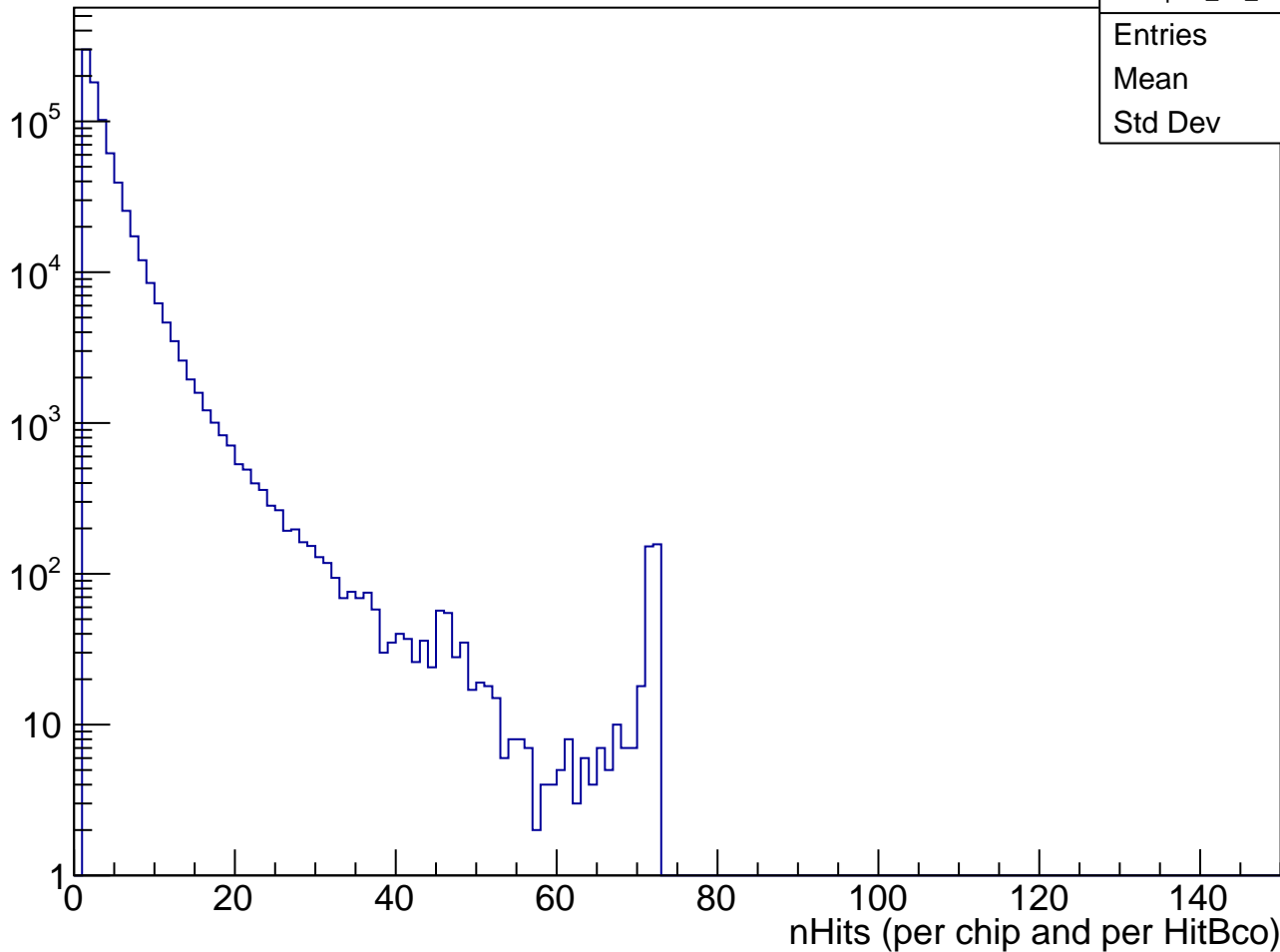
Mean 2.992

Std Dev 3.58

nHits (per chip and per HitBco)

nChipHit_F5_Fch5_Chip9

Entries



nChipHit_F5_Fch5_Chip9

Entries 776105

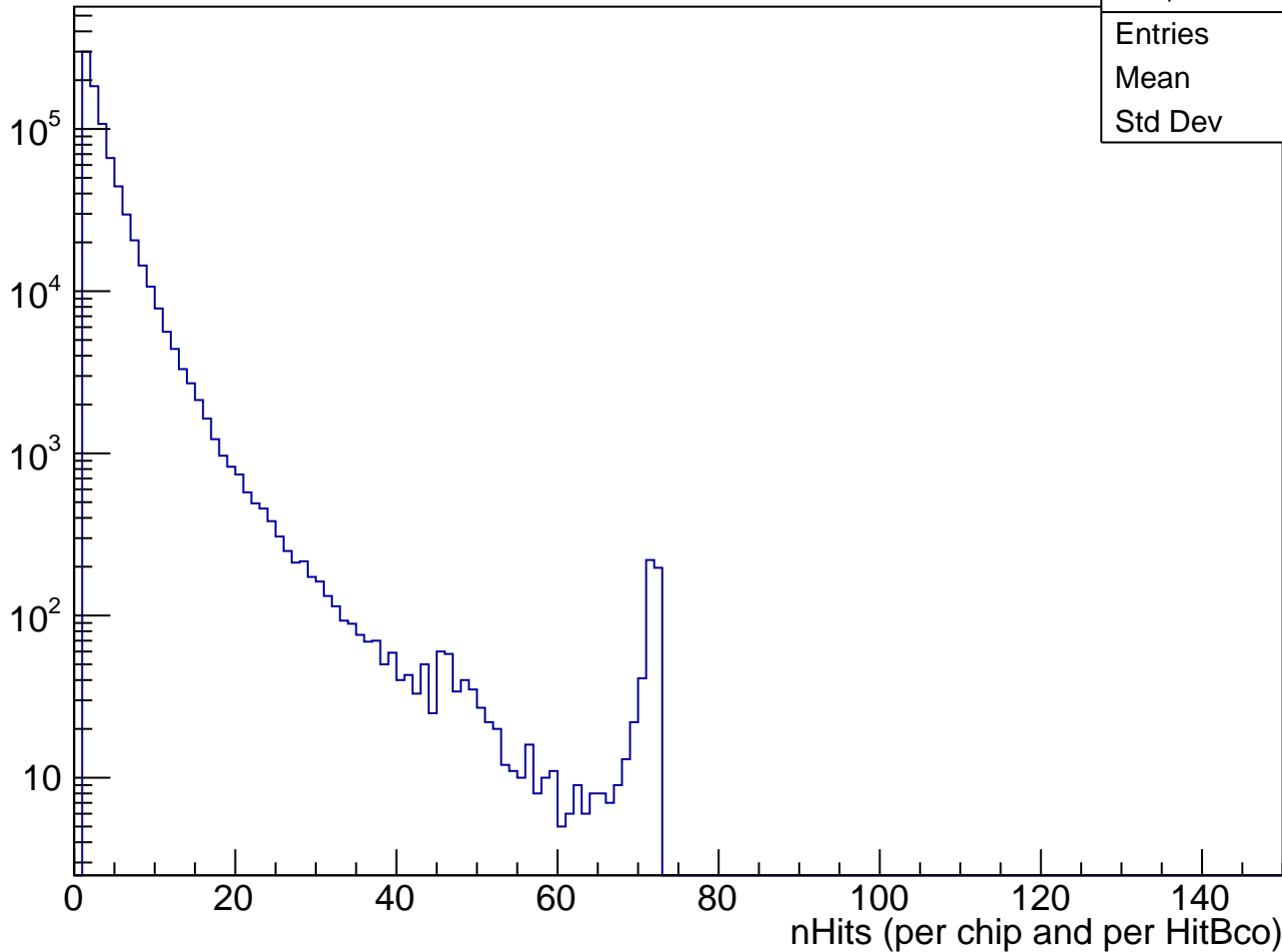
Mean 2.971

Std Dev 3.529

nHits (per chip and per HitBco)

nChipHit_F5_Fch6_Chip0

Entries

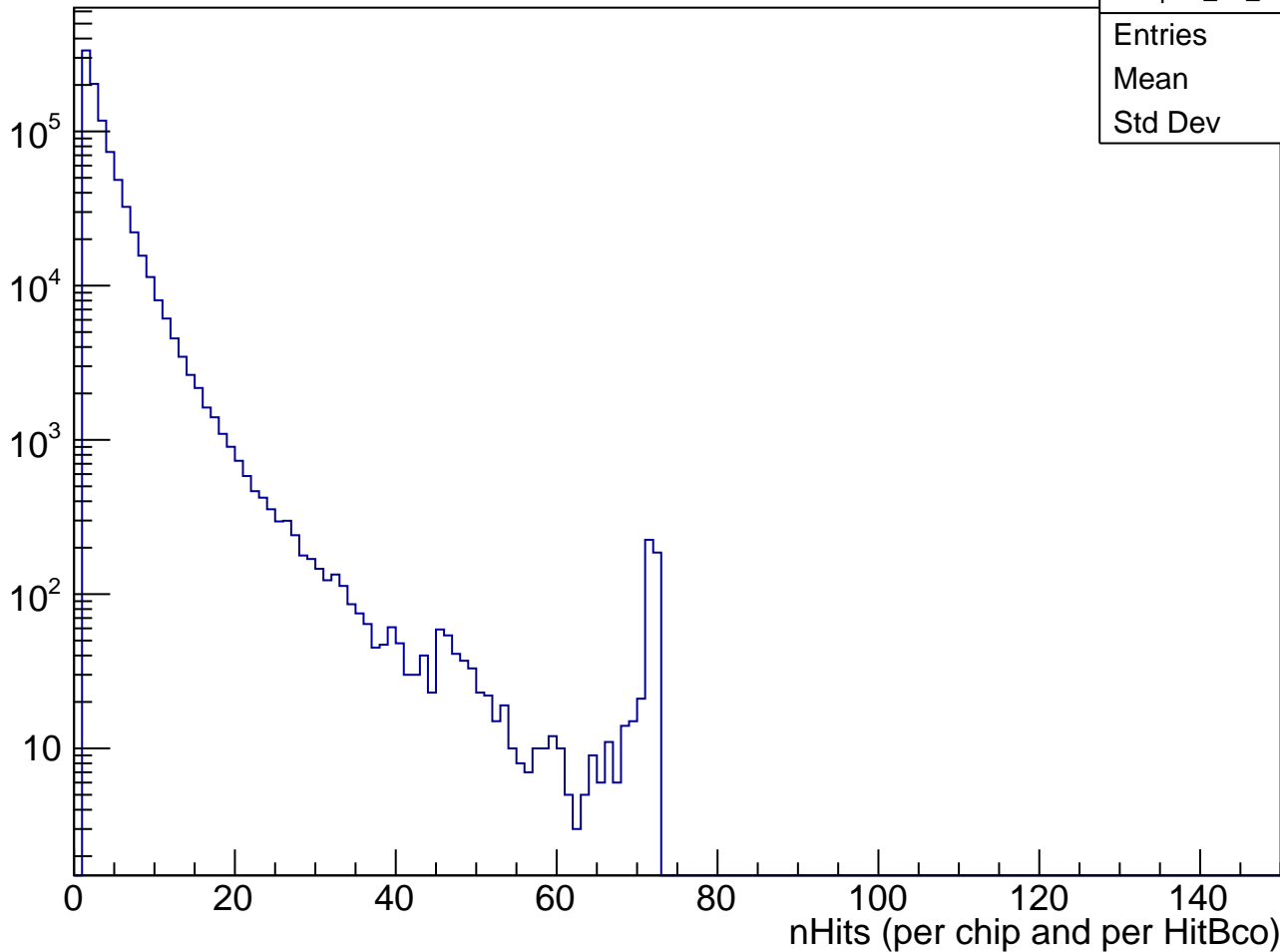


nChipHit_F5_Fch6_Chip0	
Entries	813157
Mean	3.172
Std Dev	3.842

nHits (per chip and per HitBco)

nChipHit_F5_Fch6_Chip1

Entries

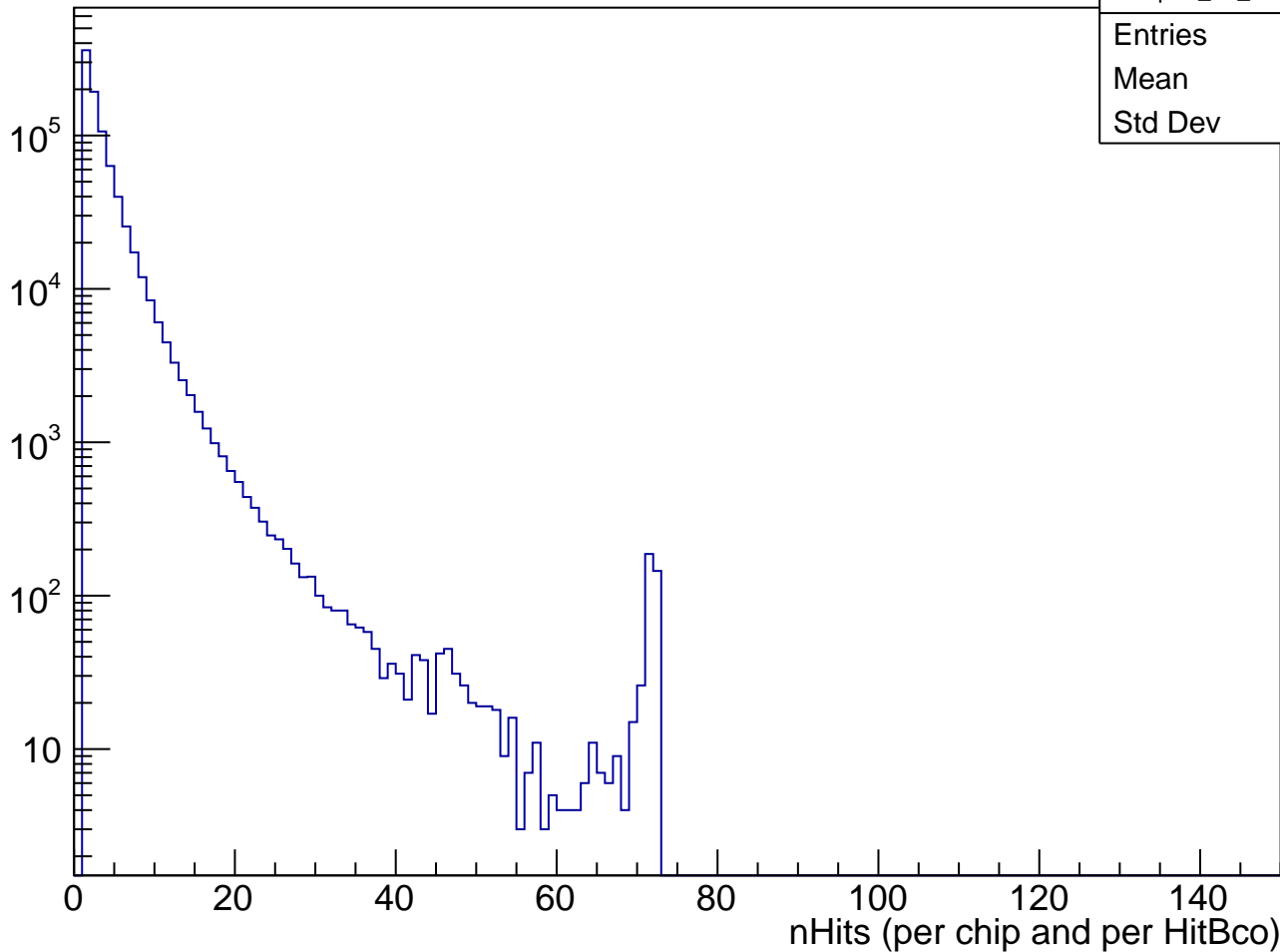


nChipHit_F5_Fch6_Chip1	
Entries	896737
Mean	3.11
Std Dev	3.685

nHits (per chip and per HitBco)

nChipHit_F5_Fch6_Chip10

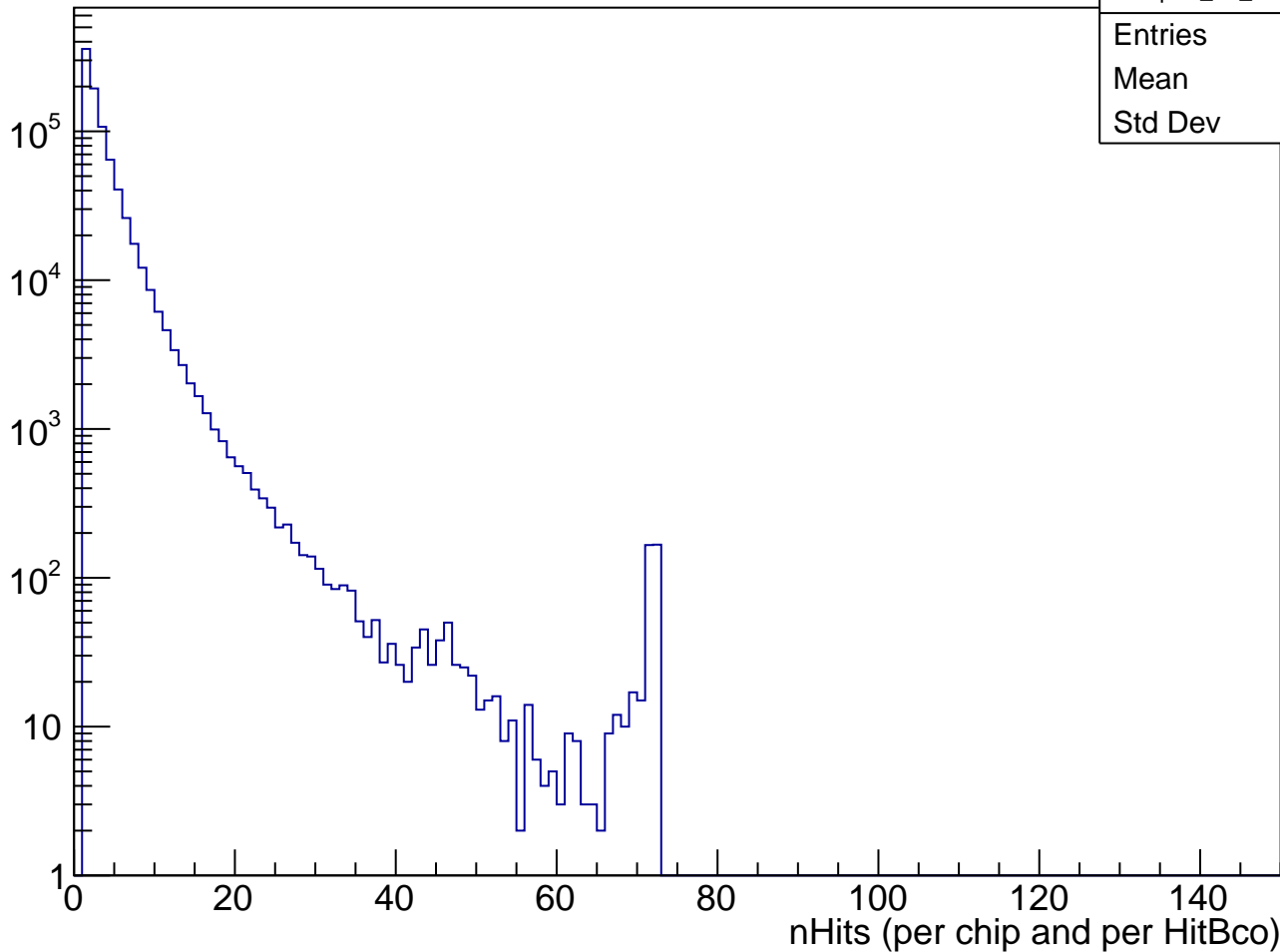
Entries



nChipHit_F5_Fch6_Chip10	
Entries	852998
Mean	2.808
Std Dev	3.384

nChipHit_F5_Fch6_Chip11

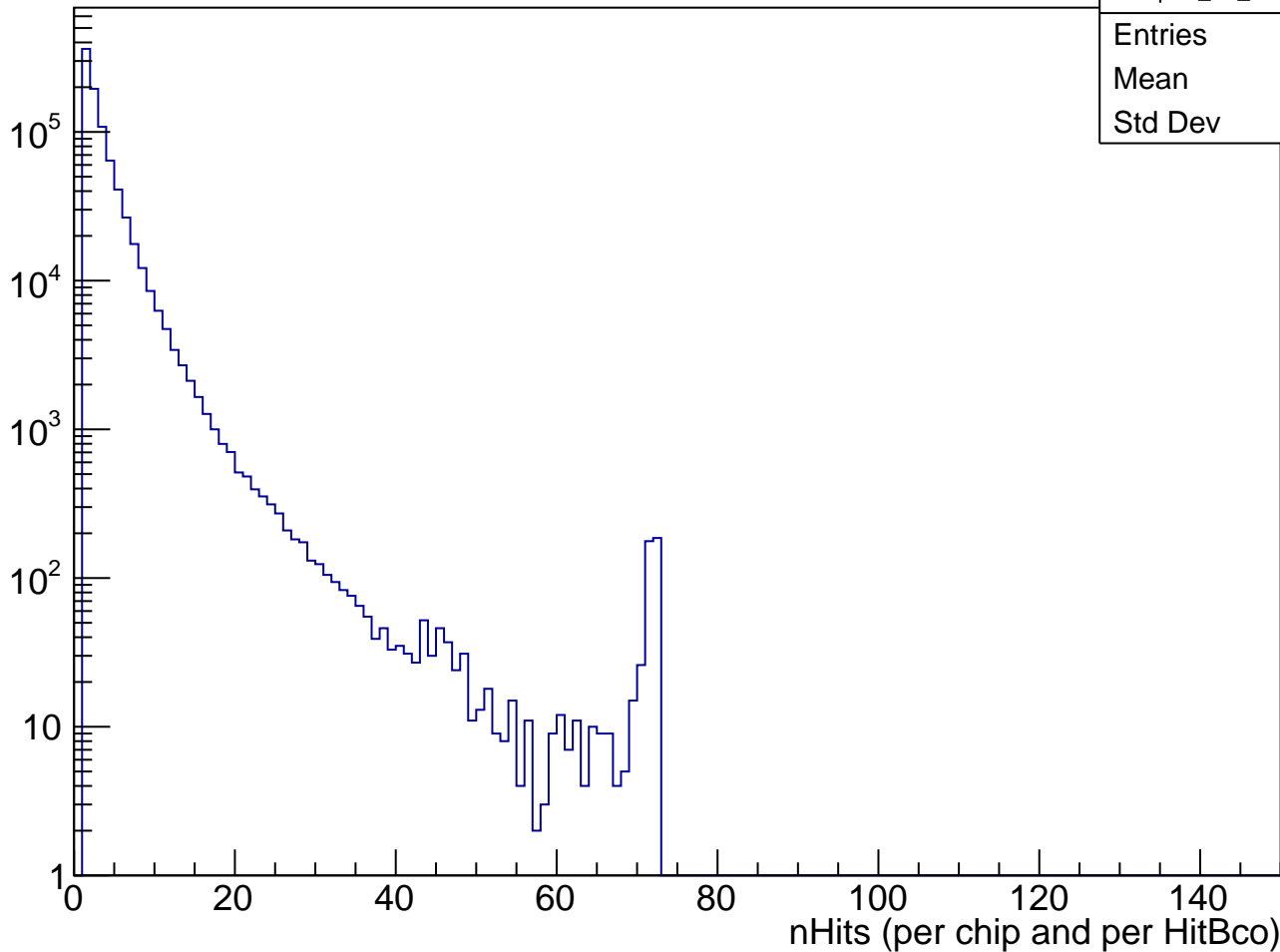
Entries



nChipHit_F5_Fch6_Chip11	
Entries	857374
Mean	2.833
Std Dev	3.4

nChipHit_F5_Fch6_Chip12

Entries

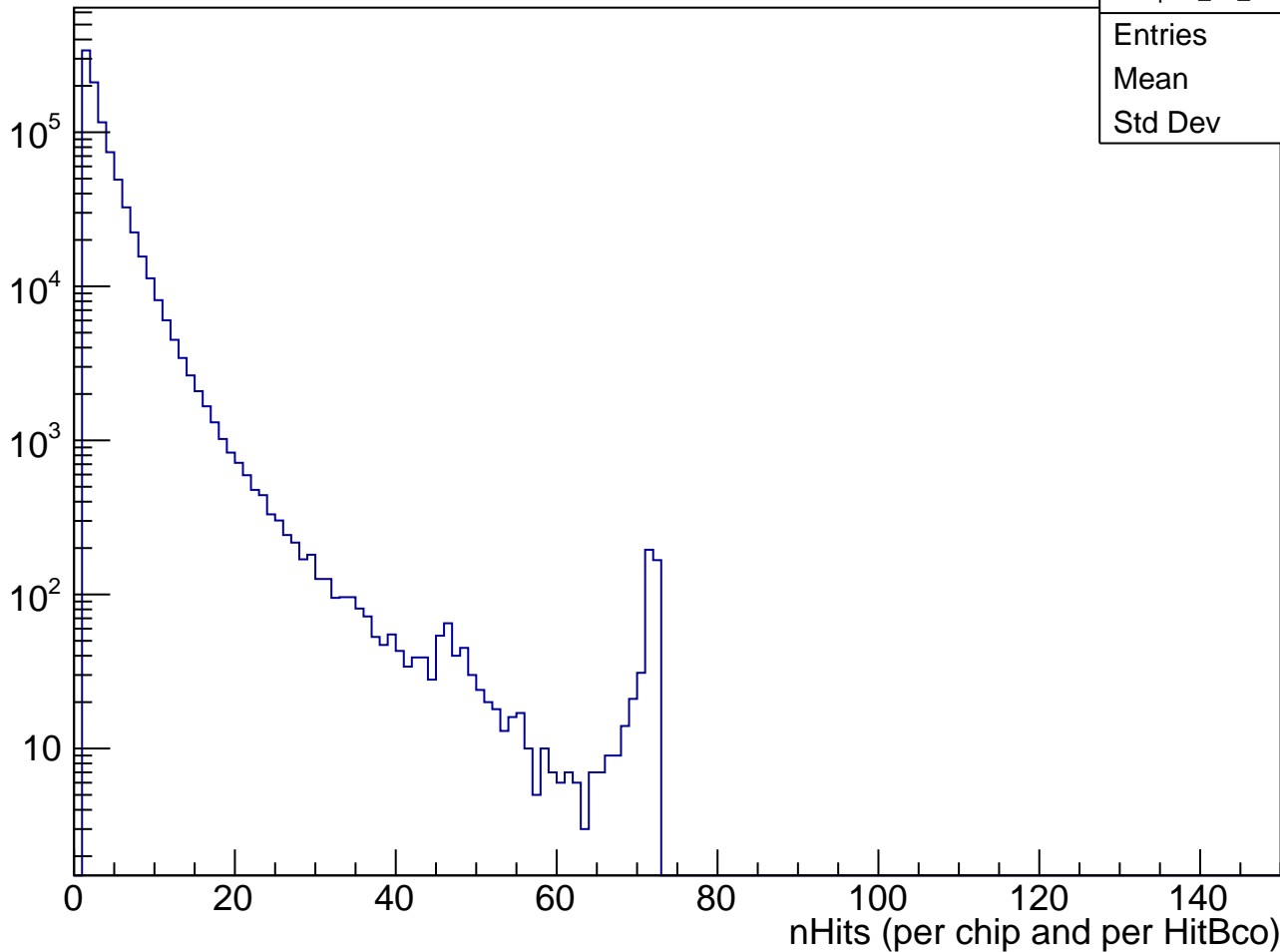


nChipHit_F5_Fch6_Chip12	
Entries	864372
Mean	2.837
Std Dev	3.447

nHits (per chip and per HitBco)

nChipHit_F5_Fch6_Chip13

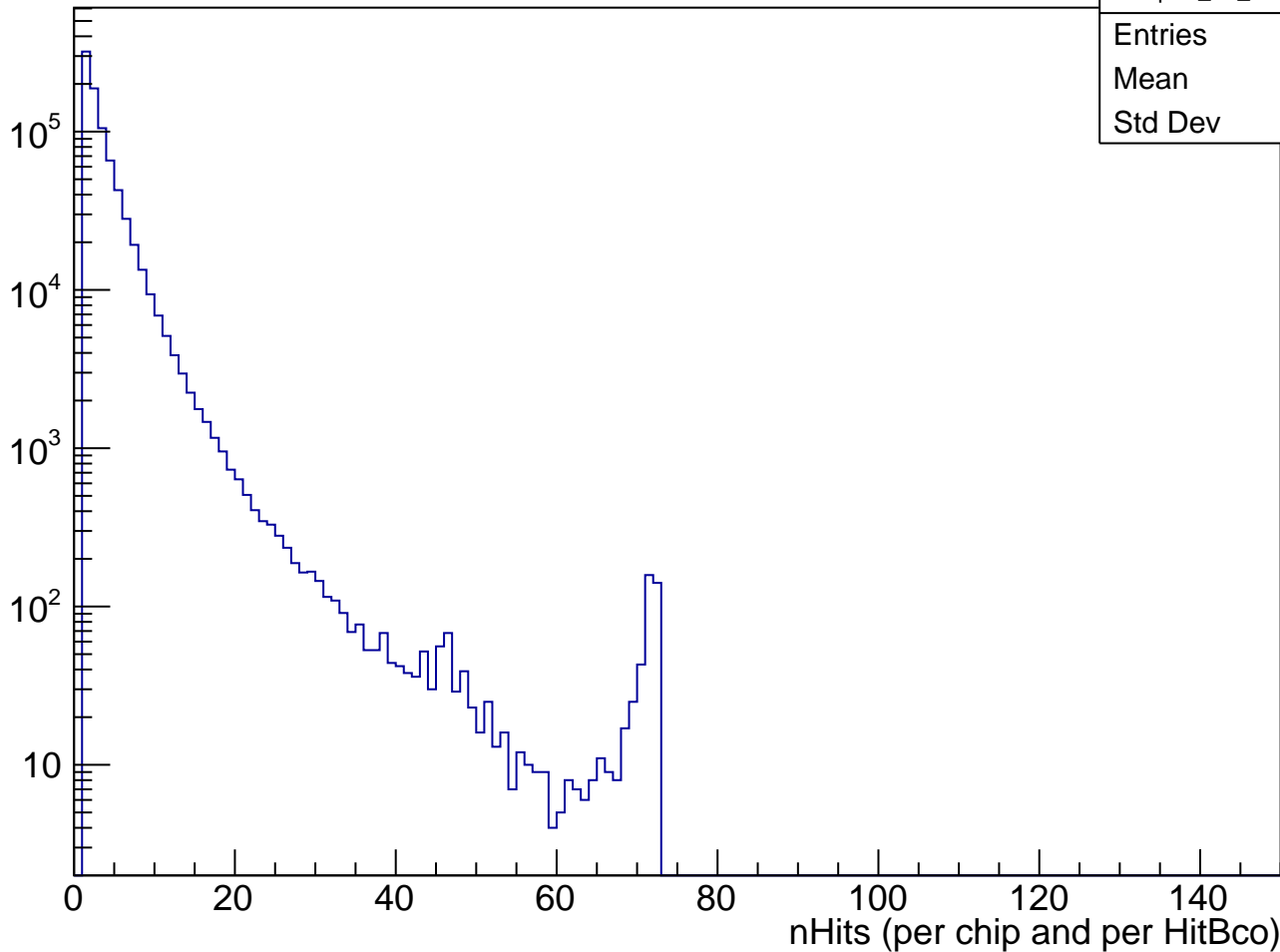
Entries



nChipHit_F5_Fch6_Chip13	
Entries	908875
Mean	3.082
Std Dev	3.628

nChipHit_F5_Fch6_Chip14

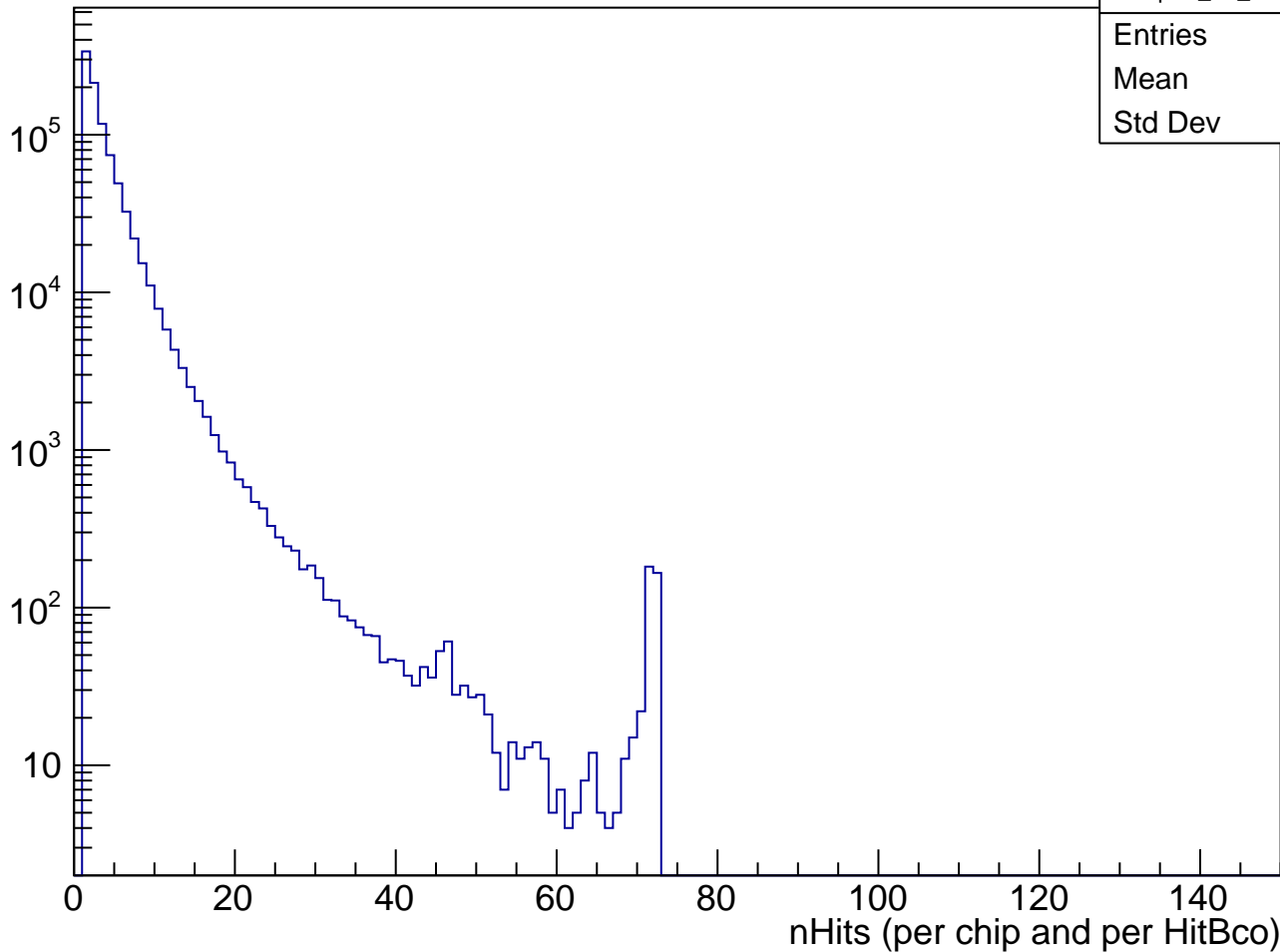
Entries



nChipHit_F5_Fch6_Chip14	
Entries	823672
Mean	3.015
Std Dev	3.617

nChipHit_F5_Fch6_Chip15

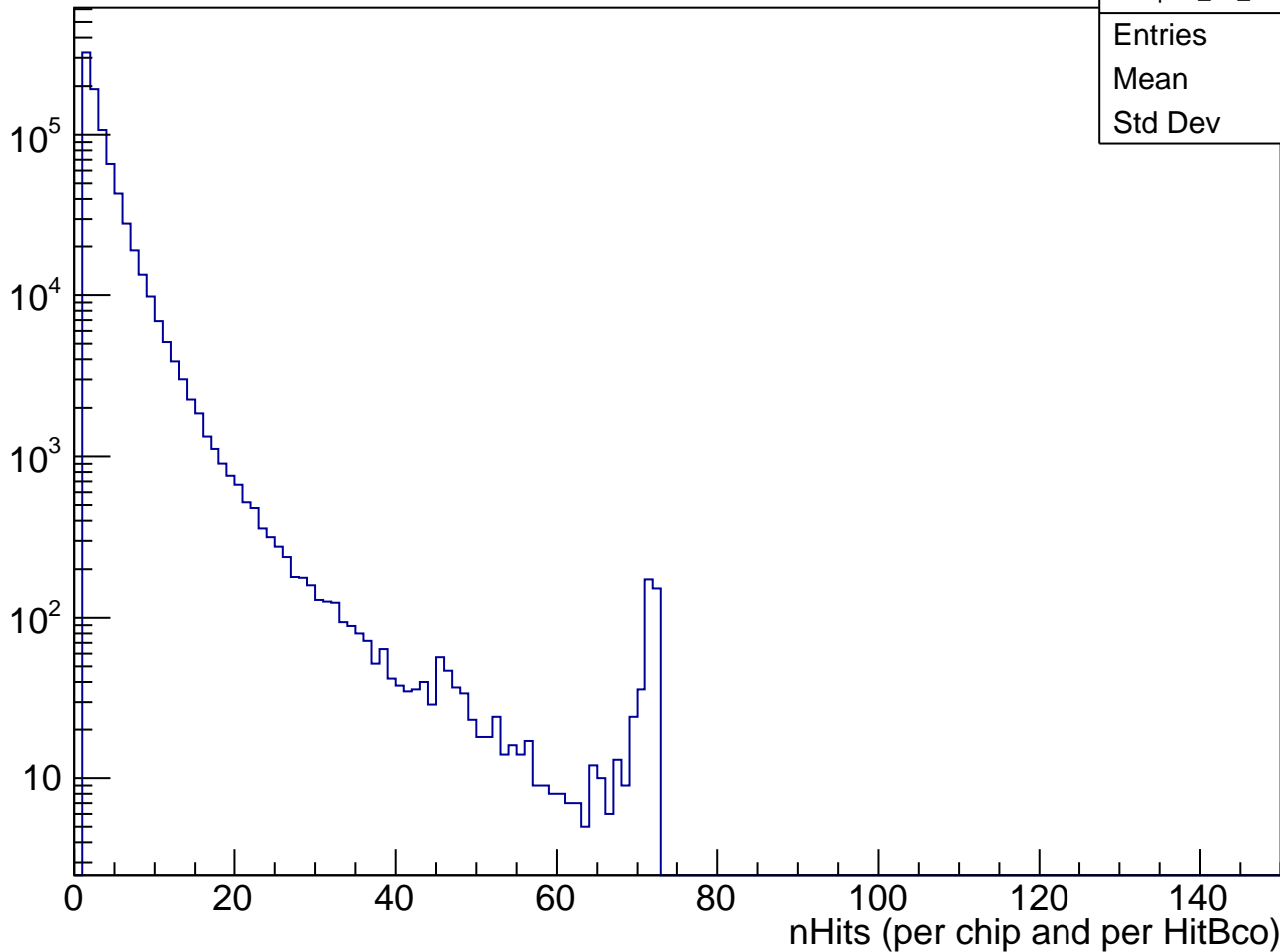
Entries



nChipHit_F5_Fch6_Chip15	
Entries	908446
Mean	3.061
Std Dev	3.571

nChipHit_F5_Fch6_Chip16

Entries

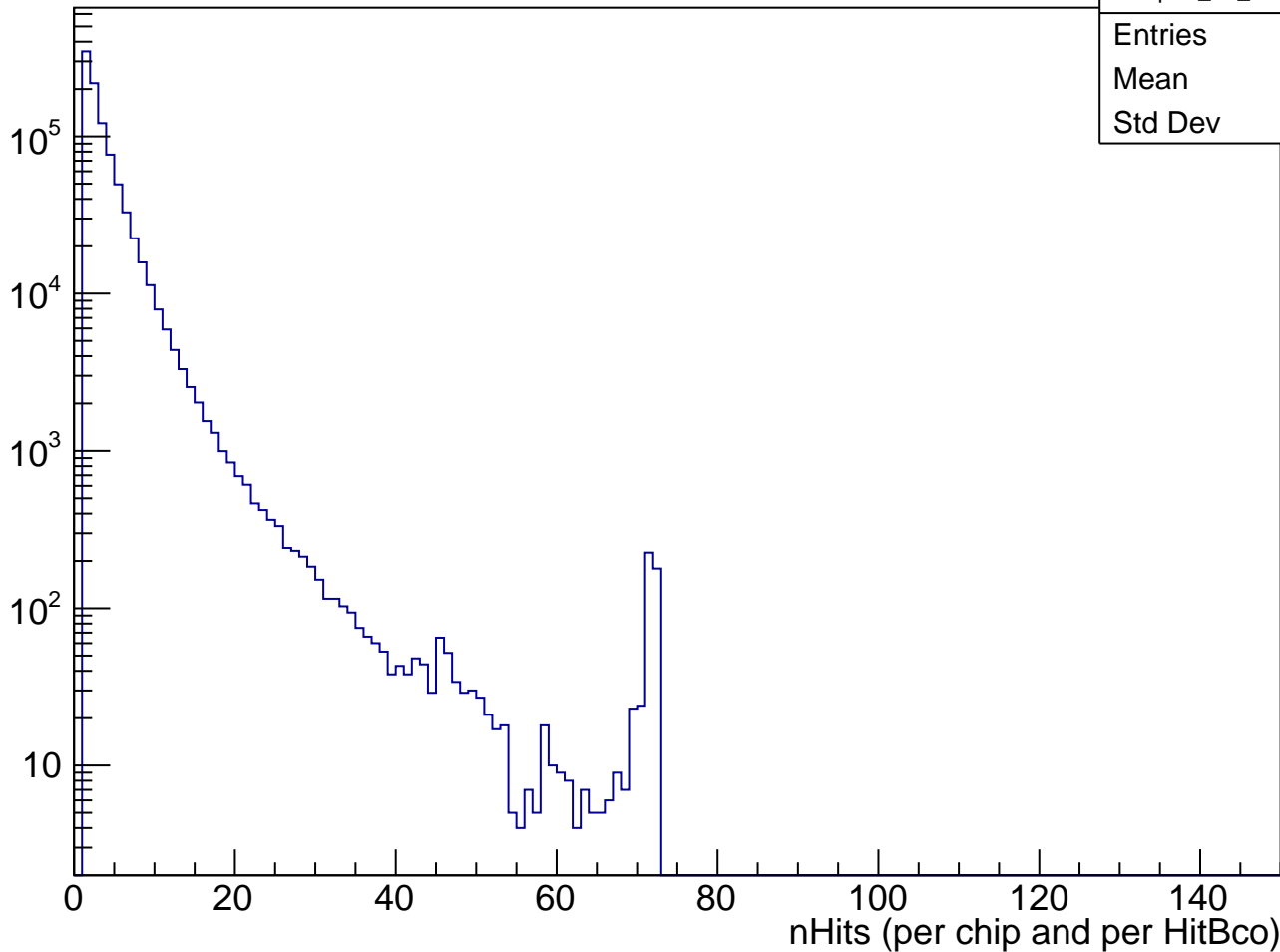


nChipHit_F5_Fch6_Chip16	
Entries	833994
Mean	3.009
Std Dev	3.624

nHits (per chip and per HitBco)

nChipHit_F5_Fch6_Chip17

Entries

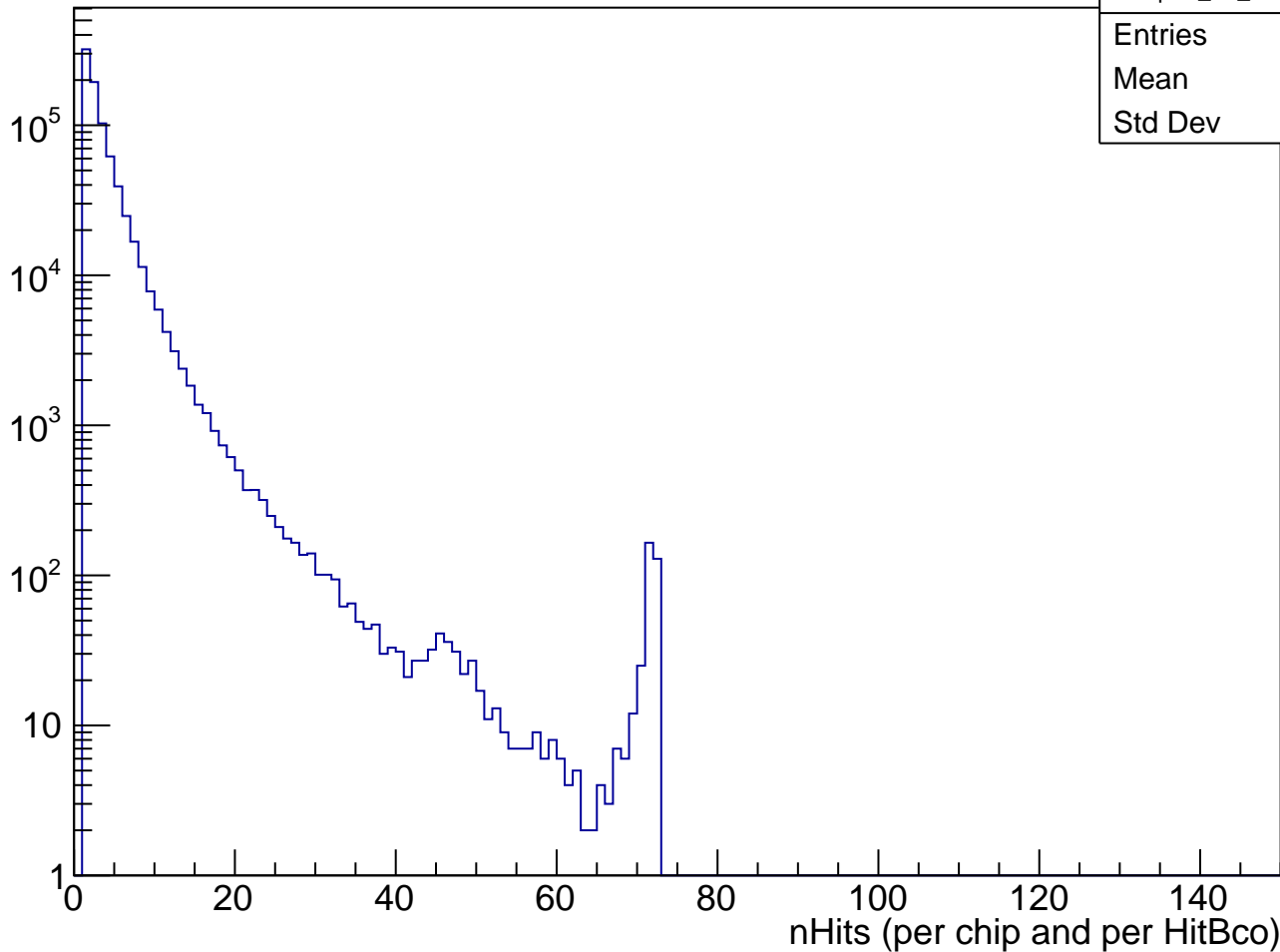


nChipHit_F5_Fch6_Chip17	
Entries	931474
Mean	3.058
Std Dev	3.604

nHits (per chip and per HitBco)

nChipHit_F5_Fch6_Chip18

Entries

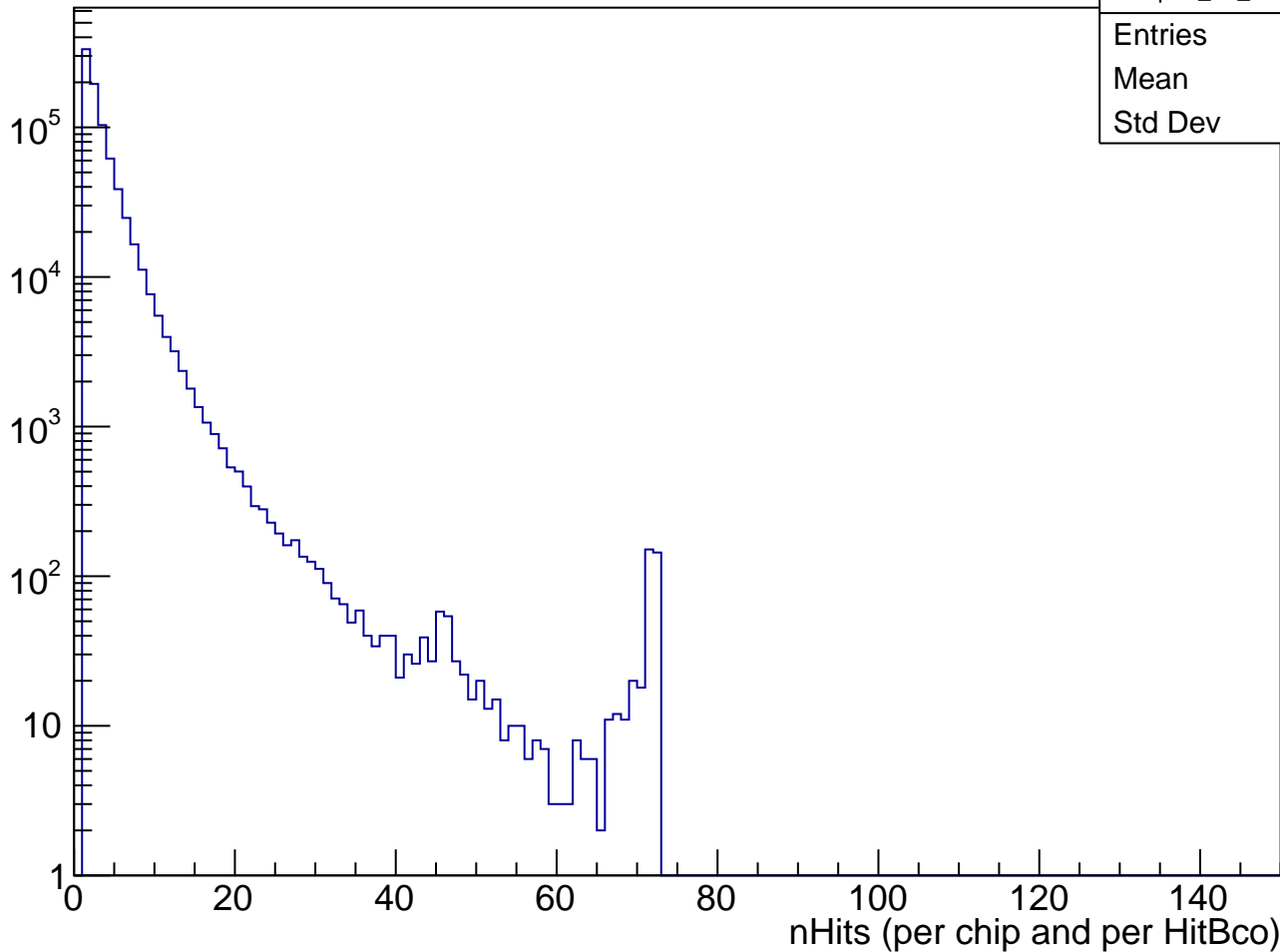


nChipHit_F5_Fch6_Chip18	
Entries	805913
Mean	2.848
Std Dev	3.349

nHits (per chip and per HitBco)

nChipHit_F5_Fch6_Chip19

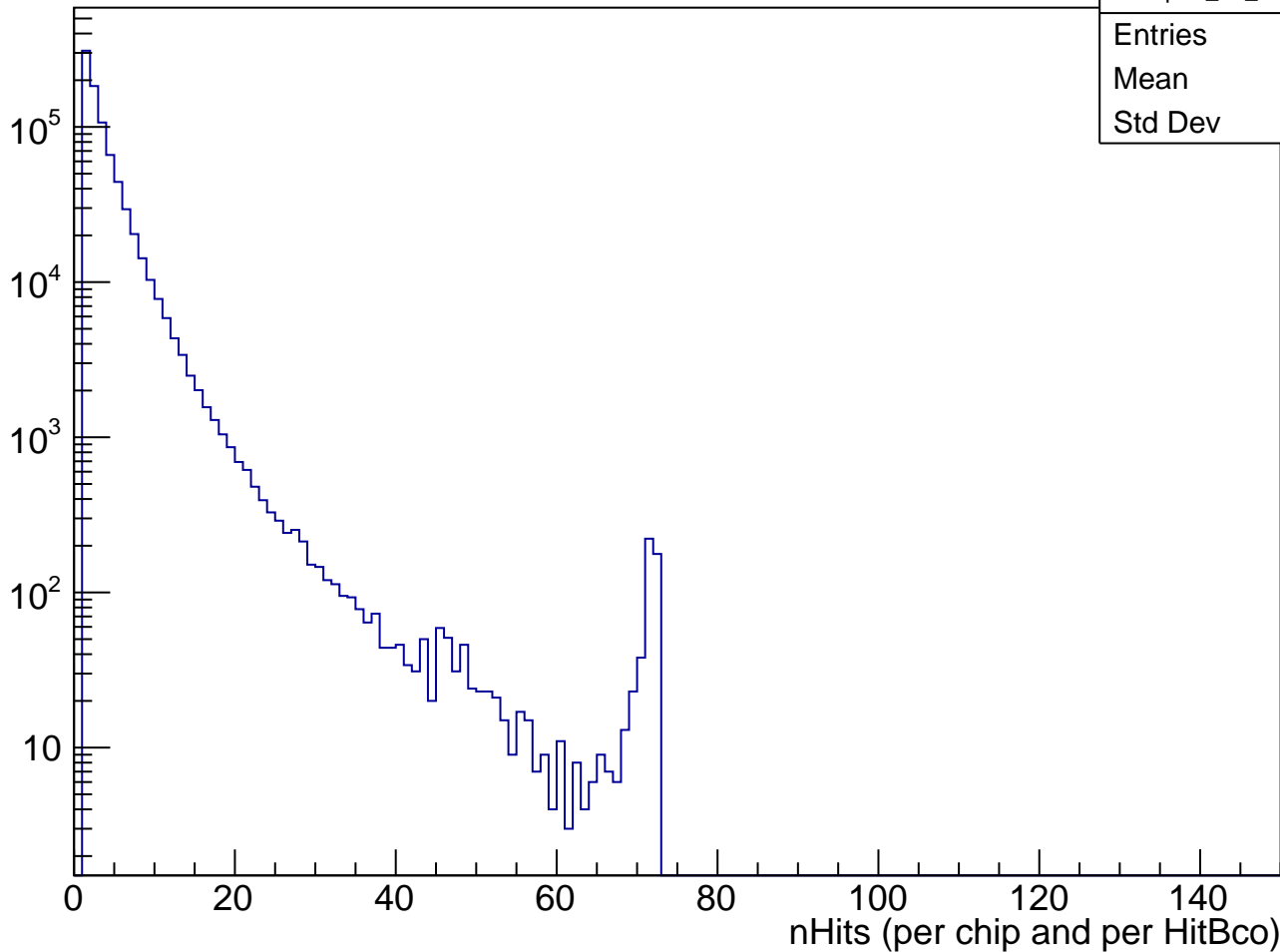
Entries



nChipHit_F5_Fch6_Chip19	
Entries	817672
Mean	2.802
Std Dev	3.324

nChipHit_F5_Fch6_Chip2

Entries



nChipHit_F5_Fch6_Chip2

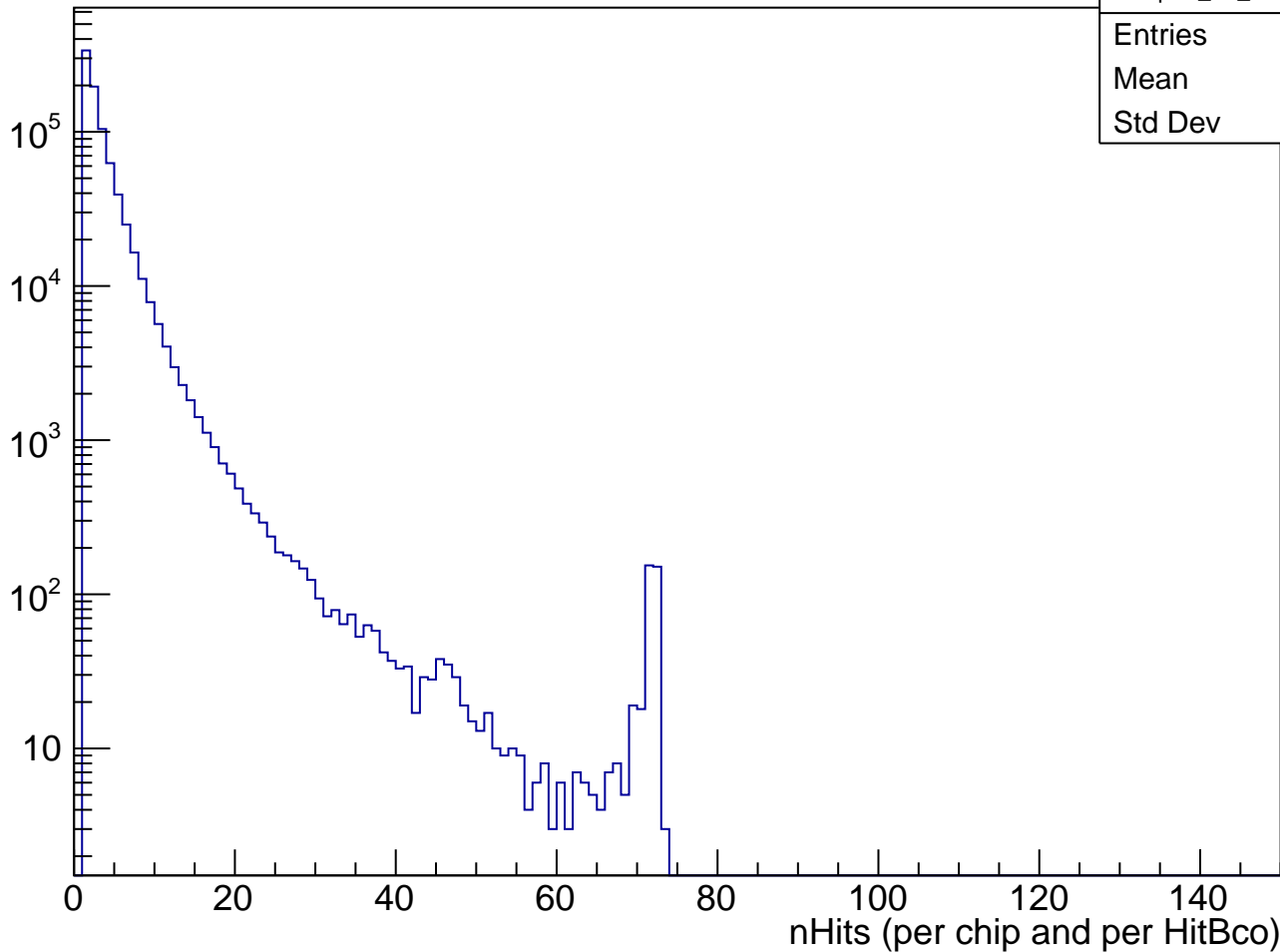
Entries 821018

Mean 3.131

Std Dev 3.784

nChipHit_F5_Fch6_Chip20

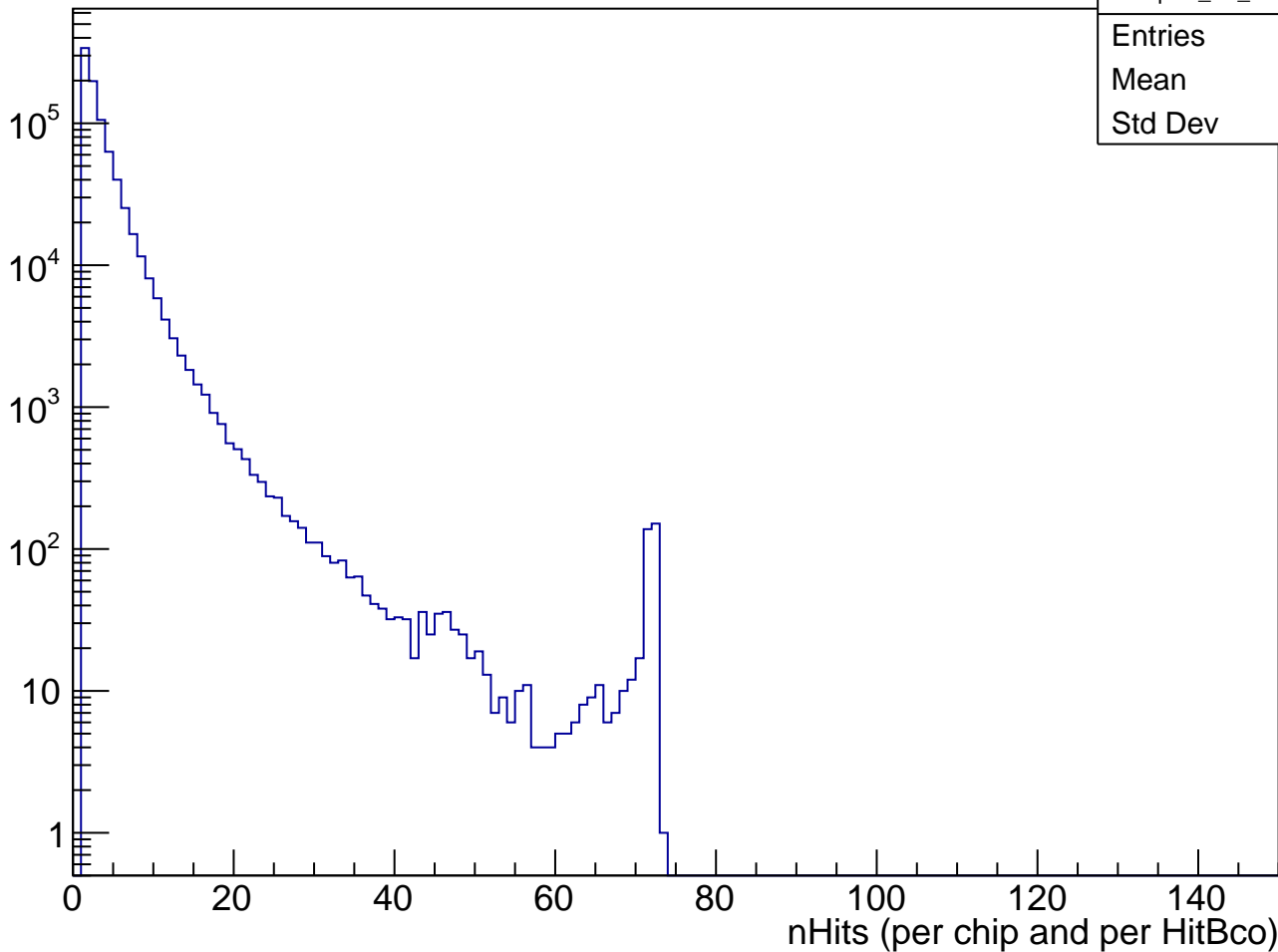
Entries



nChipHit_F5_Fch6_Chip20	
Entries	826127
Mean	2.798
Std Dev	3.312

nChipHit_F5_Fch6_Chip21

Entries

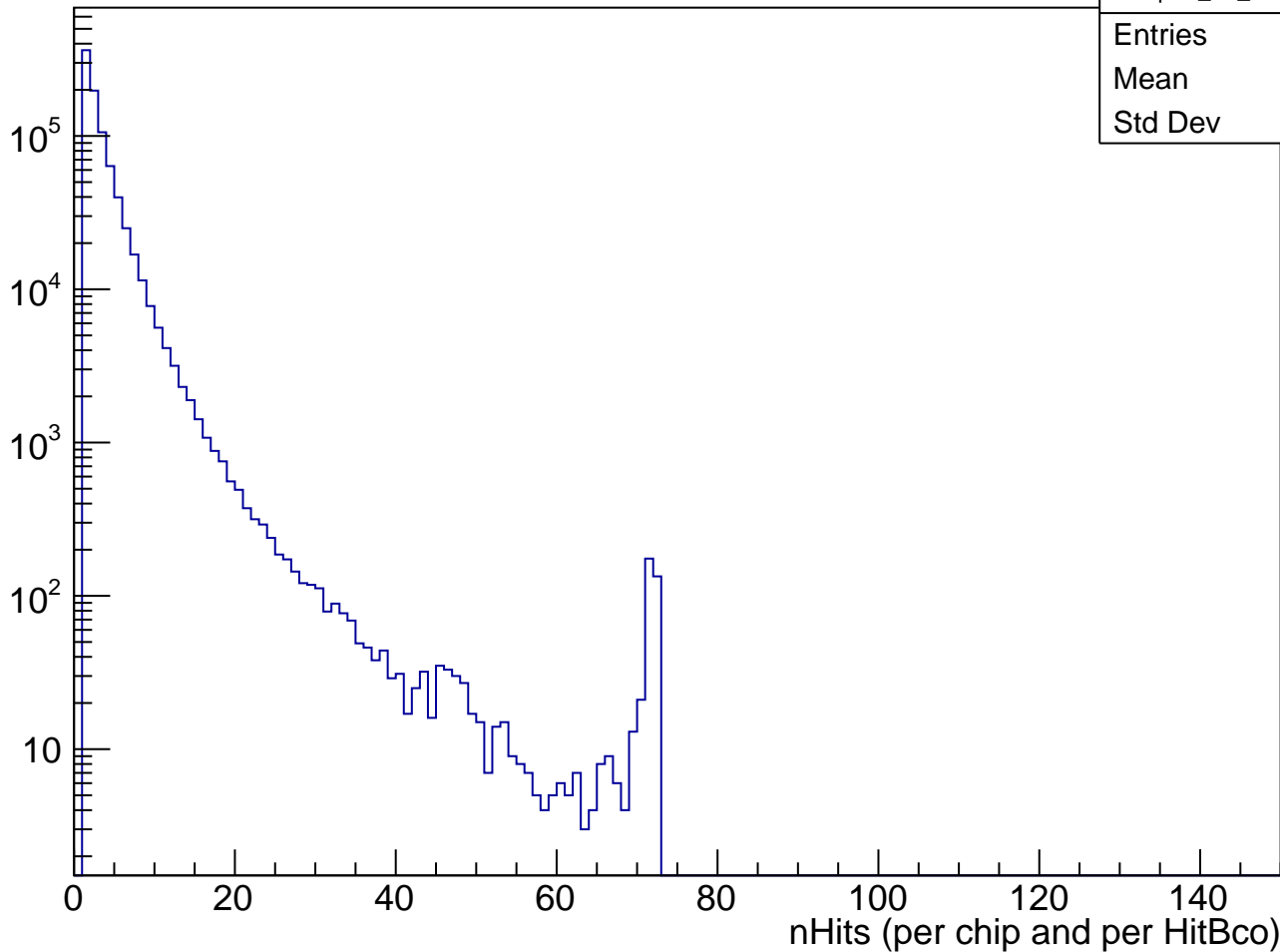


nChipHit_F5_Fch6_Chip21	
Entries	833717
Mean	2.808
Std Dev	3.306

nHits (per chip and per HitBco)

nChipHit_F5_Fch6_Chip22

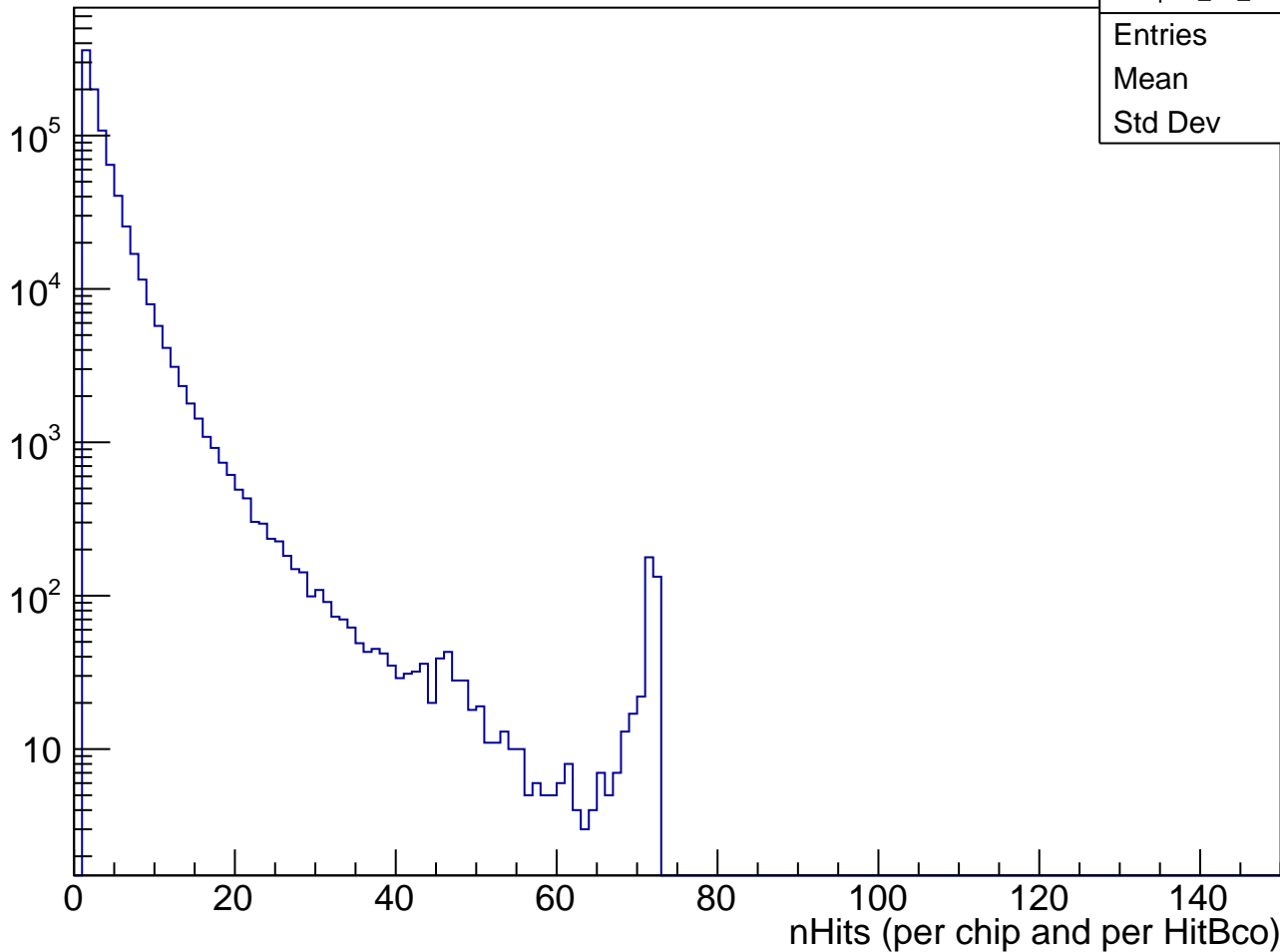
Entries



nChipHit_F5_Fch6_Chip22	
Entries	856210
Mean	2.749
Std Dev	3.258

nChipHit_F5_Fch6_Chip23

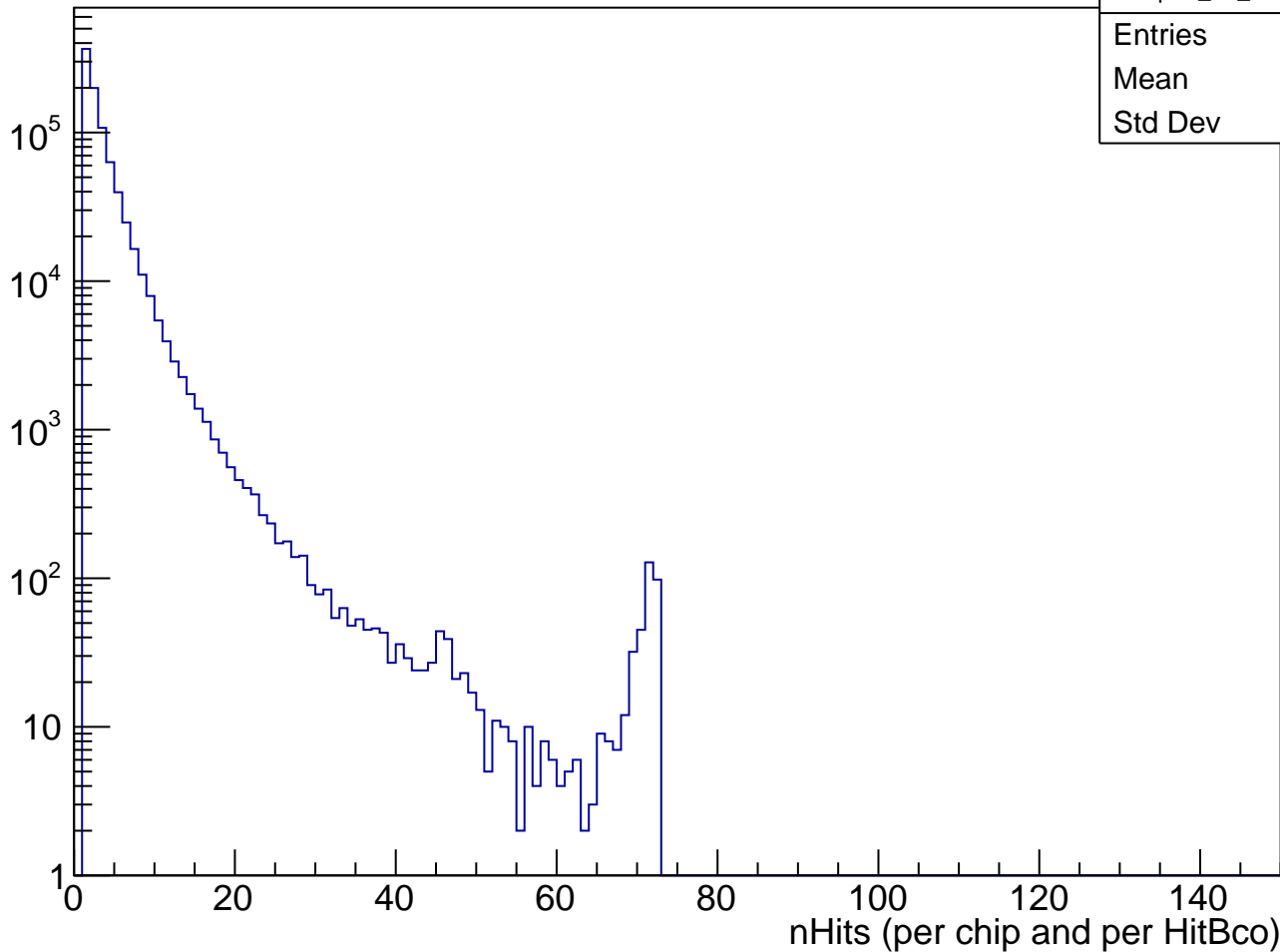
Entries



nChipHit_F5_Fch6_Chip23	
Entries	860560
Mean	2.766
Std Dev	3.284

nChipHit_F5_Fch6_Chip24

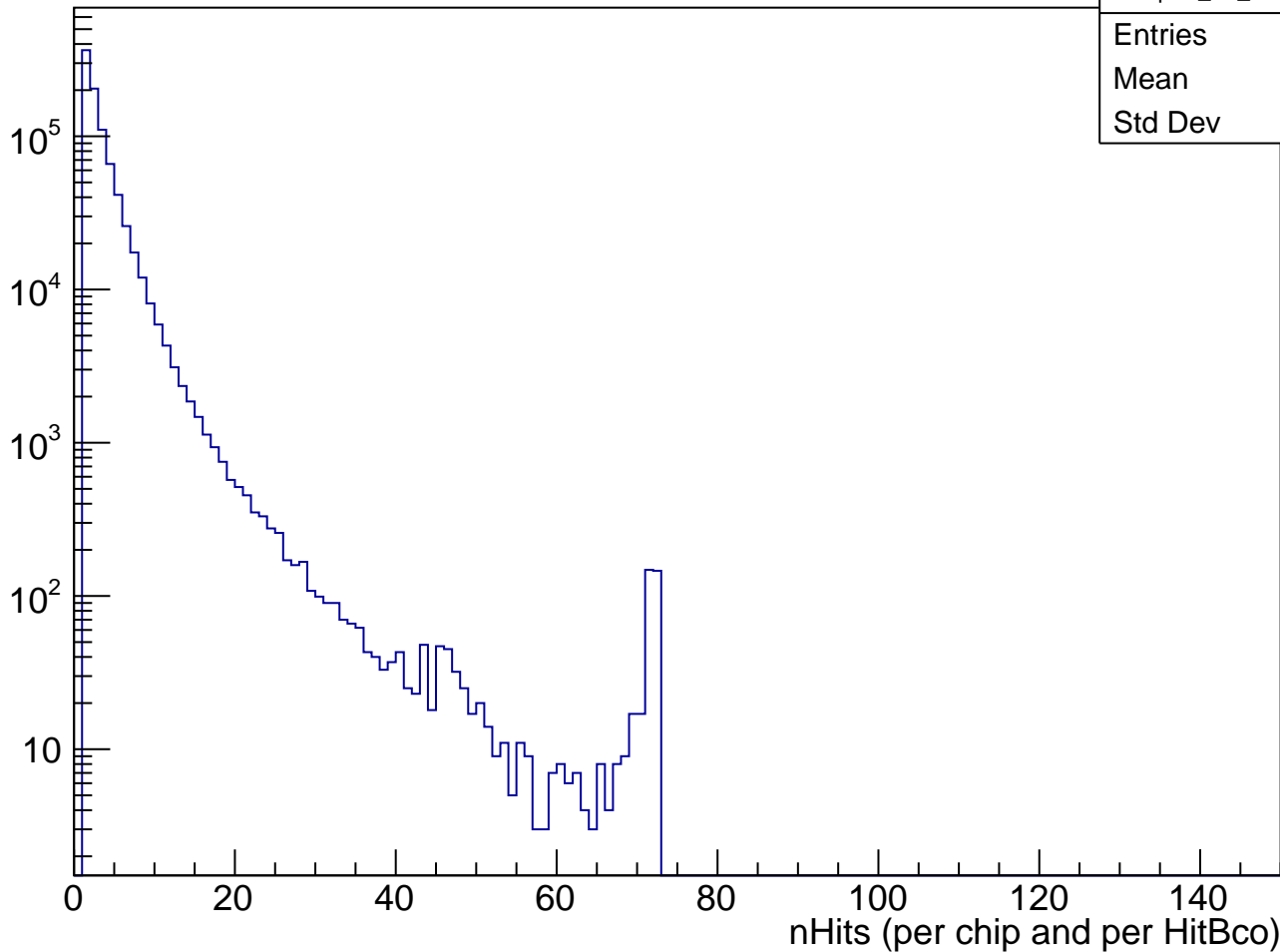
Entries



nChipHit_F5_Fch6_Chip24	
Entries	860113
Mean	2.722
Std Dev	3.187

nChipHit_F5_Fch6_Chip25

Entries

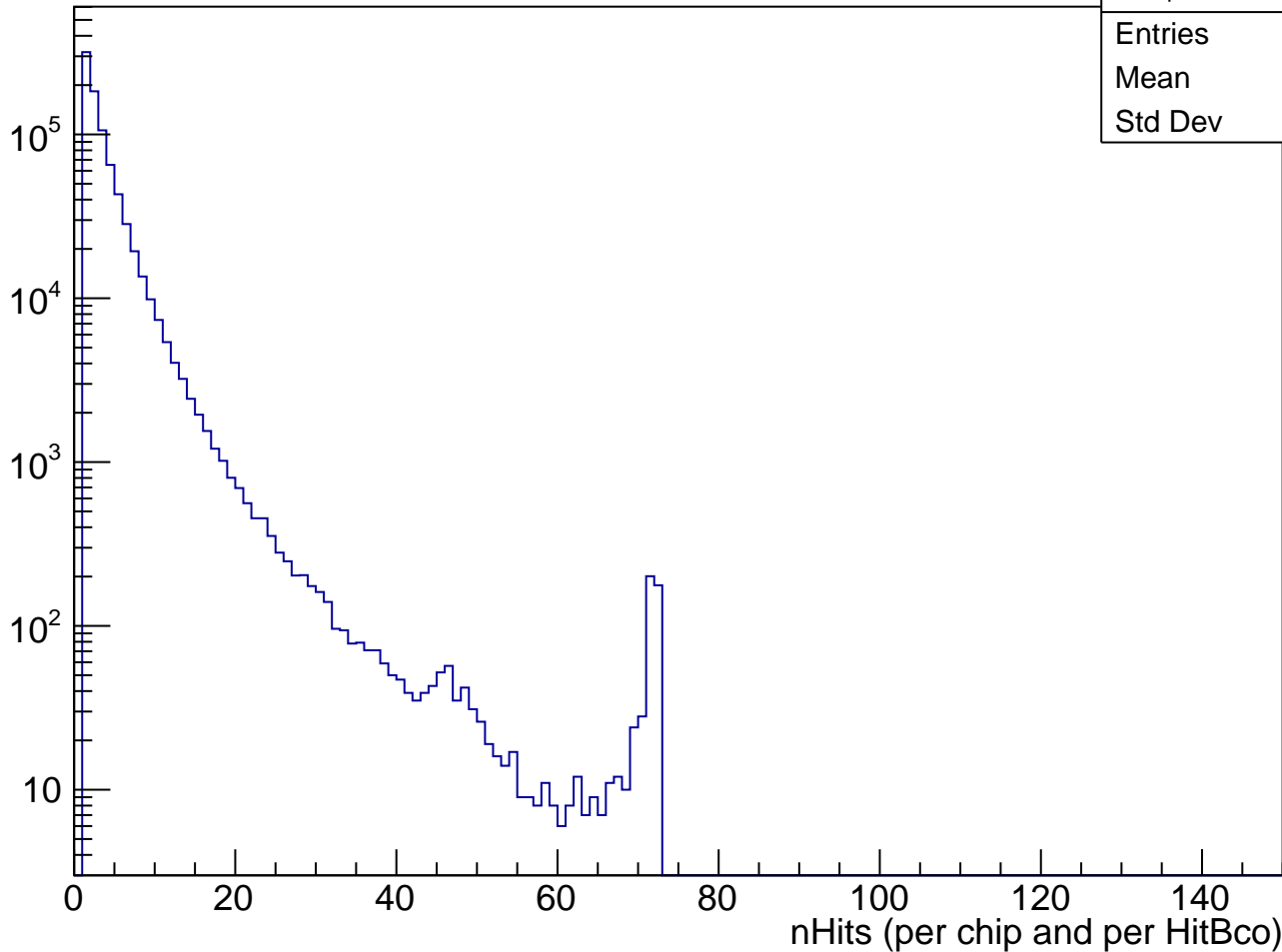


nChipHit_F5_Fch6_Chip25	
Entries	877185
Mean	2.776
Std Dev	3.276

nHits (per chip and per HitBco)

nChipHit_F5_Fch6_Chip3

Entries



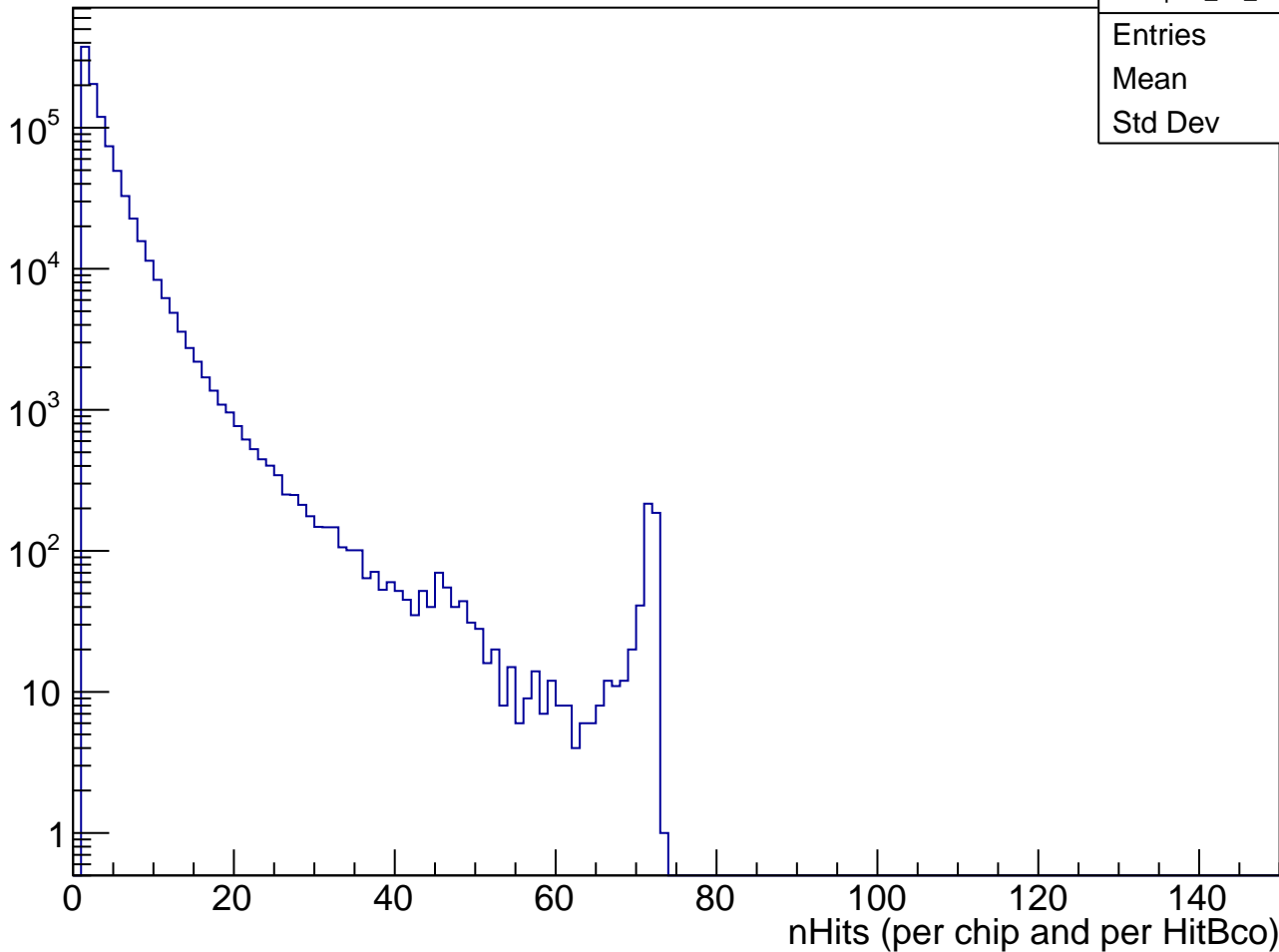
nChipHit_F5_Fch6_Chip3

Entries	821292
Mean	3.073
Std Dev	3.751

nHits (per chip and per HitBco)

nChipHit_F5_Fch6_Chip4

Entries

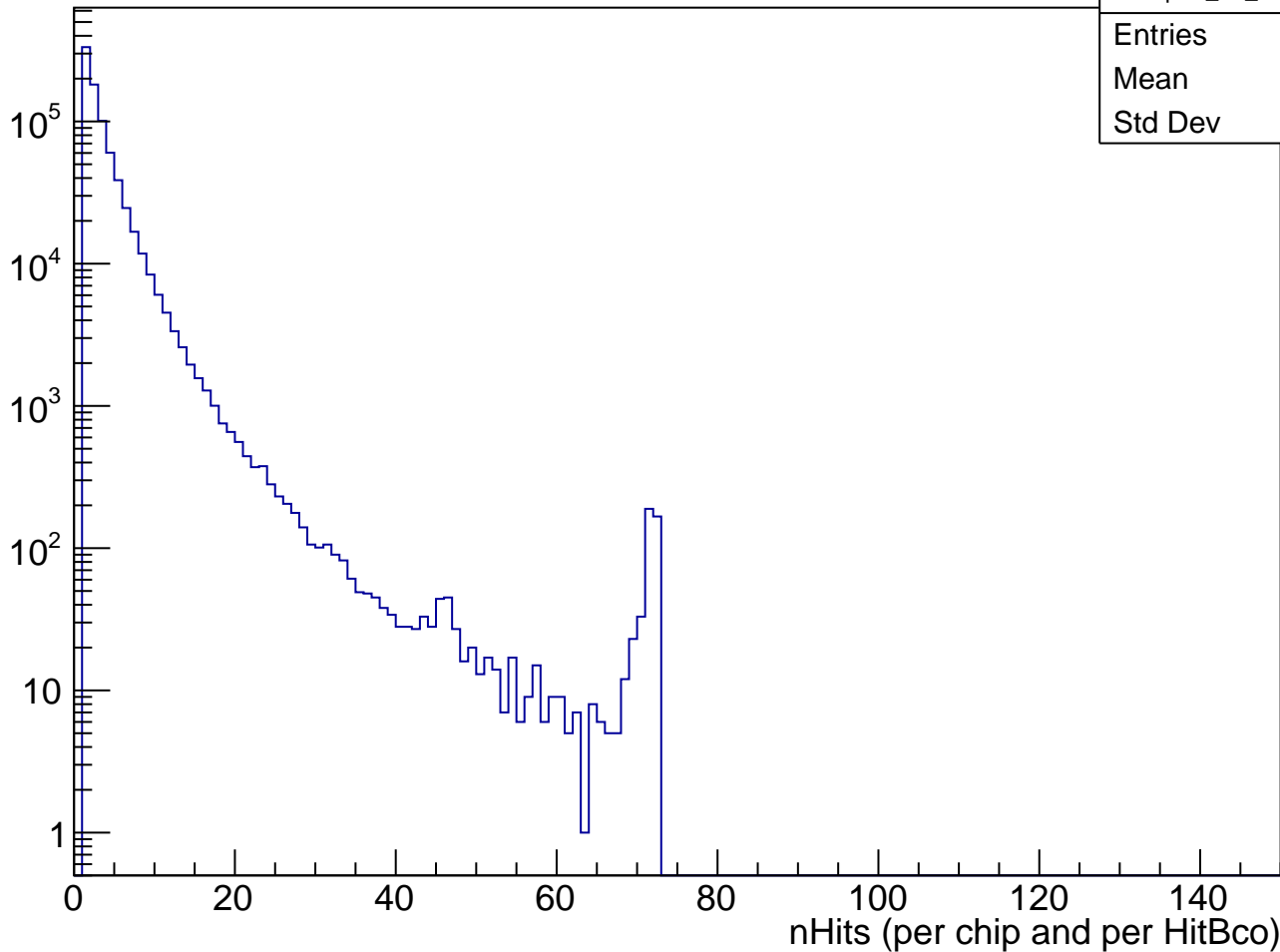


nChipHit_F5_Fch6_Chip4

Entries	943193
Mean	3.048
Std Dev	3.694

nChipHit_F5_Fch6_Chip5

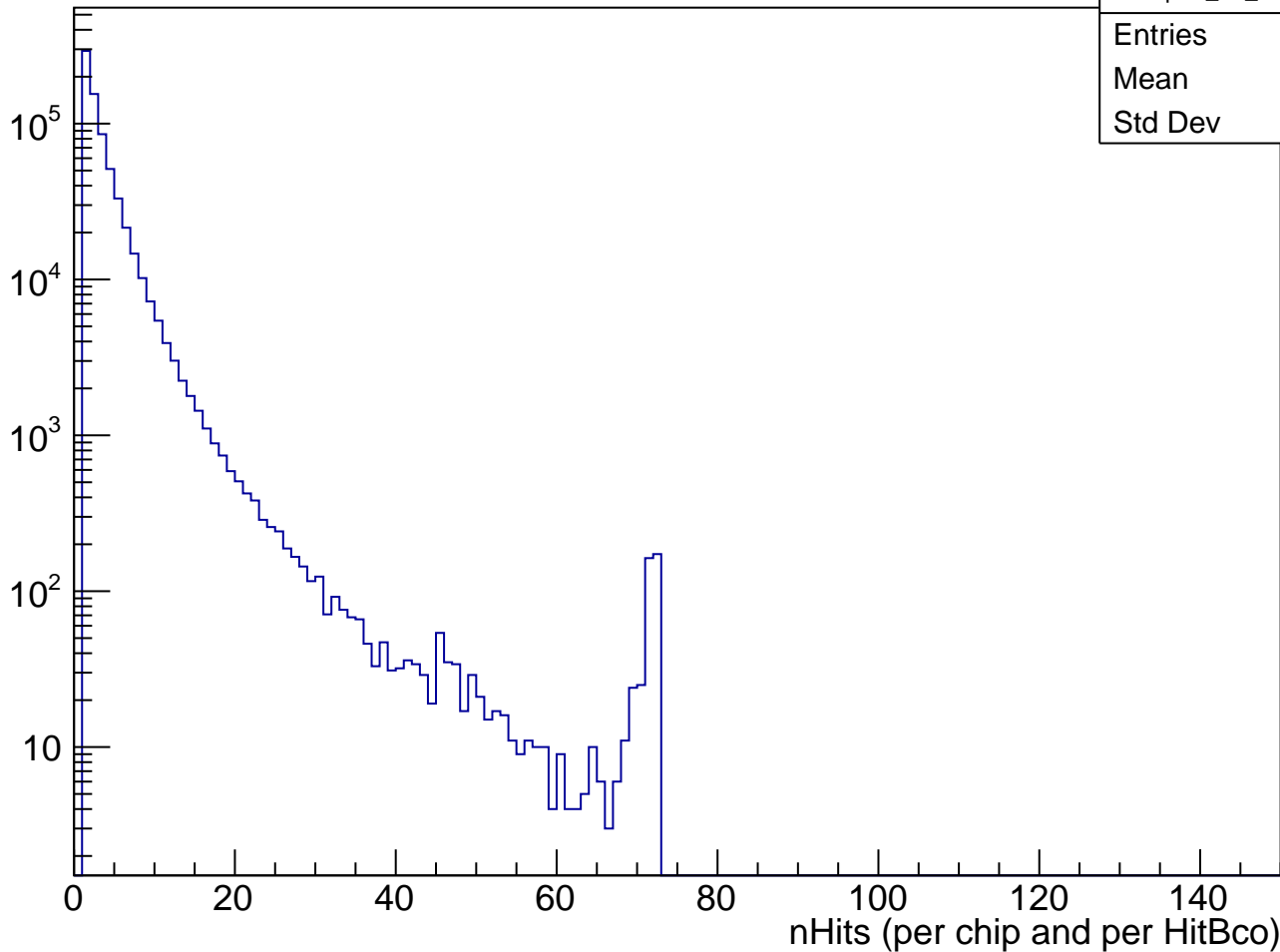
Entries



nChipHit_F5_Fch6_Chip5	
Entries	804406
Mean	2.869
Std Dev	3.503

nChipHit_F5_Fch6_Chip6

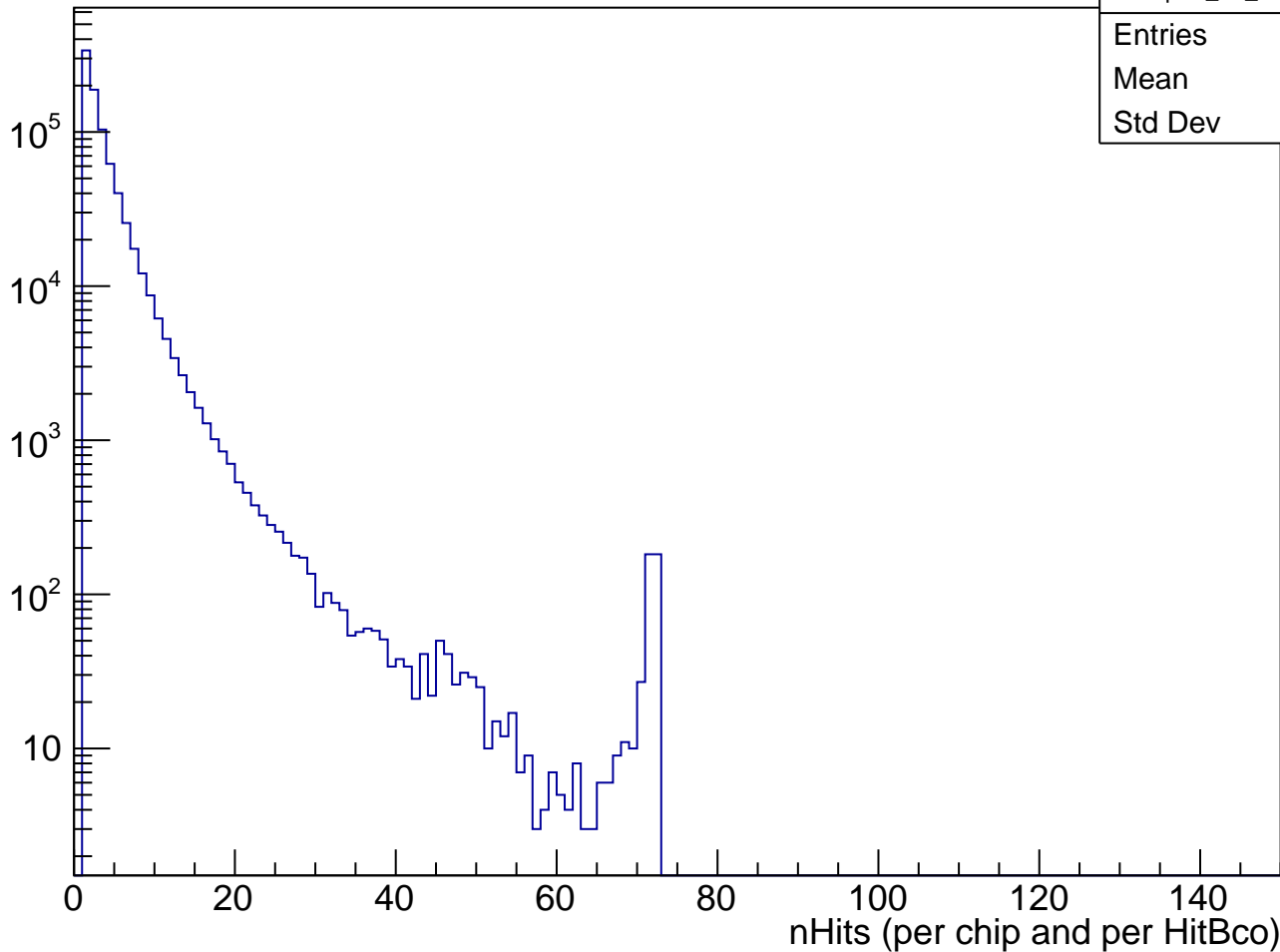
Entries



nChipHit_F5_Fch6_Chip6	
Entries	696543
Mean	2.887
Std Dev	3.624

nChipHit_F5_Fch6_Chip7

Entries

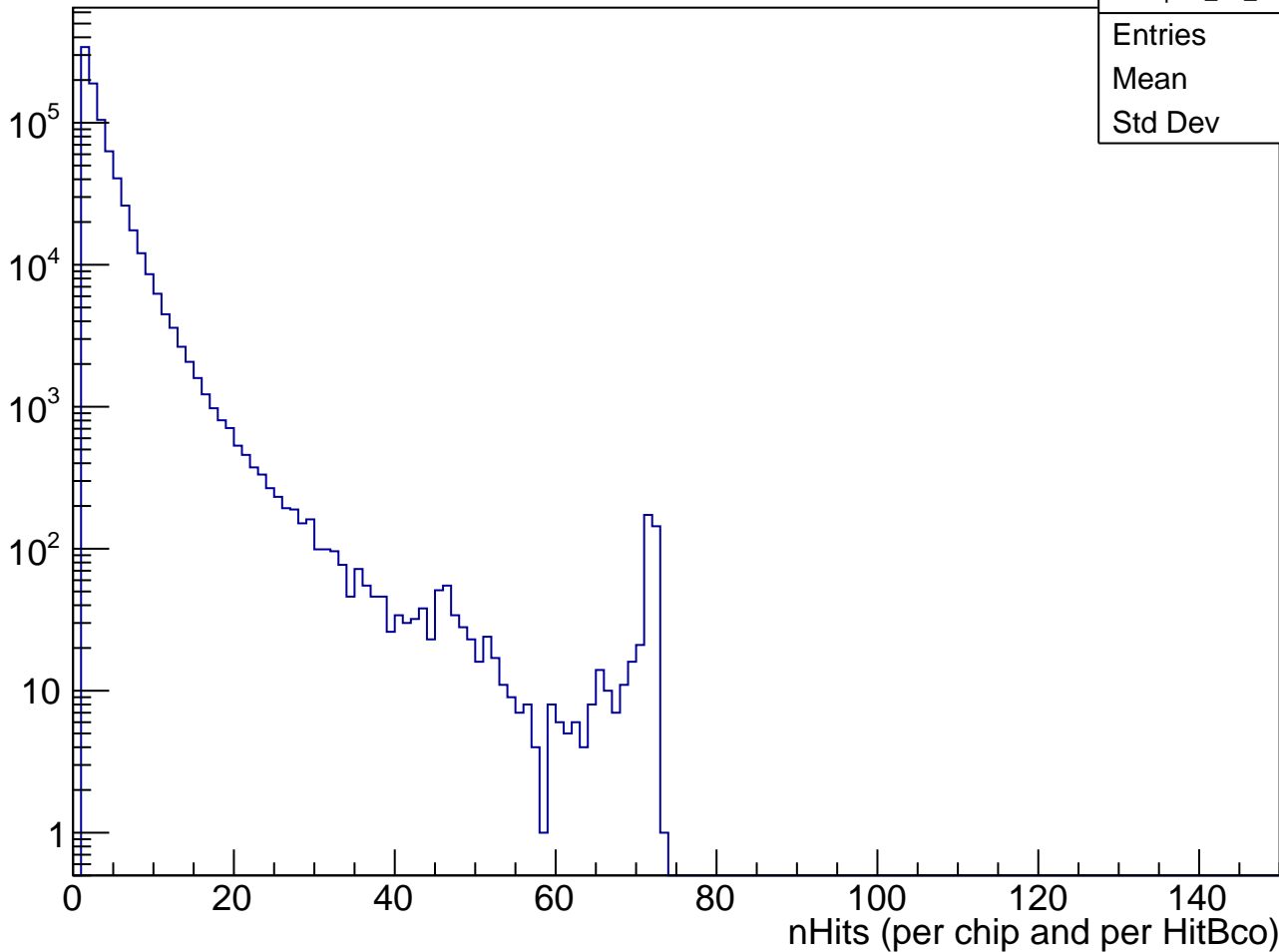


nChipHit_F5_Fch6_Chip7	
Entries	824966
Mean	2.88
Std Dev	3.495

nHits (per chip and per HitBco)

nChipHit_F5_Fch6_Chip8

Entries



nChipHit_F5_Fch6_Chip8

Entries 831992

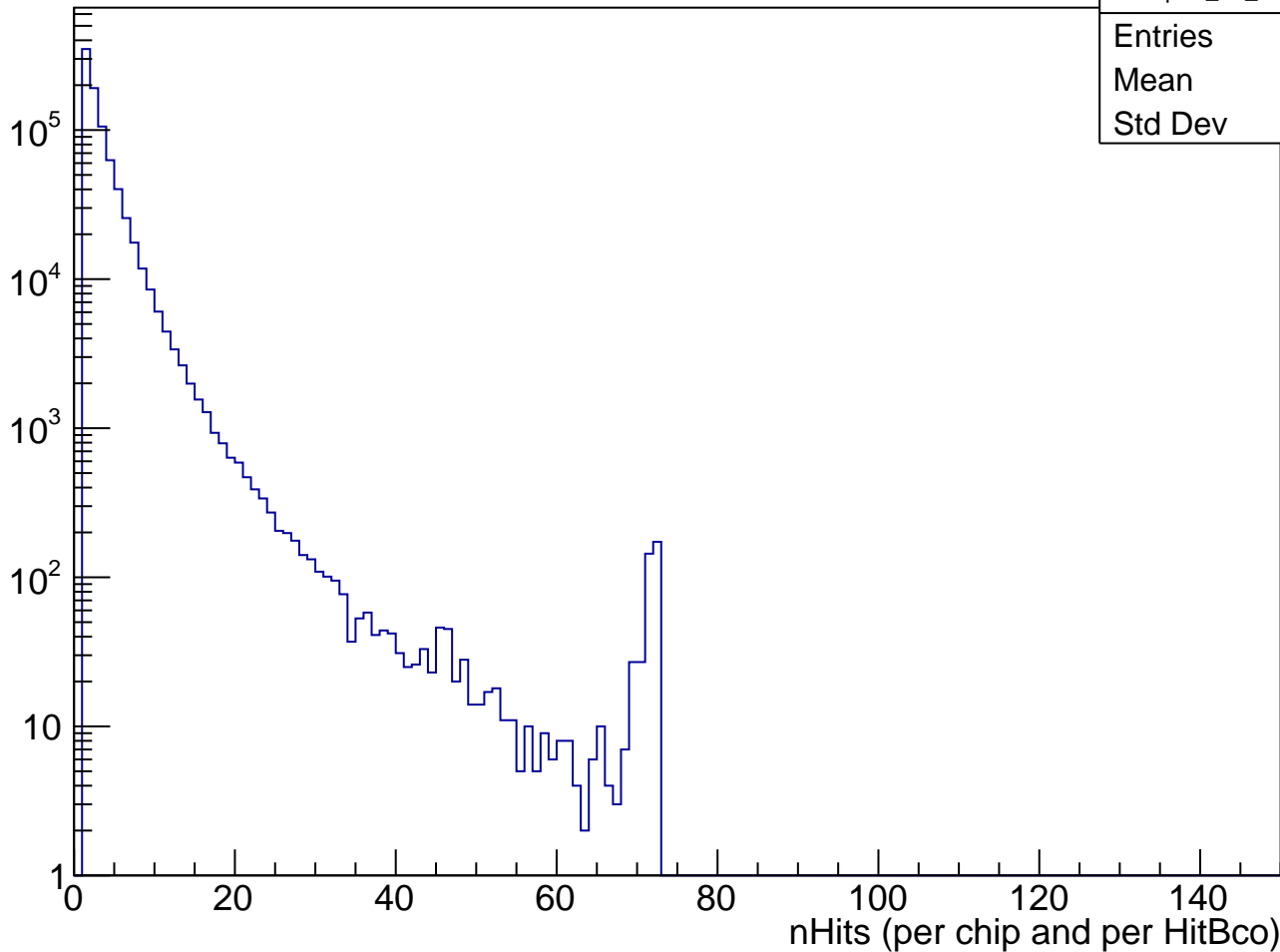
Mean 2.871

Std Dev 3.45

nHits (per chip and per HitBco)

nChipHit_F5_Fch6_Chip9

Entries



nChipHit_F5_Fch6_Chip9

Entries 840821

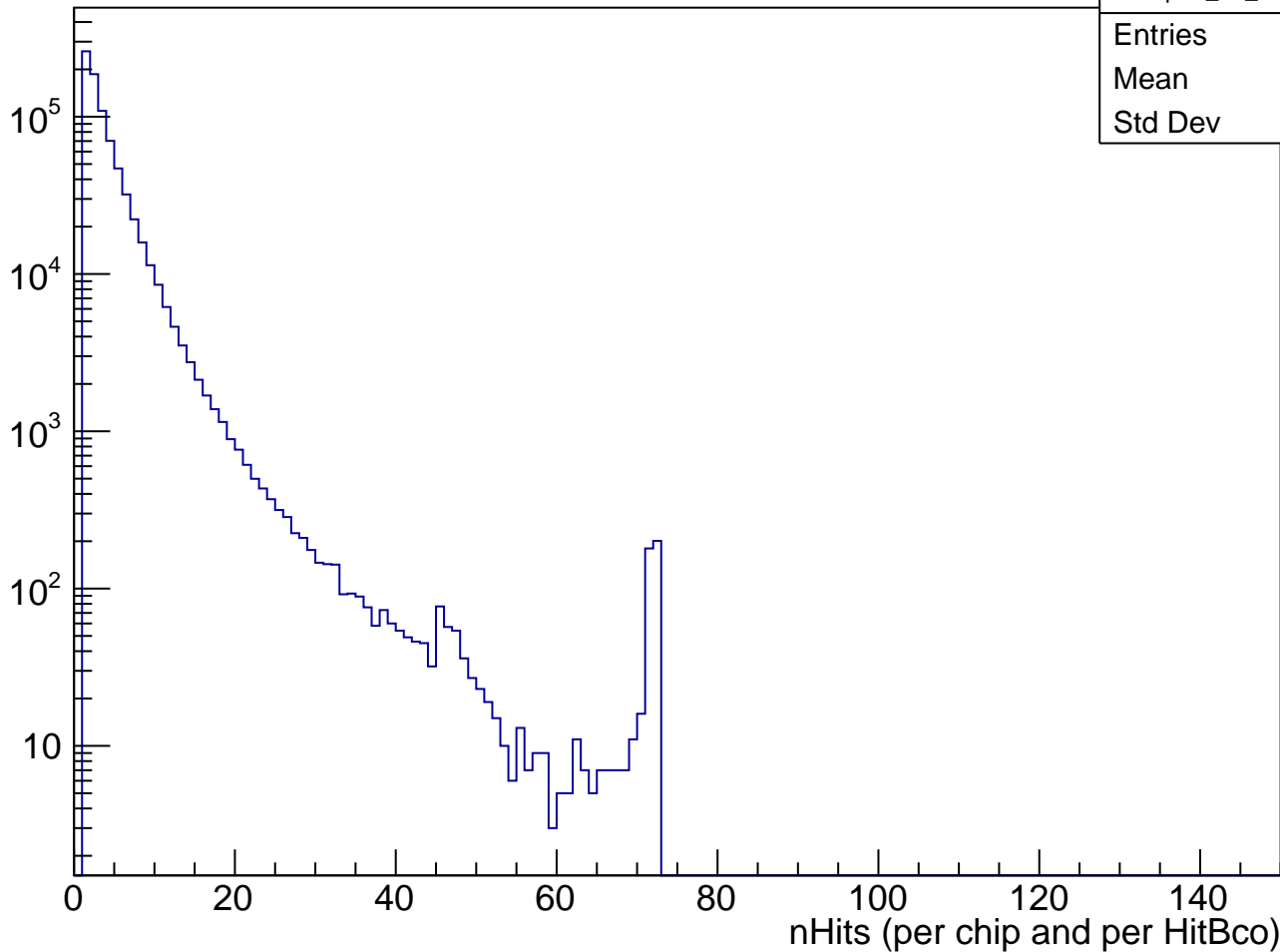
Mean 2.835

Std Dev 3.403

nHits (per chip and per HitBco)

nChipHit_F5_Fch7_Chip0

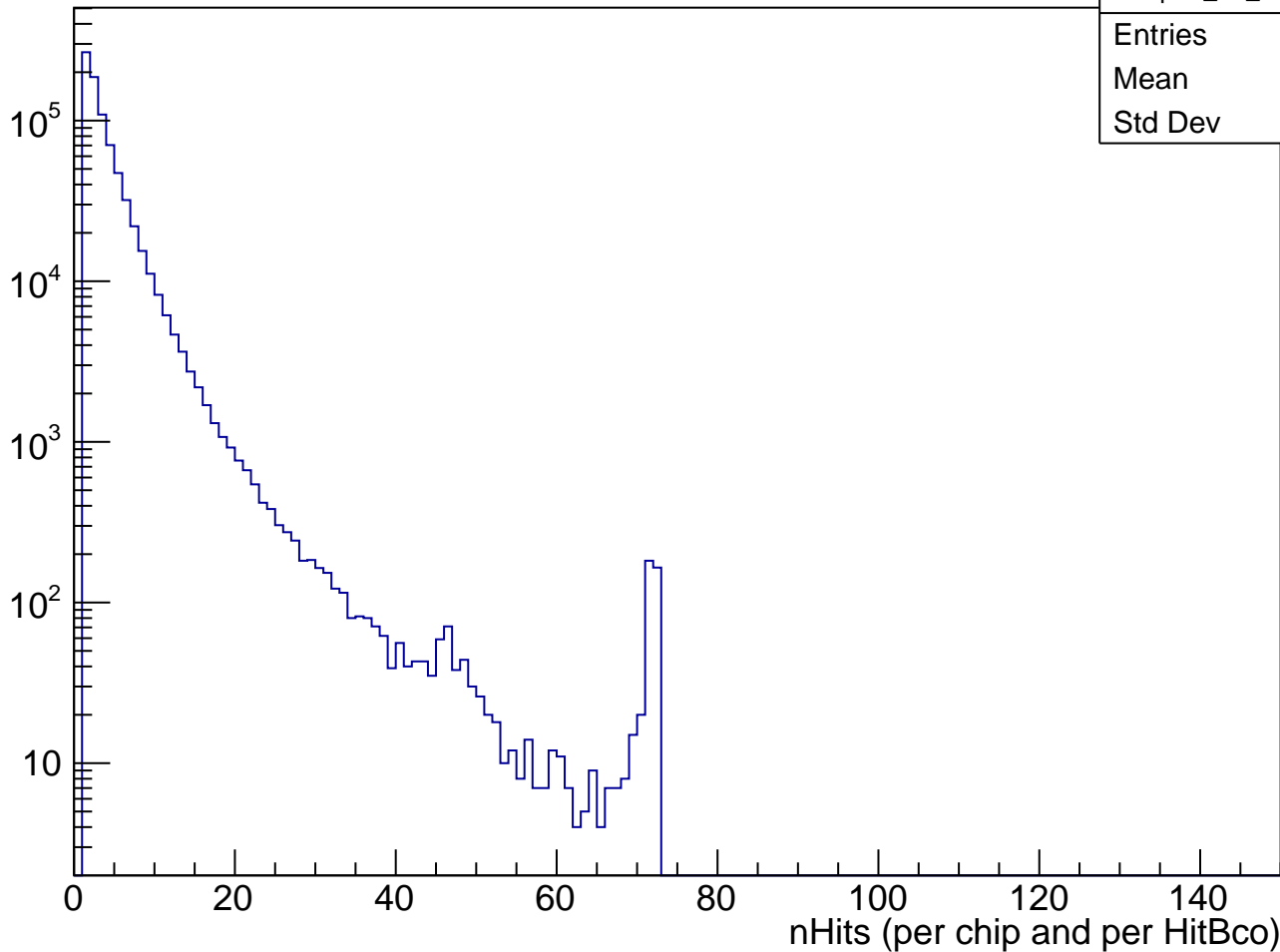
Entries



nChipHit_F5_Fch7_Chip0	
Entries	793358
Mean	3.339
Std Dev	3.864

nChipHit_F5_Fch7_Chip1

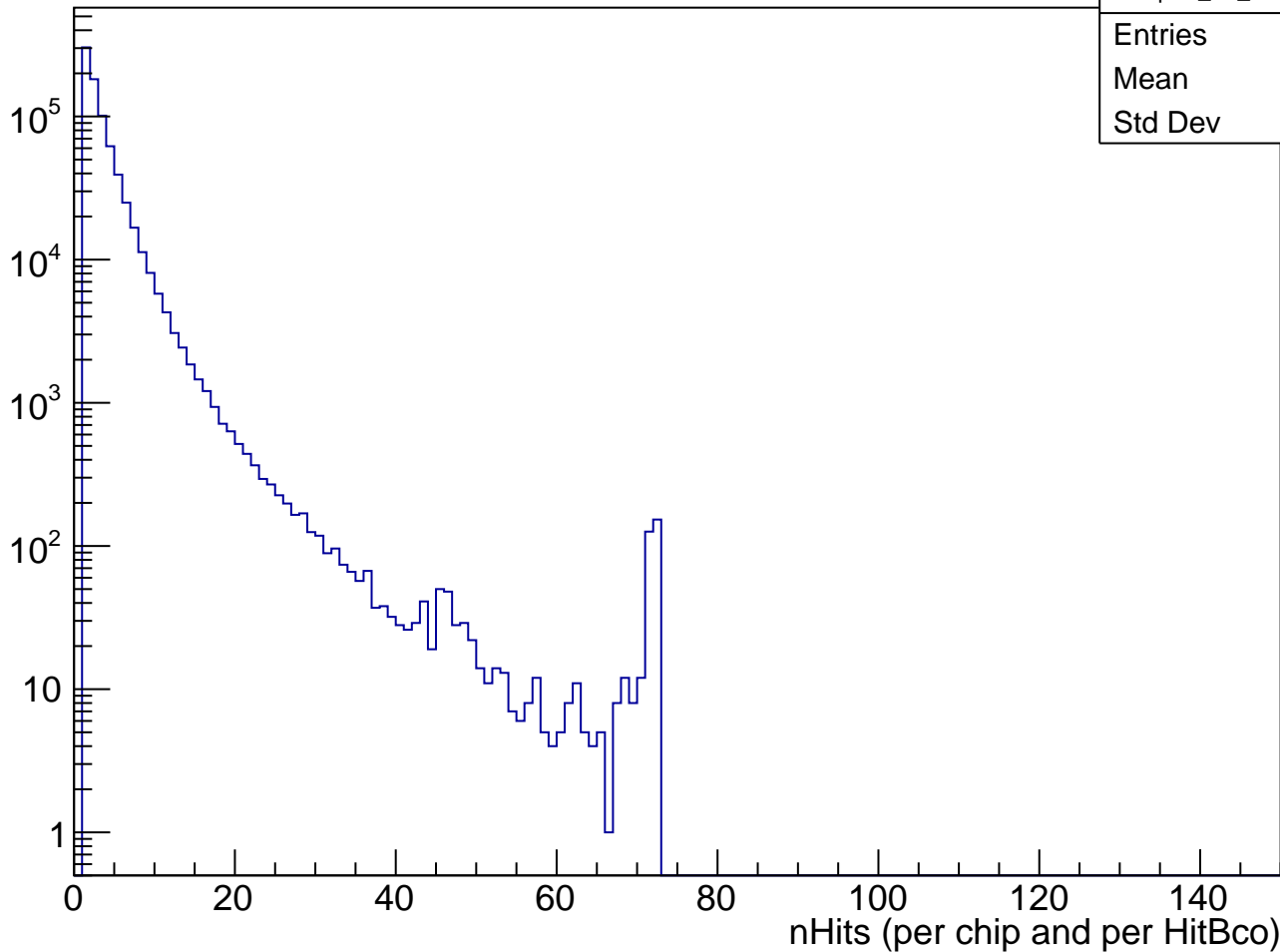
Entries



nChipHit_F5_Fch7_Chip1	
Entries	798616
Mean	3.314
Std Dev	3.835

nChipHit_F5_Fch7_Chip10

Entries

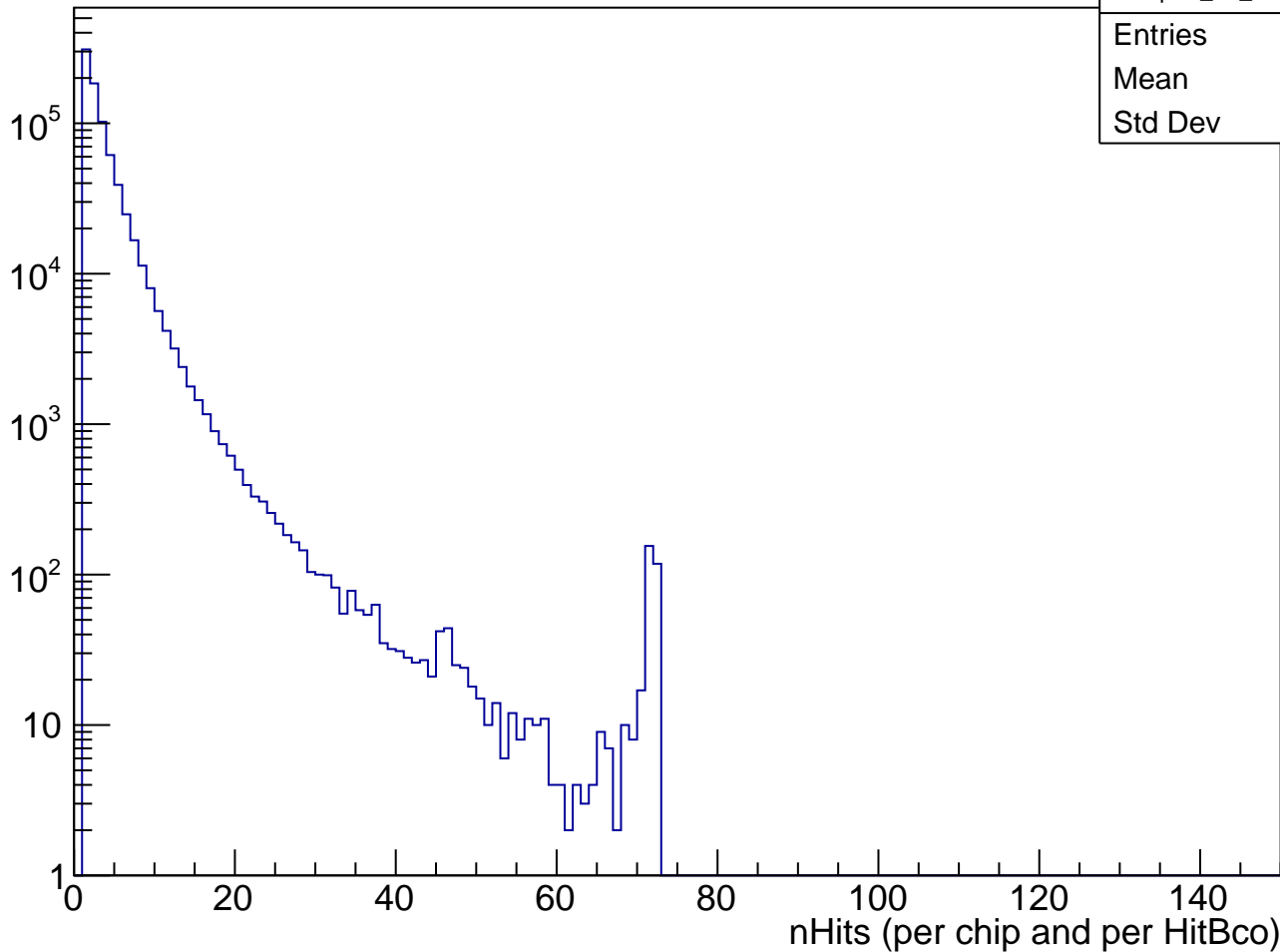


nChipHit_F5_Fch7_Chip10	
Entries	775760
Mean	2.911
Std Dev	3.42

nHits (per chip and per HitBco)

nChipHit_F5_Fch7_Chip11

Entries

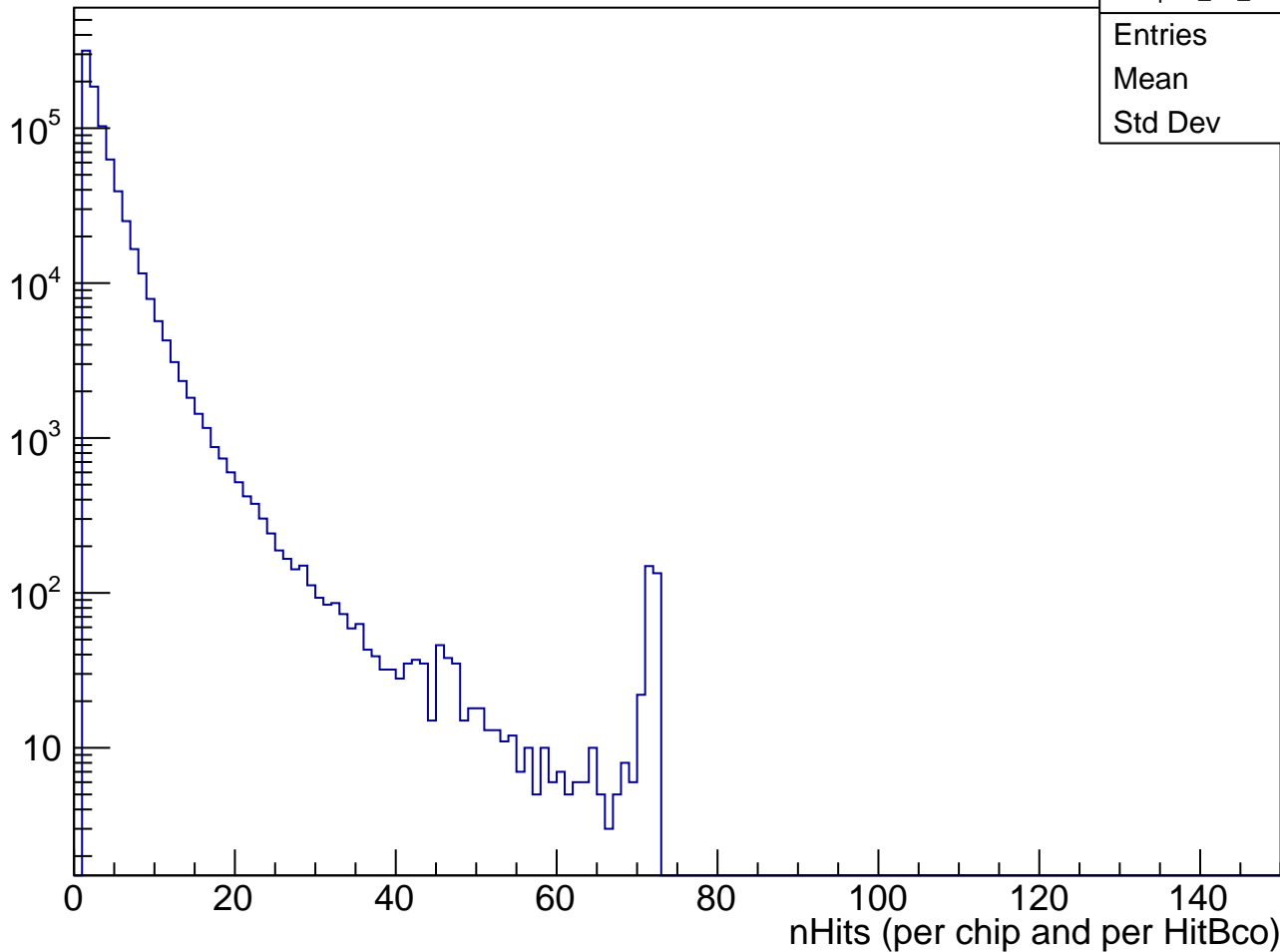


nChipHit_F5_Fch7_Chip11	
Entries	783915
Mean	2.879
Std Dev	3.364

nHits (per chip and per HitBco)

nChipHit_F5_Fch7_Chip12

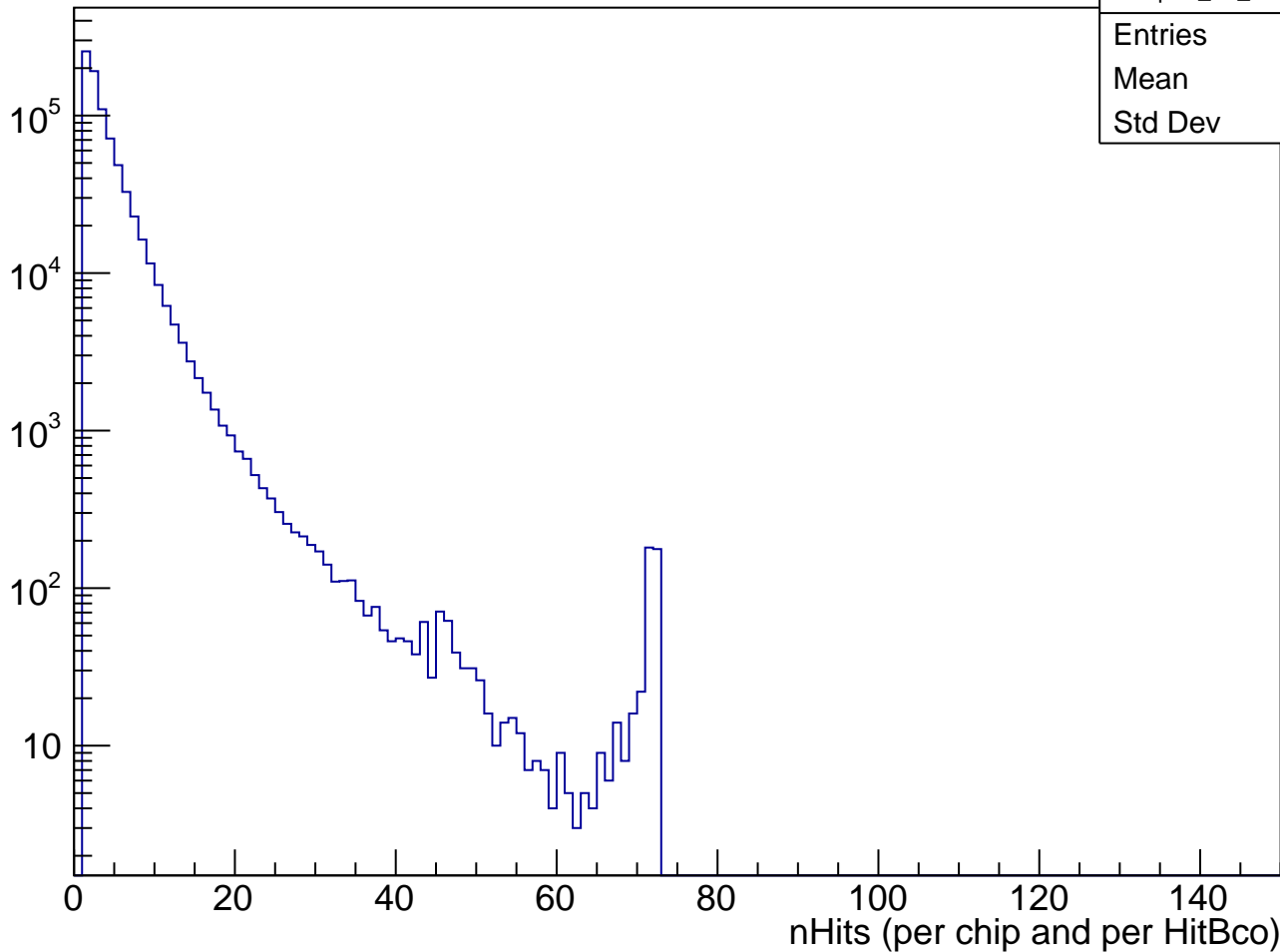
Entries



nChipHit_F5_Fch7_Chip12	
Entries	794113
Mean	2.865
Std Dev	3.358

nChipHit_F5_Fch7_Chip13

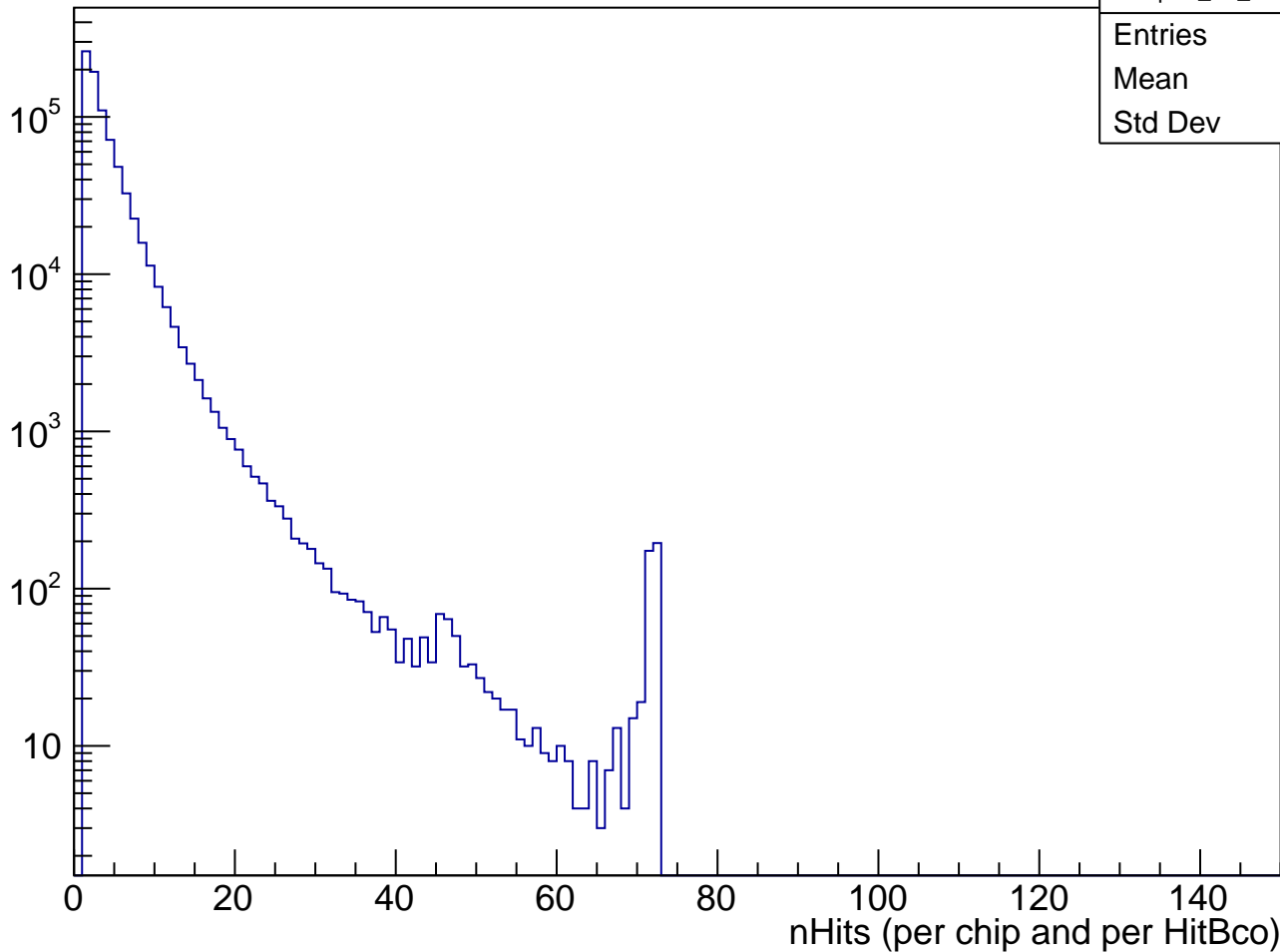
Entries



nChipHit_F5_Fch7_Chip13	
Entries	798882
Mean	3.358
Std Dev	3.842

nChipHit_F5_Fch7_Chip14

Entries



nChipHit_F5_Fch7_Chip14

Entries 805147

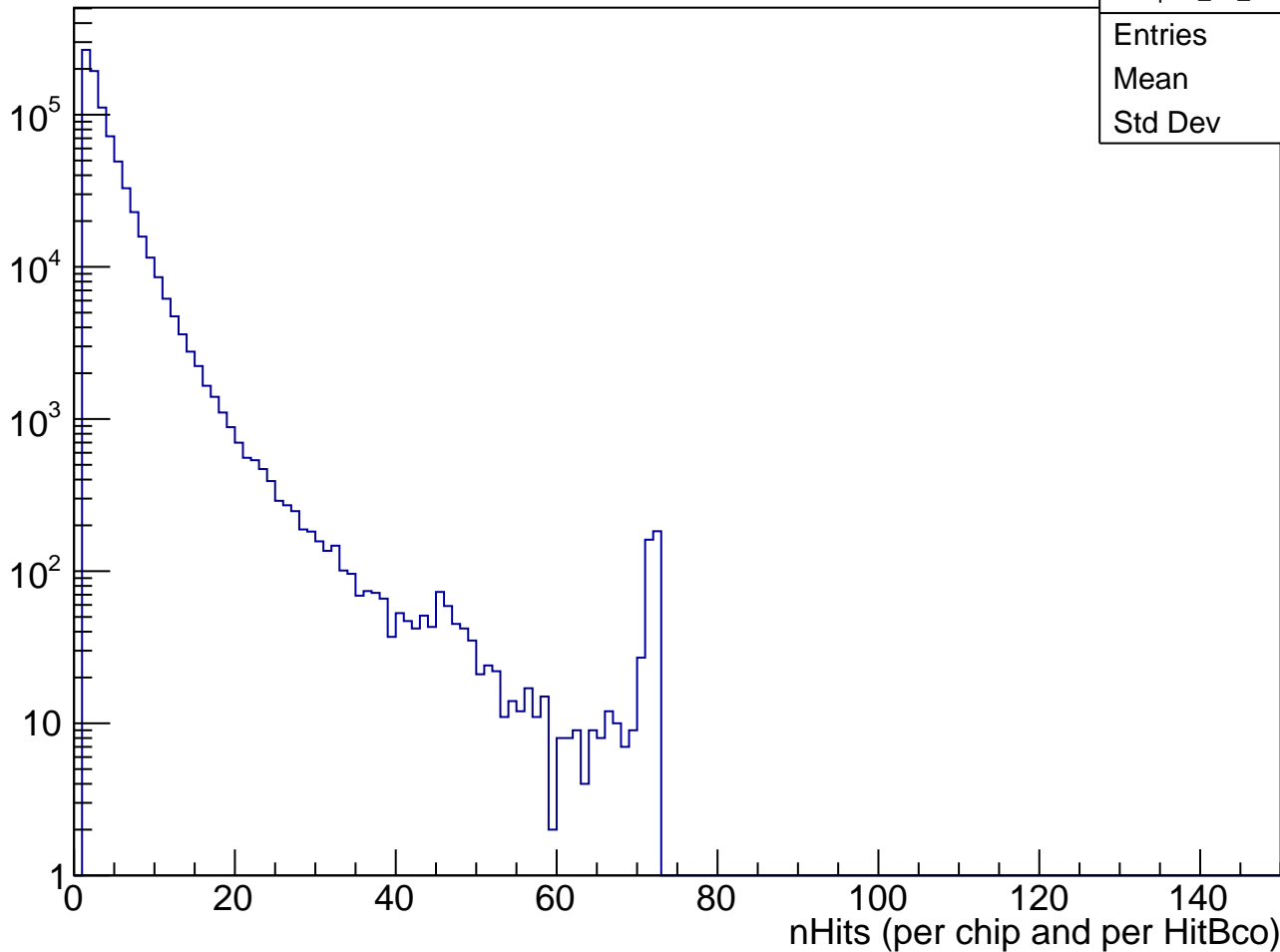
Mean 3.321

Std Dev 3.821

nHits (per chip and per HitBco)

nChipHit_F5_Fch7_Chip15

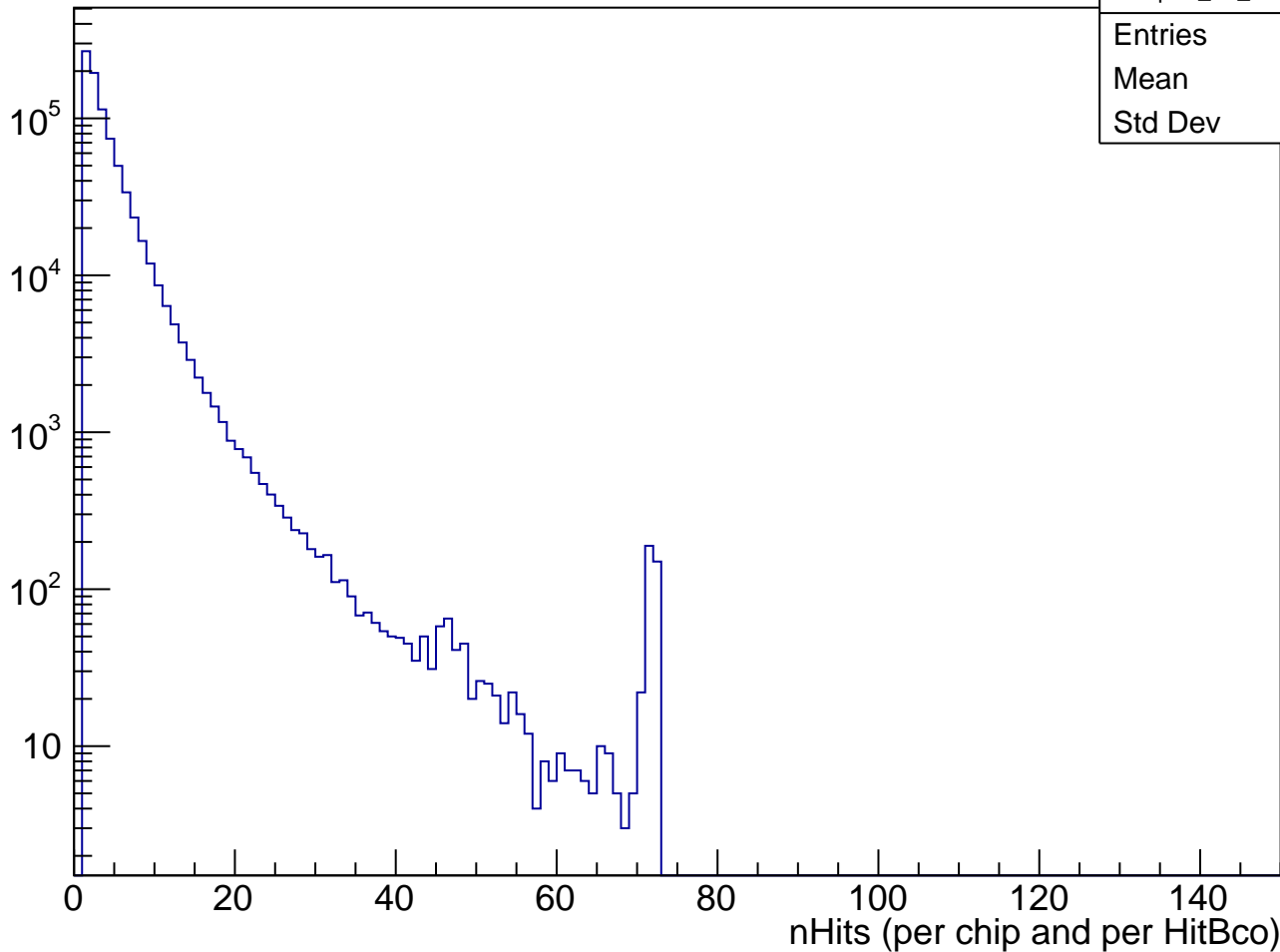
Entries



nChipHit_F5_Fch7_Chip15	
Entries	815299
Mean	3.323
Std Dev	3.826

nChipHit_F5_Fch7_Chip16

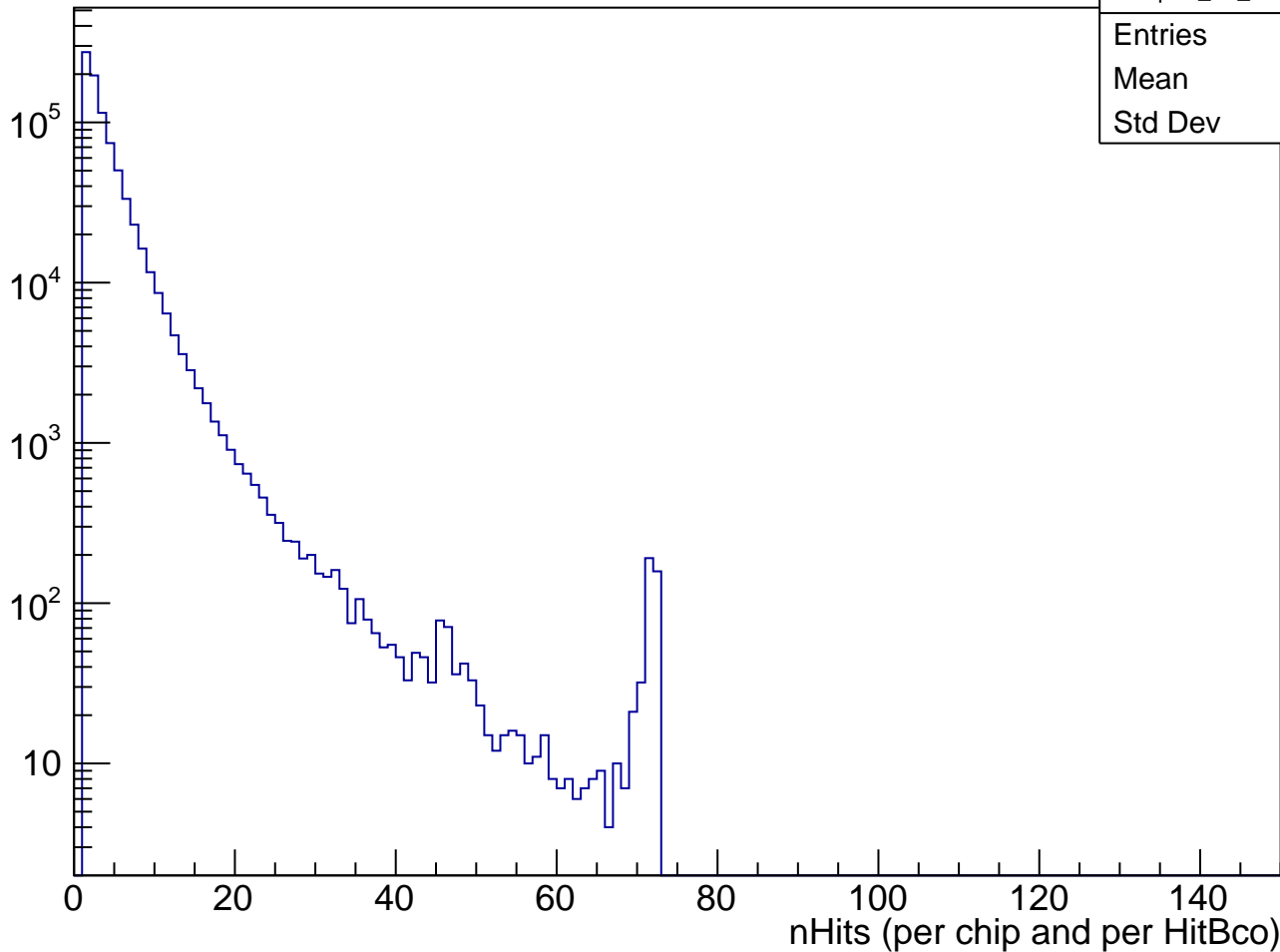
Entries



nChipHit_F5_Fch7_Chip16	
Entries	826275
Mean	3.346
Std Dev	3.809

nChipHit_F5_Fch7_Chip17

Entries

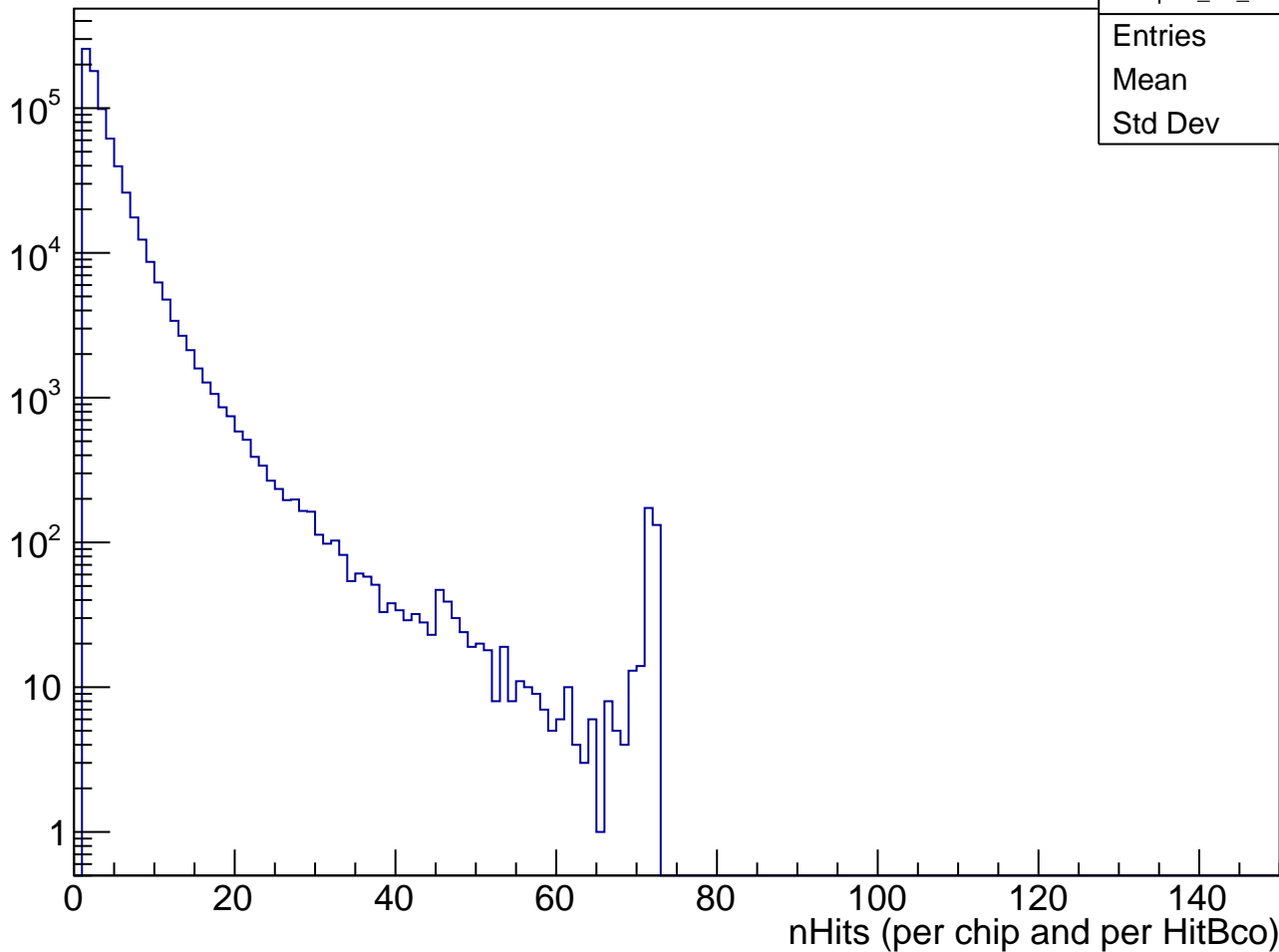


nChipHit_F5_Fch7_Chip17	
Entries	833764
Mean	3.314
Std Dev	3.815

nHits (per chip and per HitBco)

nChipHit_F5_Fch7_Chip18

Entries

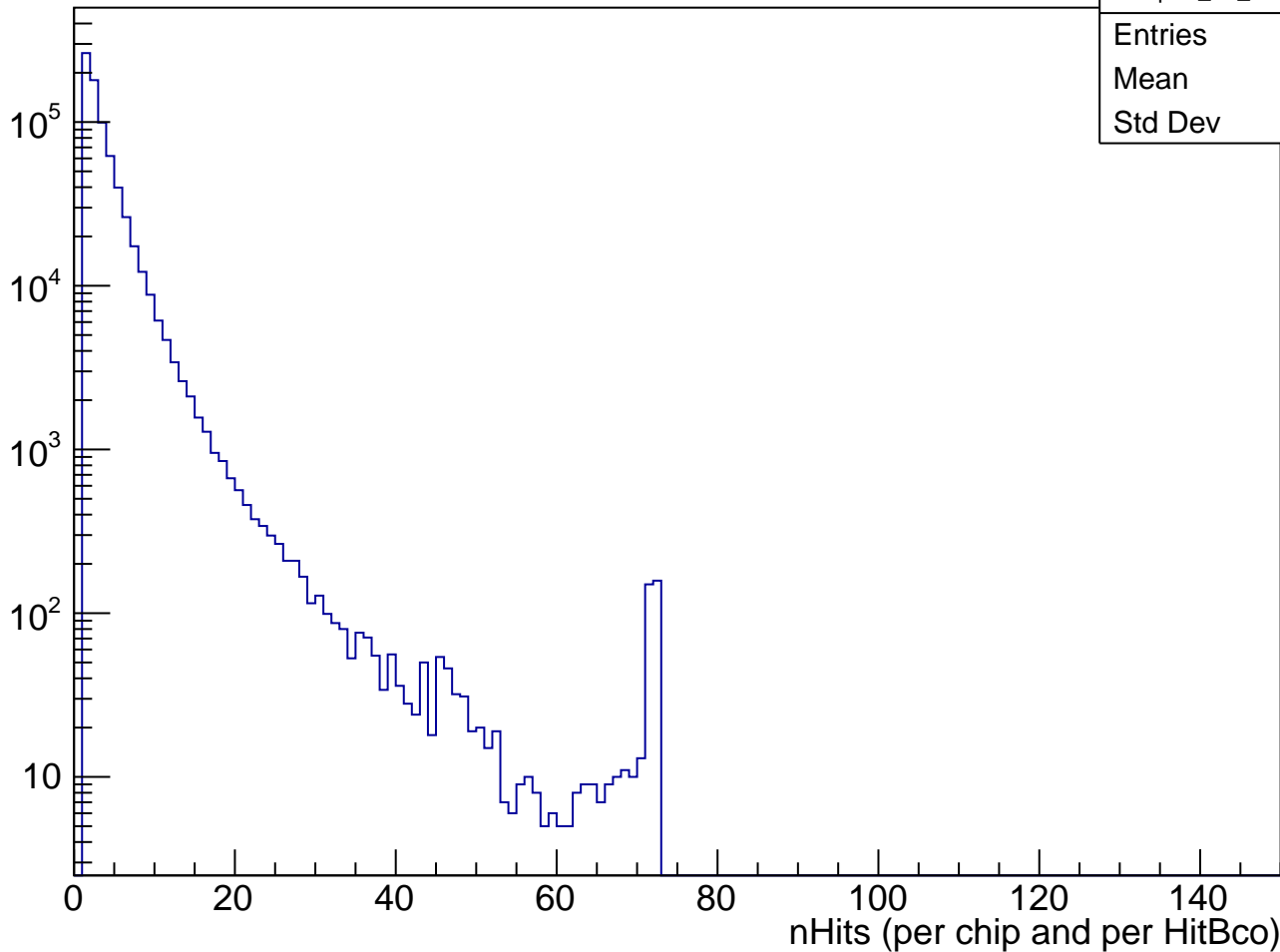


nChipHit_F5_Fch7_Chip18	
Entries	730965
Mean	3.101
Std Dev	3.597

nHits (per chip and per HitBco)

nChipHit_F5_Fch7_Chip19

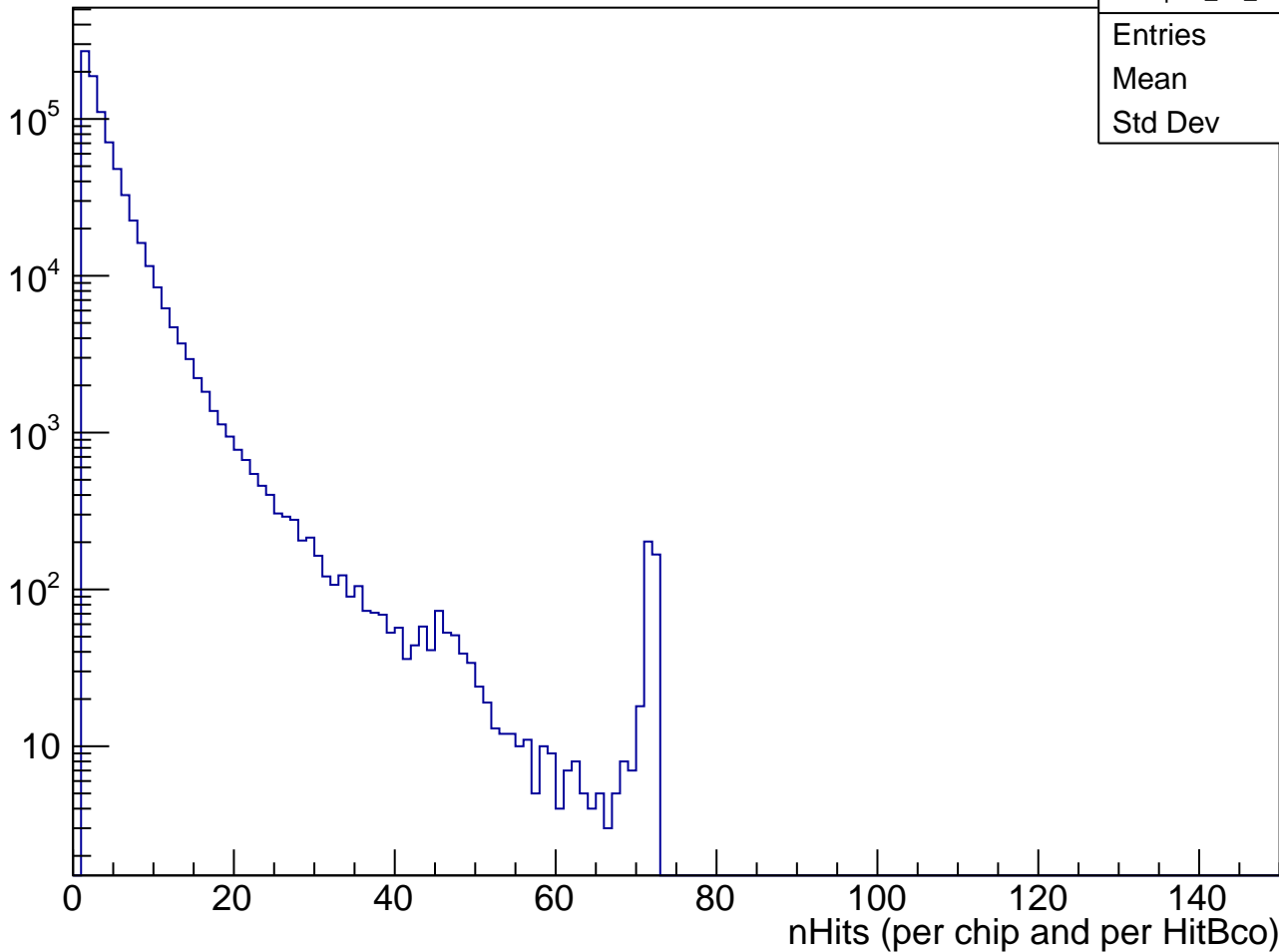
Entries



nChipHit_F5_Fch7_Chip19	
Entries	738690
Mean	3.079
Std Dev	3.605

nChipHit_F5_Fch7_Chip2

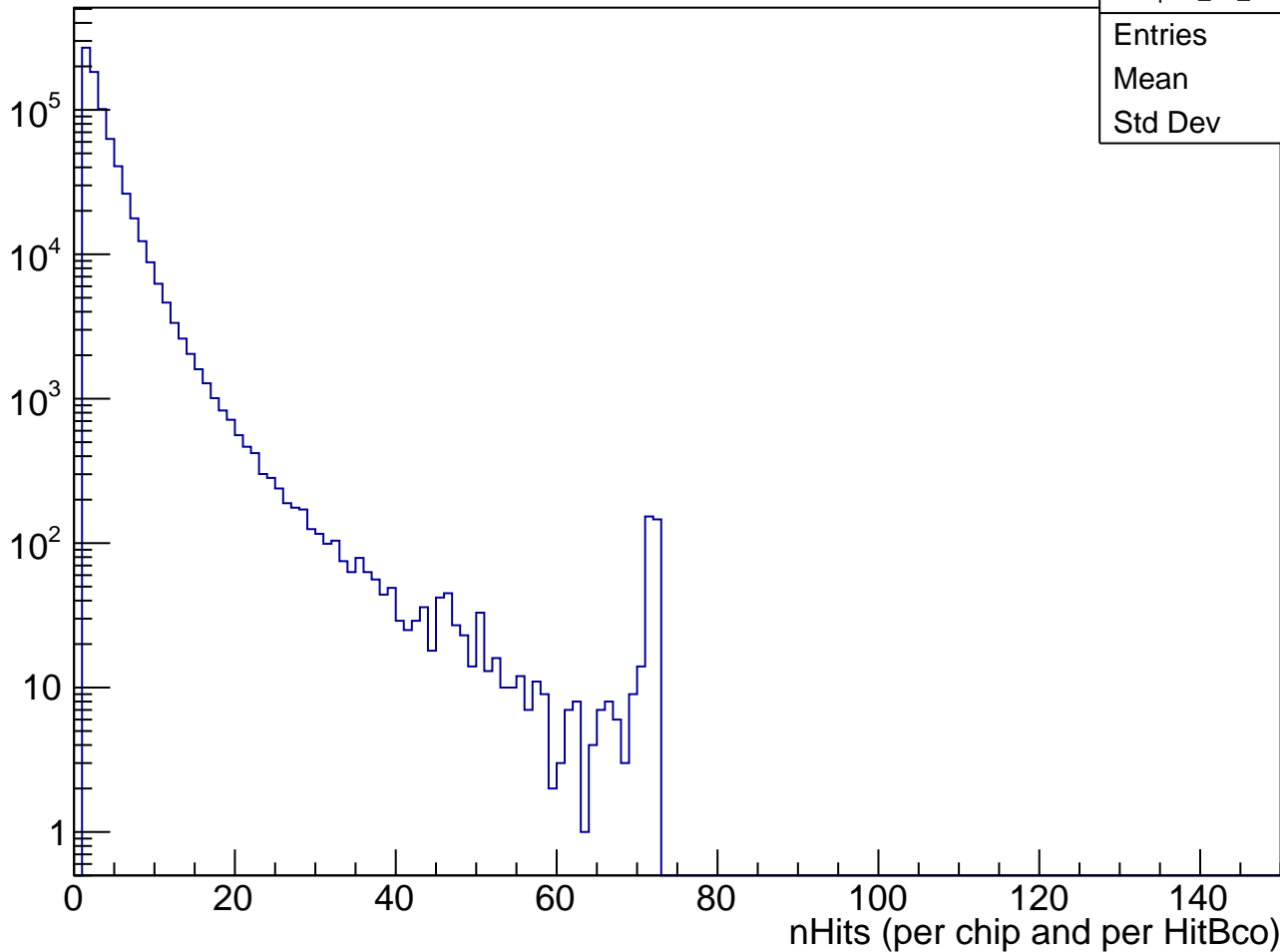
Entries



nChipHit_F5_Fch7_Chip2	
Entries	810523
Mean	3.331
Std Dev	3.858

nChipHit_F5_Fch7_Chip20

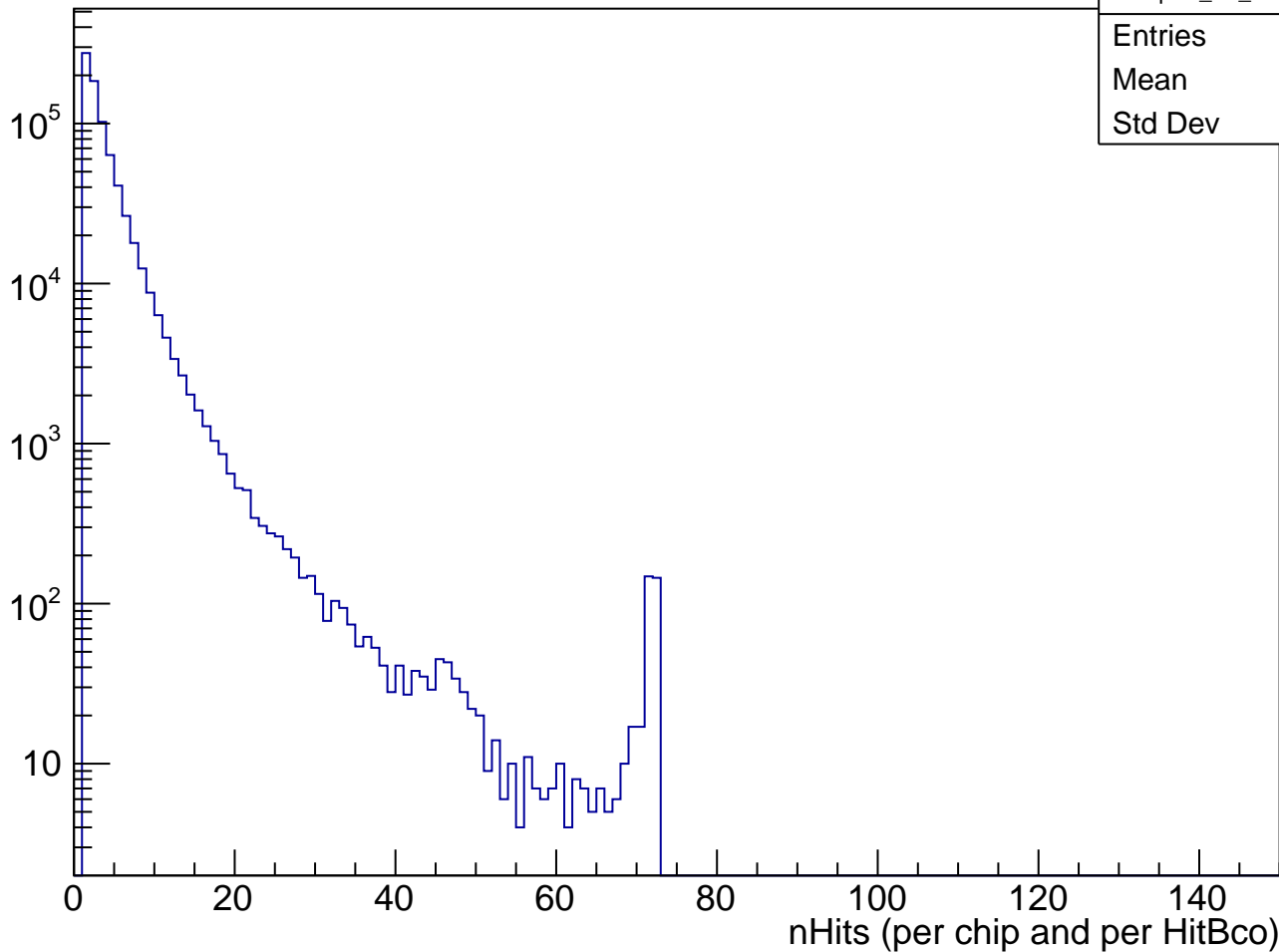
Entries



nChipHit_F5_Fch7_Chip20	
Entries	751139
Mean	3.06
Std Dev	3.544

nChipHit_F5_Fch7_Chip21

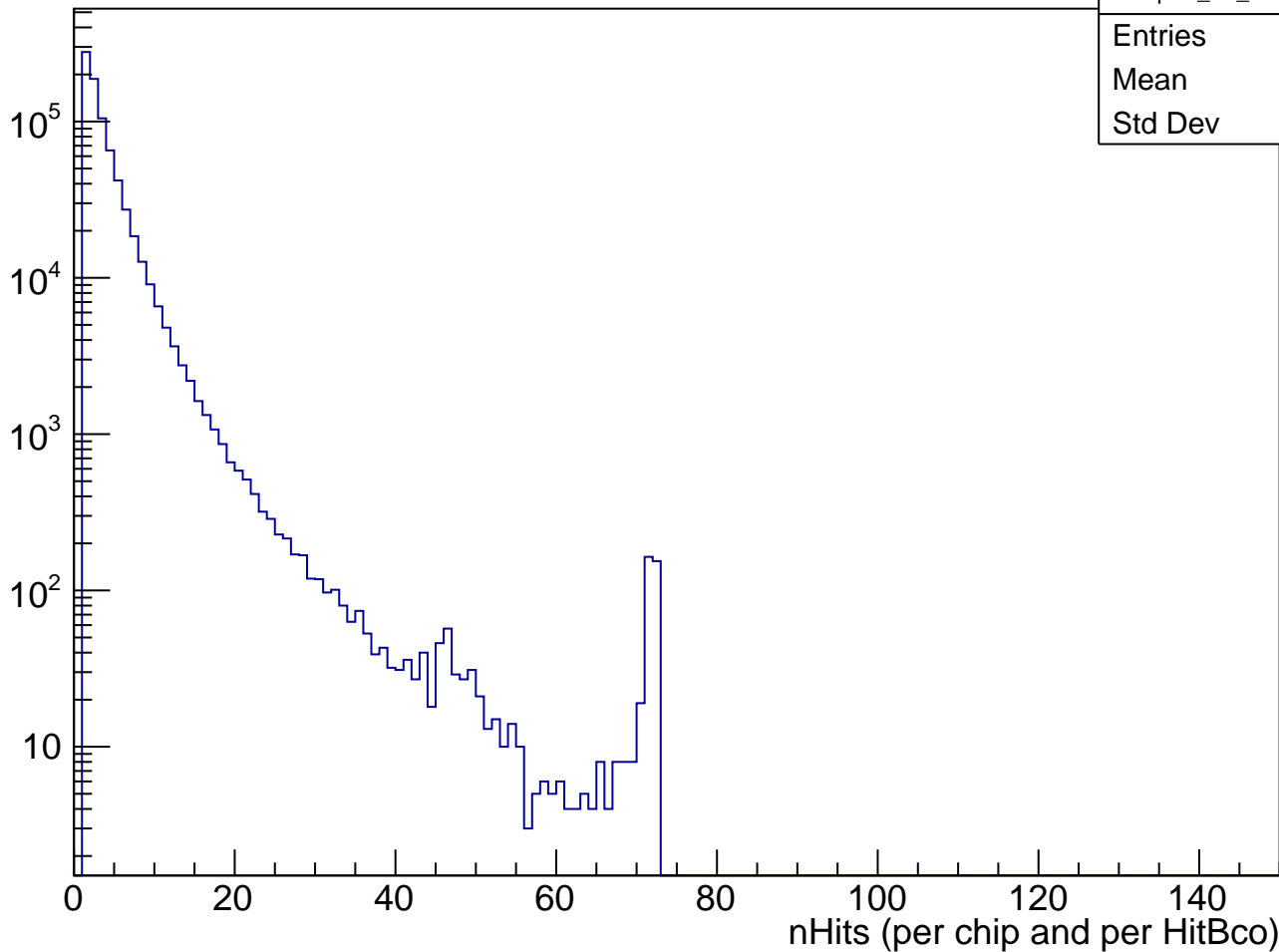
Entries



nChipHit_F5_Fch7_Chip21	
Entries	761253
Mean	3.047
Std Dev	3.54

nChipHit_F5_Fch7_Chip22

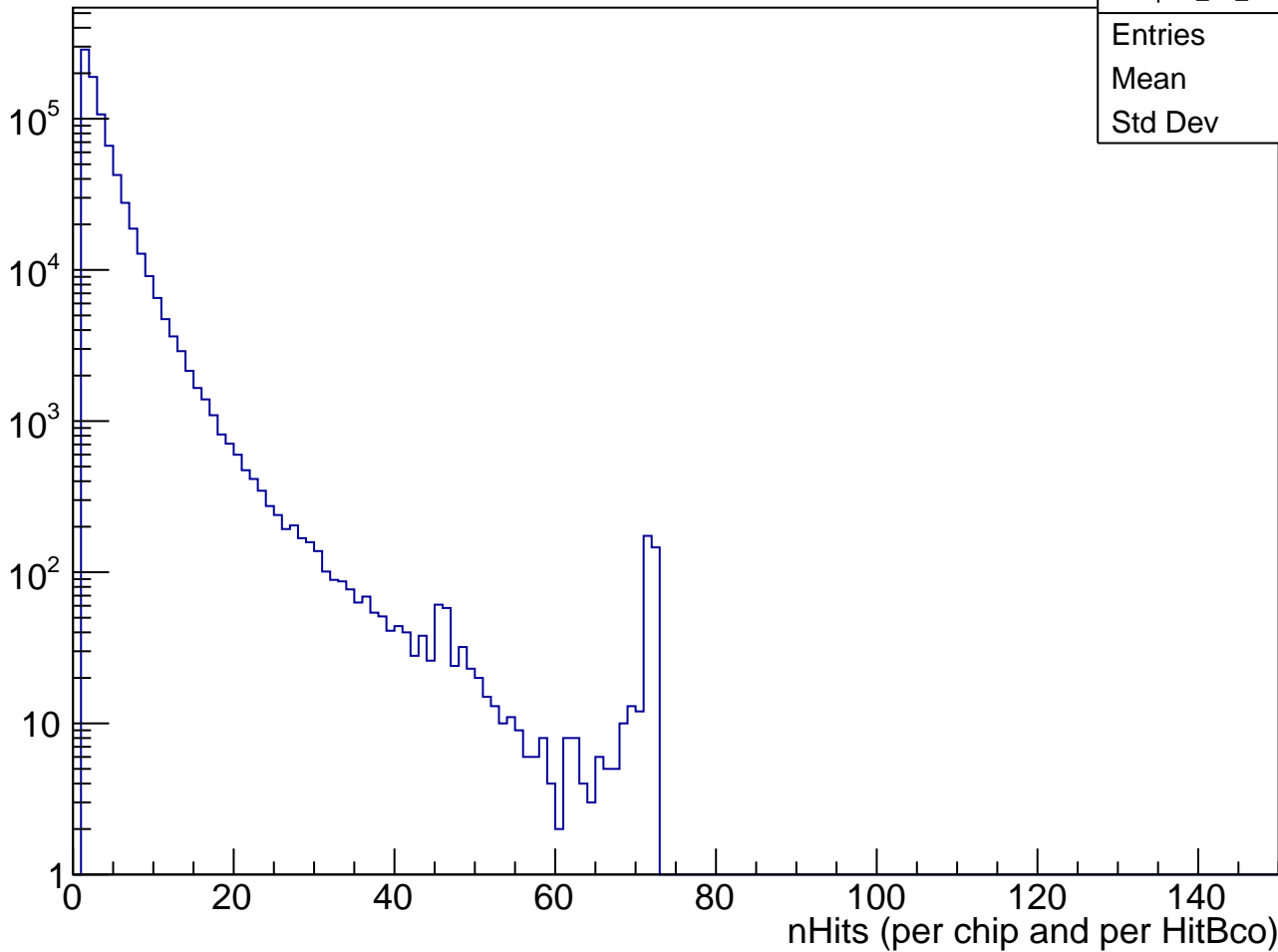
Entries



nChipHit_F5_Fch7_Chip22	
Entries	775366
Mean	3.066
Std Dev	3.549

nChipHit_F5_Fch7_Chip23

Entries



nChipHit_F5_Fch7_Chip23

Entries 790254

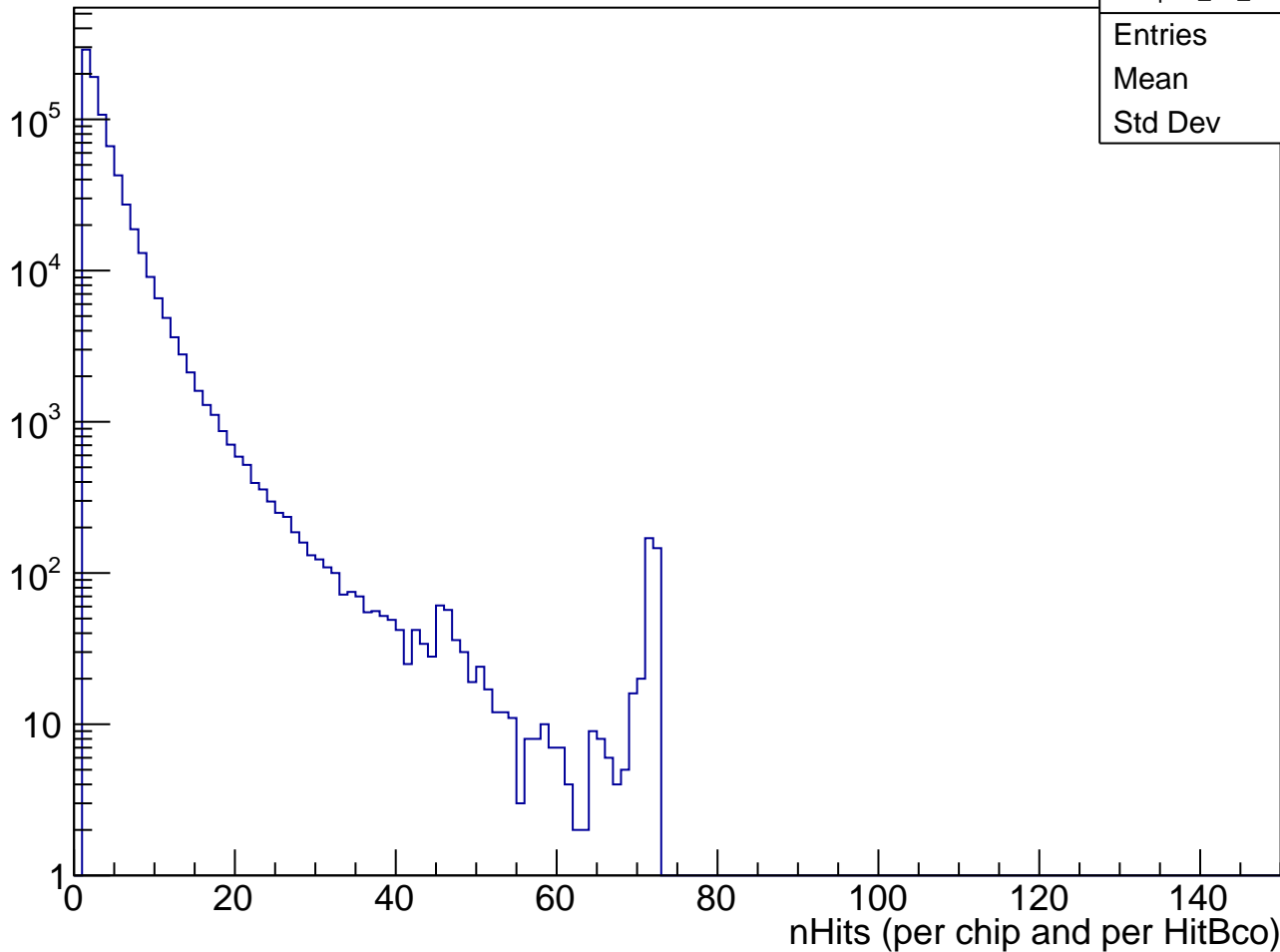
Mean 3.056

Std Dev 3.556

nHits (per chip and per HitBco)

nChipHit_F5_Fch7_Chip24

Entries



nChipHit_F5_Fch7_Chip24

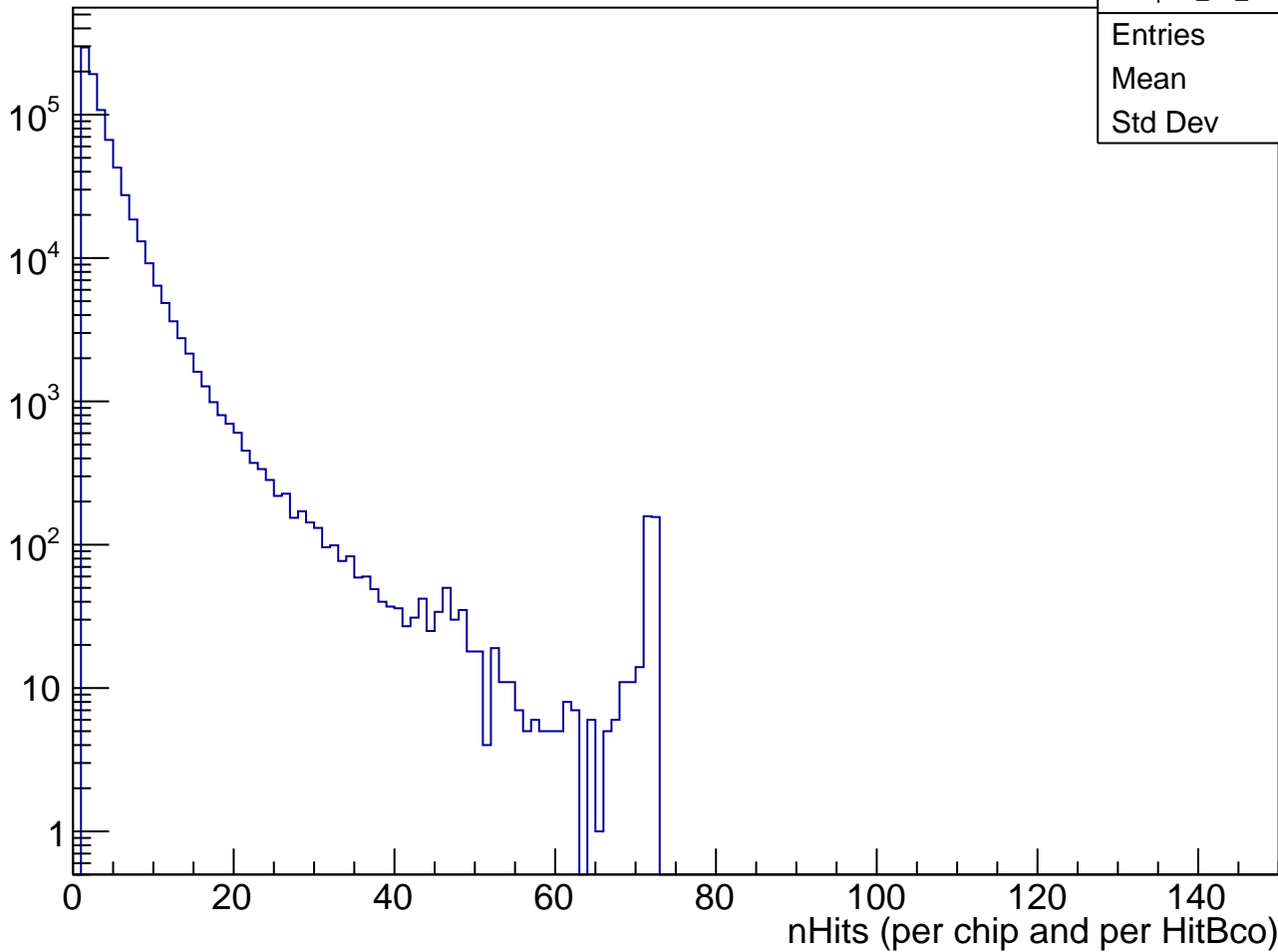
Entries 794854

Mean 3.048

Std Dev 3.555

nChipHit_F5_Fch7_Chip25

Entries

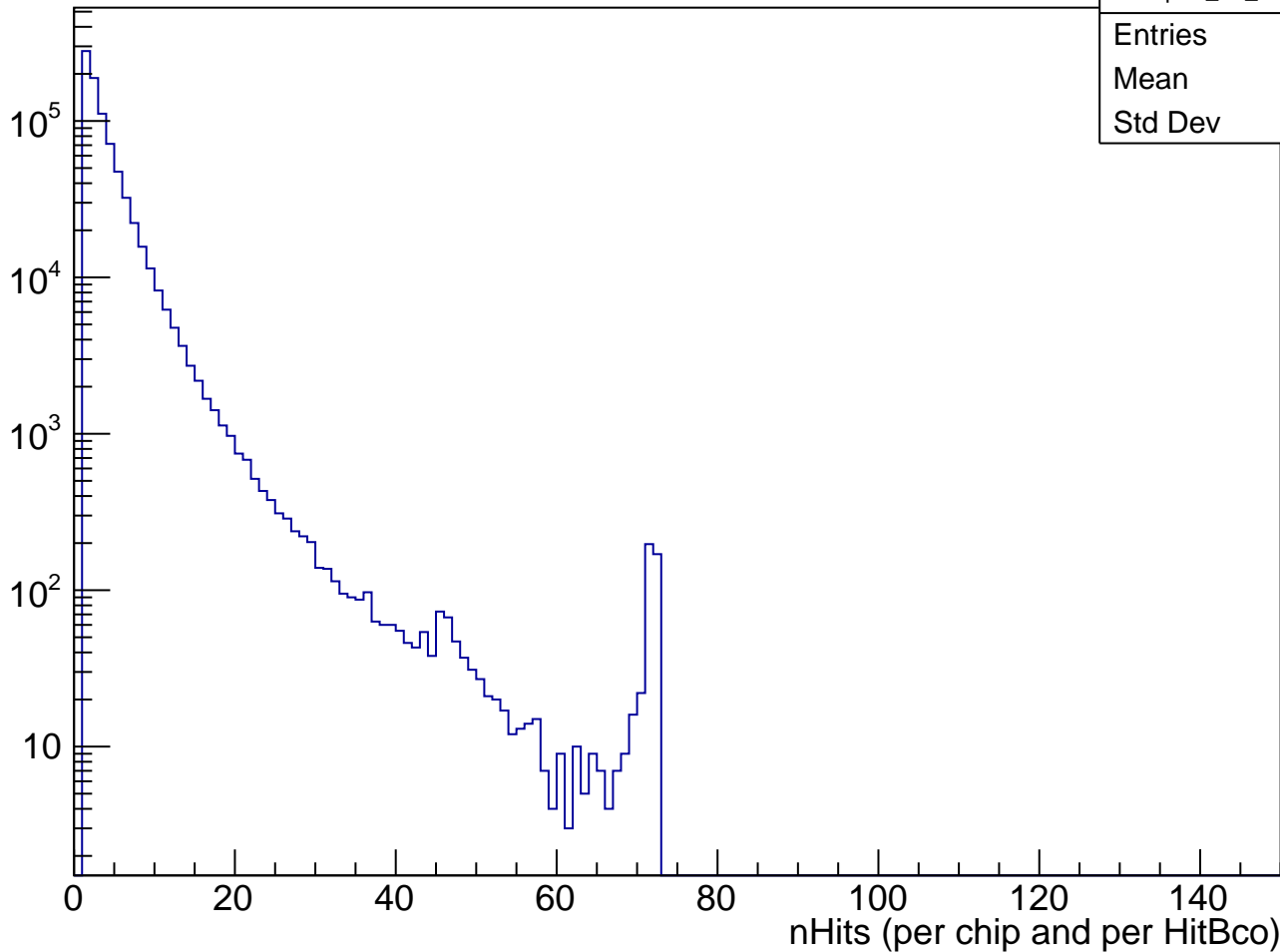


nChipHit_F5_Fch7_Chip25	
Entries	801947
Mean	3.019
Std Dev	3.486

nHits (per chip and per HitBco)

nChipHit_F5_Fch7_Chip3

Entries



nChipHit_F5_Fch7_Chip3

Entries 818866

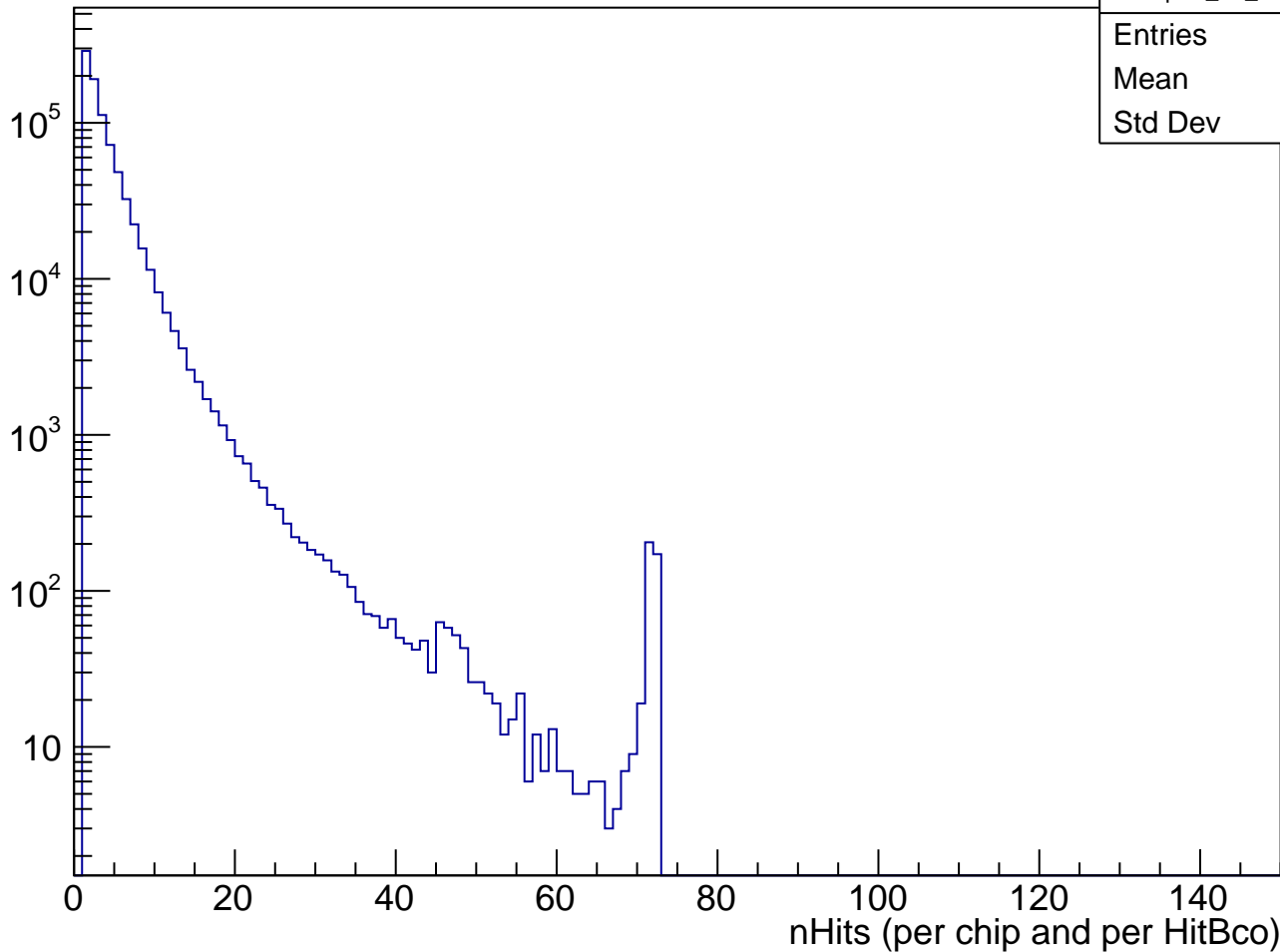
Mean 3.29

Std Dev 3.848

nHits (per chip and per HitBco)

nChipHit_F5_Fch7_Chip4

Entries



nChipHit_F5_Fch7_Chip4

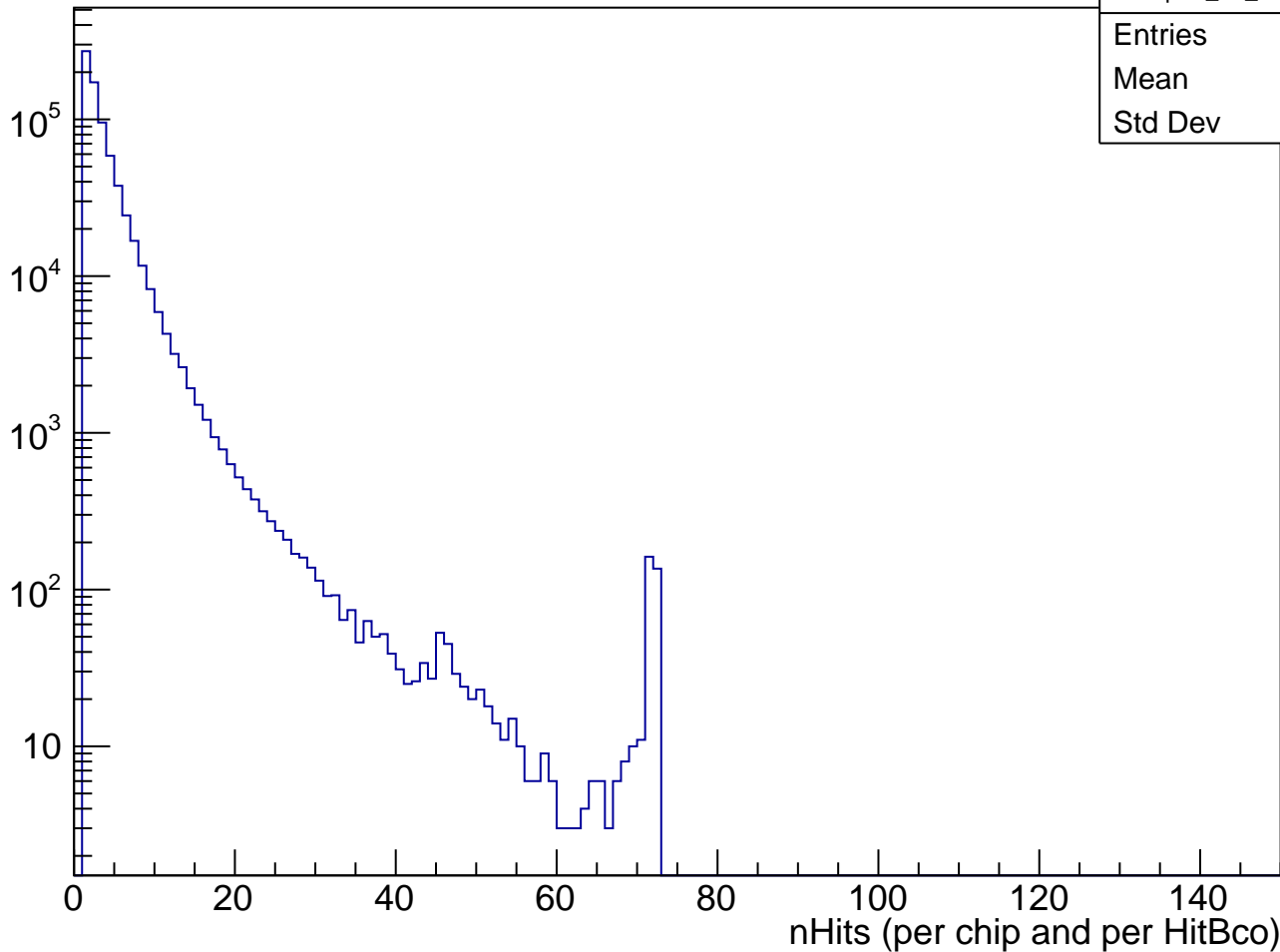
Entries 833010

Mean 3.258

Std Dev 3.81

nChipHit_F5_Fch7_Chip5

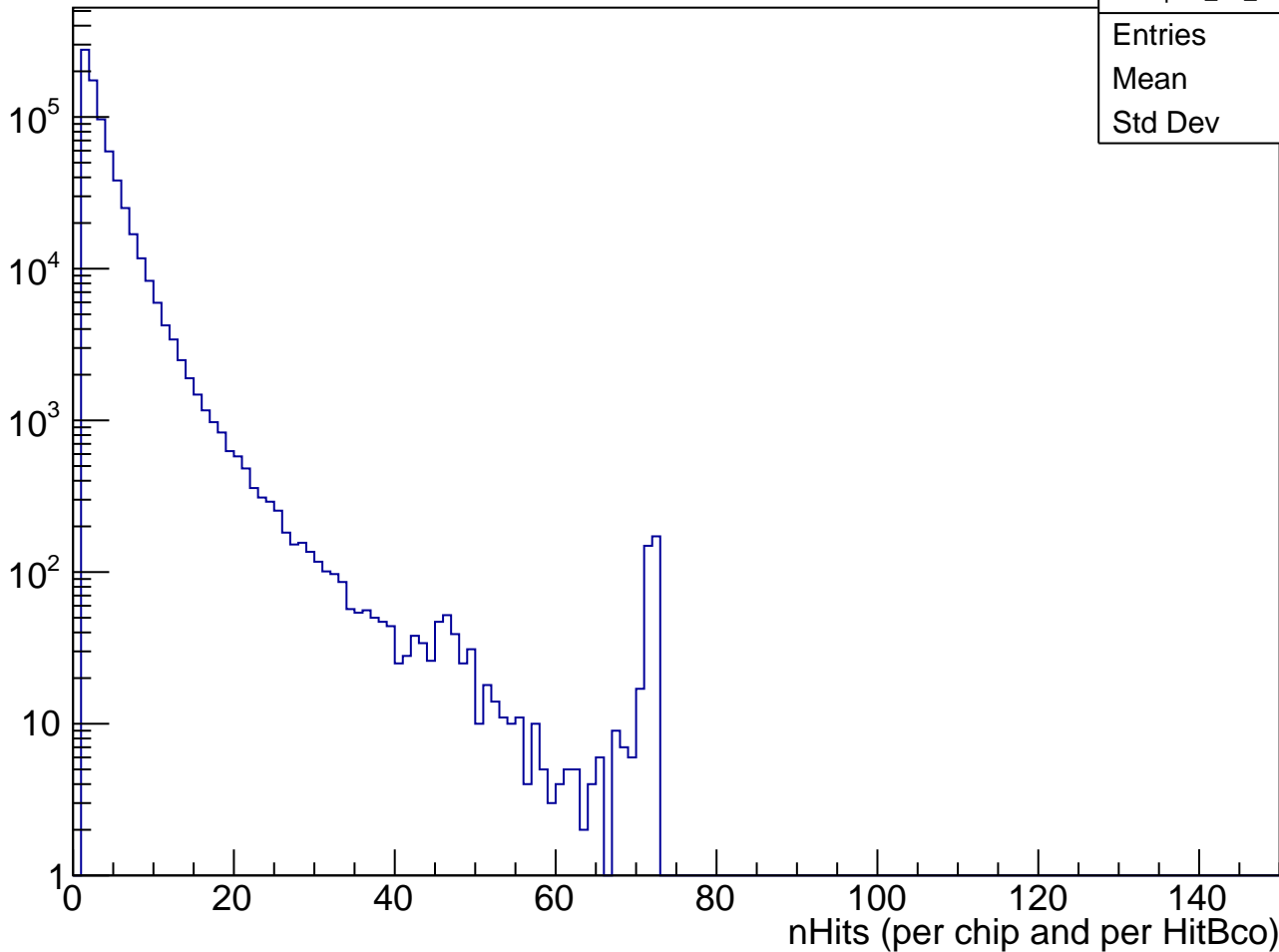
Entries



nChipHit_F5_Fch7_Chip5	
Entries	724927
Mean	3.01
Std Dev	3.55

nChipHit_F5_Fch7_Chip6

Entries



nChipHit_F5_Fch7_Chip6

Entries 735326

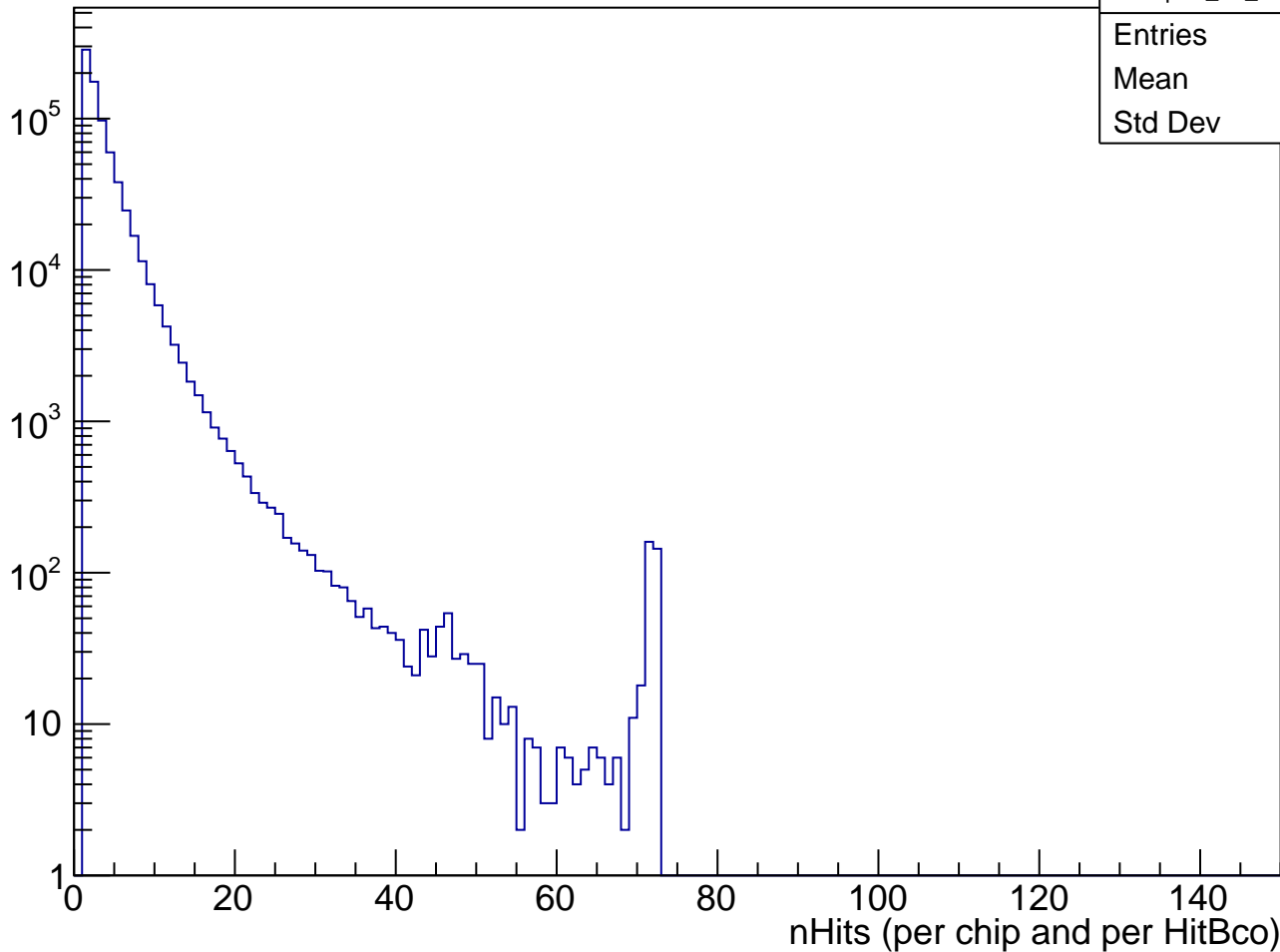
Mean 3.005

Std Dev 3.563

nHits (per chip and per HitBco)

nChipHit_F5_Fch7_Chip7

Entries



nChipHit_F5_Fch7_Chip7

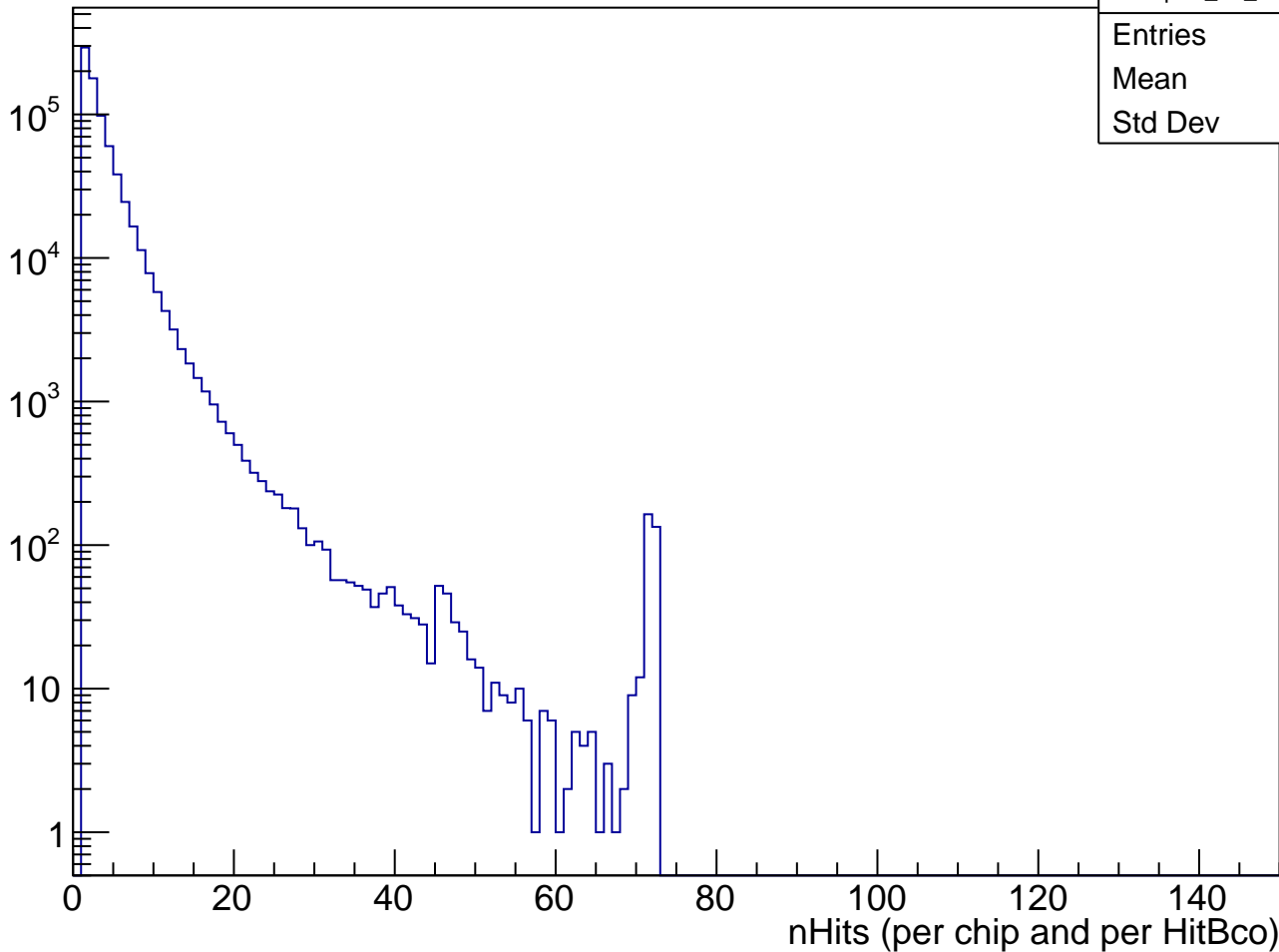
Entries 743357

Mean 2.961

Std Dev 3.496

nChipHit_F5_Fch7_Chip8

Entries



nChipHit_F5_Fch7_Chip8

Entries 753623

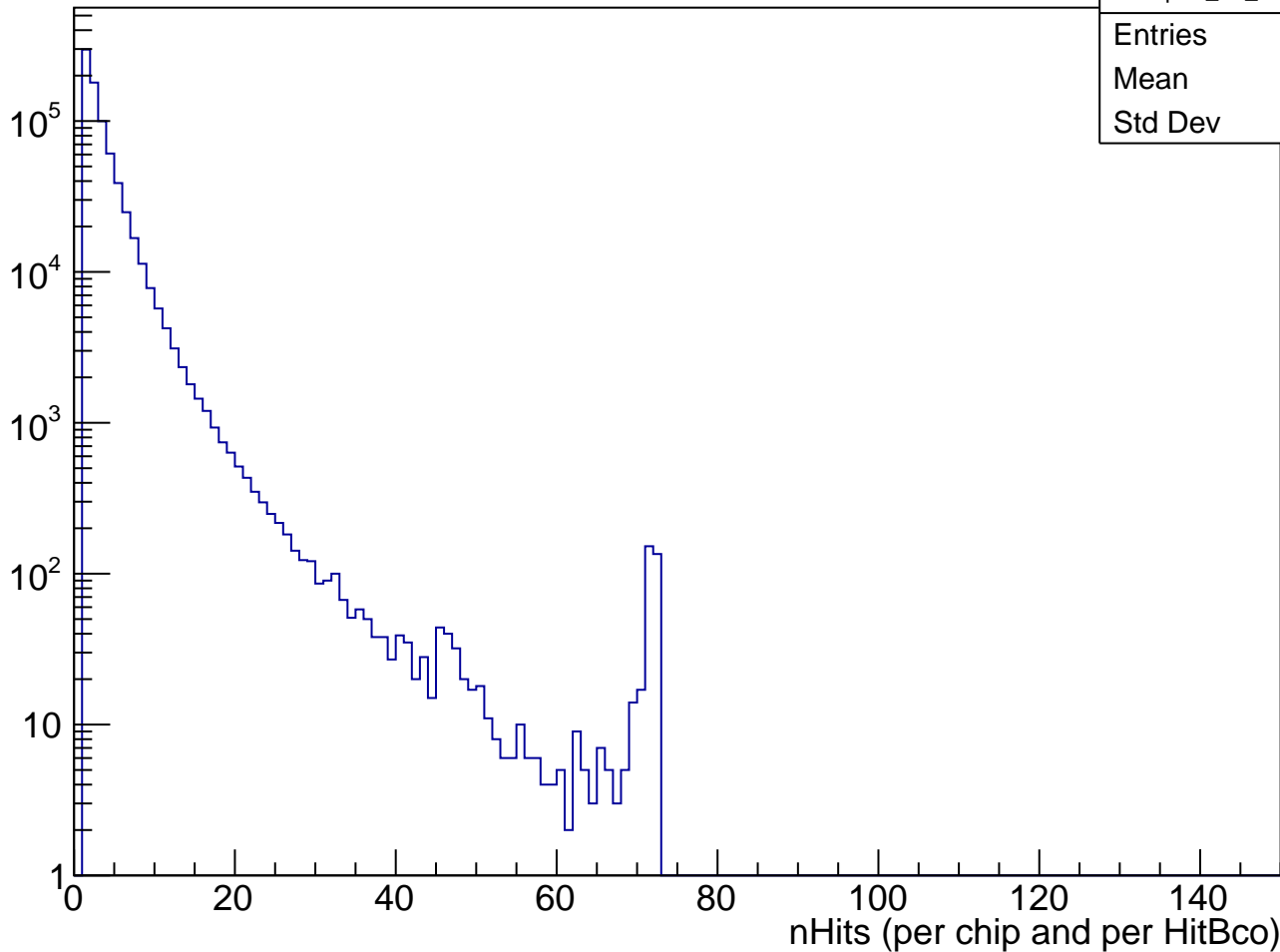
Mean 2.921

Std Dev 3.405

nHits (per chip and per HitBco)

nChipHit_F5_Fch7_Chip9

Entries



nChipHit_F5_Fch7_Chip9

Entries 764012

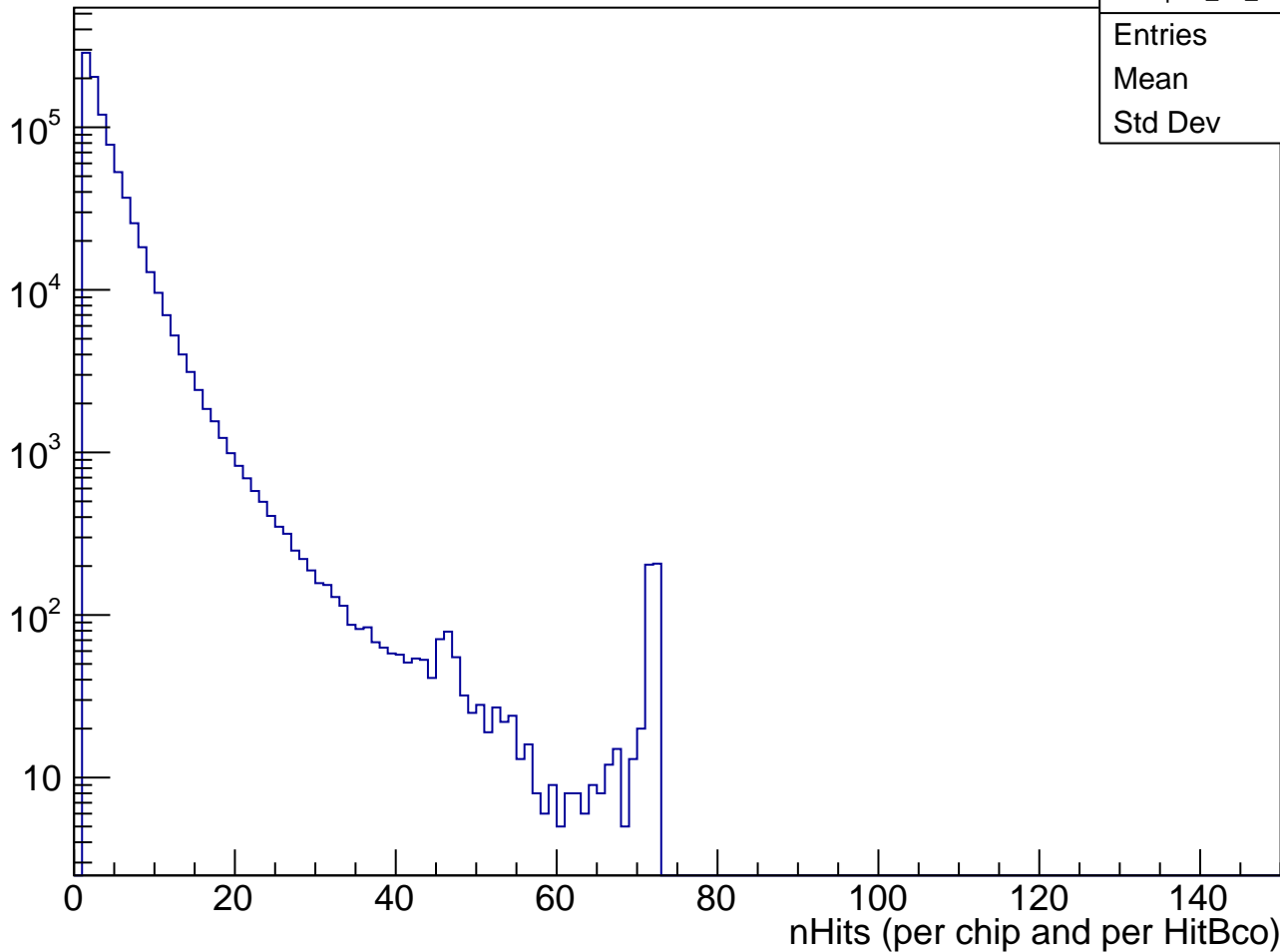
Mean 2.911

Std Dev 3.395

nHits (per chip and per HitBco)

nChipHit_F5_Fch8_Chip0

Entries



nChipHit_F5_Fch8_Chip0

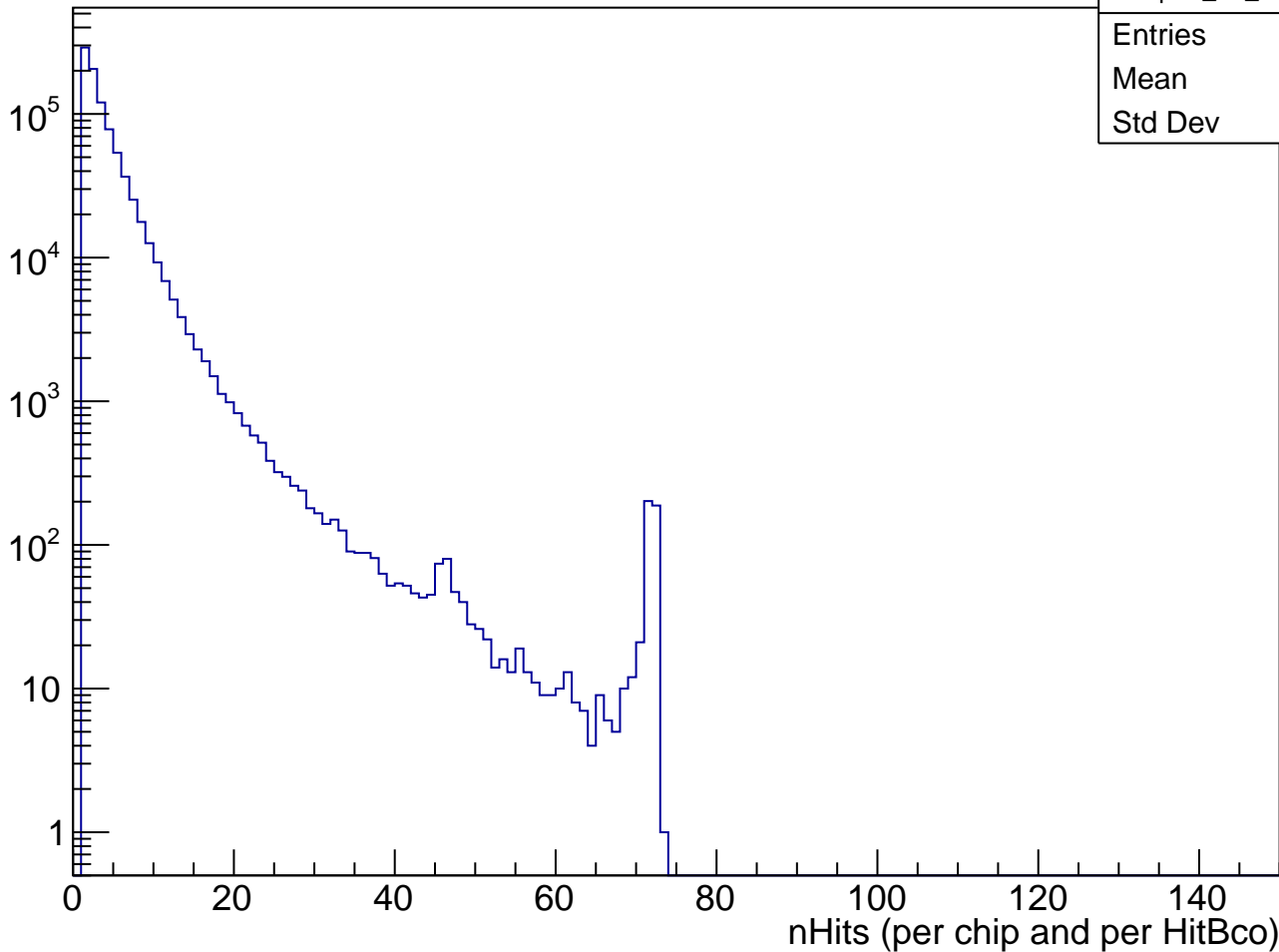
Entries 879297

Mean 3.368

Std Dev 3.877

nChipHit_F5_Fch8_Chip1

Entries

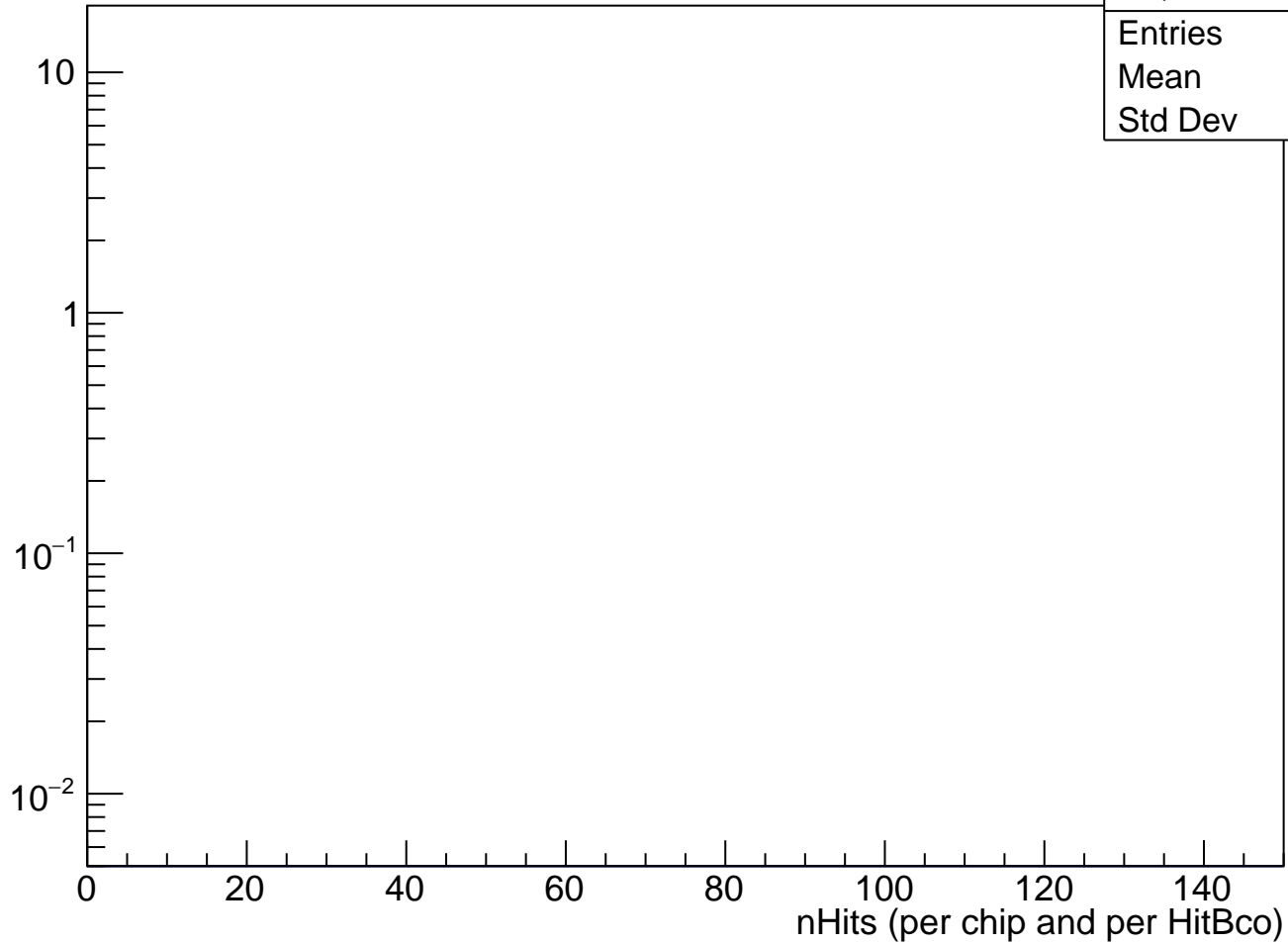


nChipHit_F5_Fch8_Chip1	
Entries	882580
Mean	3.337
Std Dev	3.831

nHits (per chip and per HitBco)

nChipHit_F5_Fch8_Chip10

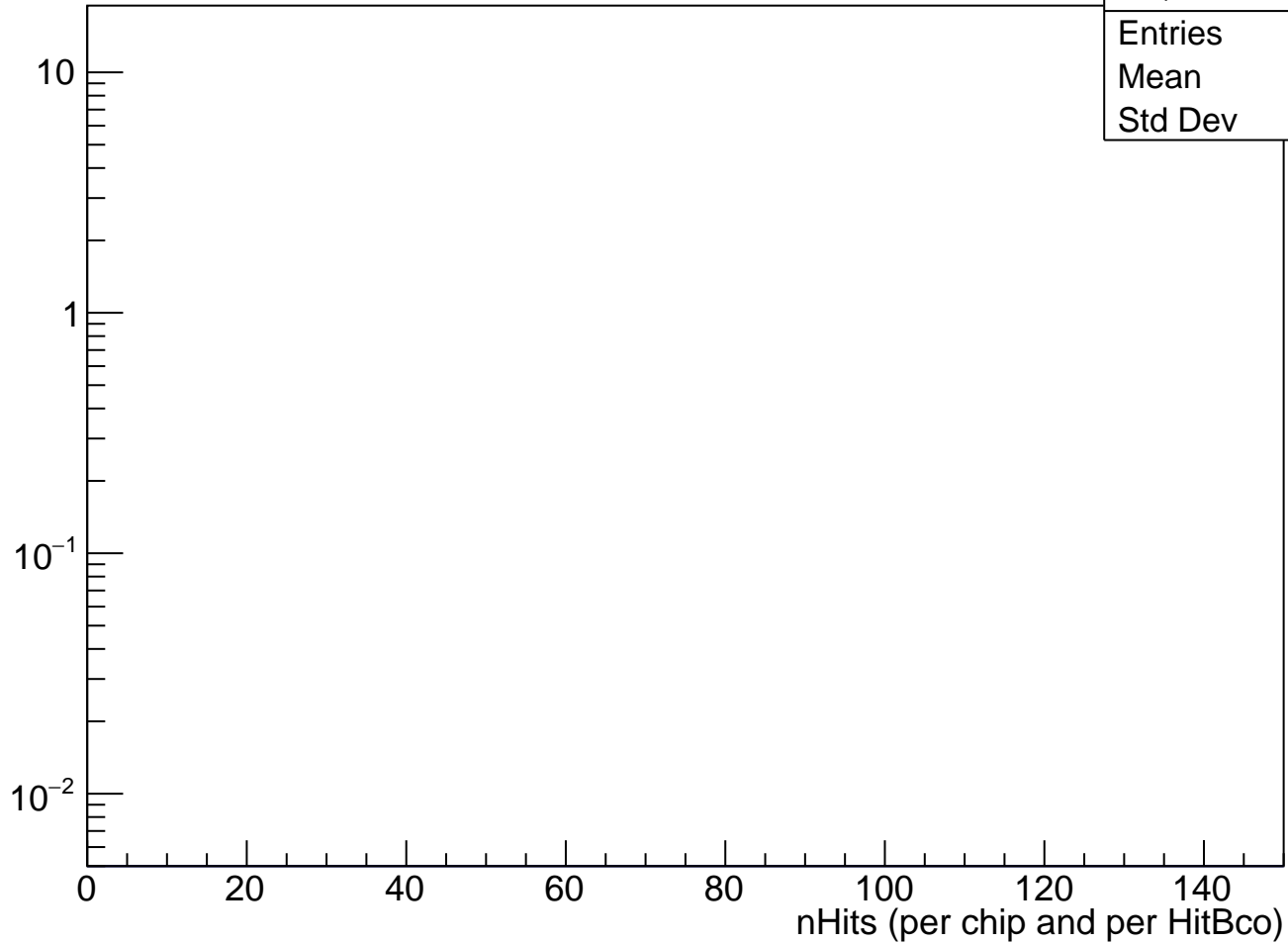
Entries



nChipHit_F5_Fch8_Chip10	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch8_Chip11

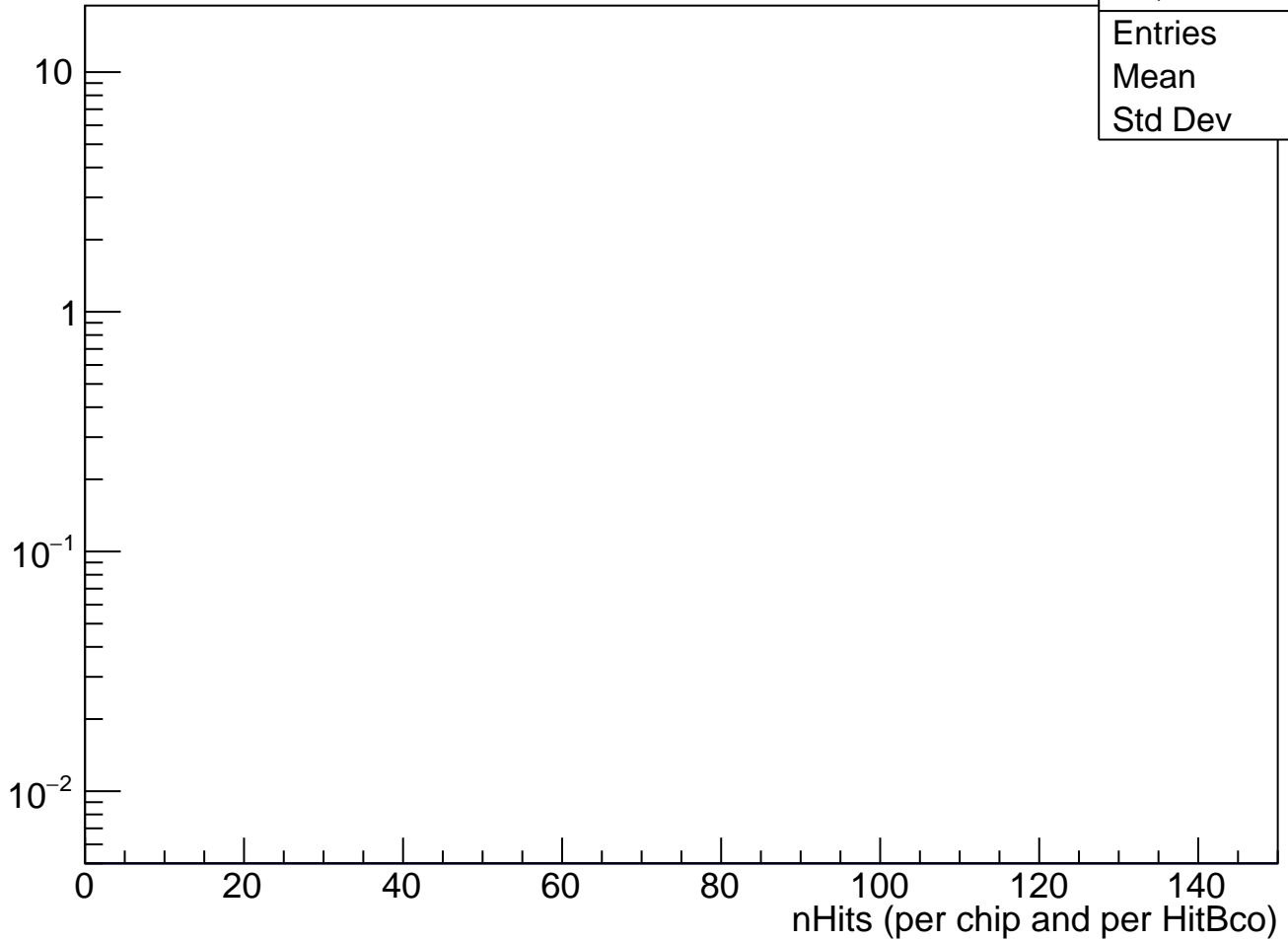
Entries



nChipHit_F5_Fch8_Chip11	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch8_Chip12

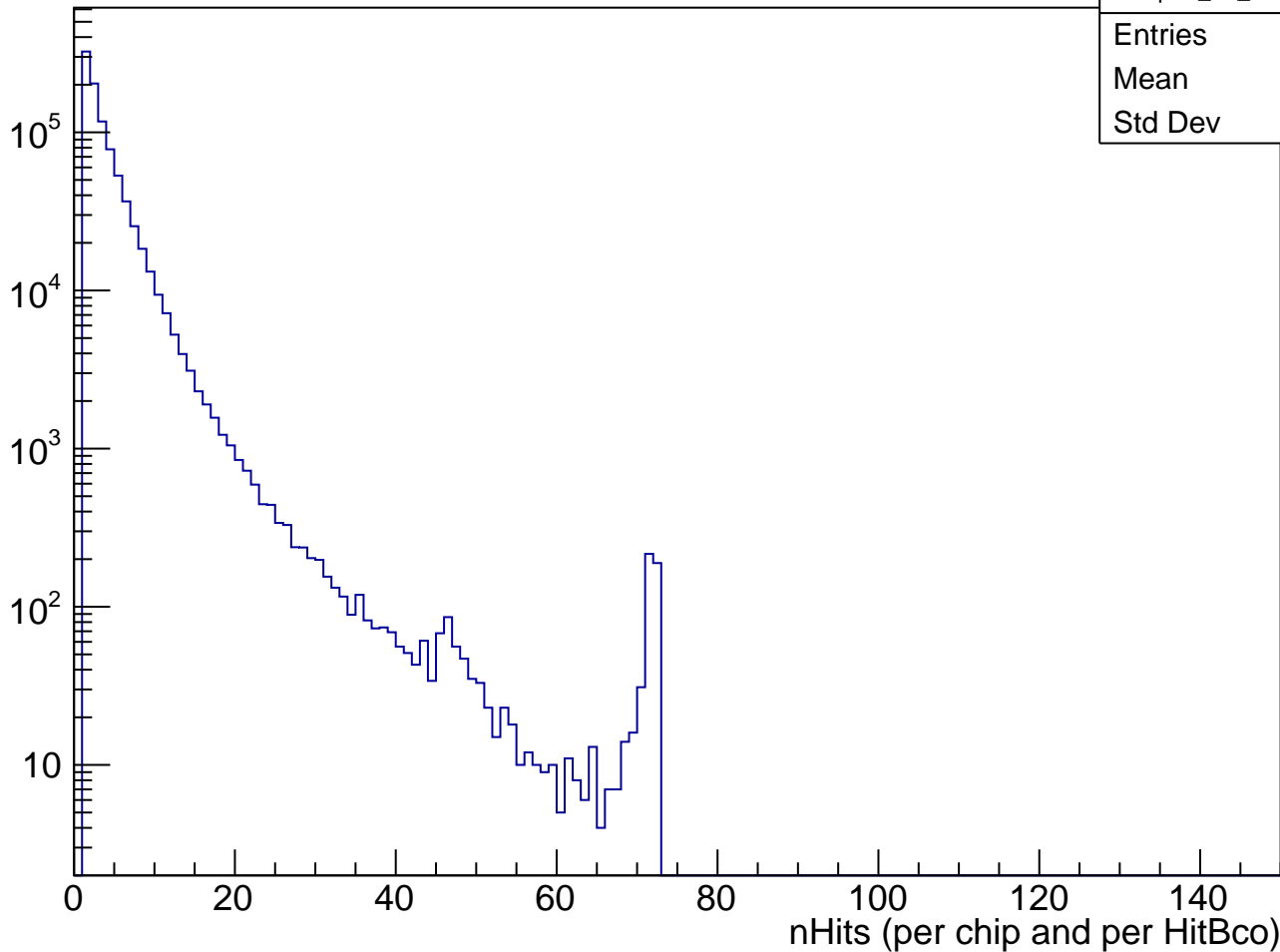
Entries



nChipHit_F5_Fch8_Chip12	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch8_Chip13

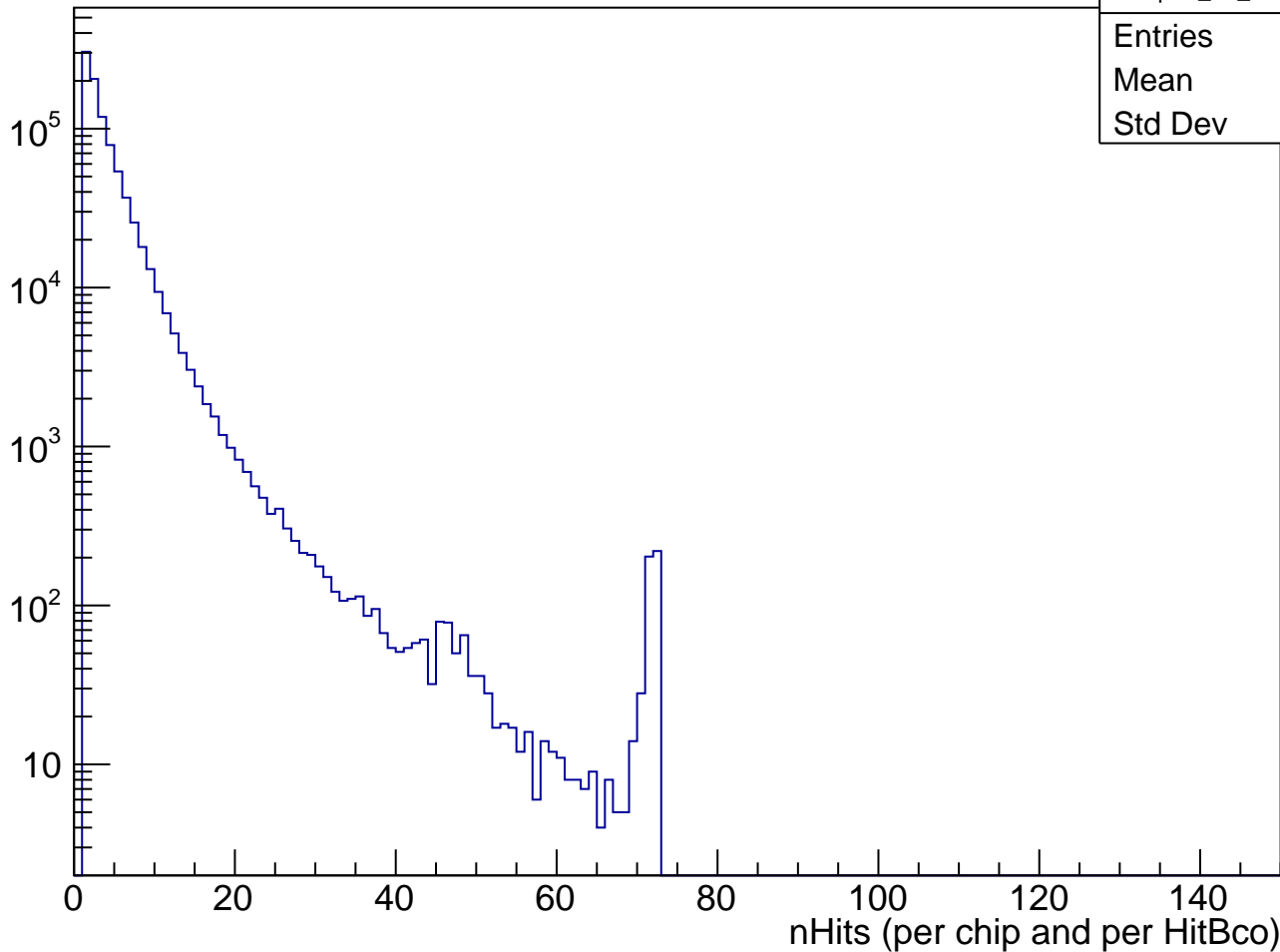
Entries



nChipHit_F5_Fch8_Chip13	
Entries	913447
Mean	3.282
Std Dev	3.86

nChipHit_F5_Fch8_Chip14

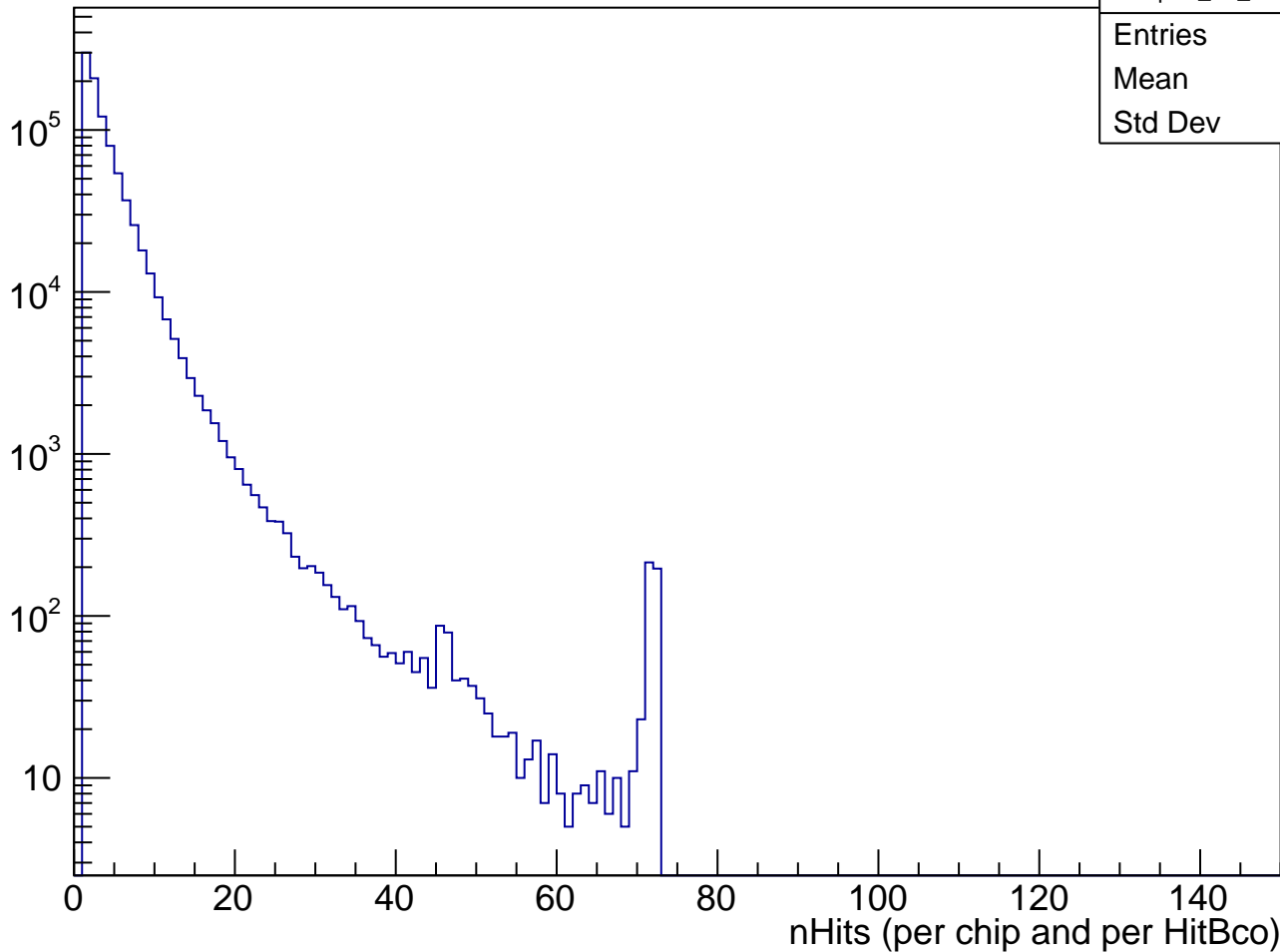
Entries



nChipHit_F5_Fch8_Chip14	
Entries	898371
Mean	3.321
Std Dev	3.877

nChipHit_F5_Fch8_Chip15

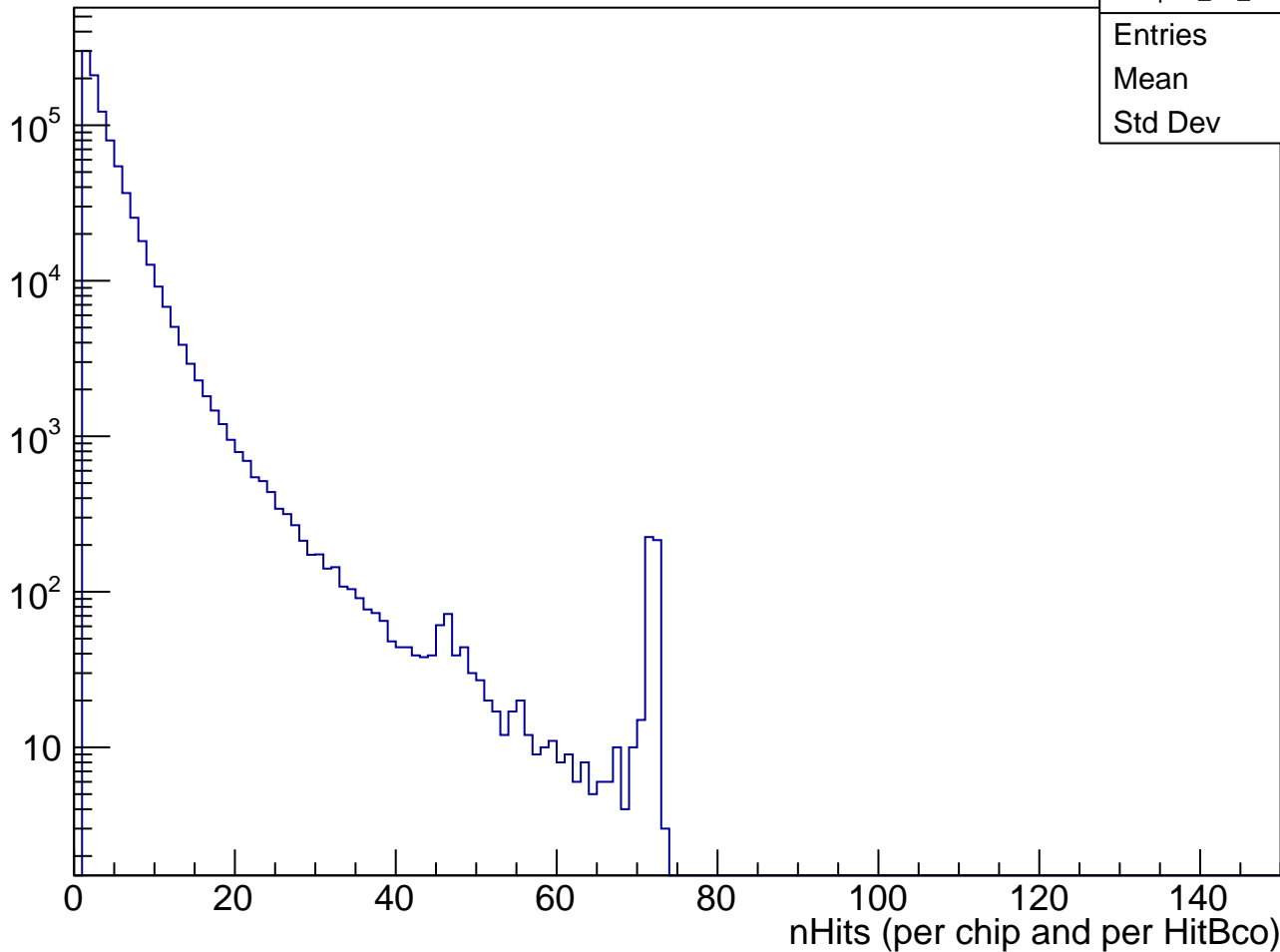
Entries



nChipHit_F5_Fch8_Chip15	
Entries	899805
Mean	3.318
Std Dev	3.83

nChipHit_F5_Fch8_Chip16

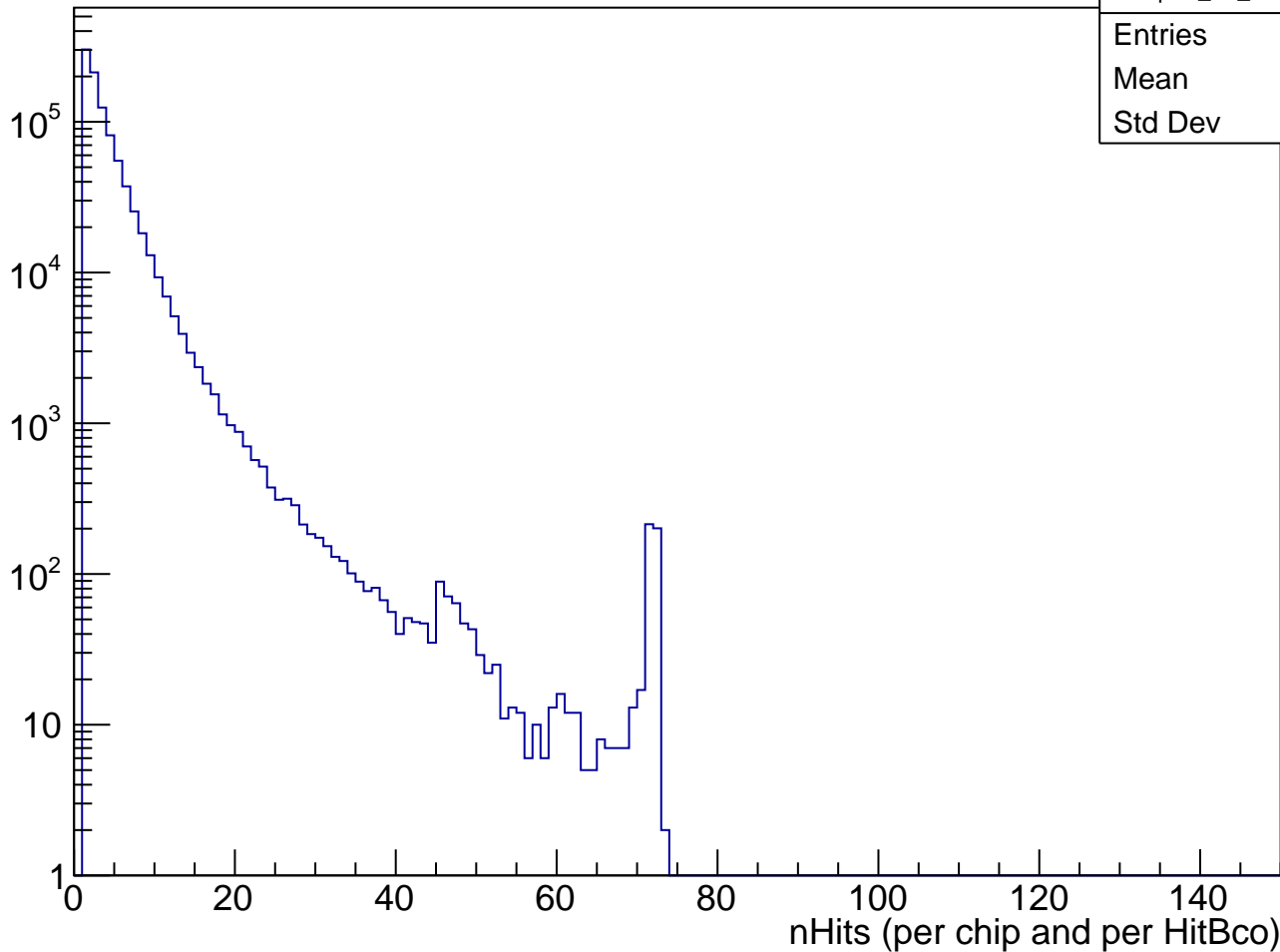
Entries



nChipHit_F5_Fch8_Chip16	
Entries	902006
Mean	3.304
Std Dev	3.808

nChipHit_F5_Fch8_Chip17

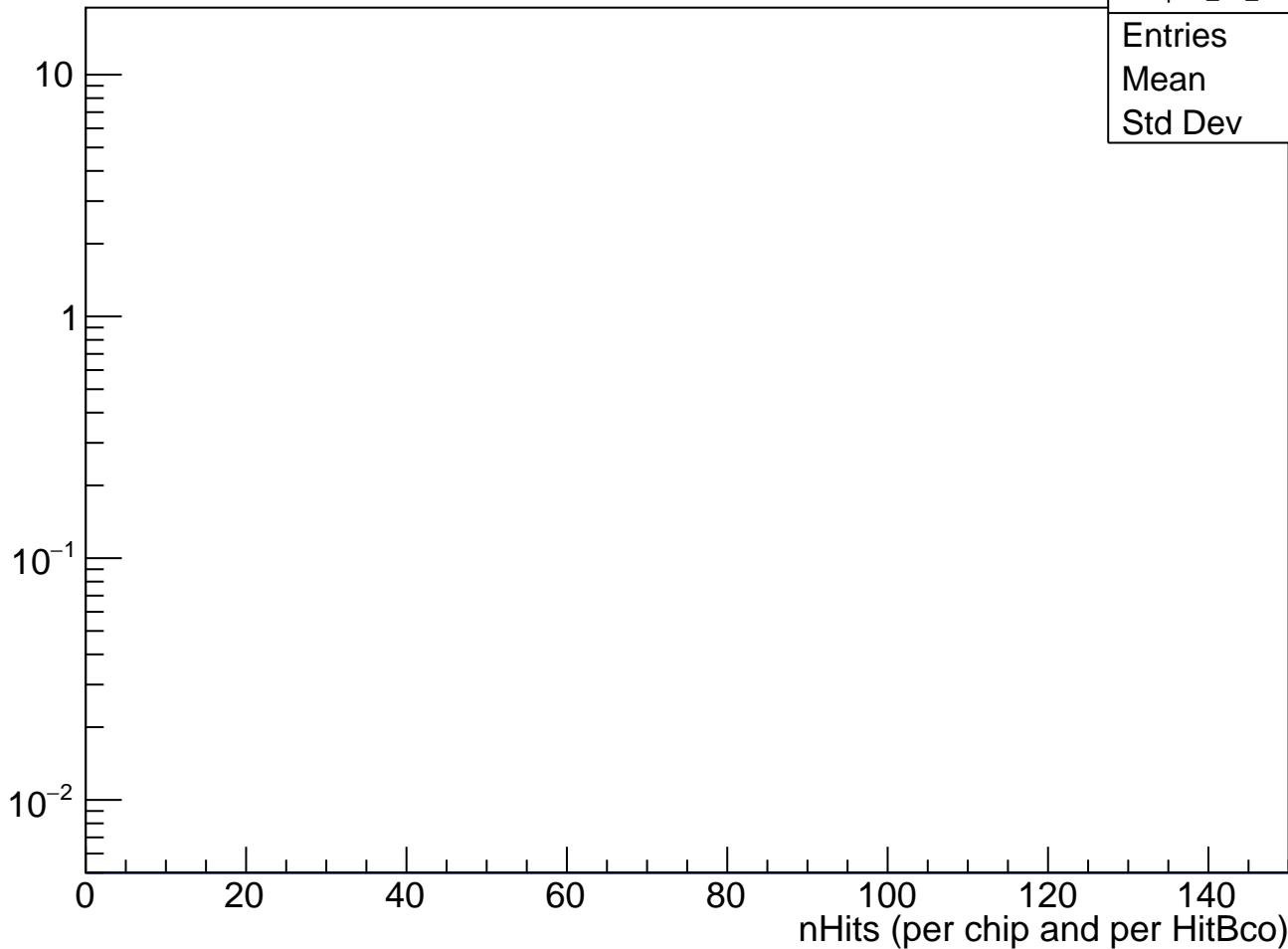
Entries



nChipHit_F5_Fch8_Chip17	
Entries	912527
Mean	3.315
Std Dev	3.816

nChipHit_F5_Fch8_Chip18

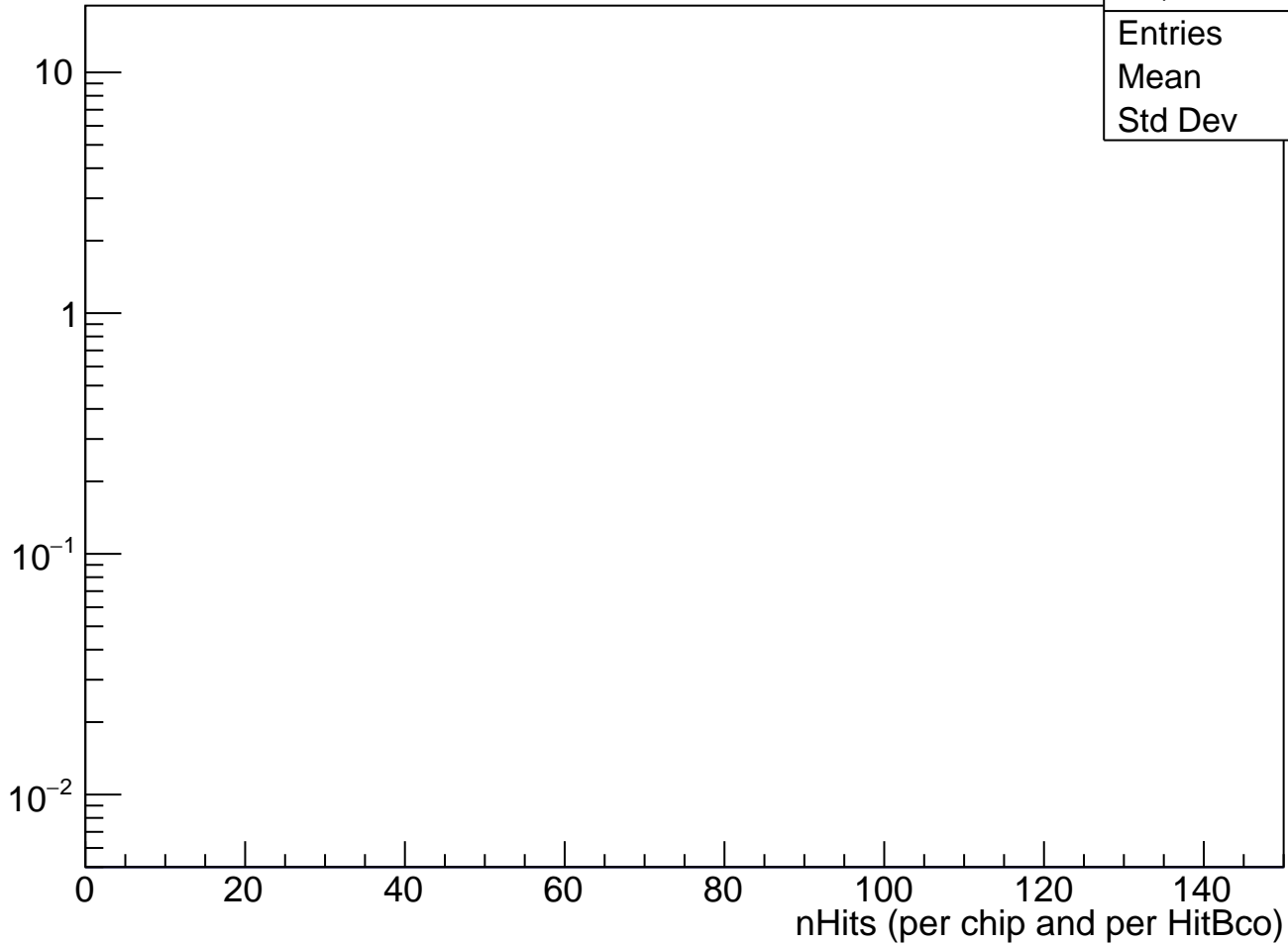
Entries



nChipHit_F5_Fch8_Chip18	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch8_Chip19

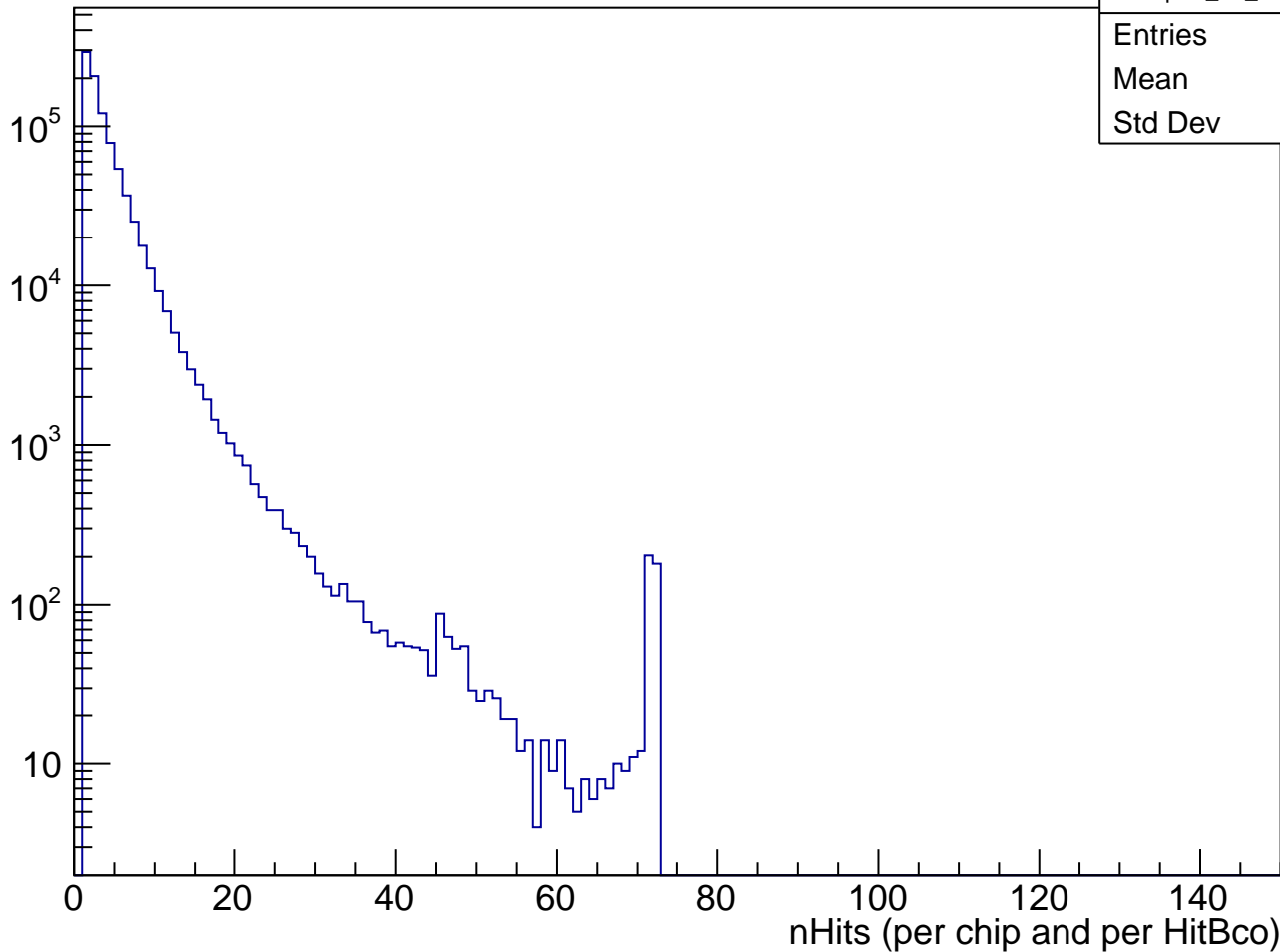
Entries



nChipHit_F5_Fch8_Chip19	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch8_Chip2

Entries



nChipHit_F5_Fch8_Chip2

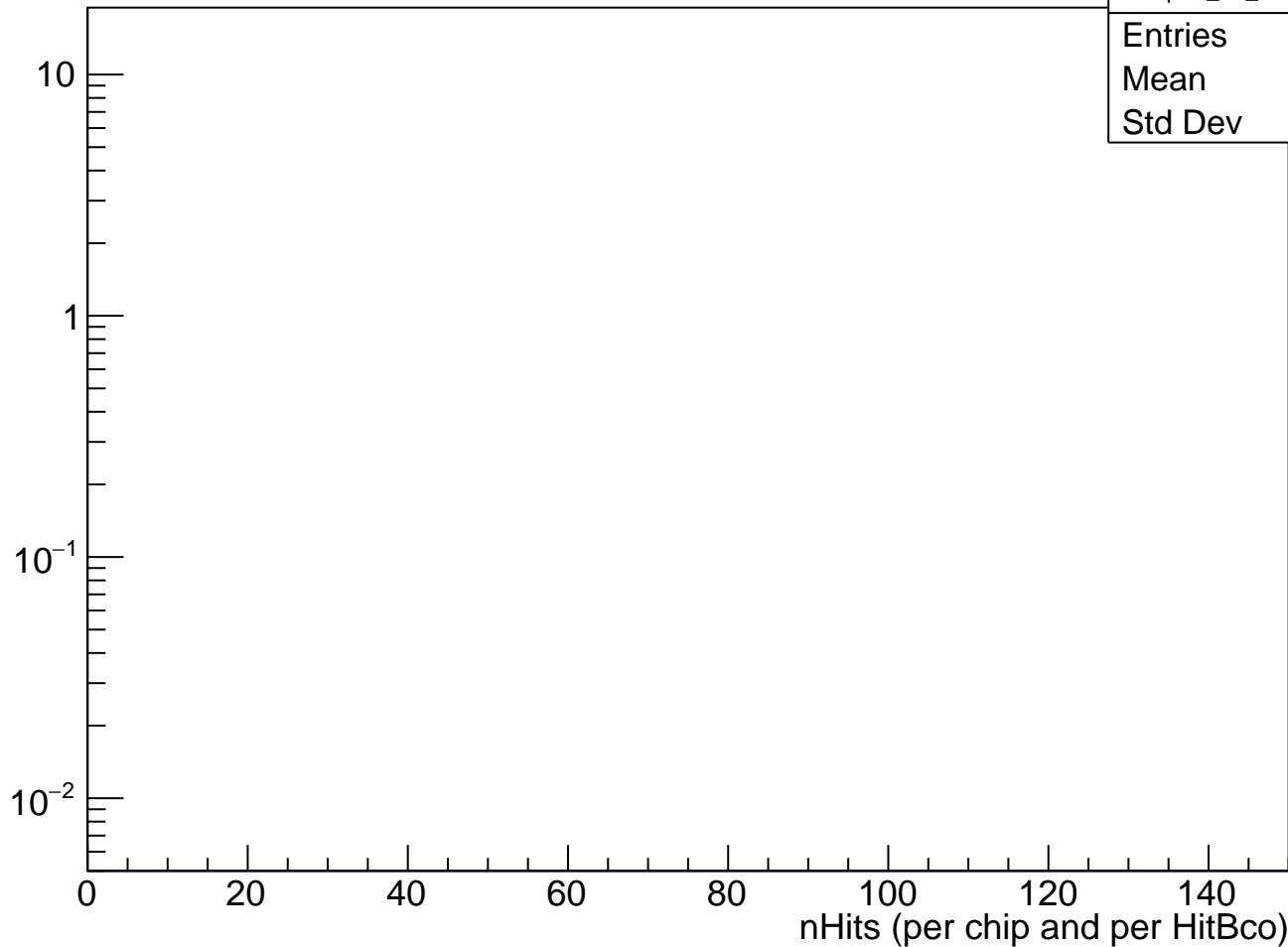
Entries 886878

Mean 3.339

Std Dev 3.841

nChipHit_F5_Fch8_Chip20

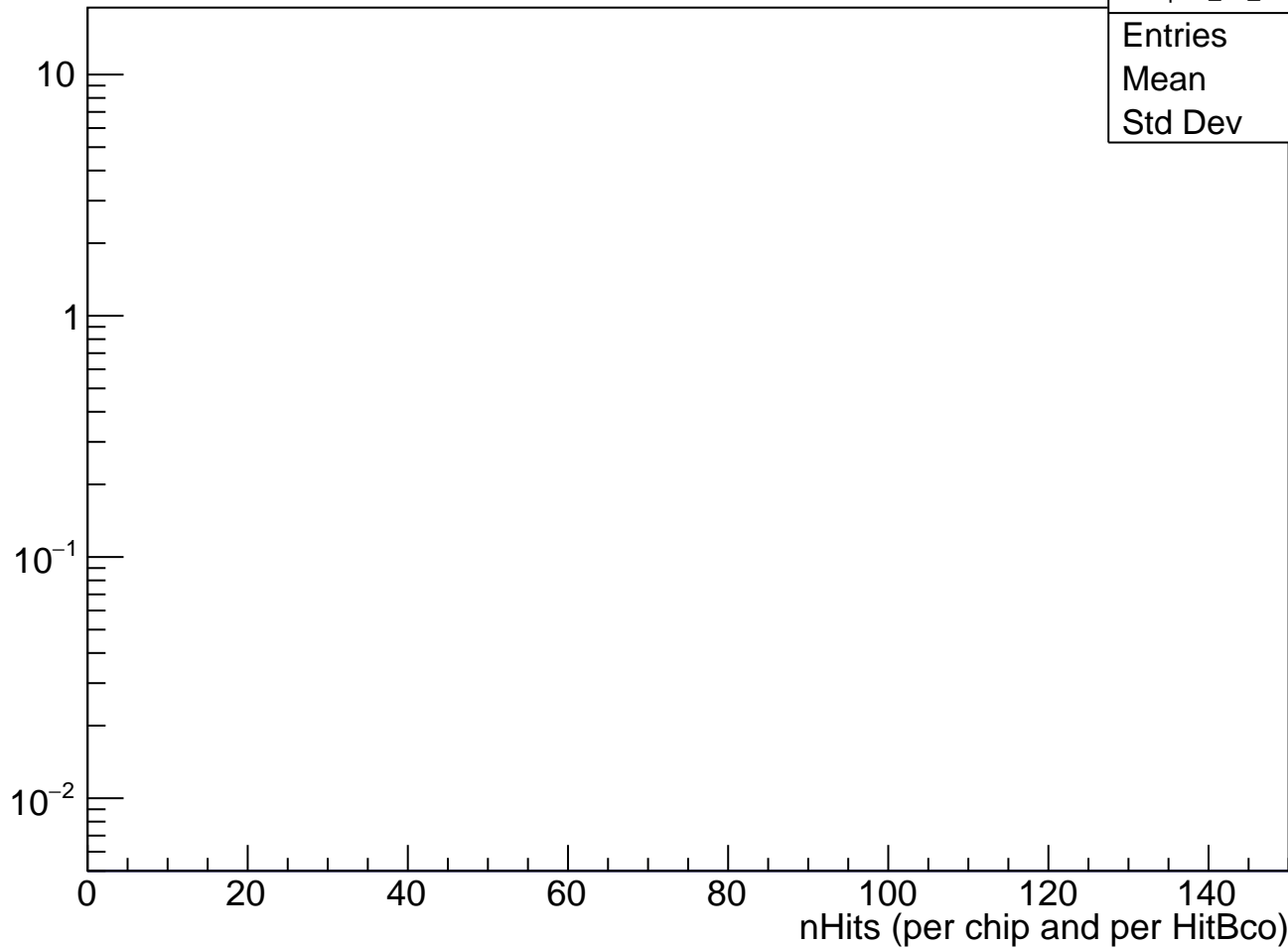
Entries



nChipHit_F5_Fch8_Chip20	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch8_Chip21

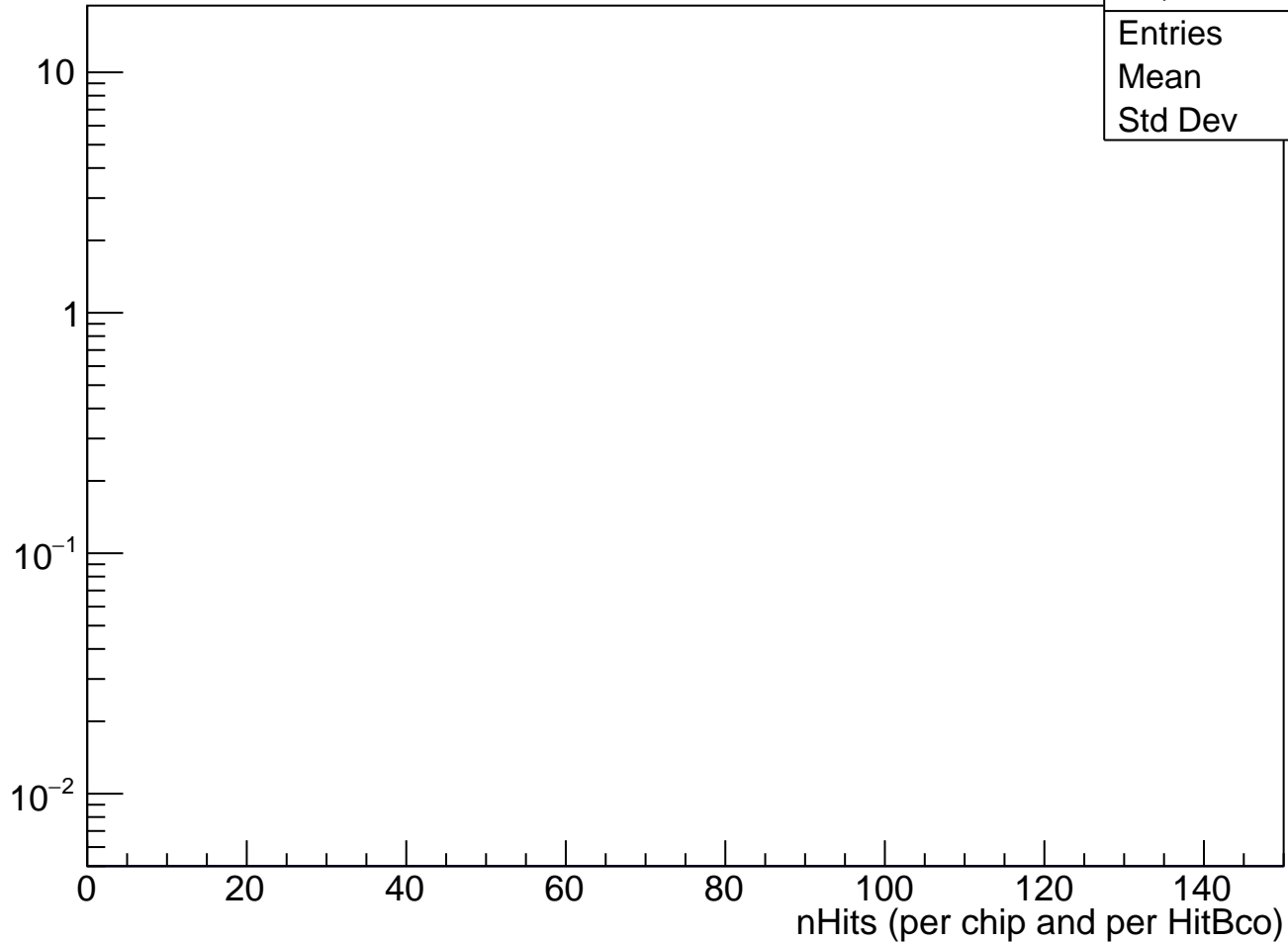
Entries



nChipHit_F5_Fch8_Chip21	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch8_Chip22

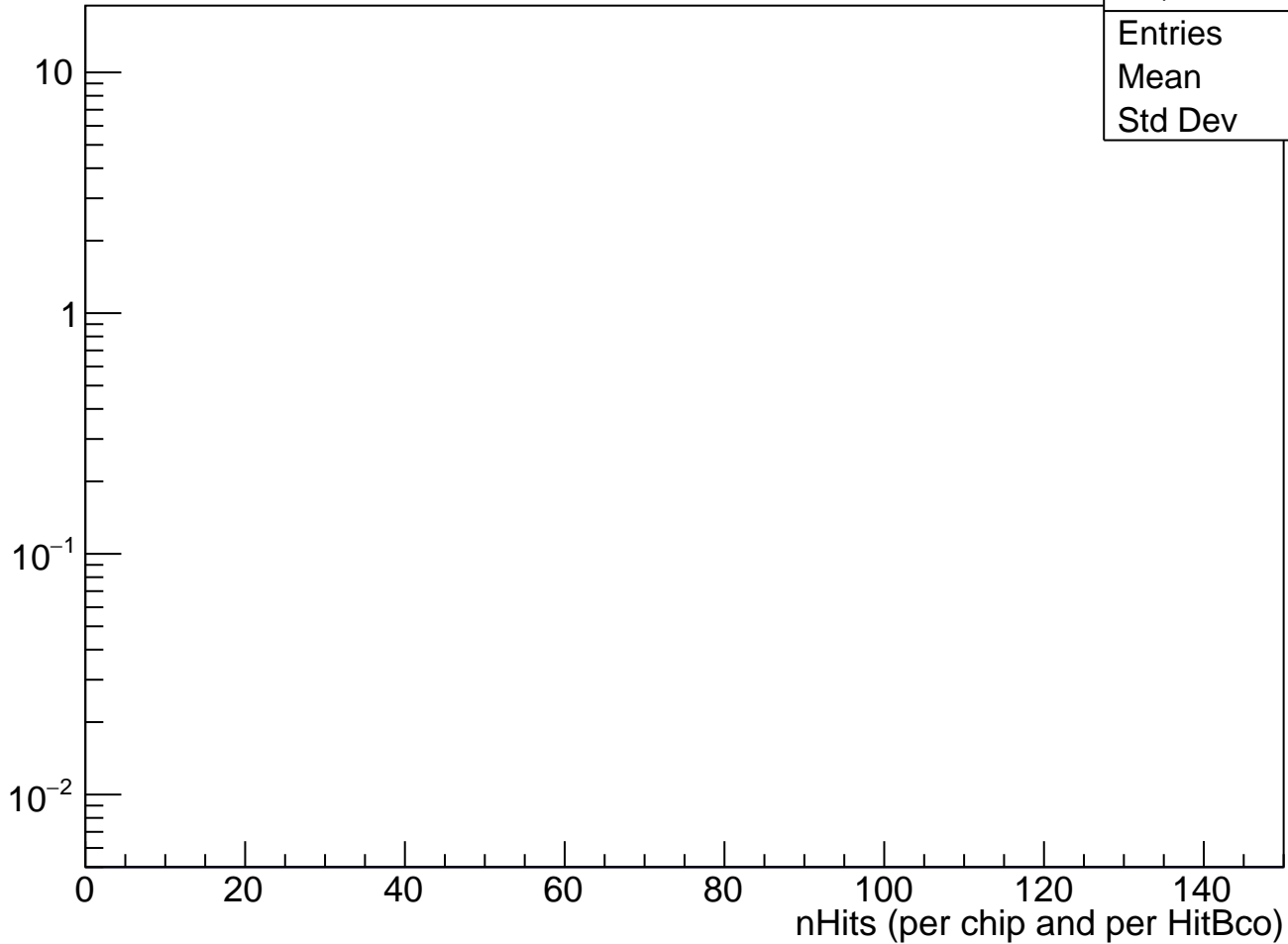
Entries



nChipHit_F5_Fch8_Chip22	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch8_Chip23

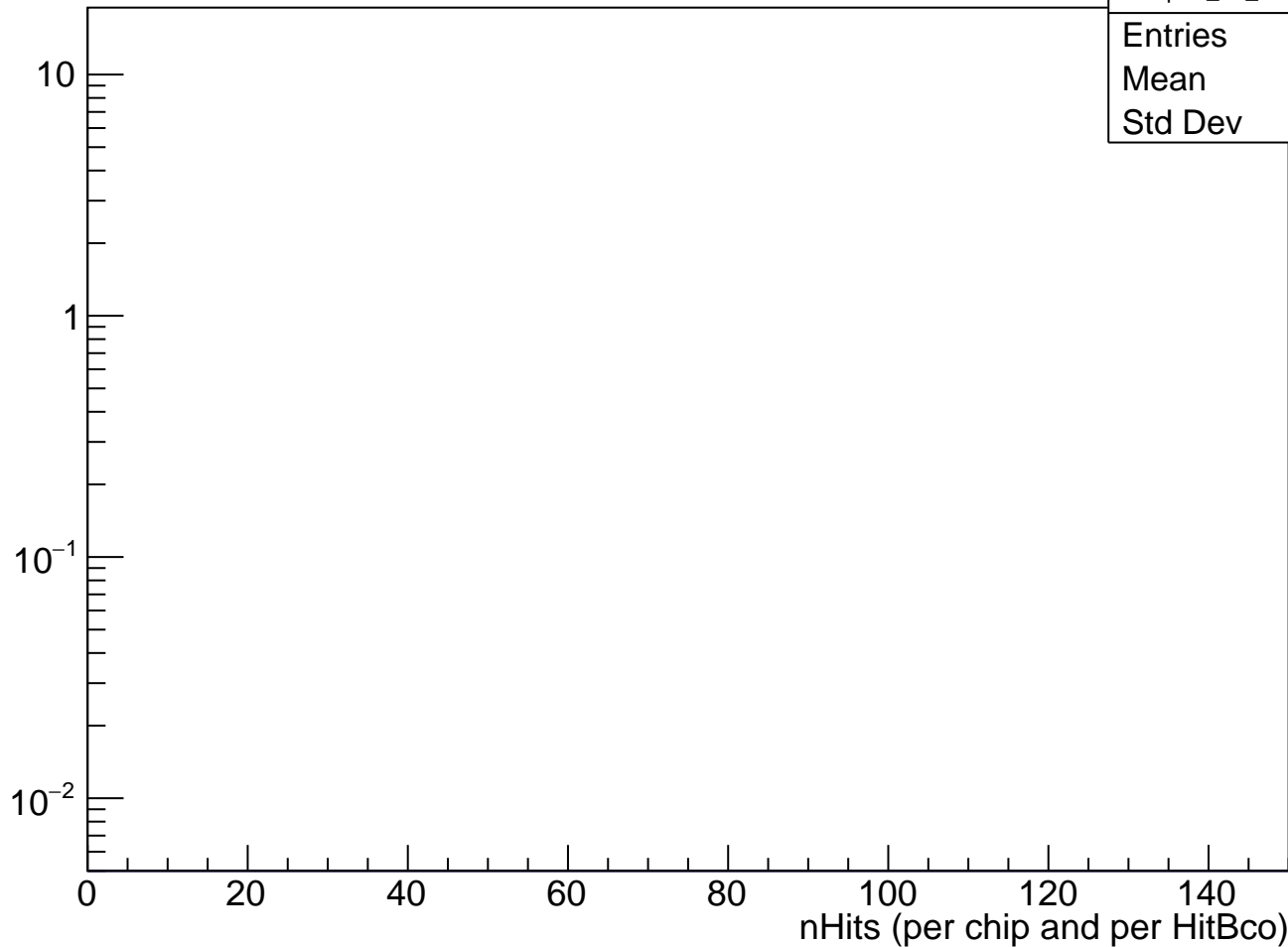
Entries



nChipHit_F5_Fch8_Chip23	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch8_Chip24

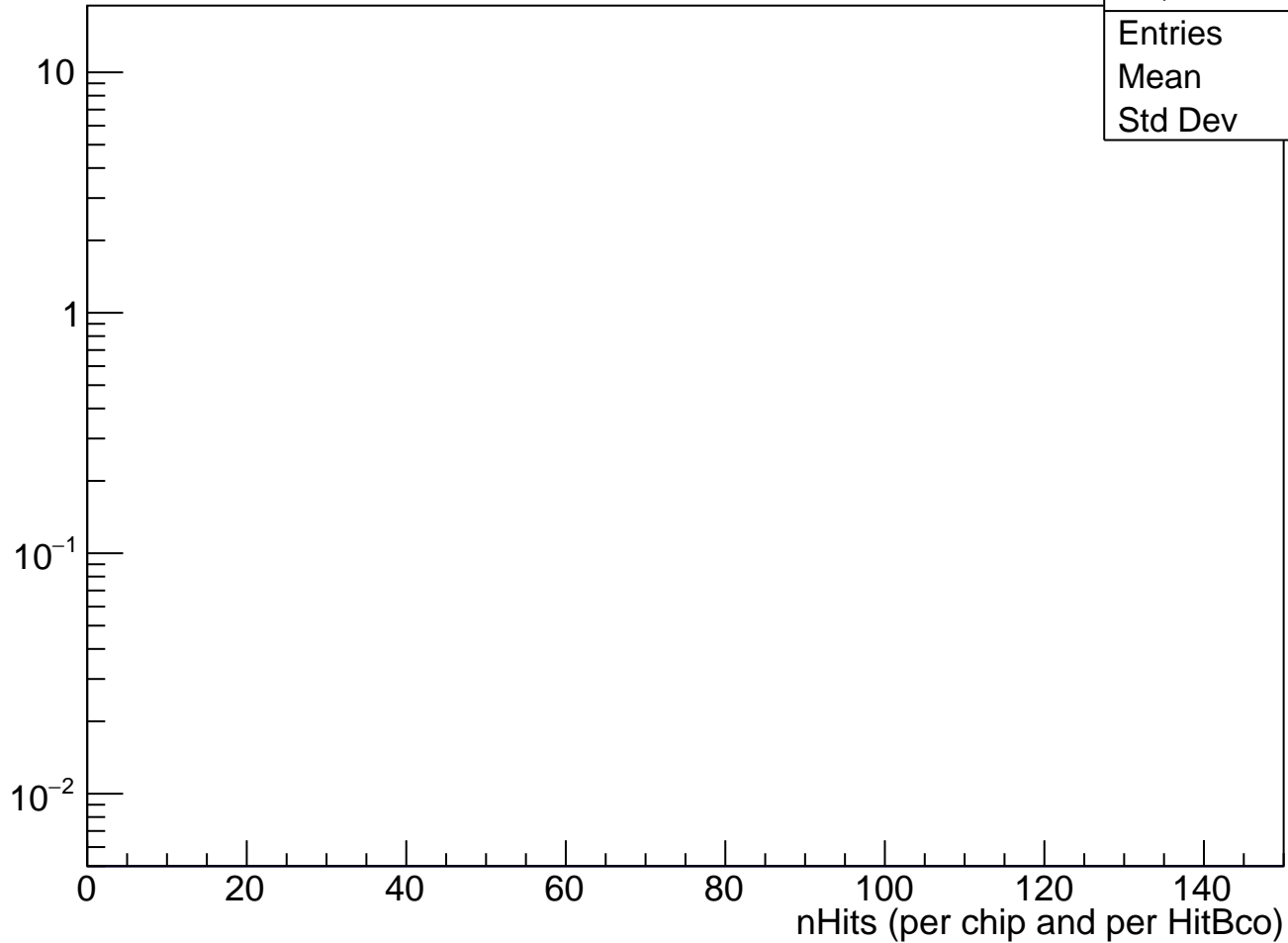
Entries



nChipHit_F5_Fch8_Chip24	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch8_Chip25

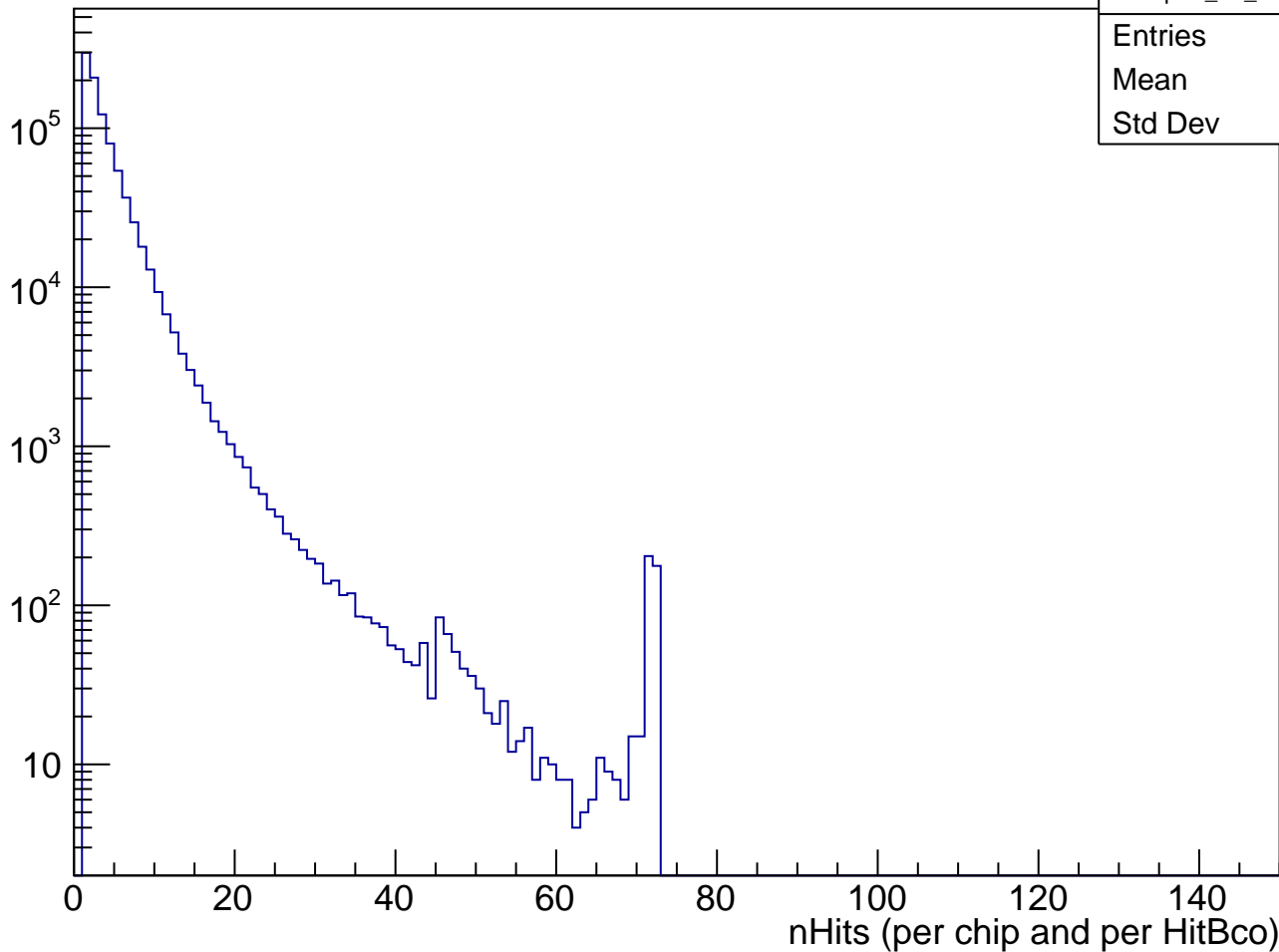
Entries



nChipHit_F5_Fch8_Chip25	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch8_Chip3

Entries

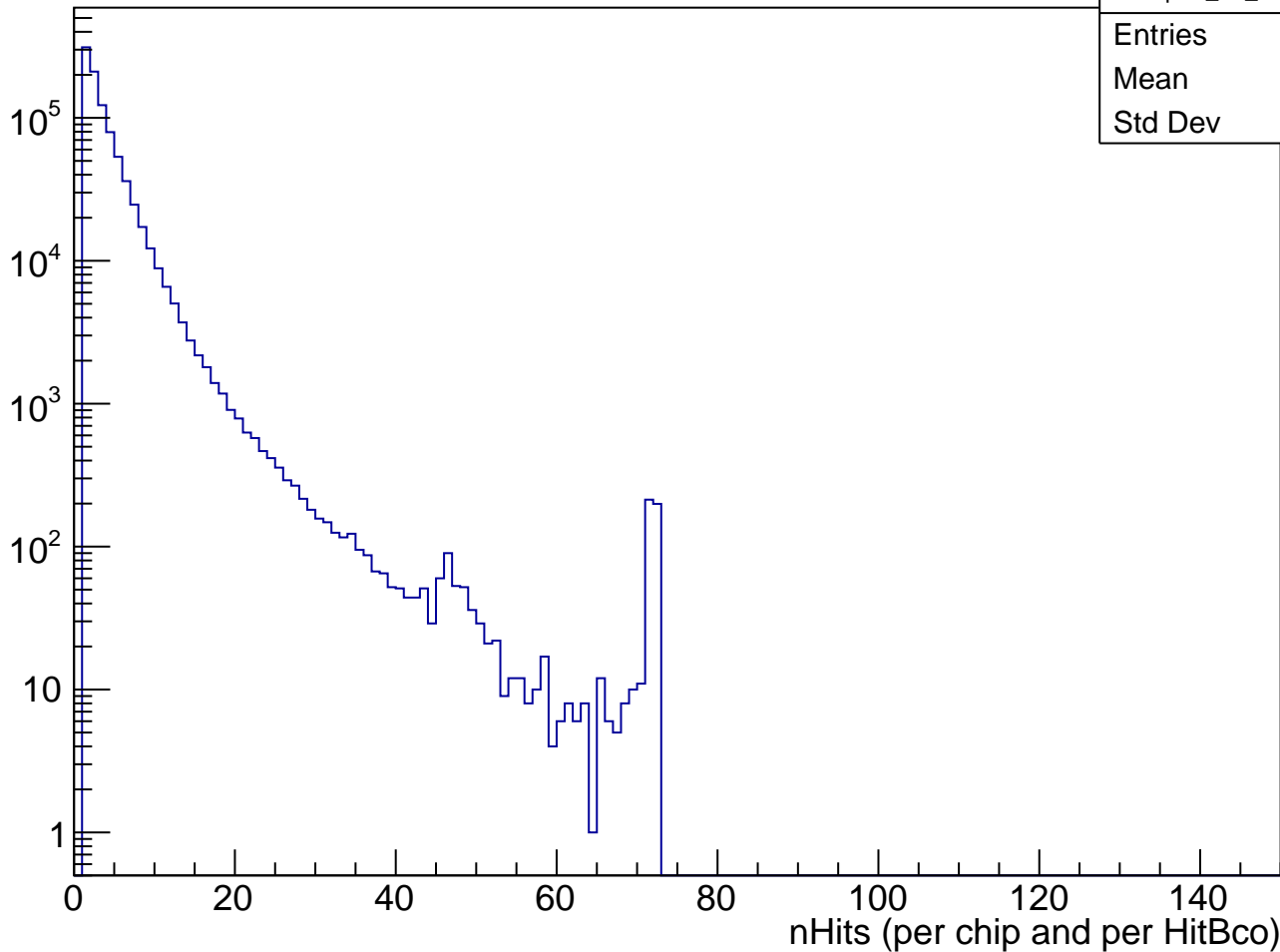


nChipHit_F5_Fch8_Chip3

Entries	898282
Mean	3.325
Std Dev	3.814

nChipHit_F5_Fch8_Chip4

Entries

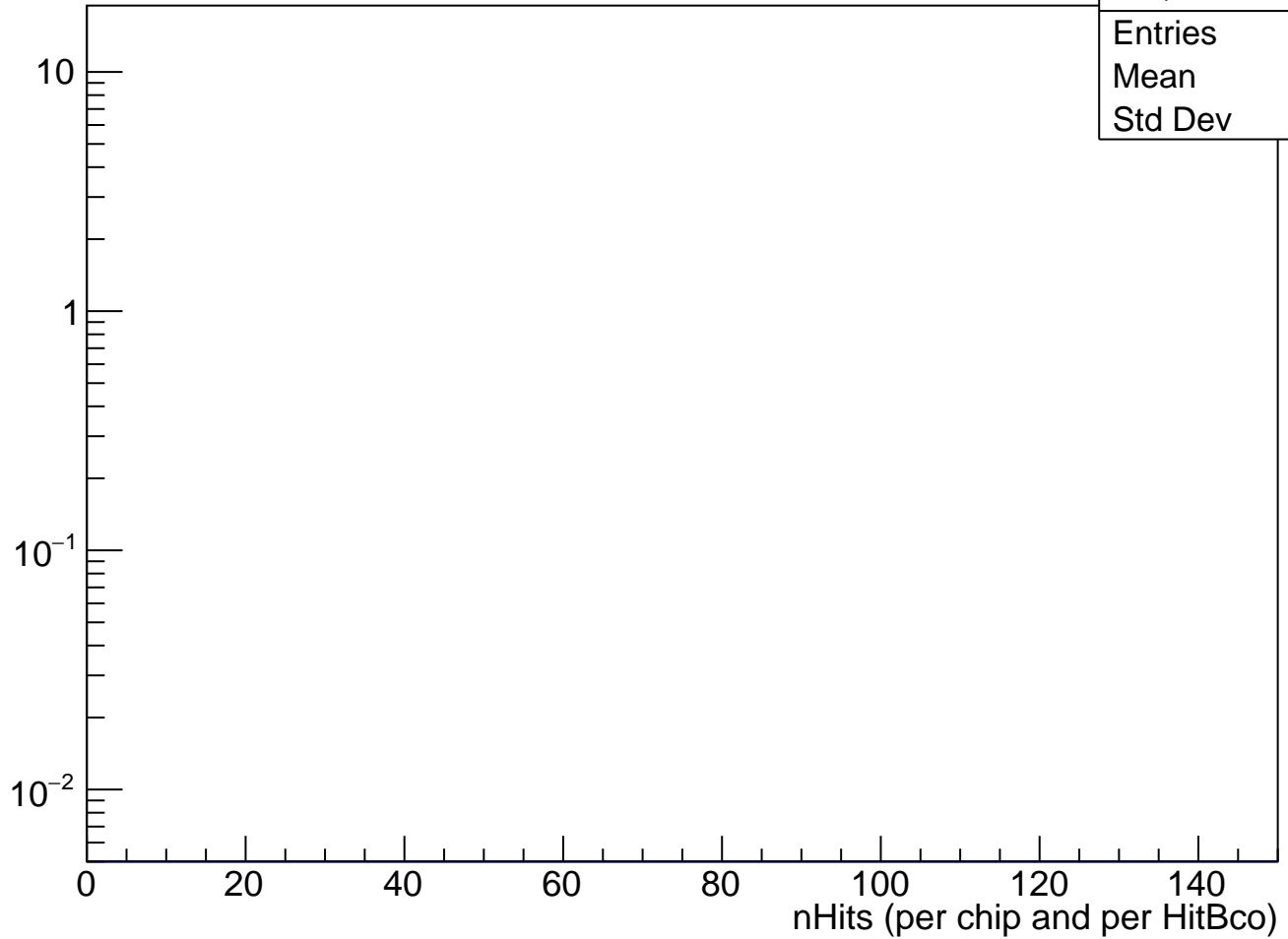


nChipHit_F5_Fch8_Chip4	
Entries	908856
Mean	3.248
Std Dev	3.762

nHits (per chip and per HitBco)

nChipHit_F5_Fch8_Chip5

Entries



nChipHit_F5_Fch8_Chip5	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch8_Chip6

nChipHit_F5_Fch8_Chip6	
Entries	0
Mean	0
Std Dev	0

Entries

10

1

10^{-1}

10^{-2}

0

20

40

60

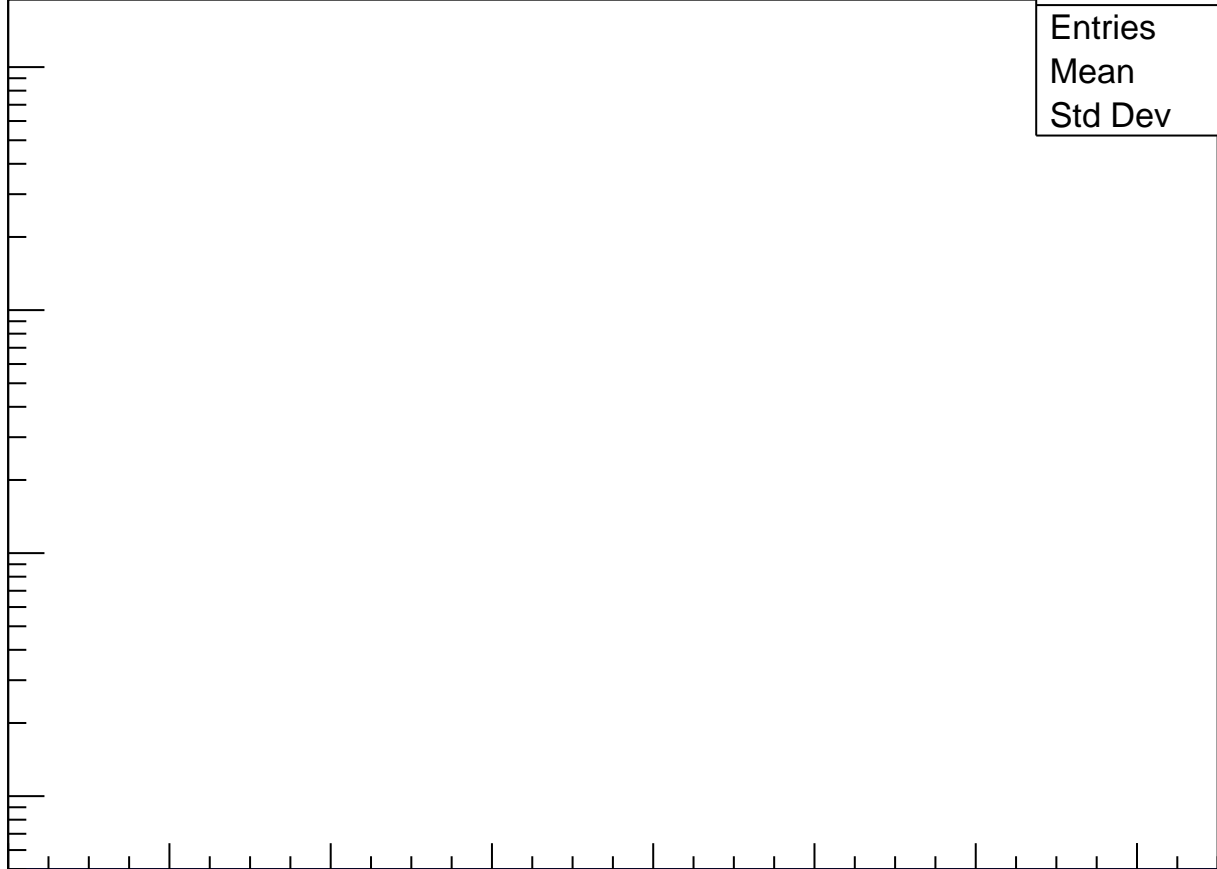
80

100

120

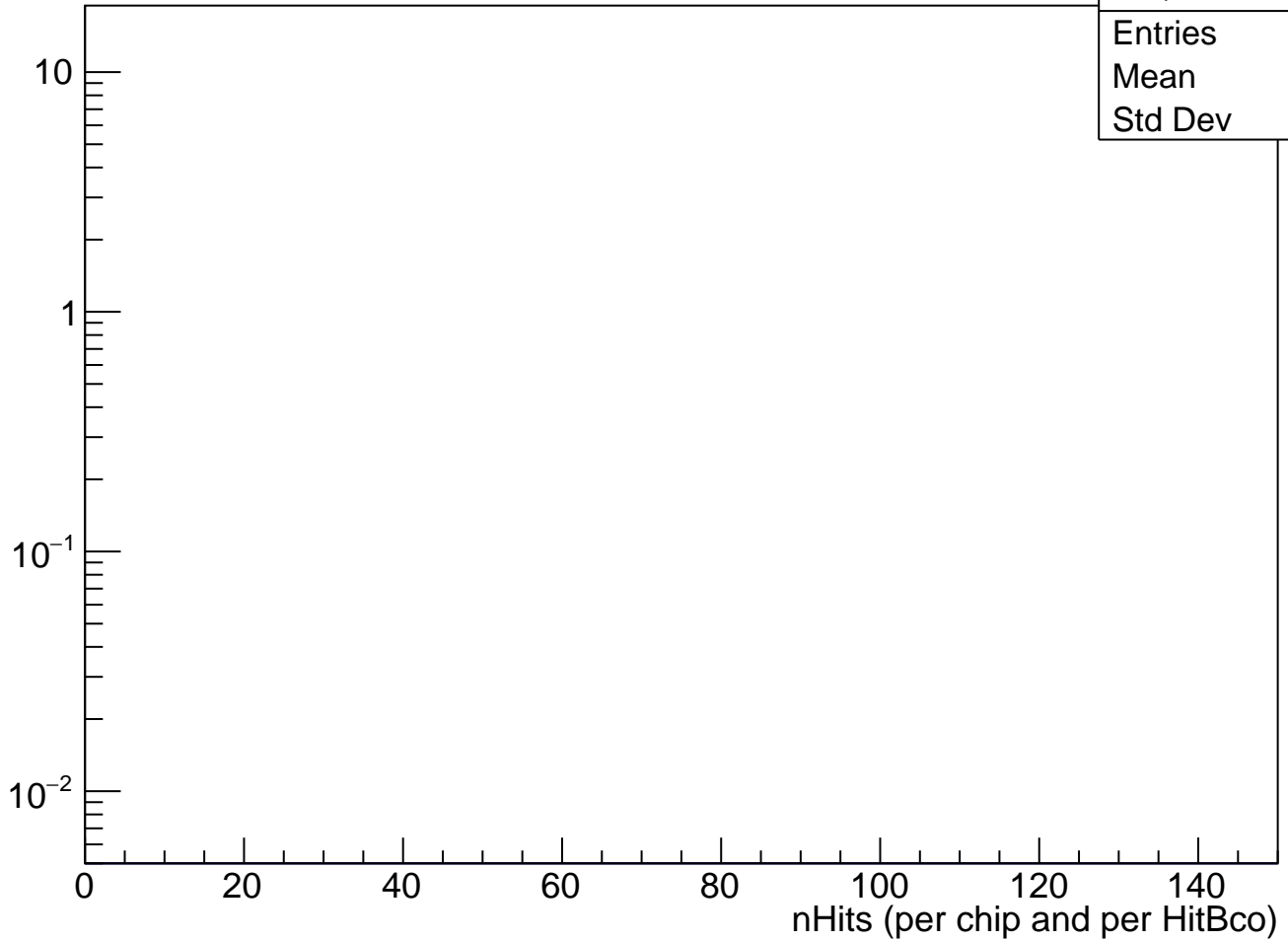
140

nHits (per chip and per HitBco)



nChipHit_F5_Fch8_Chip7

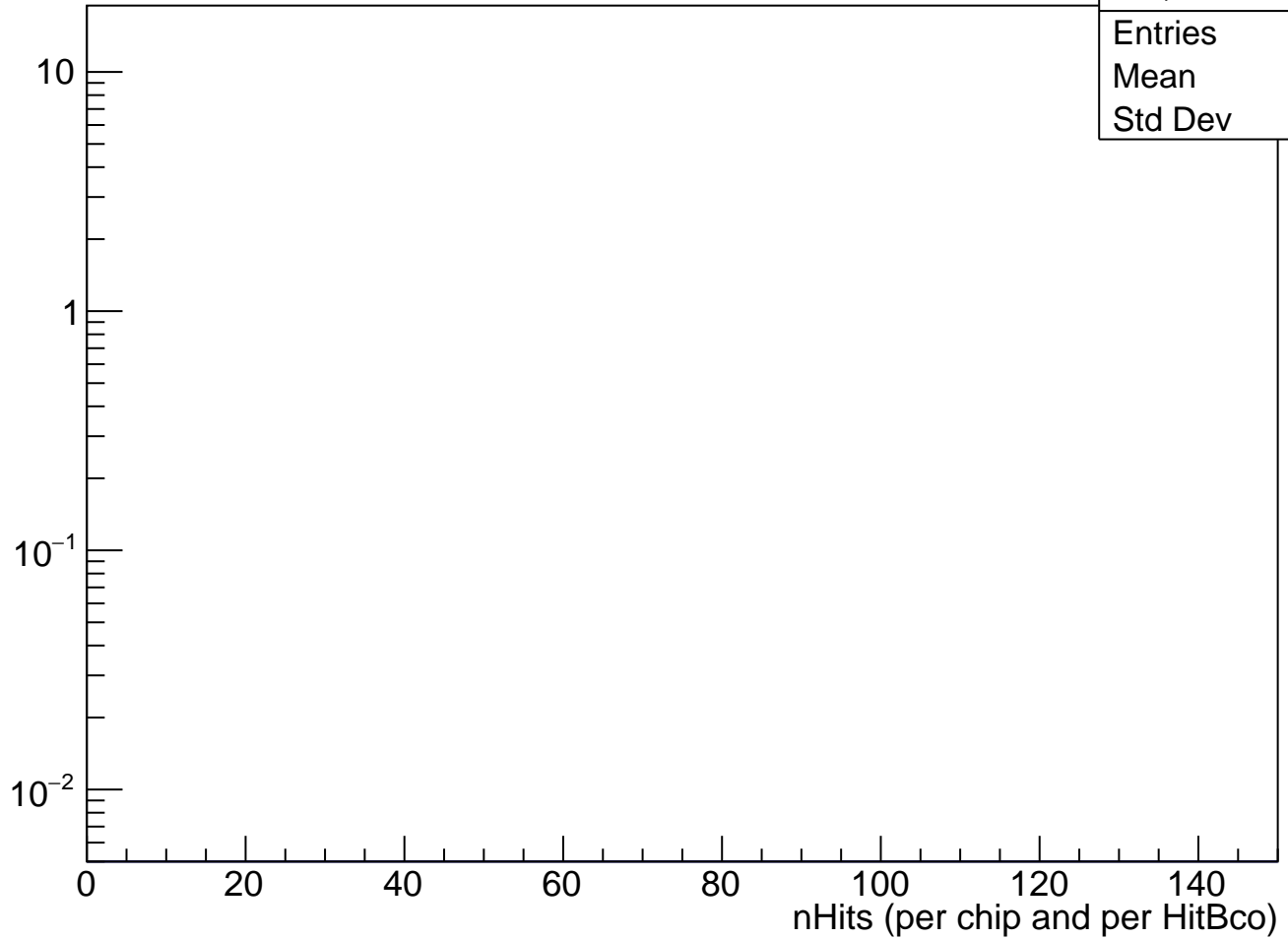
Entries



nChipHit_F5_Fch8_Chip7	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch8_Chip8

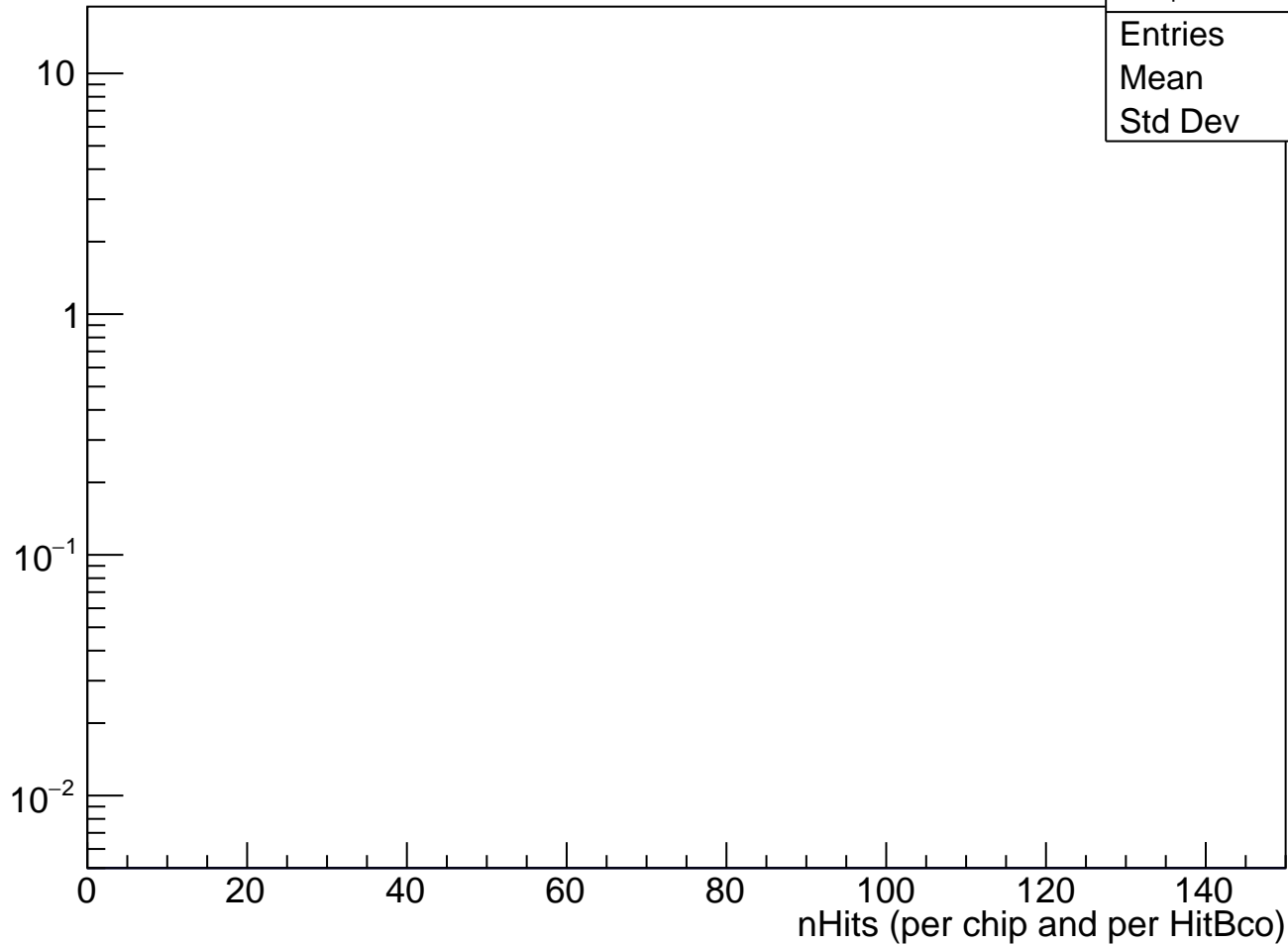
Entries



nChipHit_F5_Fch8_Chip8	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch8_Chip9

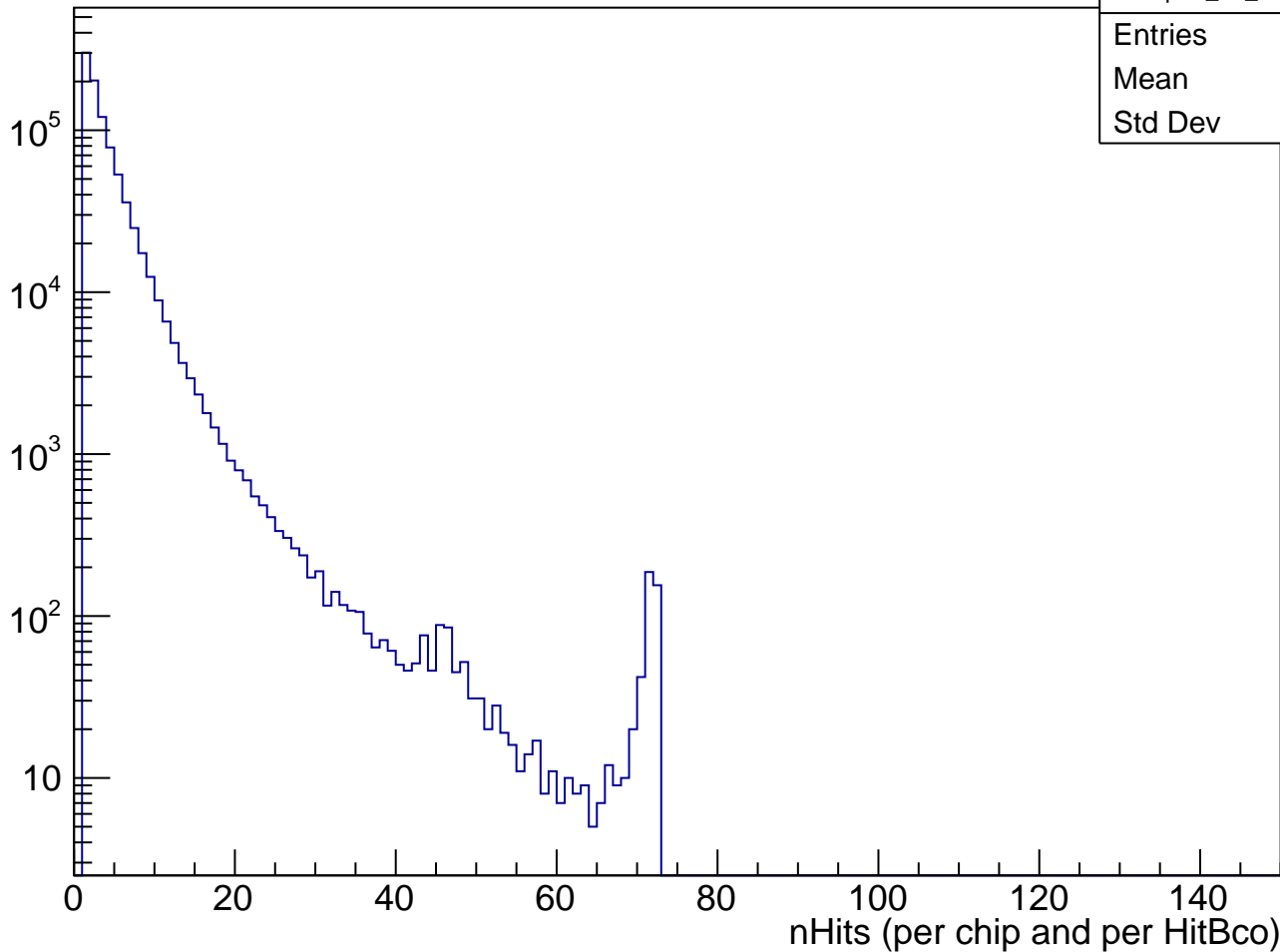
Entries



nChipHit_F5_Fch8_Chip9	
Entries	0
Mean	0
Std Dev	0

nChipHit_F5_Fch9_Chip0

Entries



nChipHit_F5_Fch9_Chip0

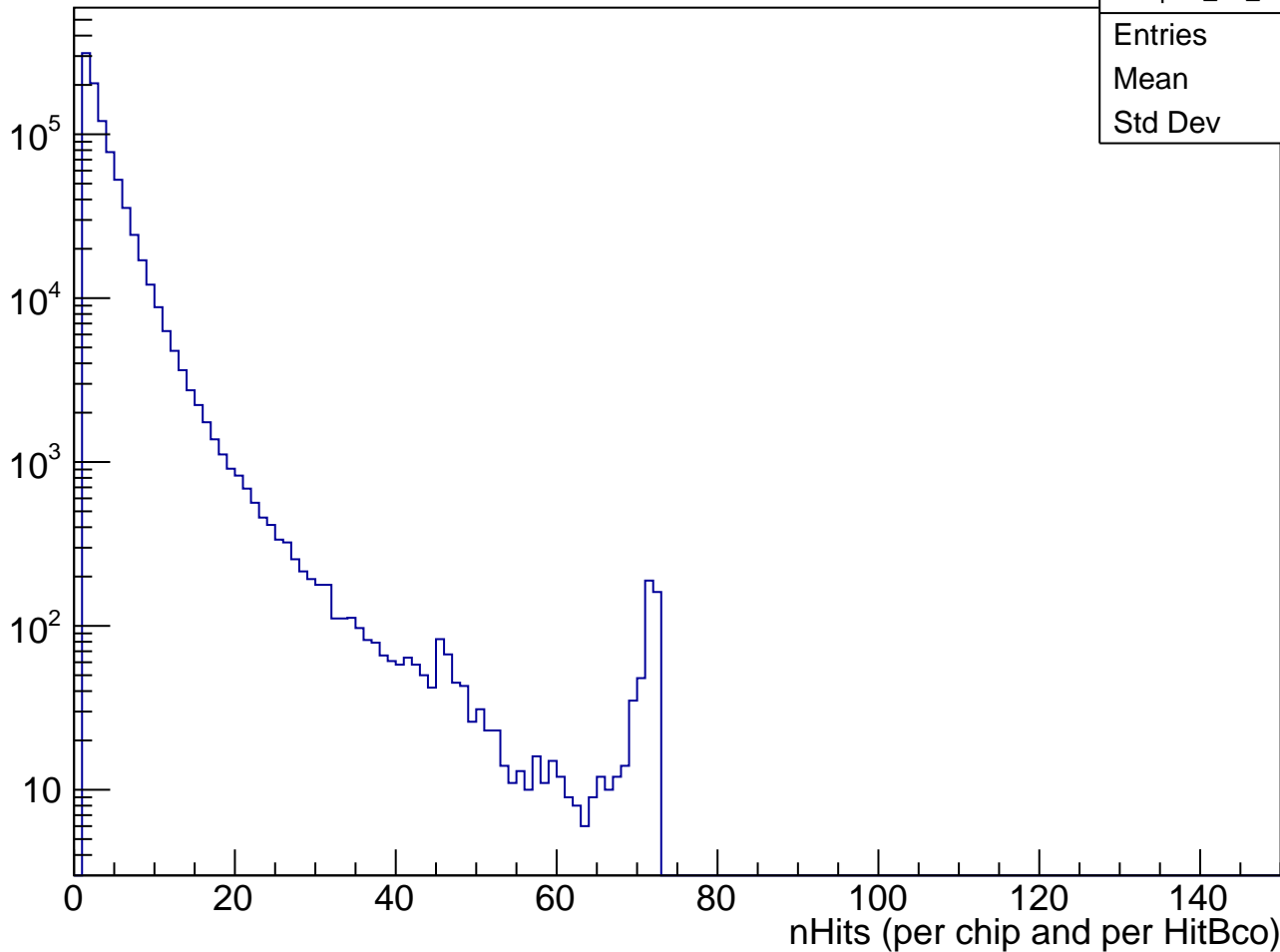
Entries 888663

Mean 3.294

Std Dev 3.82

nChipHit_F5_Fch9_Chip1

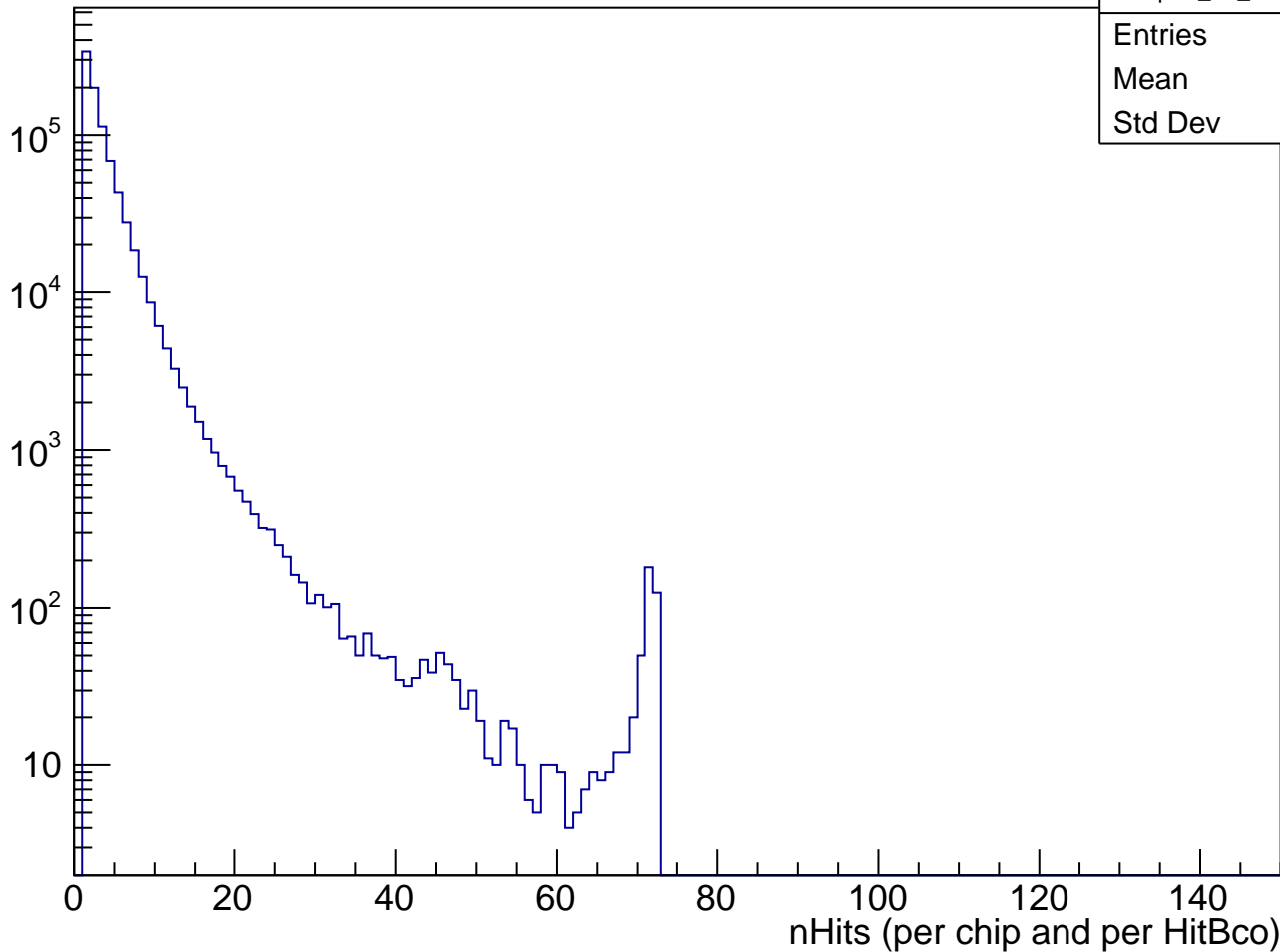
Entries



nChipHit_F5_Fch9_Chip1	
Entries	897448
Mean	3.246
Std Dev	3.81

nChipHit_F5_Fch9_Chip10

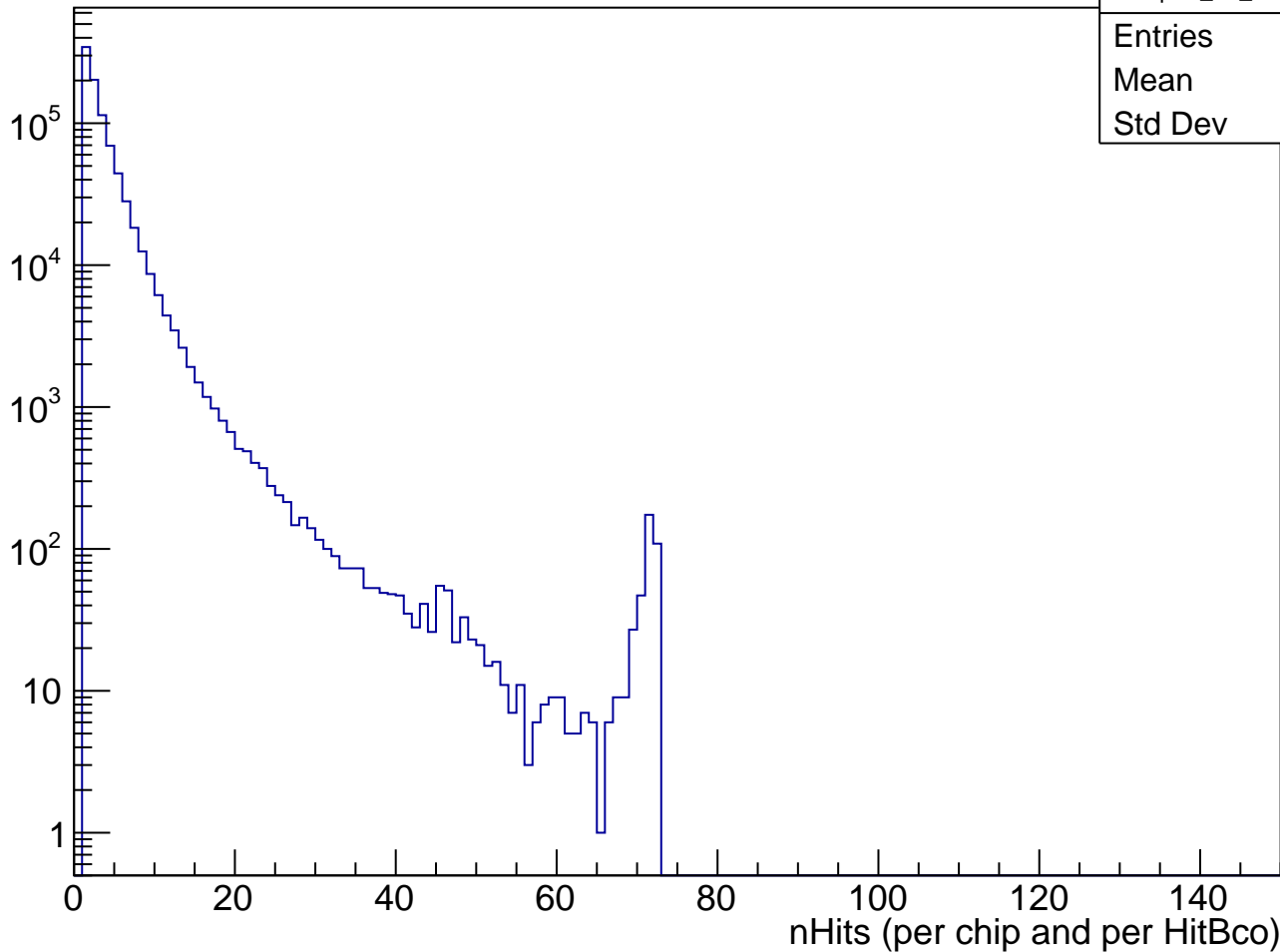
Entries



nChipHit_F5_Fch9_Chip10	
Entries	858859
Mean	2.89
Std Dev	3.429

nChipHit_F5_Fch9_Chip11

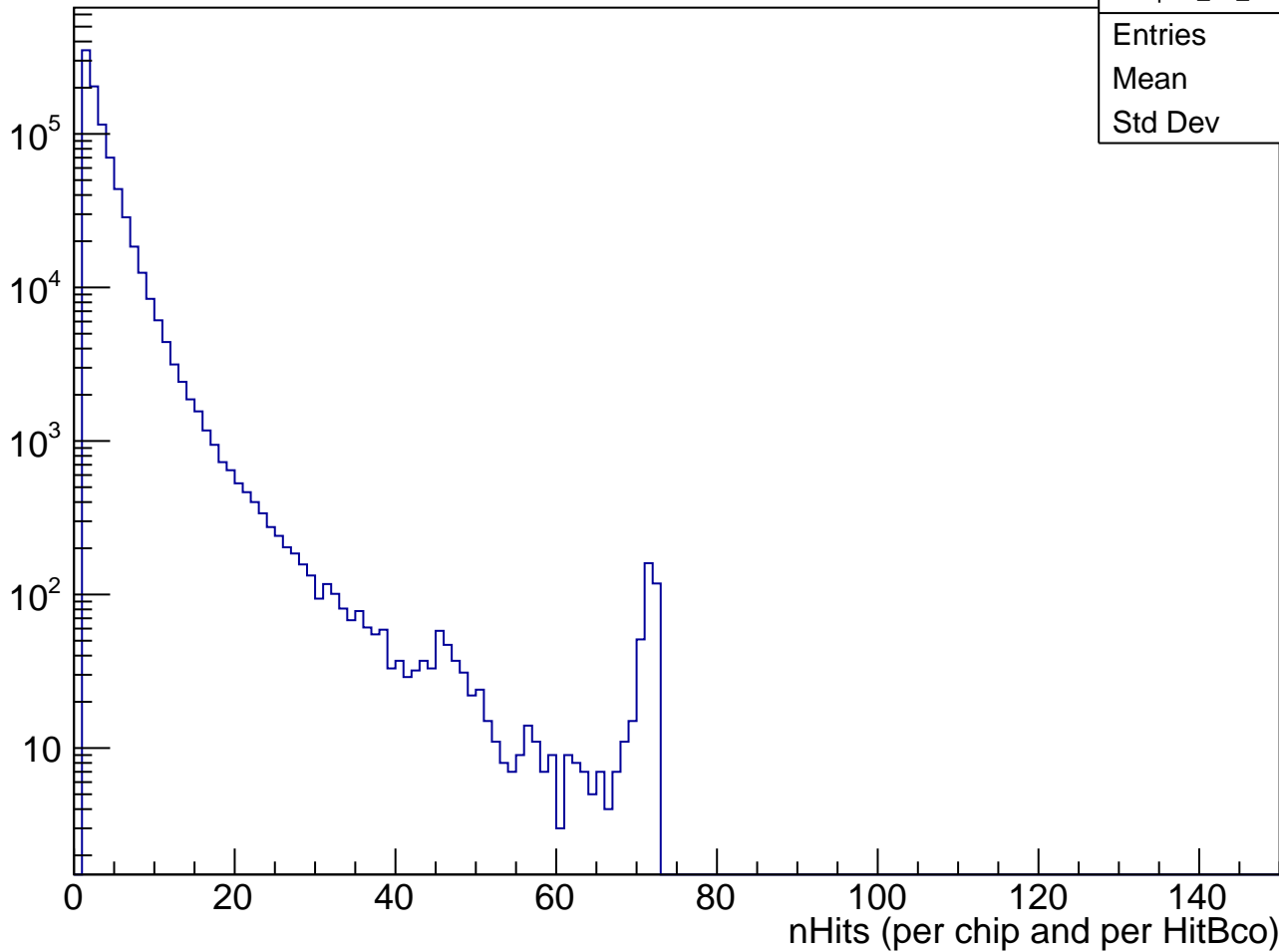
Entries



nChipHit_F5_Fch9_Chip11	
Entries	870291
Mean	2.88
Std Dev	3.386

nChipHit_F5_Fch9_Chip12

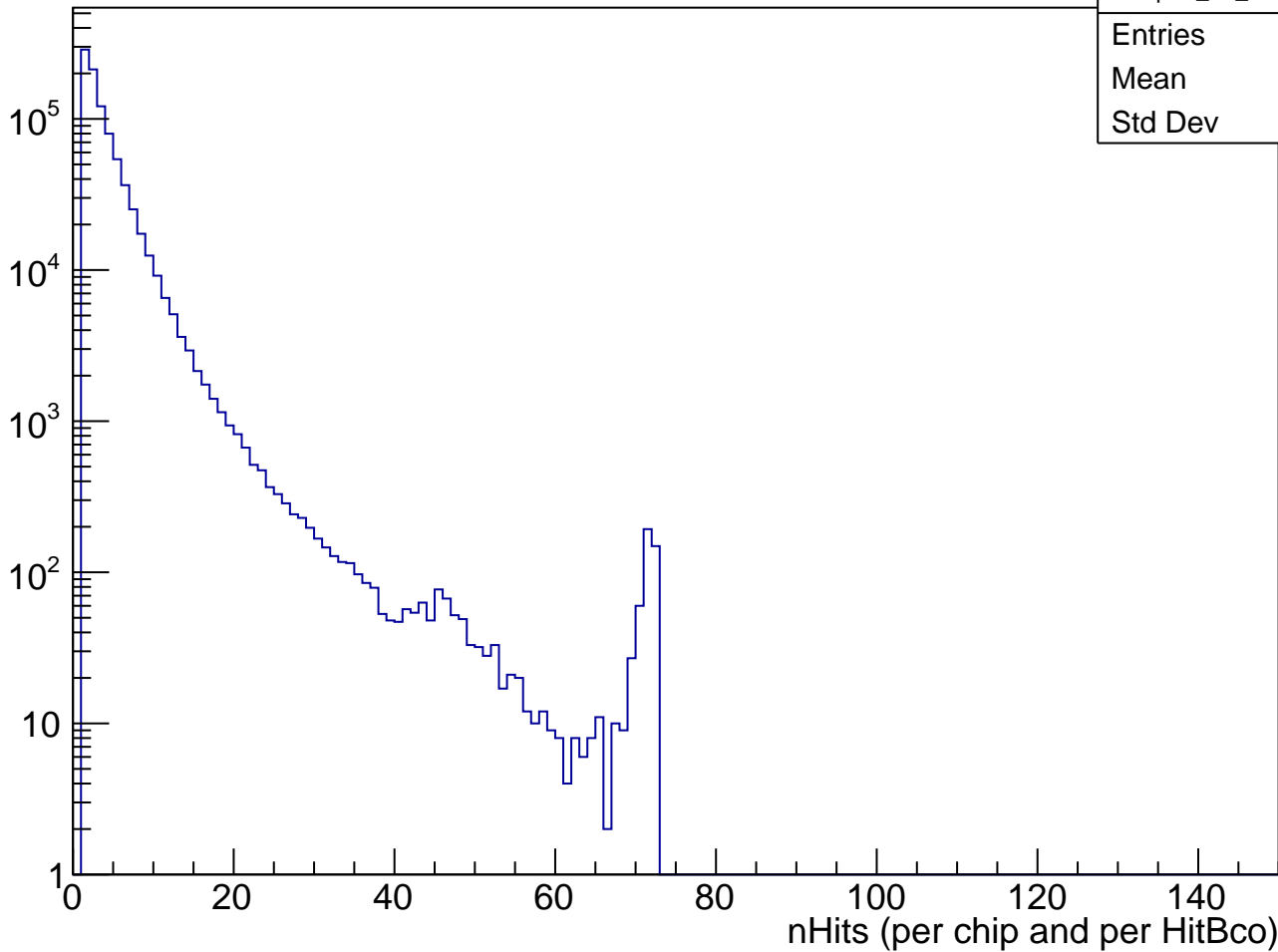
Entries



nChipHit_F5_Fch9_Chip12	
Entries	879147
Mean	2.857
Std Dev	3.36

nChipHit_F5_Fch9_Chip13

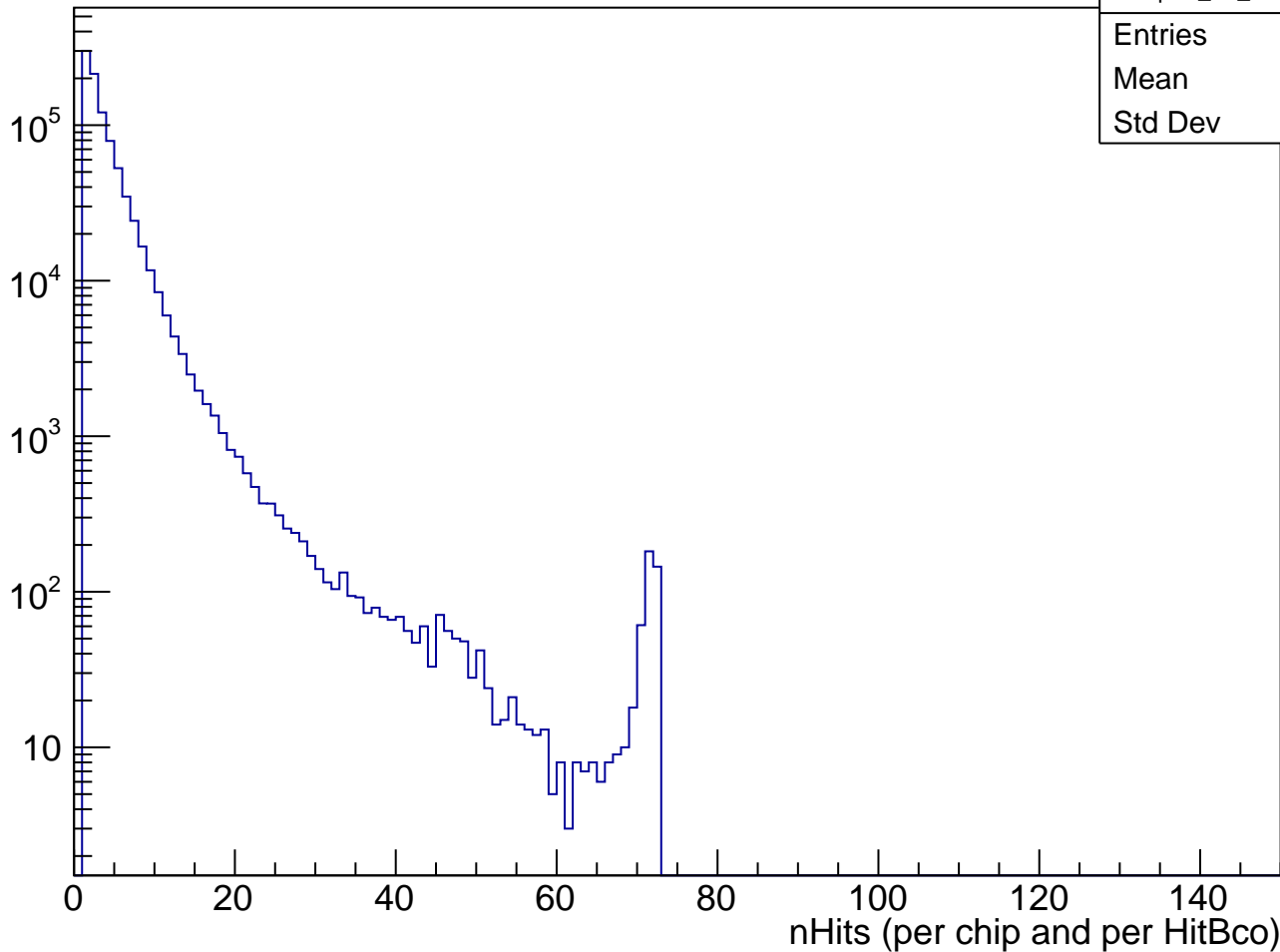
Entries



nChipHit_F5_Fch9_Chip13	
Entries	887864
Mean	3.32
Std Dev	3.814

nChipHit_F5_Fch9_Chip14

Entries

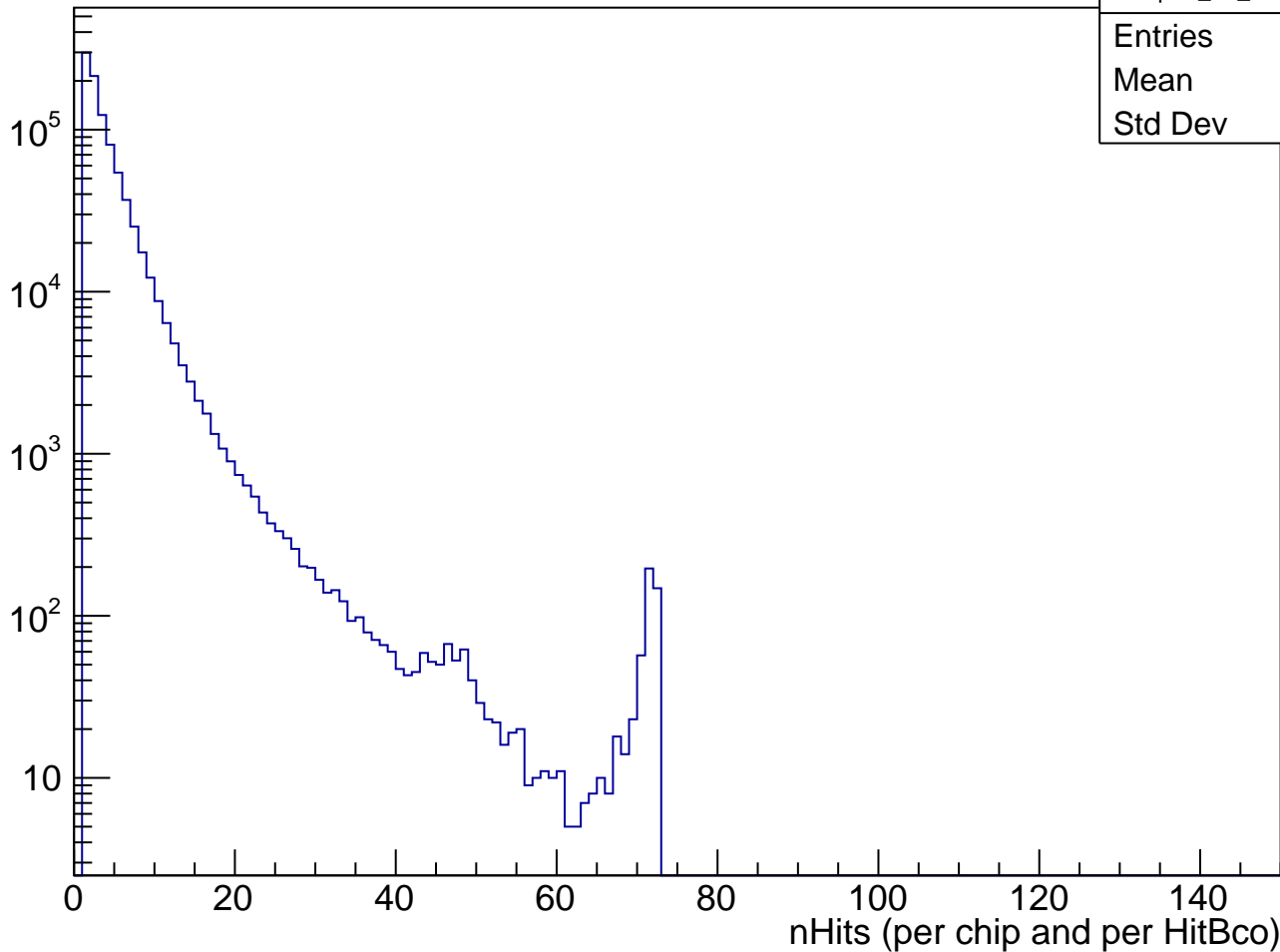


nChipHit_F5_Fch9_Chip14	
Entries	891975
Mean	3.214
Std Dev	3.697

nHits (per chip and per HitBco)

nChipHit_F5_Fch9_Chip15

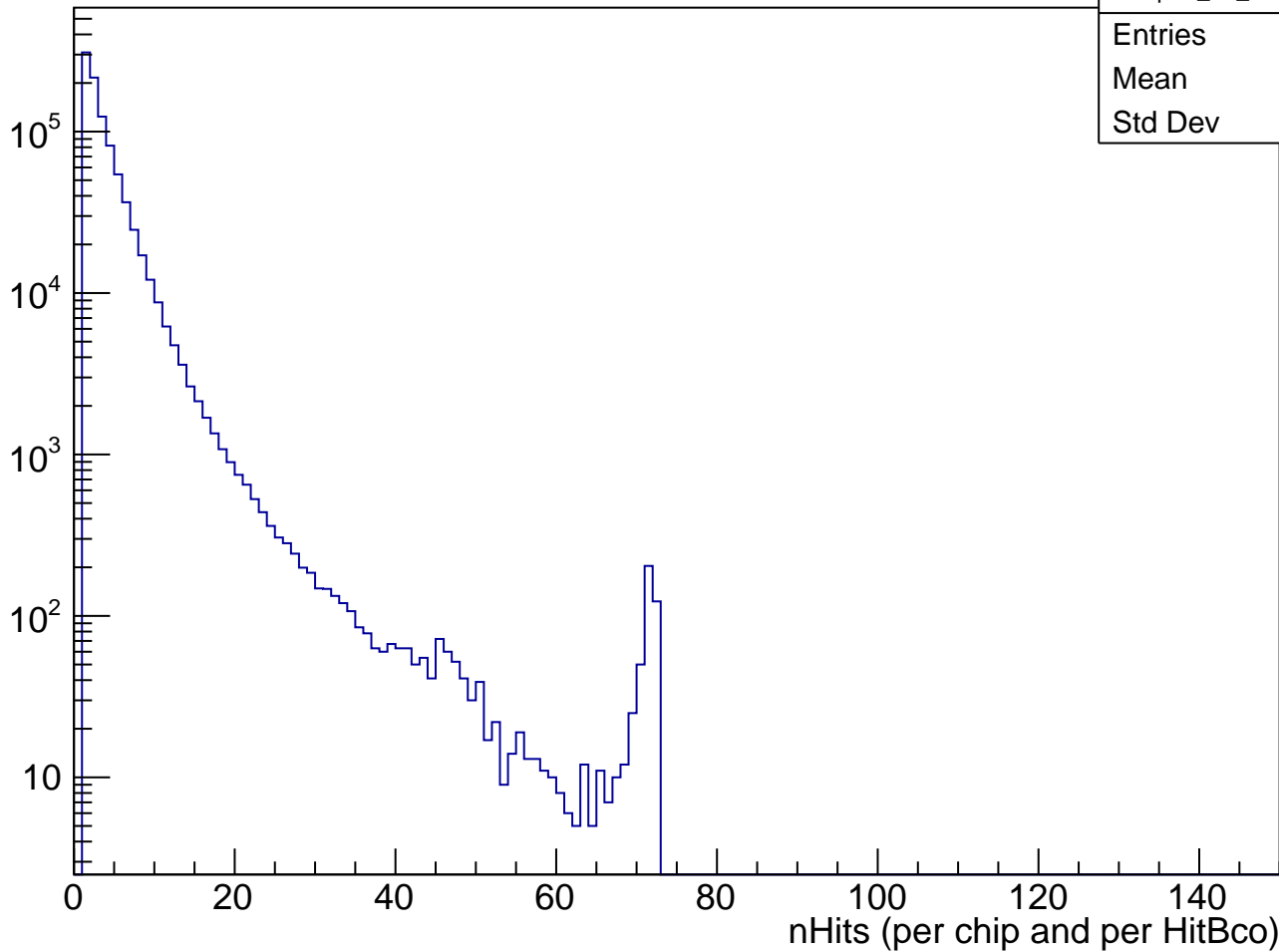
Entries



nChipHit_F5_Fch9_Chip15	
Entries	902871
Mean	3.273
Std Dev	3.765

nChipHit_F5_Fch9_Chip16

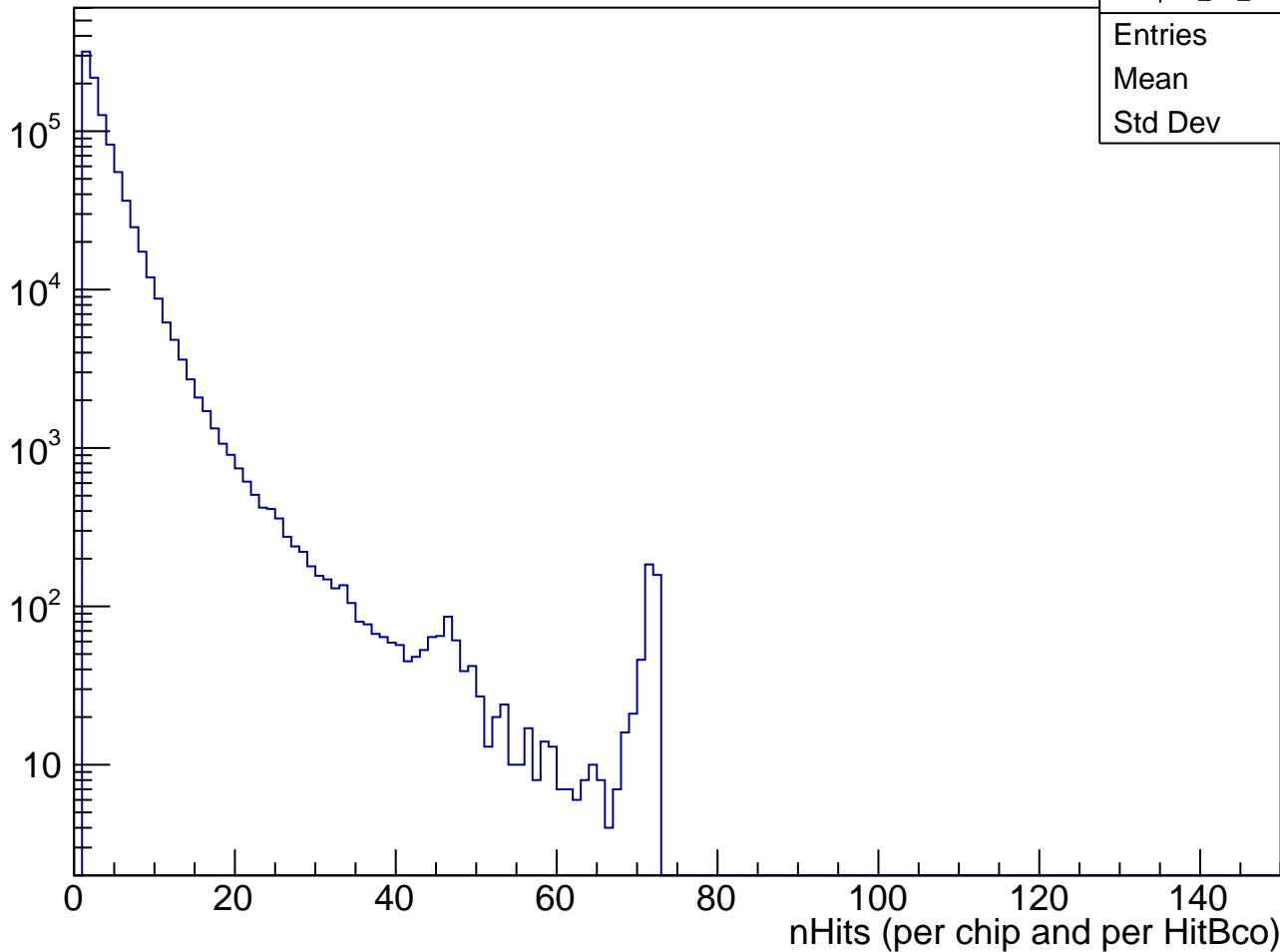
Entries



nChipHit_F5_Fch9_Chip16	
Entries	915044
Mean	3.23
Std Dev	3.707

nChipHit_F5_Fch9_Chip17

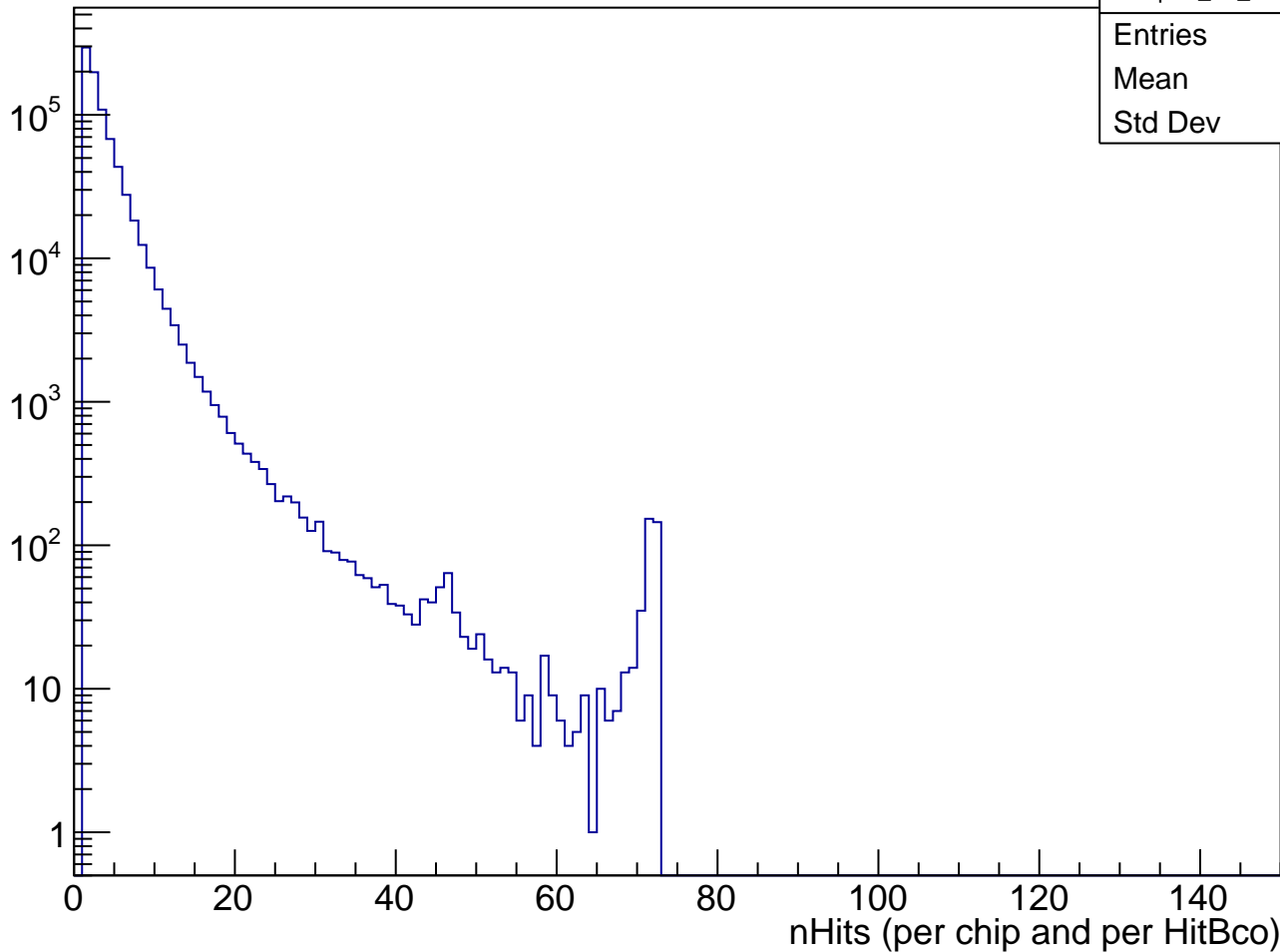
Entries



nChipHit_F5_Fch9_Chip17	
Entries	929579
Mean	3.212
Std Dev	3.702

nChipHit_F5_Fch9_Chip18

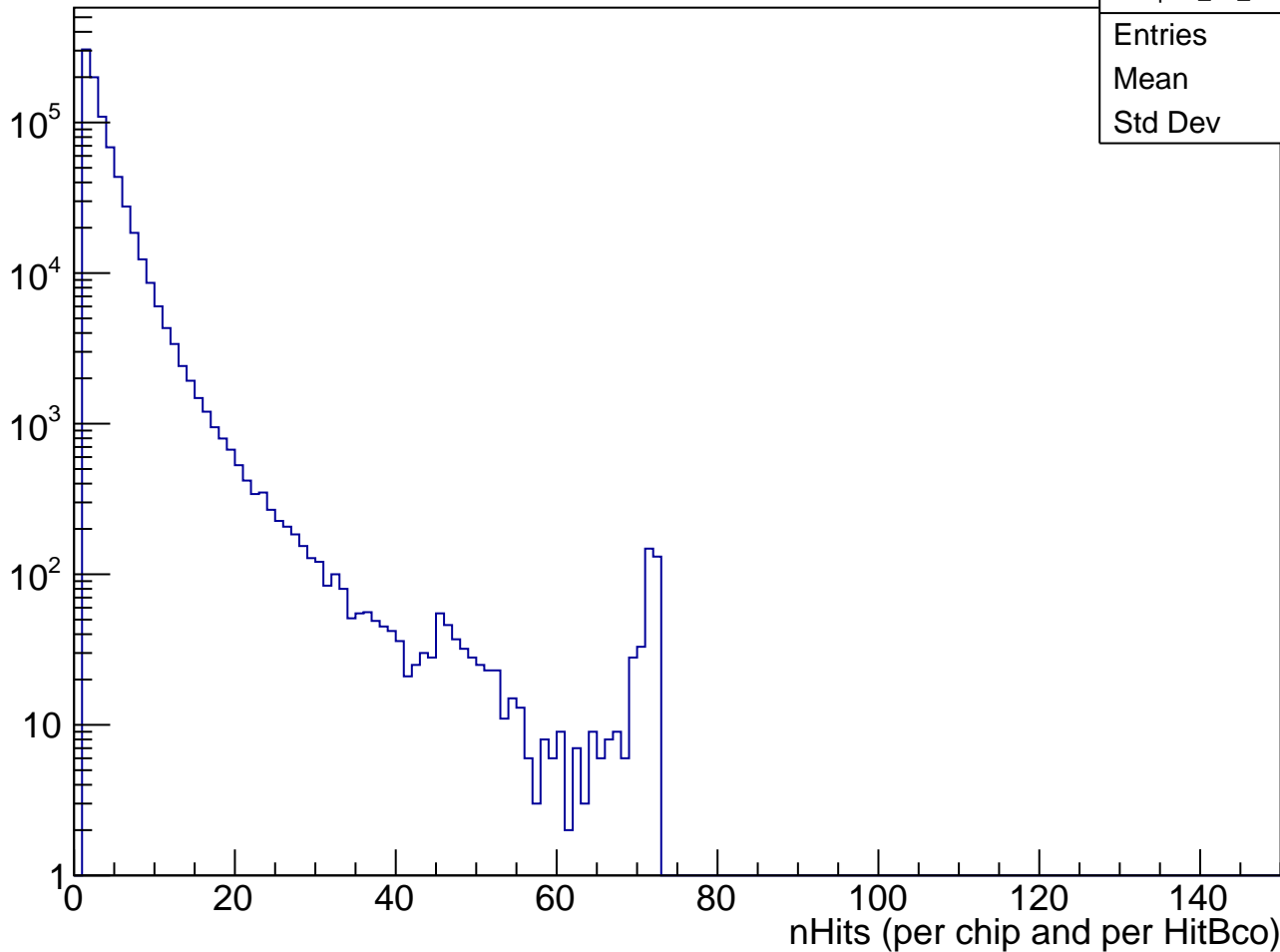
Entries



nChipHit_F5_Fch9_Chip18	
Entries	807523
Mean	2.986
Std Dev	3.474

nChipHit_F5_Fch9_Chip19

Entries

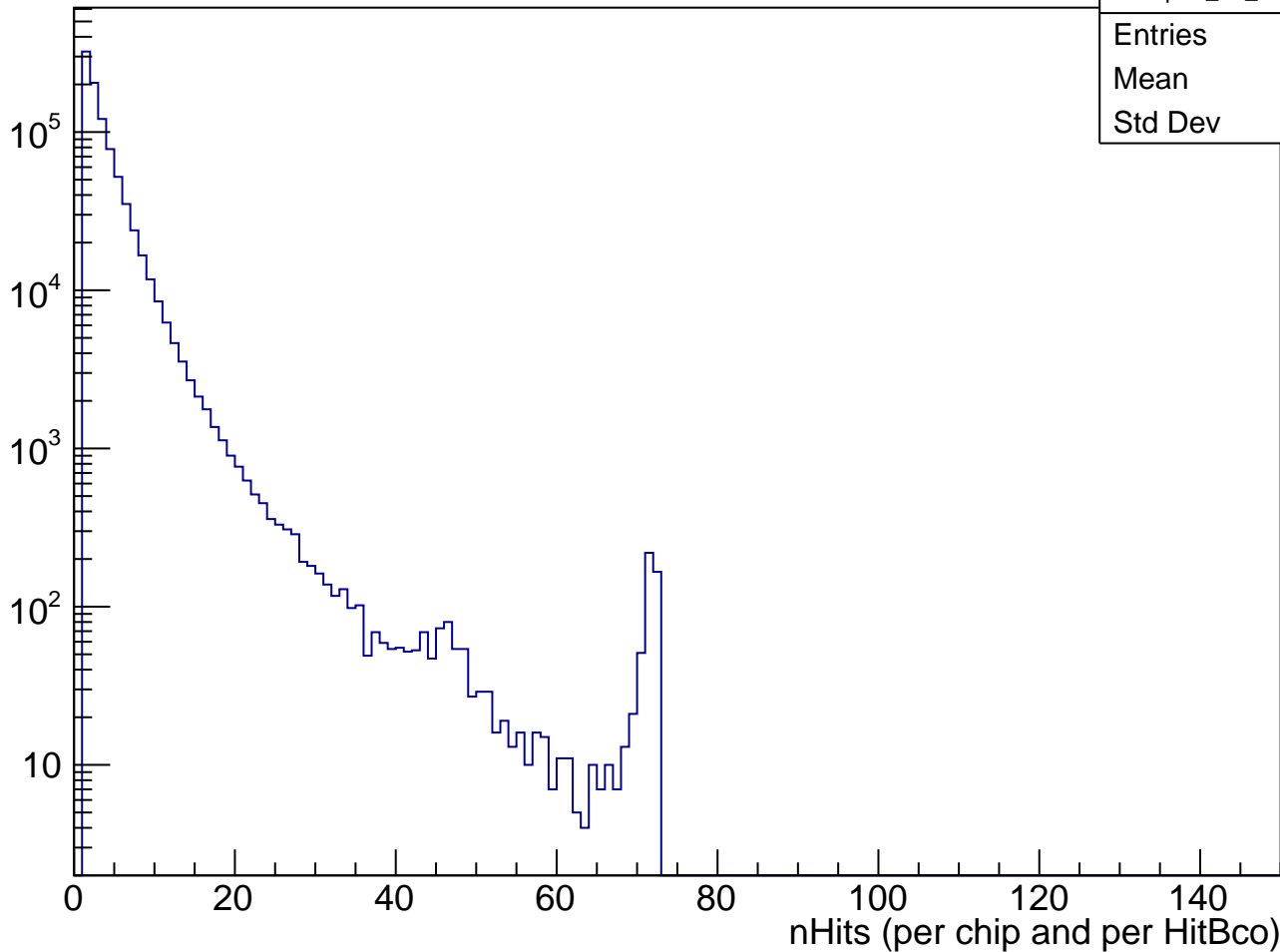


nChipHit_F5_Fch9_Chip19	
Entries	821076
Mean	2.954
Std Dev	3.427

nHits (per chip and per HitBco)

nChipHit_F5_Fch9_Chip2

Entries



nChipHit_F5_Fch9_Chip2

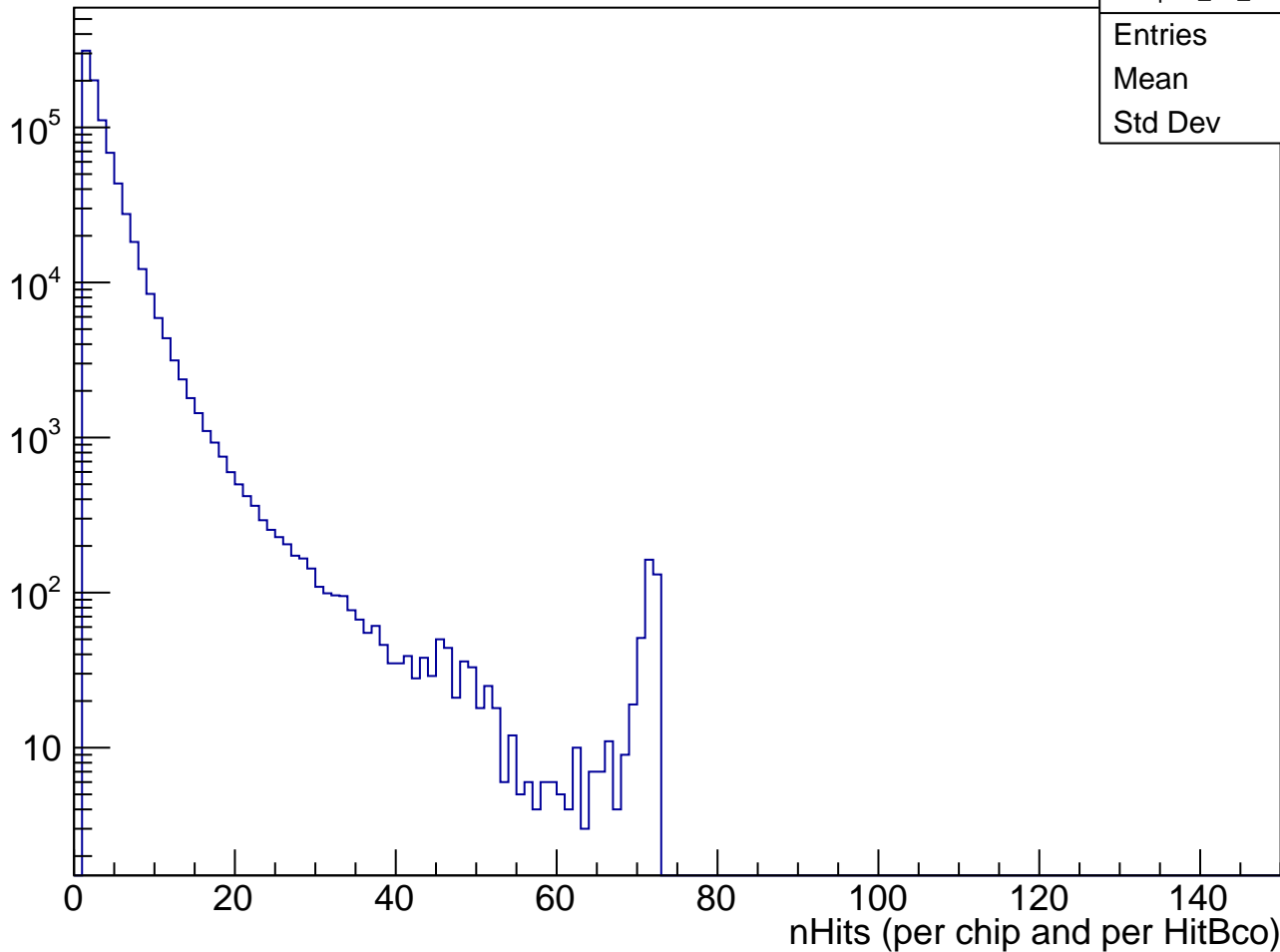
Entries 905140

Mean 3.2

Std Dev 3.779

nChipHit_F5_Fch9_Chip20

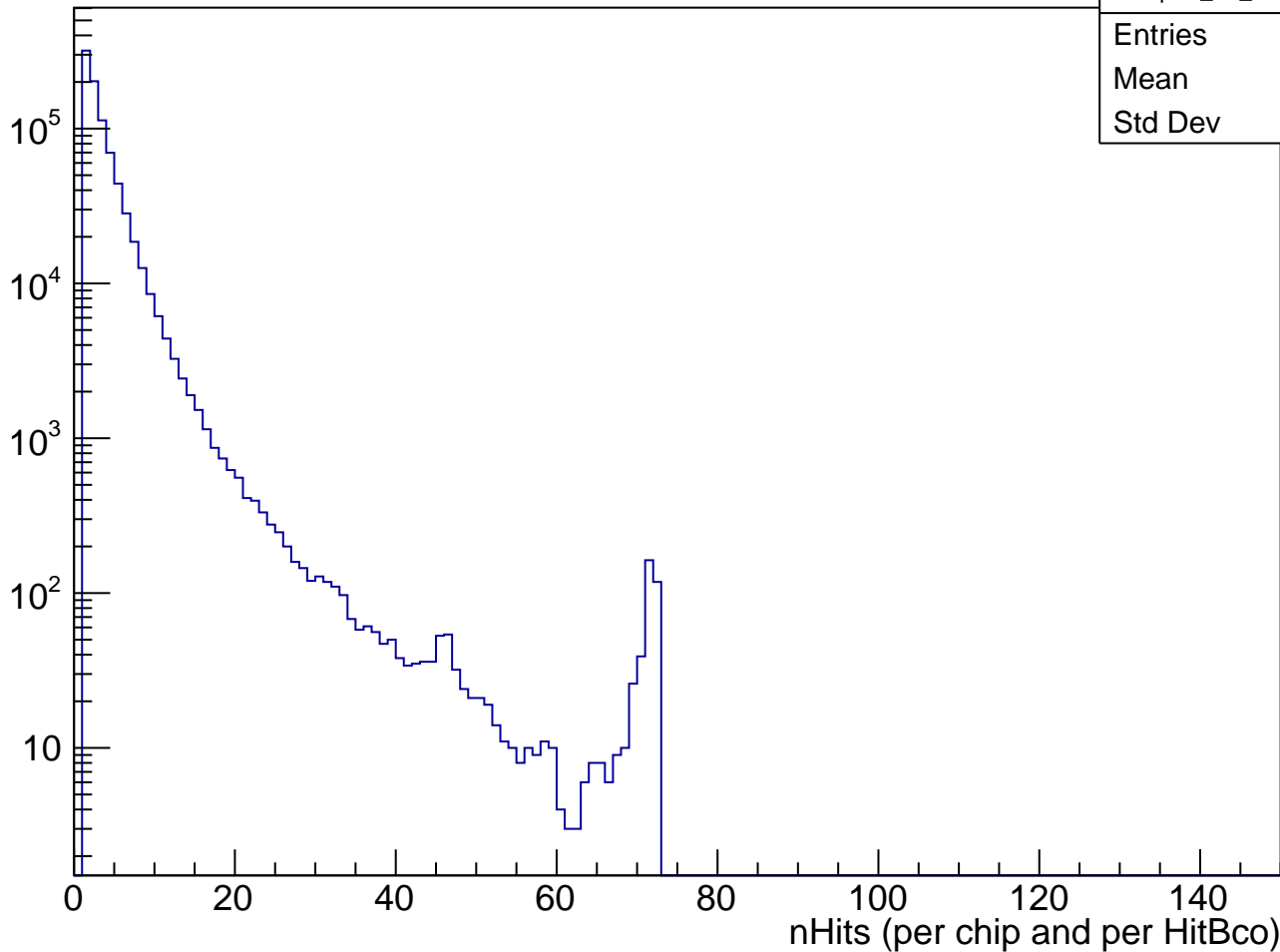
Entries



nChipHit_F5_Fch9_Chip20	
Entries	830232
Mean	2.925
Std Dev	3.411

nChipHit_F5_Fch9_Chip21

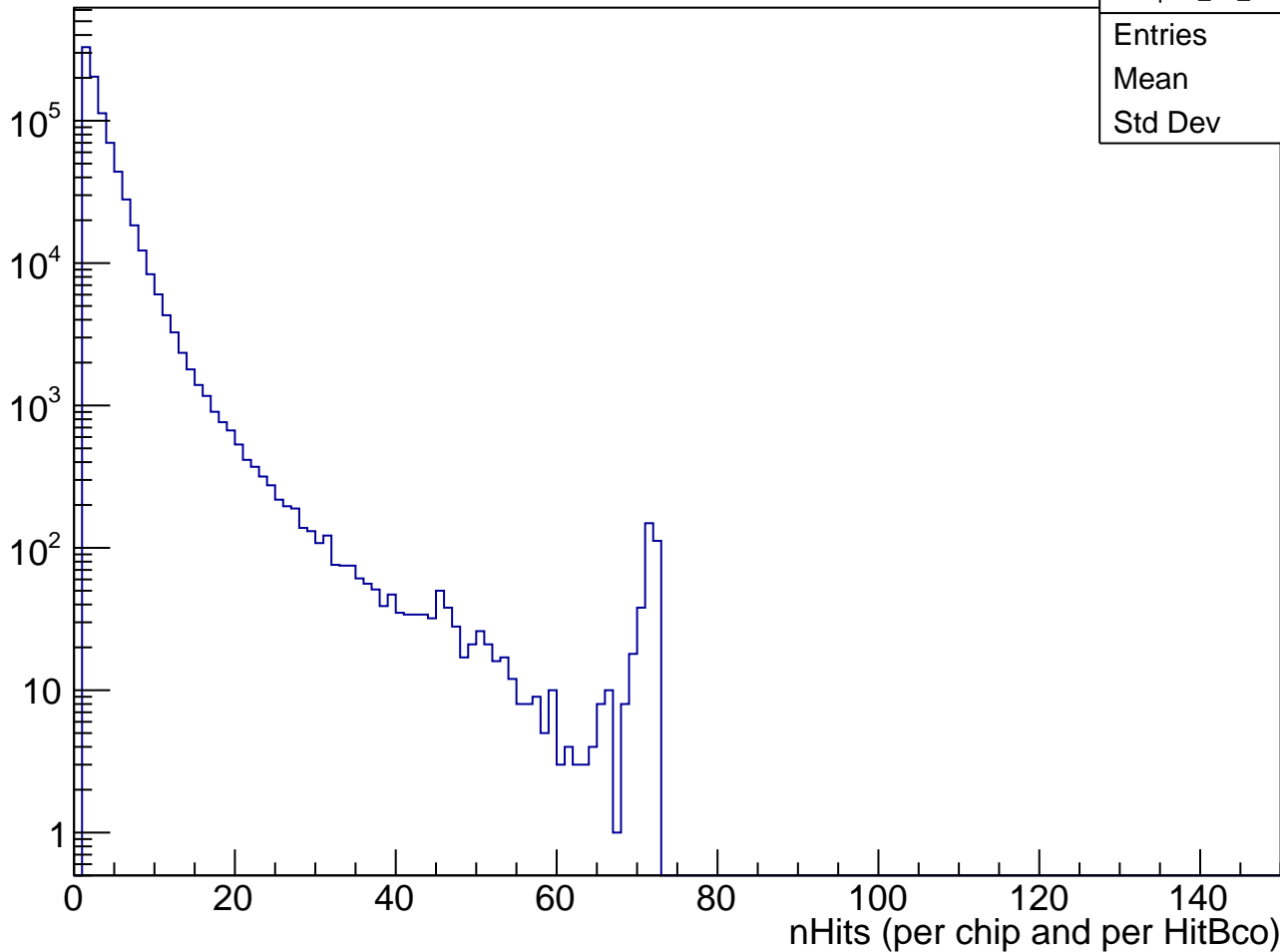
Entries



nChipHit_F5_Fch9_Chip21	
Entries	843769
Mean	2.929
Std Dev	3.409

nChipHit_F5_Fch9_Chip22

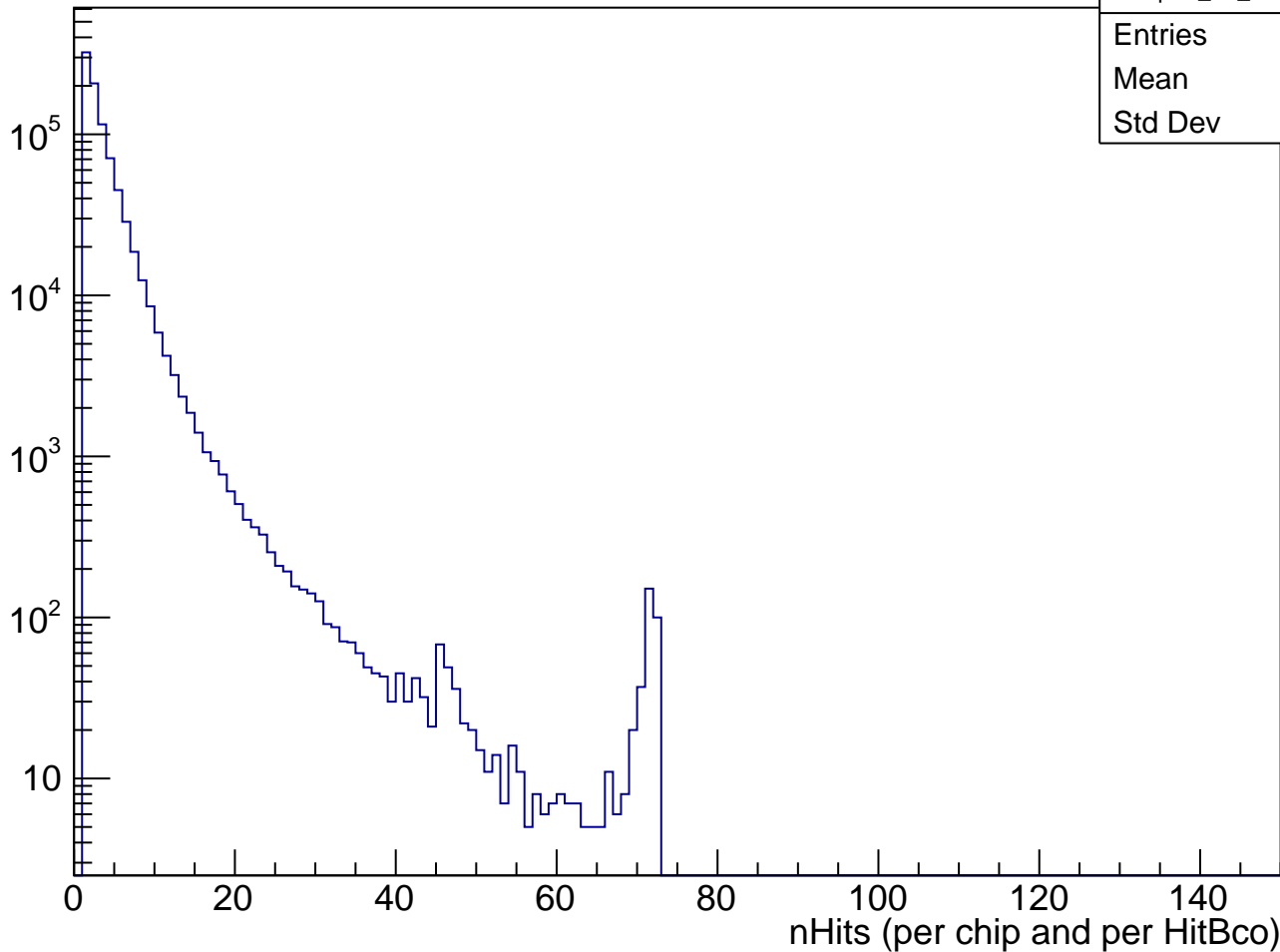
Entries



nChipHit_F5_Fch9_Chip22	
Entries	853224
Mean	2.887
Std Dev	3.331

nChipHit_F5_Fch9_Chip23

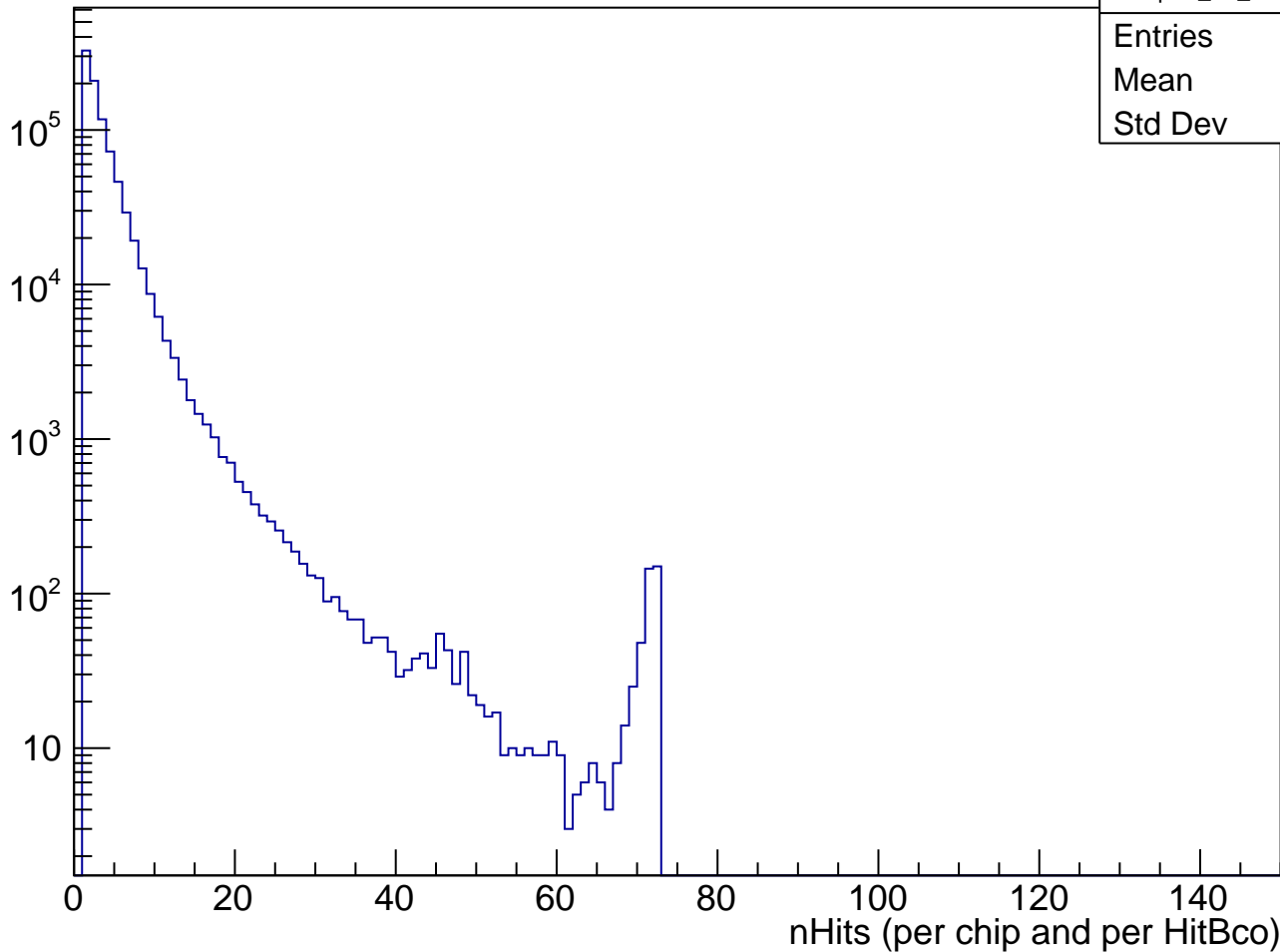
Entries



nChipHit_F5_Fch9_Chip23	
Entries	856082
Mean	2.901
Std Dev	3.311

nChipHit_F5_Fch9_Chip24

Entries

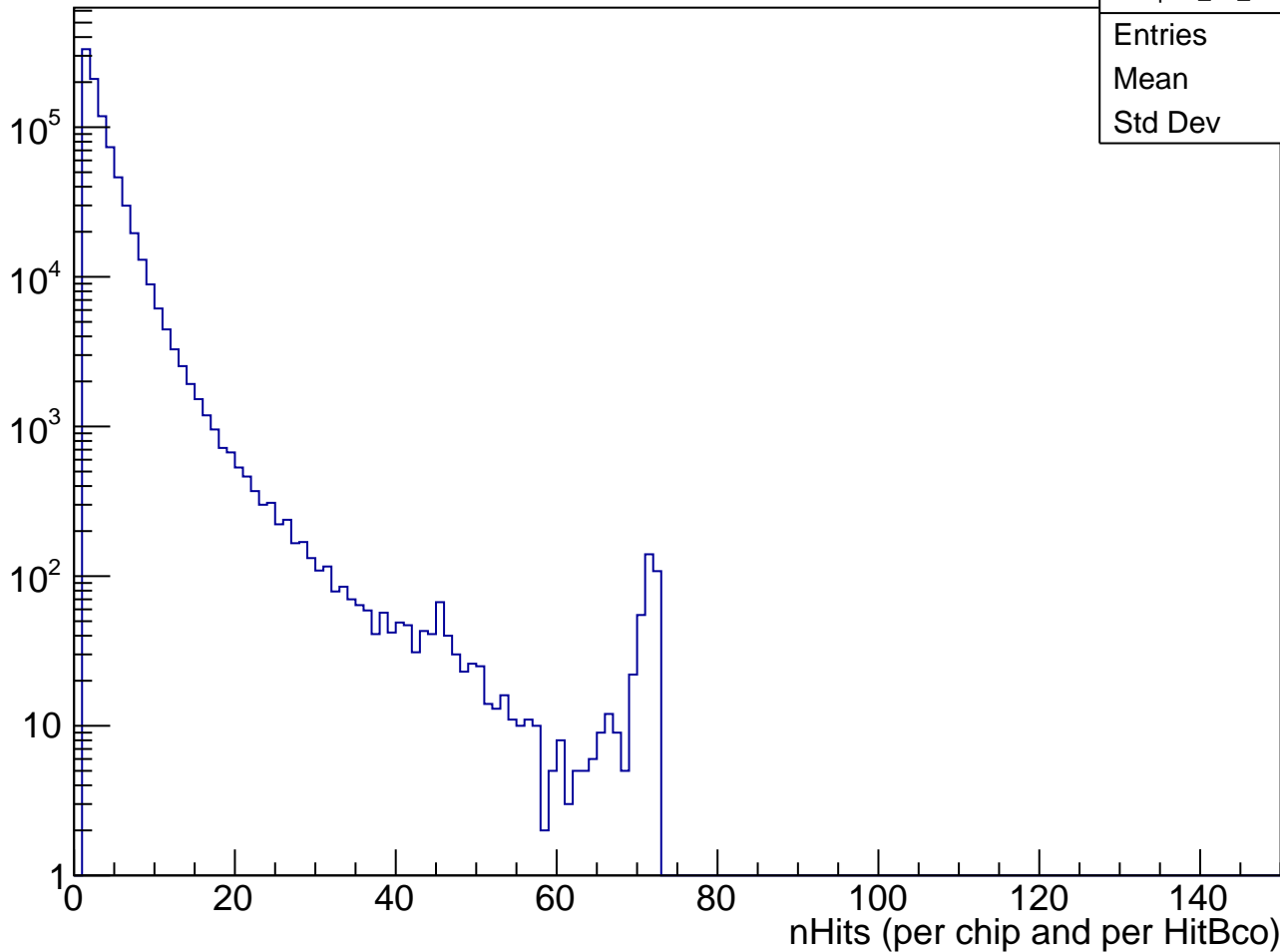


nChipHit_F5_Fch9_Chip24	
Entries	868621
Mean	2.928
Std Dev	3.399

nHits (per chip and per HitBco)

nChipHit_F5_Fch9_Chip25

Entries

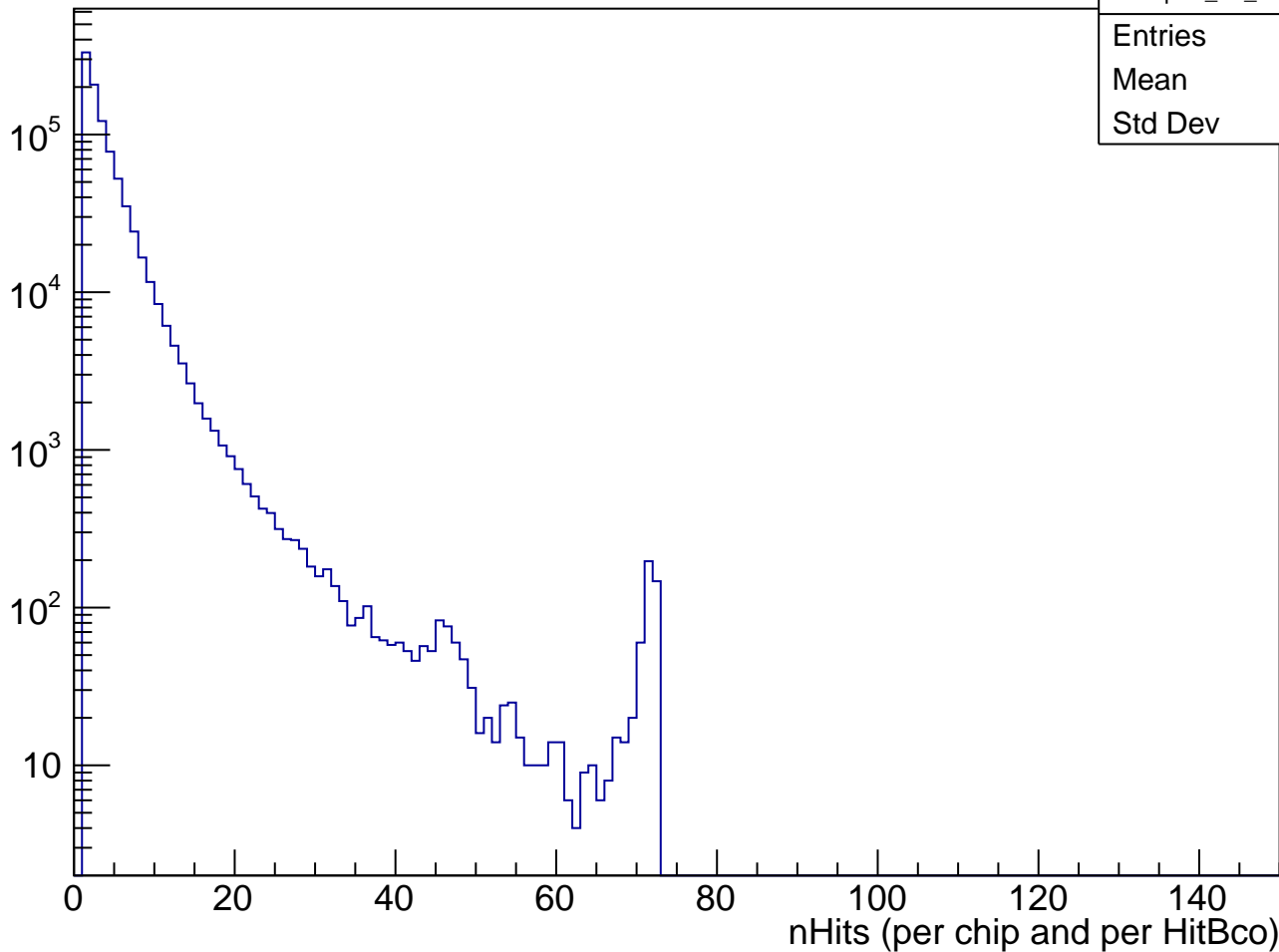


nChipHit_F5_Fch9_Chip25	
Entries	879649
Mean	2.921
Std Dev	3.355

nHits (per chip and per HitBco)

nChipHit_F5_Fch9_Chip3

Entries



nChipHit_F5_Fch9_Chip3

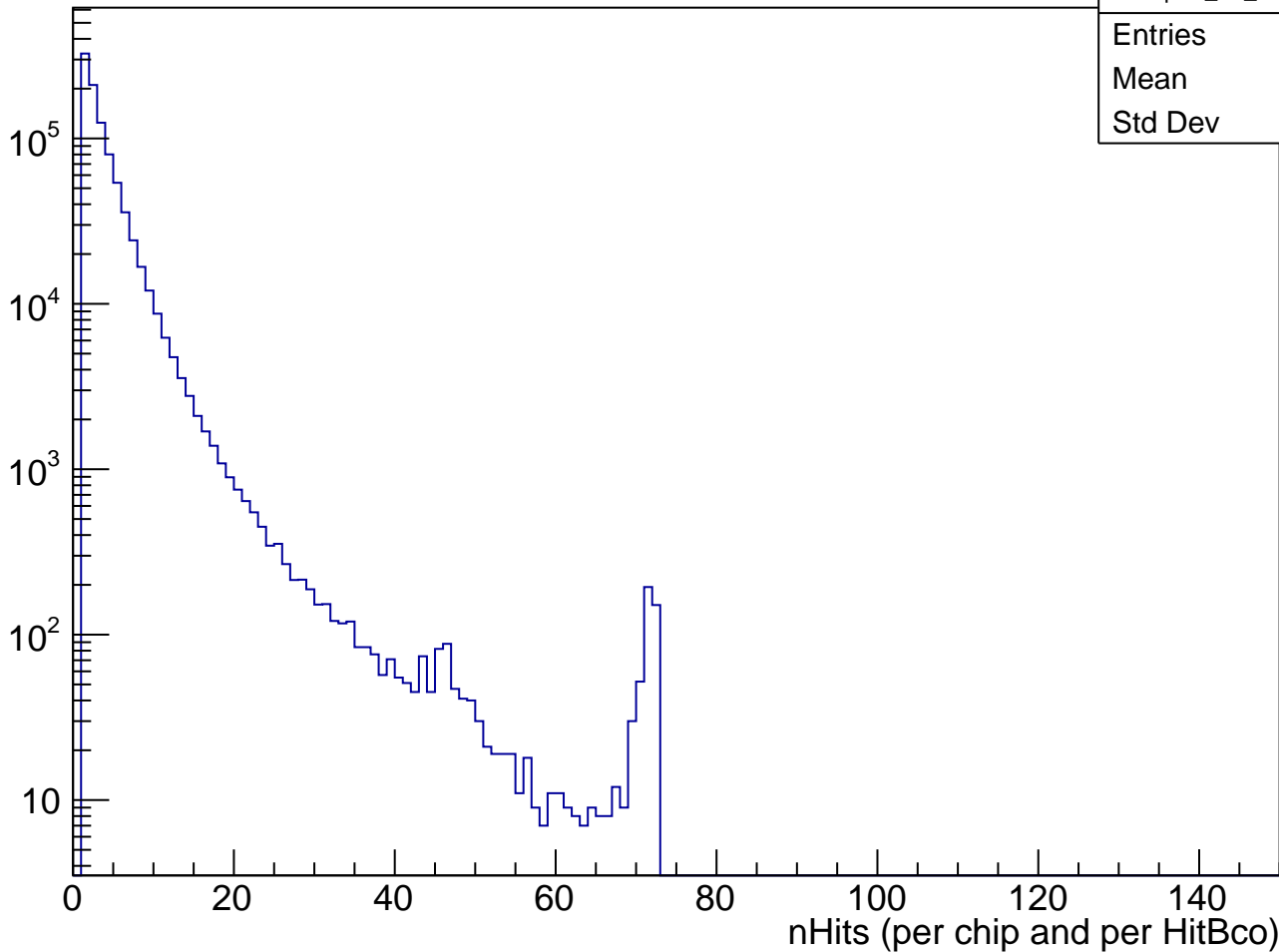
Entries 916904

Mean 3.165

Std Dev 3.732

nChipHit_F5_Fch9_Chip4

Entries

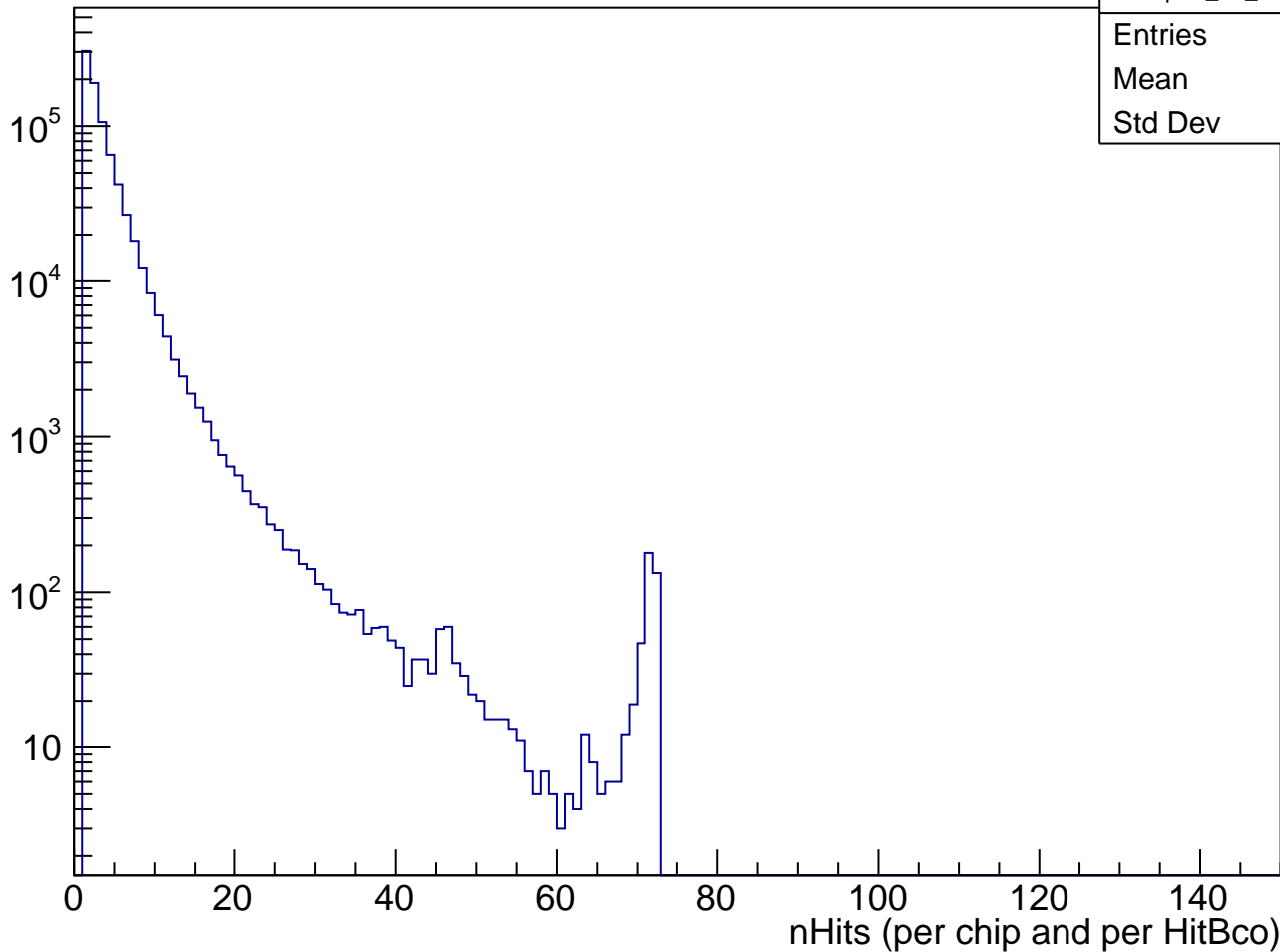


nChipHit_F5_Fch9_Chip4	
Entries	923102
Mean	3.195
Std Dev	3.736

nHits (per chip and per HitBco)

nChipHit_F5_Fch9_Chip5

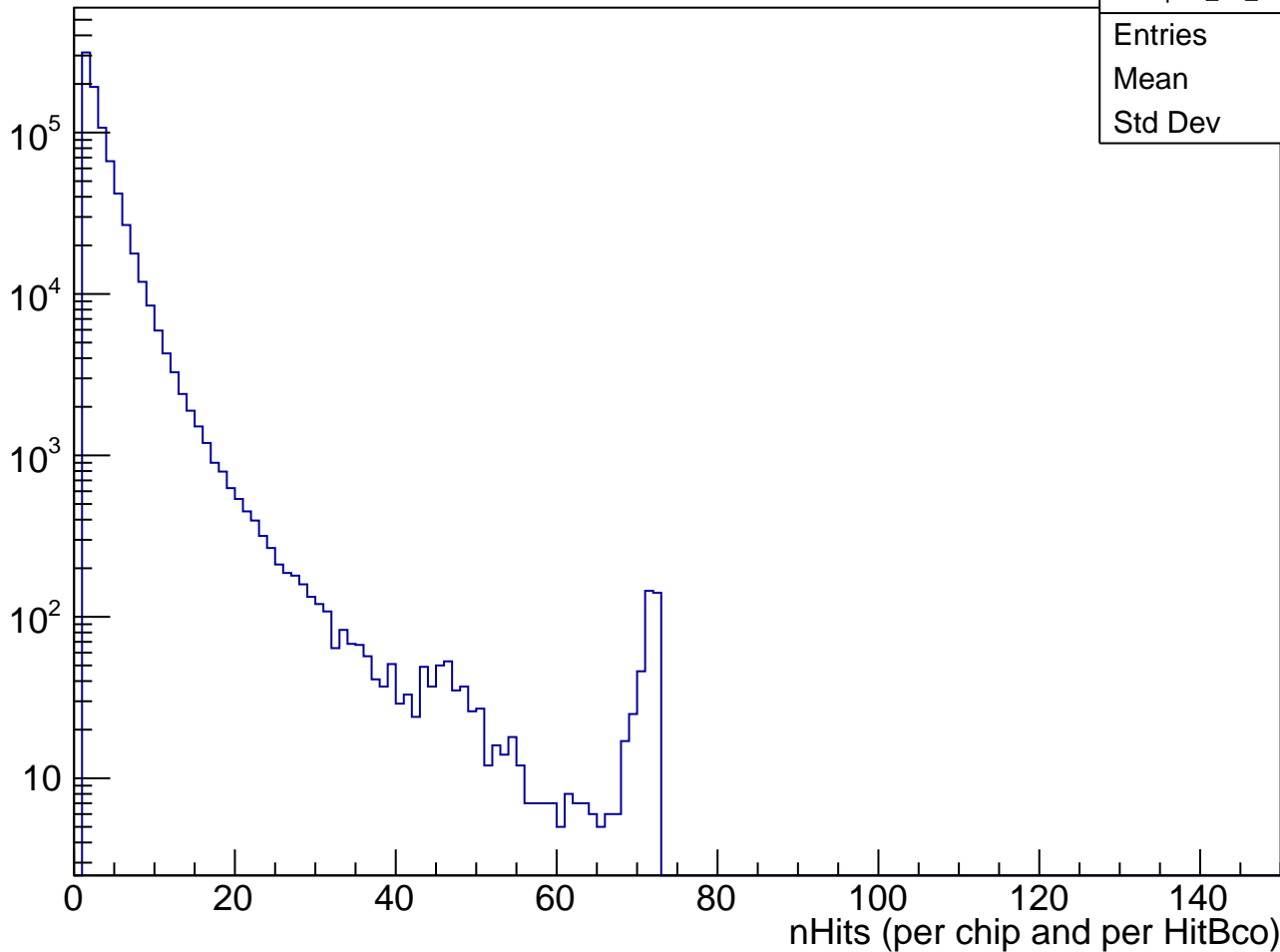
Entries



nChipHit_F5_Fch9_Chip5	
Entries	800563
Mean	2.96
Std Dev	3.514

nChipHit_F5_Fch9_Chip6

Entries



nChipHit_F5_Fch9_Chip6

Entries 812963

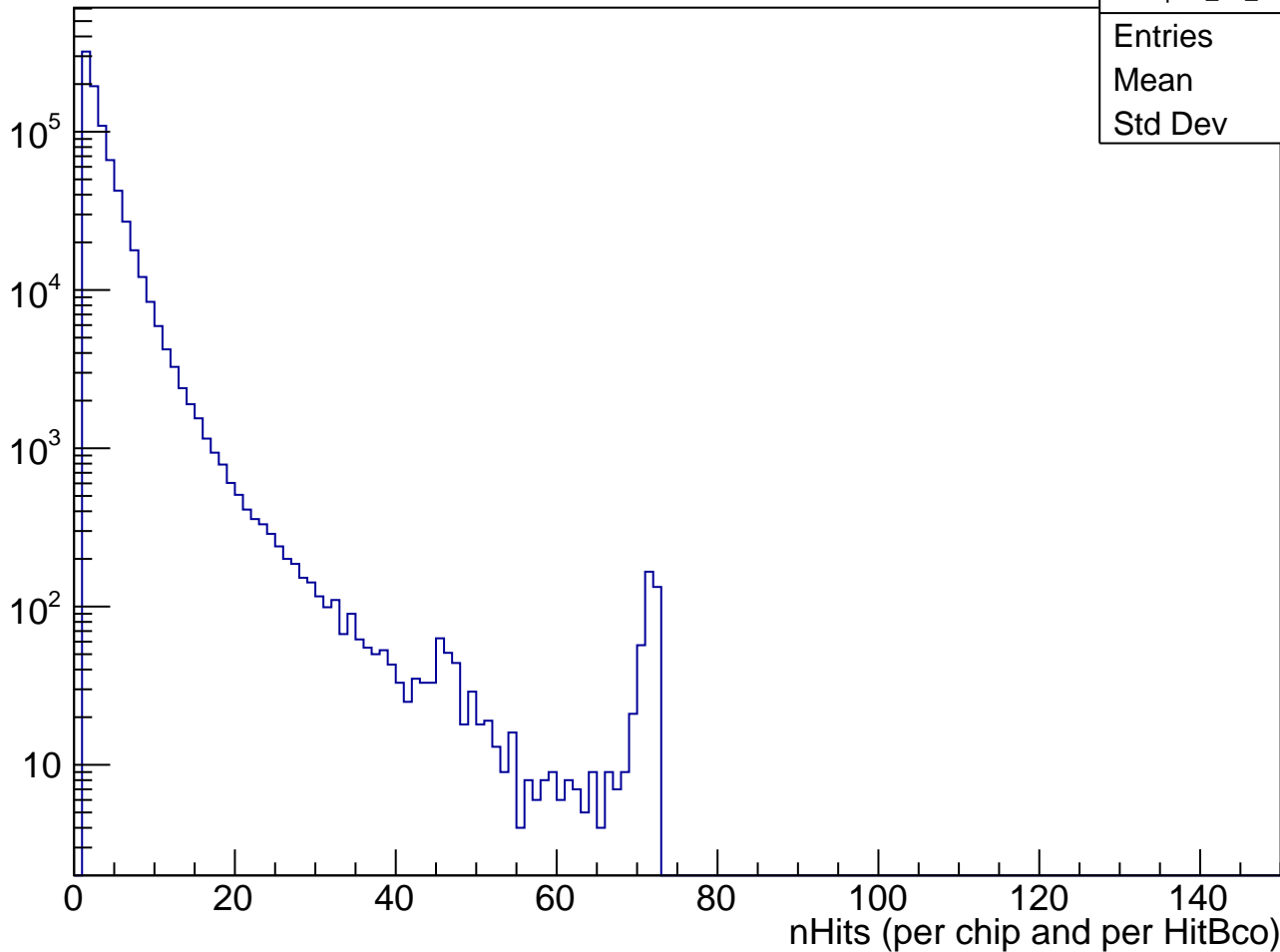
Mean 2.927

Std Dev 3.465

nHits (per chip and per HitBco)

nChipHit_F5_Fch9_Chip7

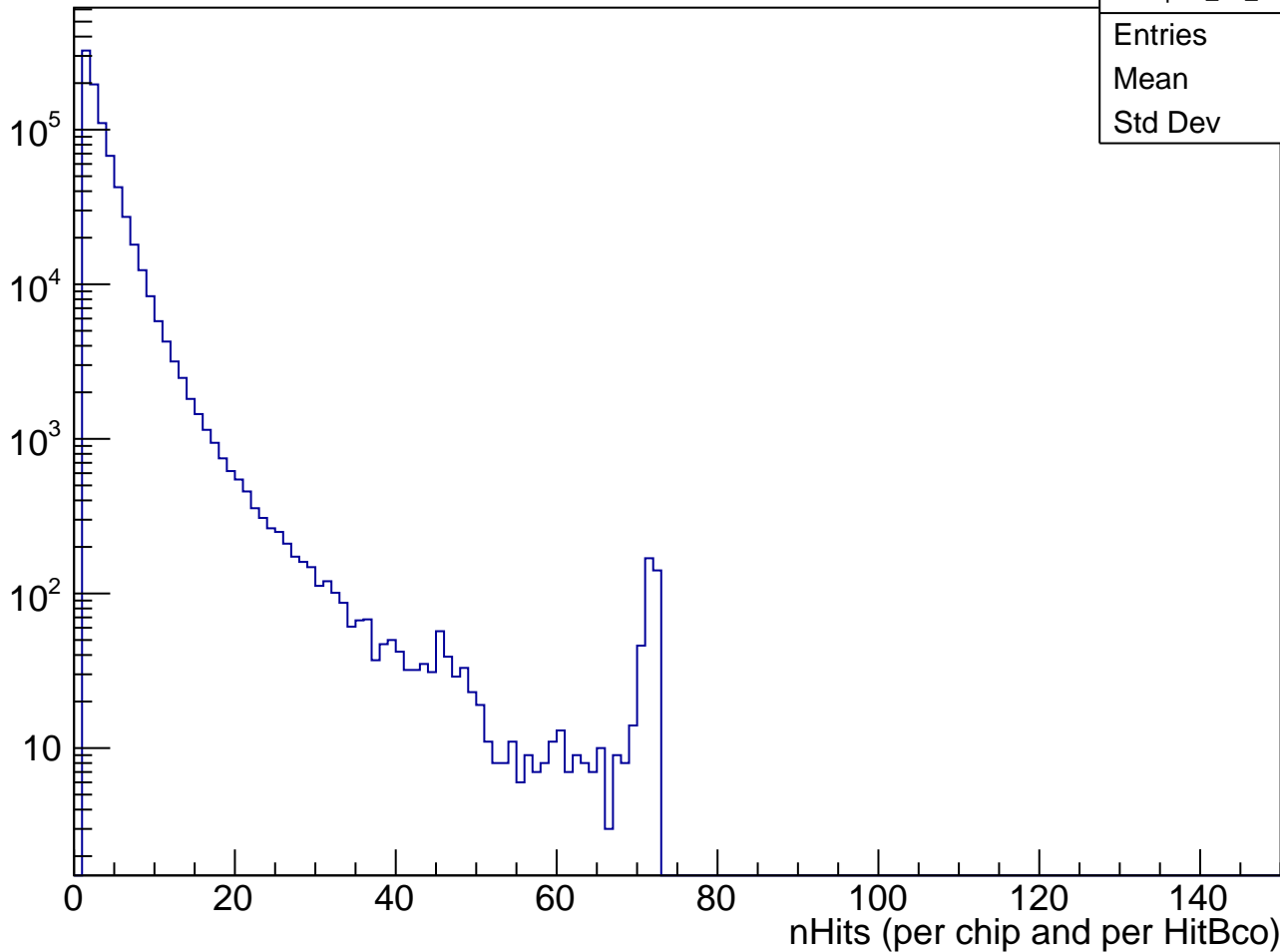
Entries



nChipHit_F5_Fch9_Chip7	
Entries	825091
Mean	2.911
Std Dev	3.457

nChipHit_F5_Fch9_Chip8

Entries



nChipHit_F5_Fch9_Chip8

Entries 834437

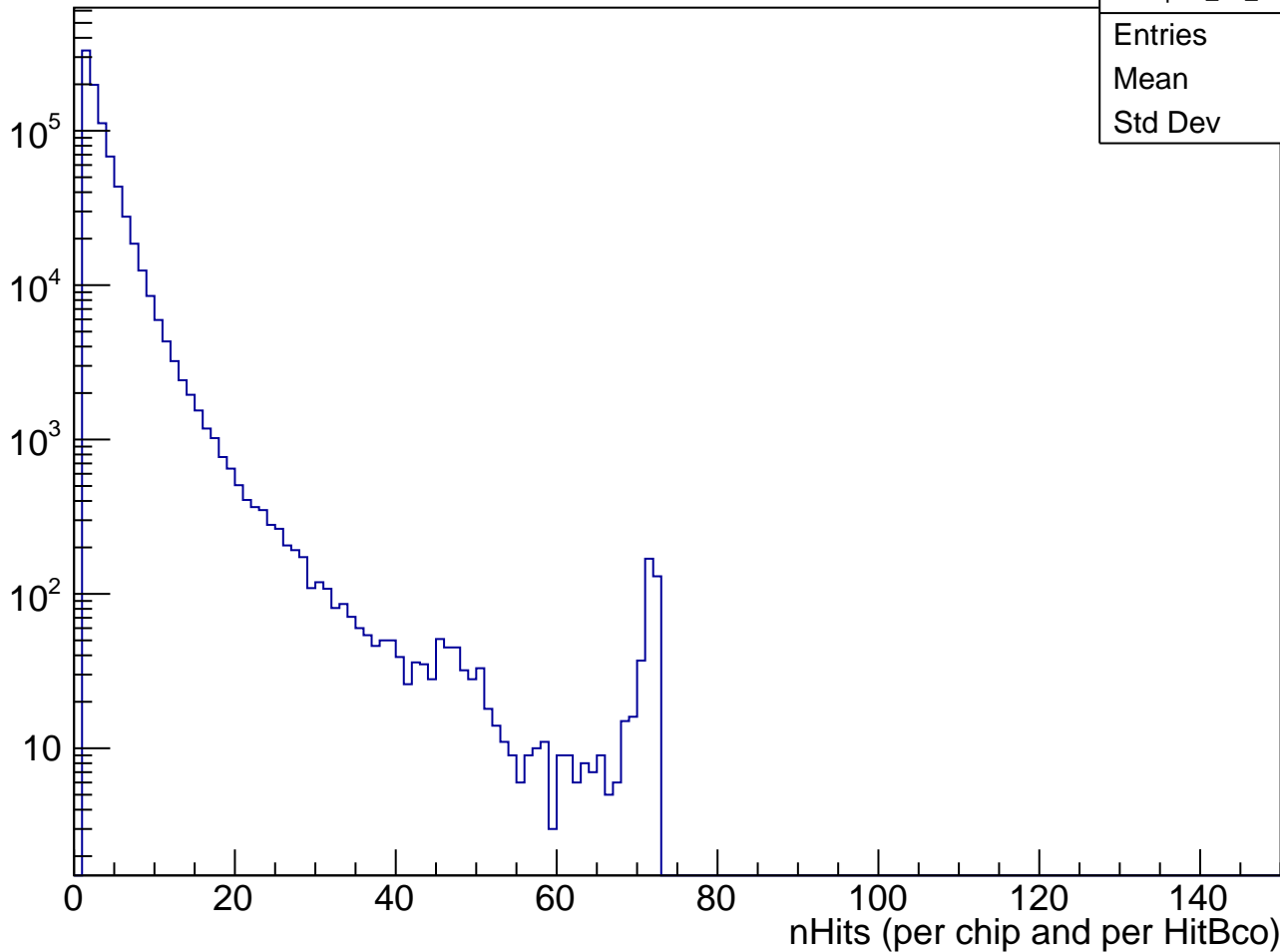
Mean 2.901

Std Dev 3.432

nHits (per chip and per HitBco)

nChipHit_F5_Fch9_Chip9

Entries



nChipHit_F5_Fch9_Chip9

Entries 847955

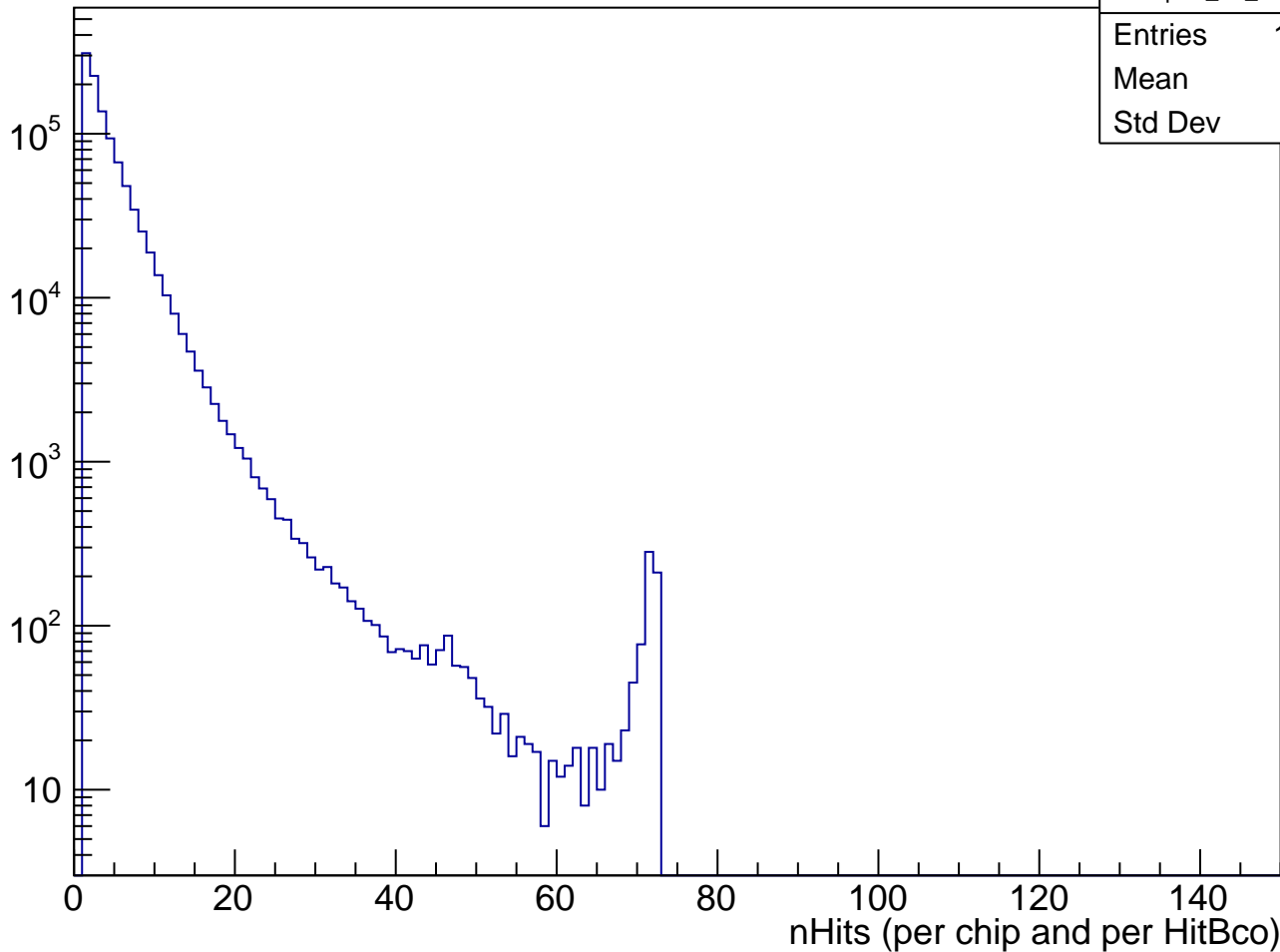
Mean 2.901

Std Dev 3.423

nHits (per chip and per HitBco)

nChipHit_F6_Fch0_Chip0

Entries



nChipHit_F6_Fch0_Chip0

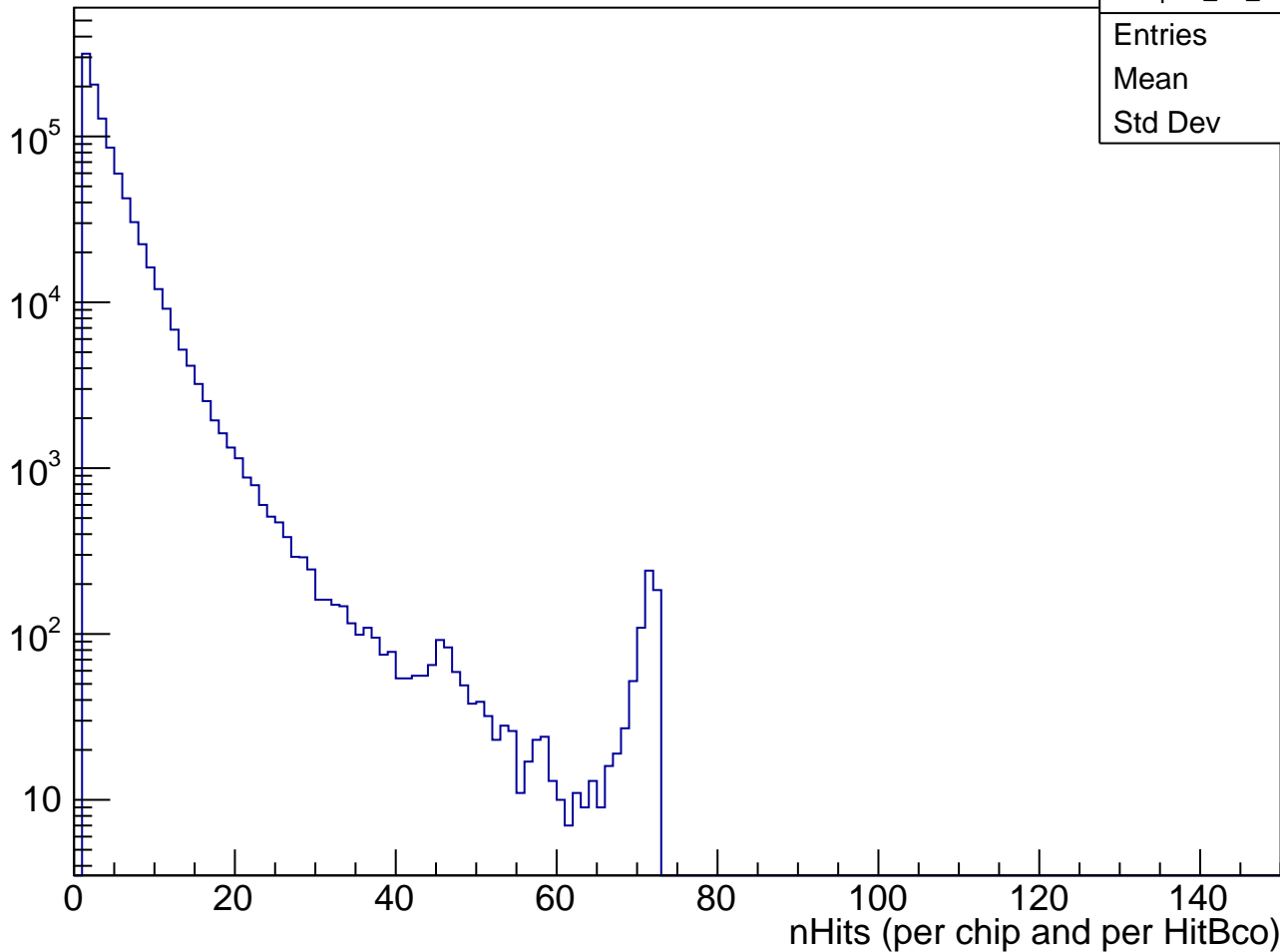
Entries 1023311

Mean 3.668

Std Dev 4.188

nChipHit_F6_Fch0_Chip1

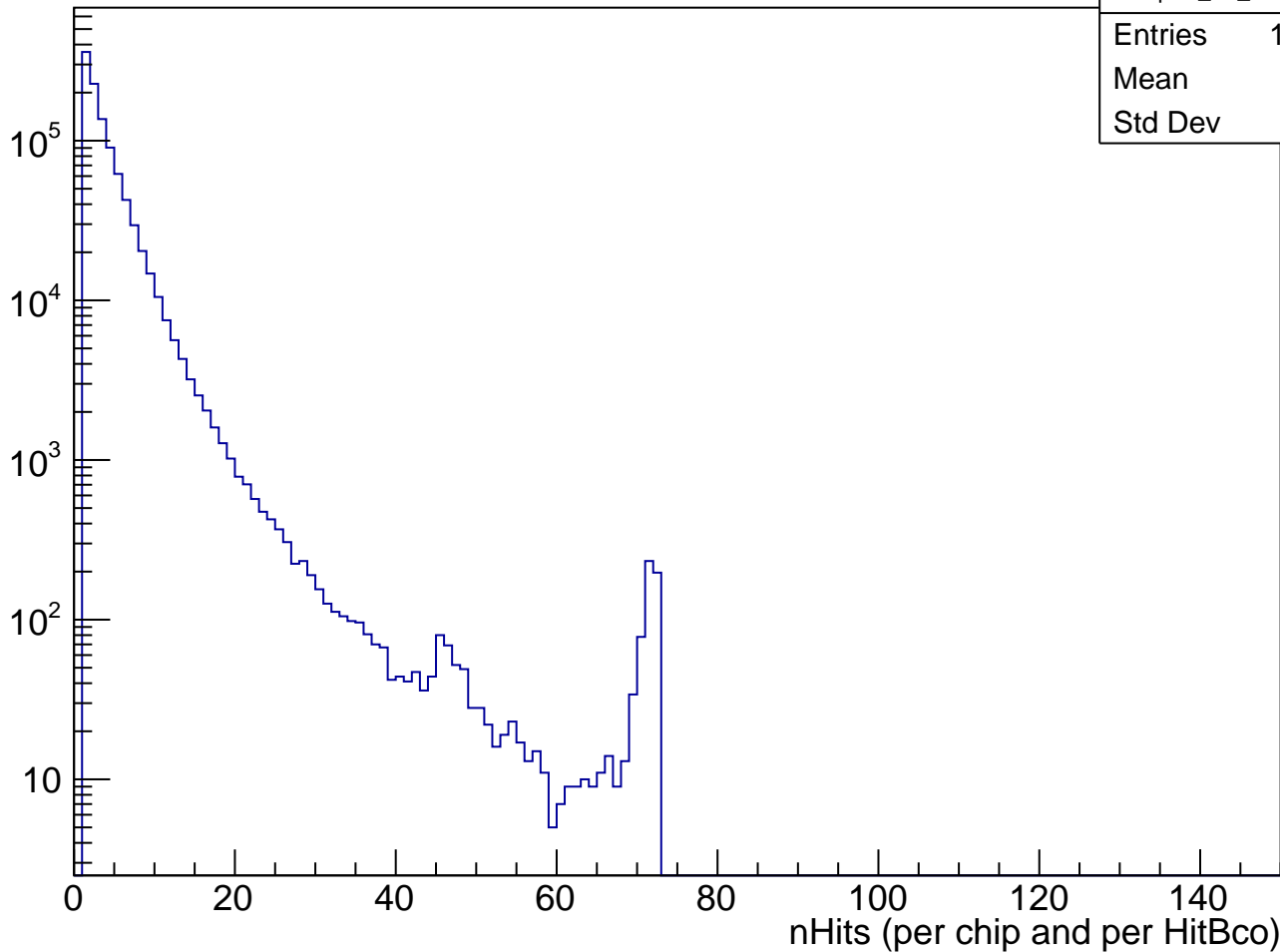
Entries



nChipHit_F6_Fch0_Chip1	
Entries	962184
Mean	3.542
Std Dev	4.144

nChipHit_F6_Fch0_Chip10

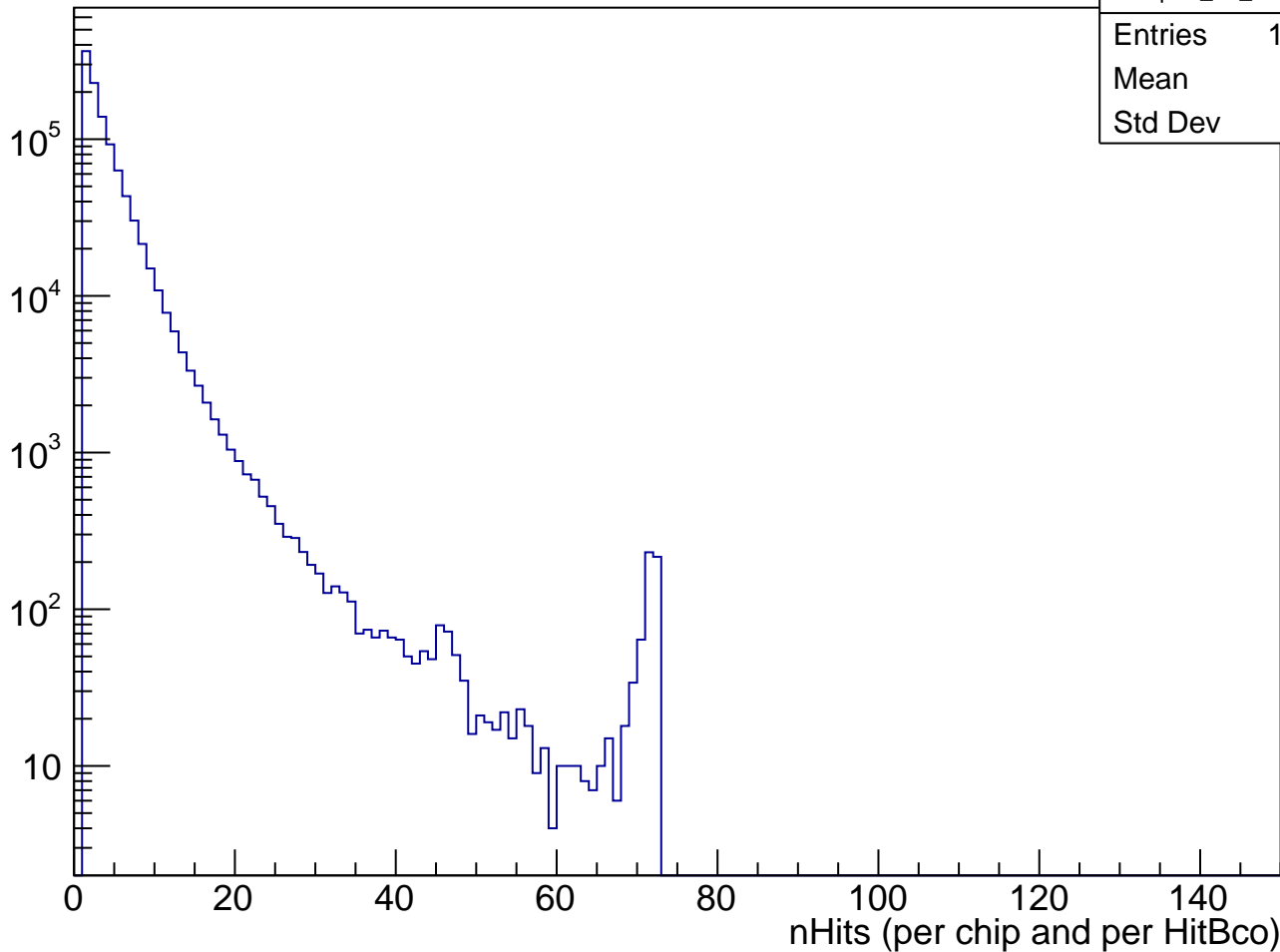
Entries



nChipHit_F6_Fch0_Chip10	
Entries	1028823
Mean	3.261
Std Dev	3.753

nChipHit_F6_Fch0_Chip11

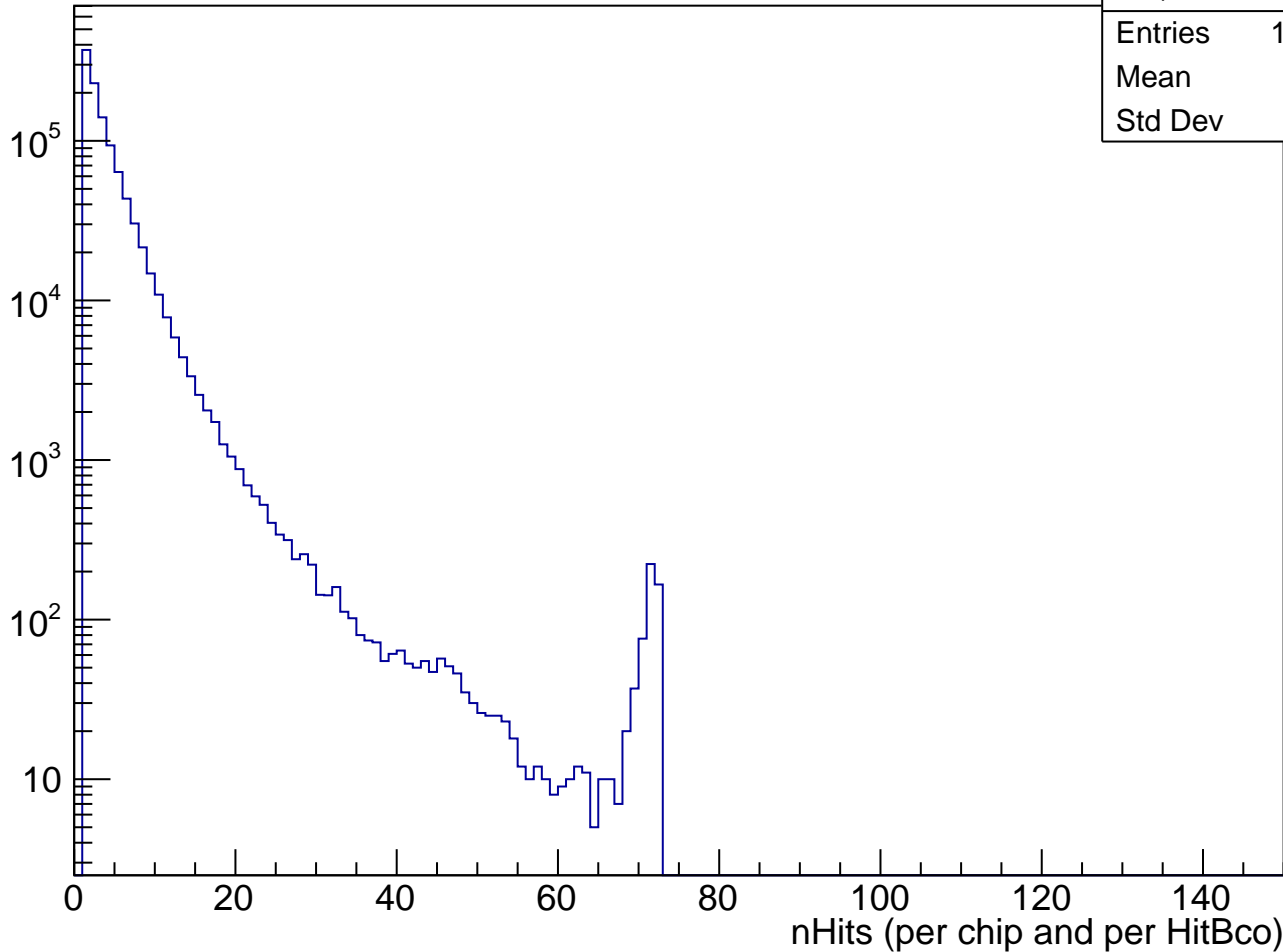
Entries



nChipHit_F6_Fch0_Chip11	
Entries	1045986
Mean	3.282
Std Dev	3.775

nChipHit_F6_Fch0_Chip12

Entries

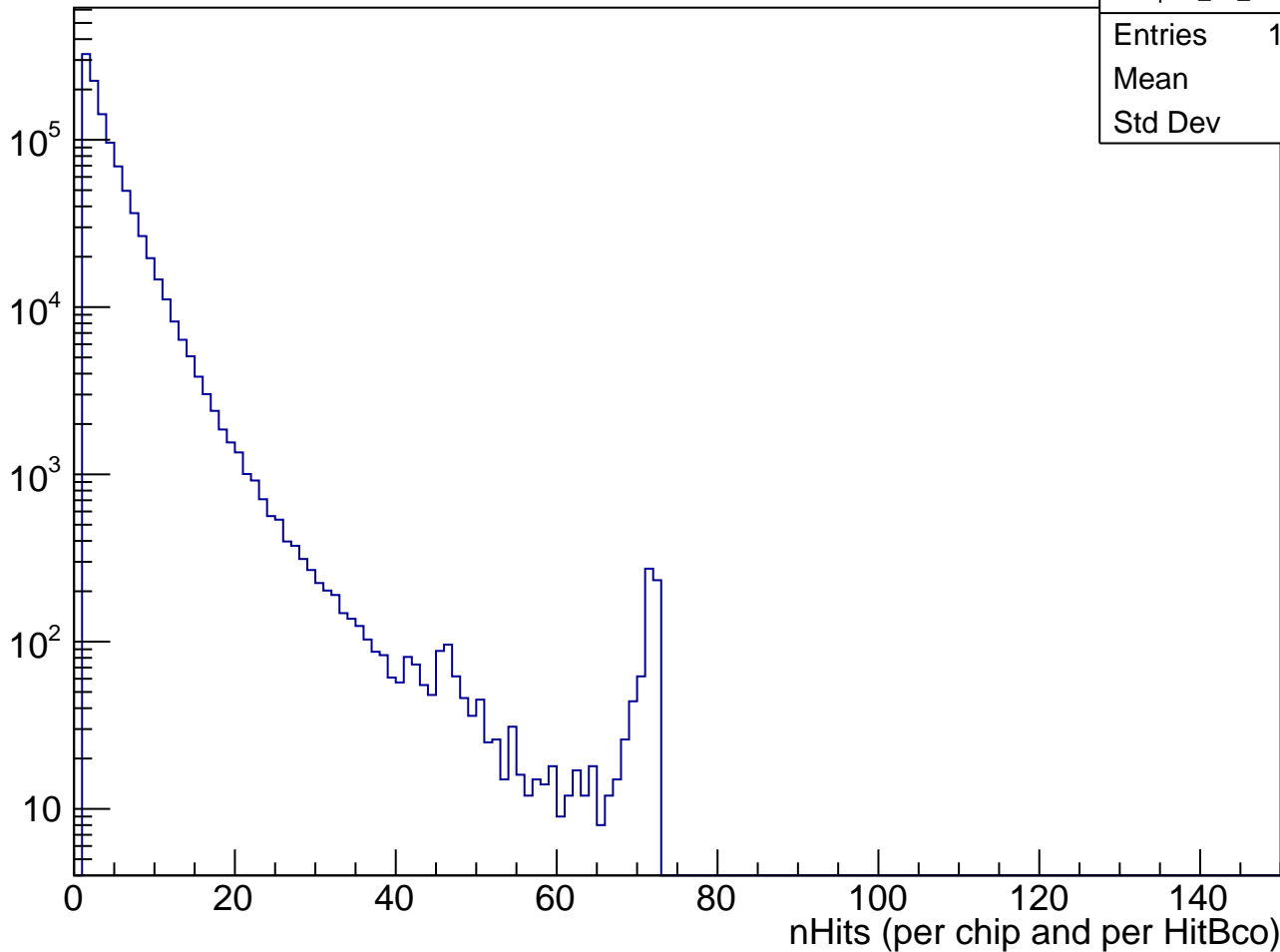


nChipHit_F6_Fch0_Chip12	
Entries	1056324
Mean	3.261
Std Dev	3.721

nHits (per chip and per HitBco)

nChipHit_F6_Fch0_Chip13

Entries

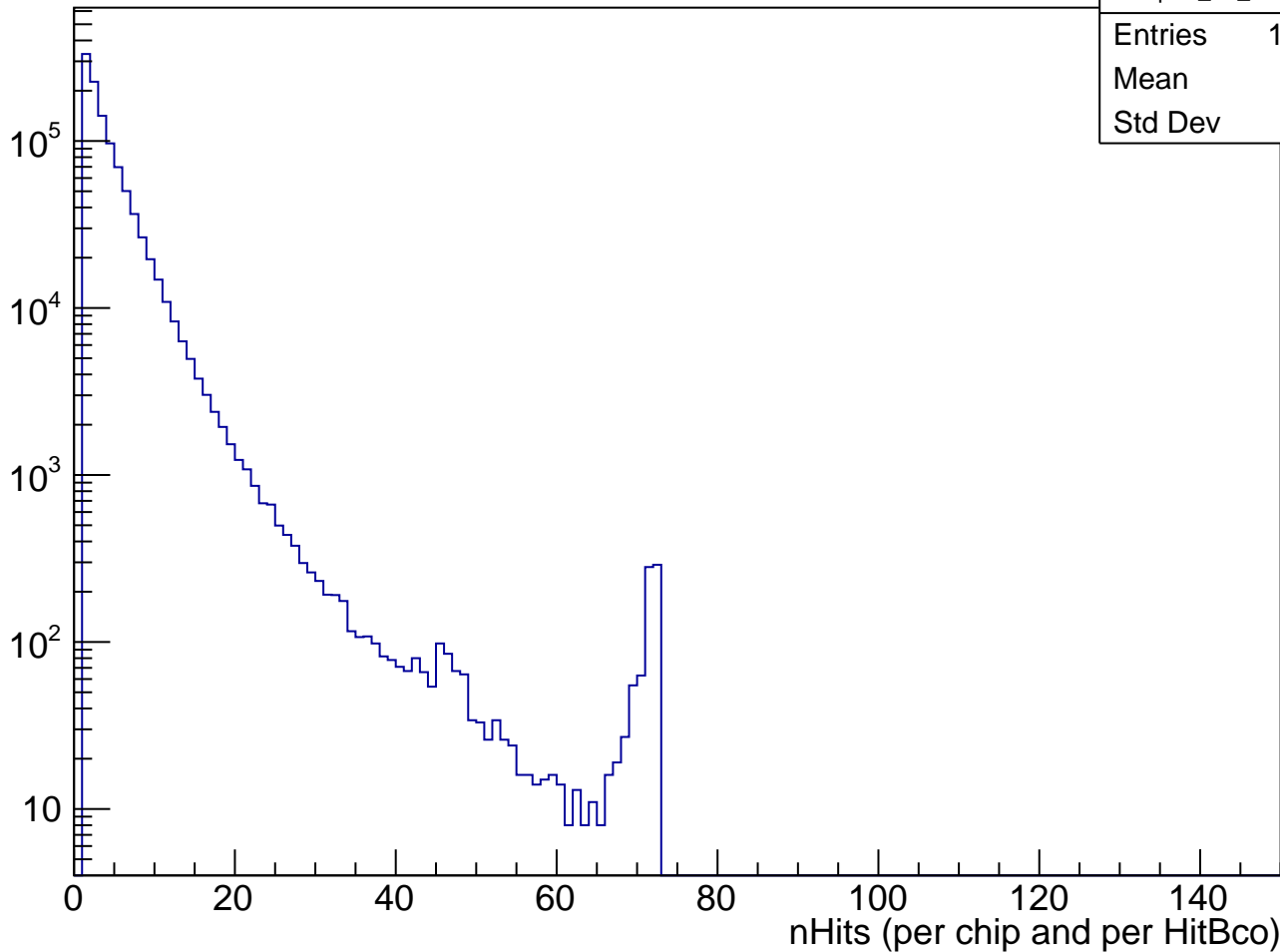


nChipHit_F6_Fch0_Chip13	
Entries	1058669
Mean	3.677
Std Dev	4.169

nHits (per chip and per HitBco)

nChipHit_F6_Fch0_Chip14

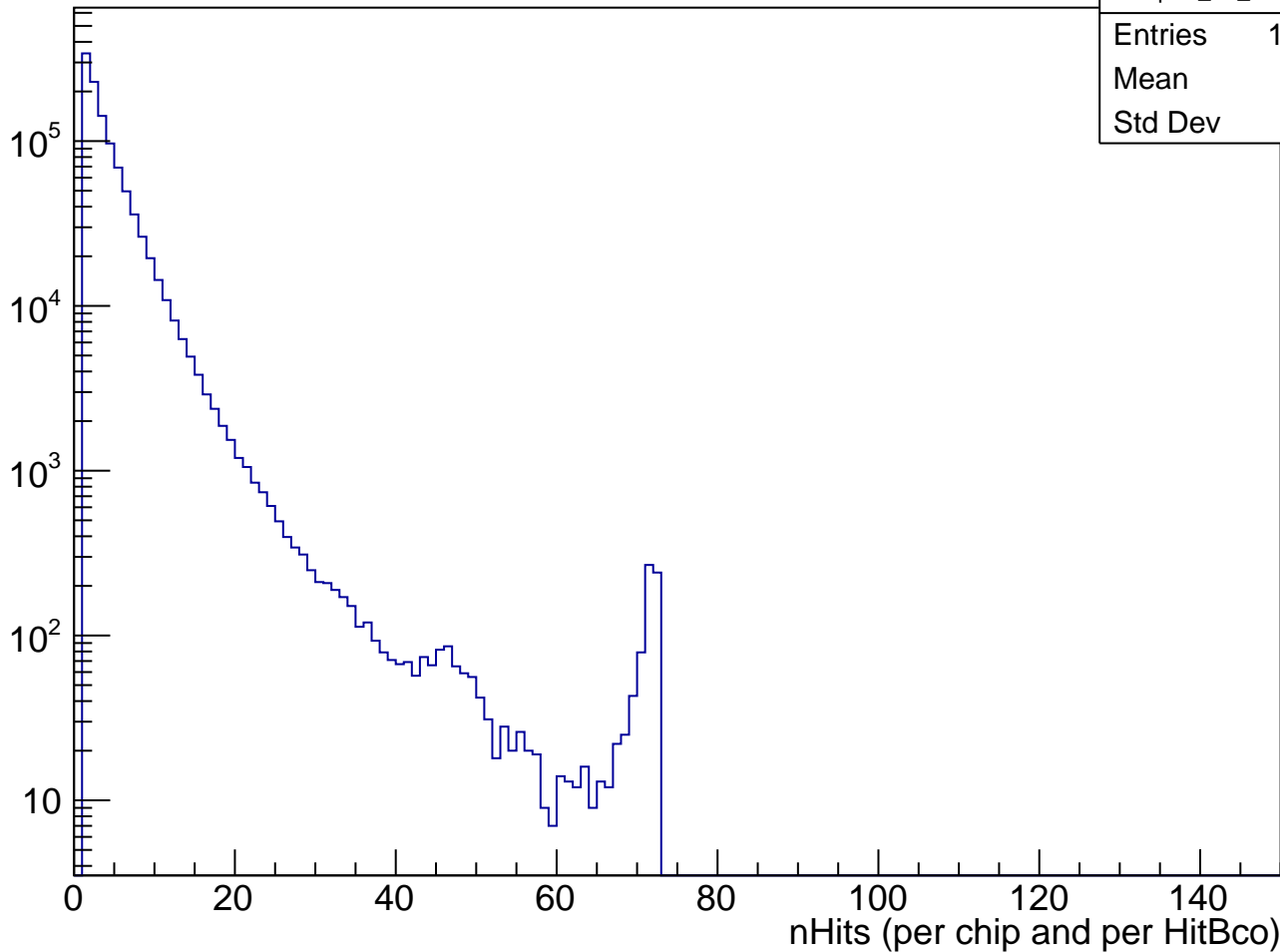
Entries



nChipHit_F6_Fch0_Chip14	
Entries	1066402
Mean	3.668
Std Dev	4.207

nChipHit_F6_Fch0_Chip15

Entries

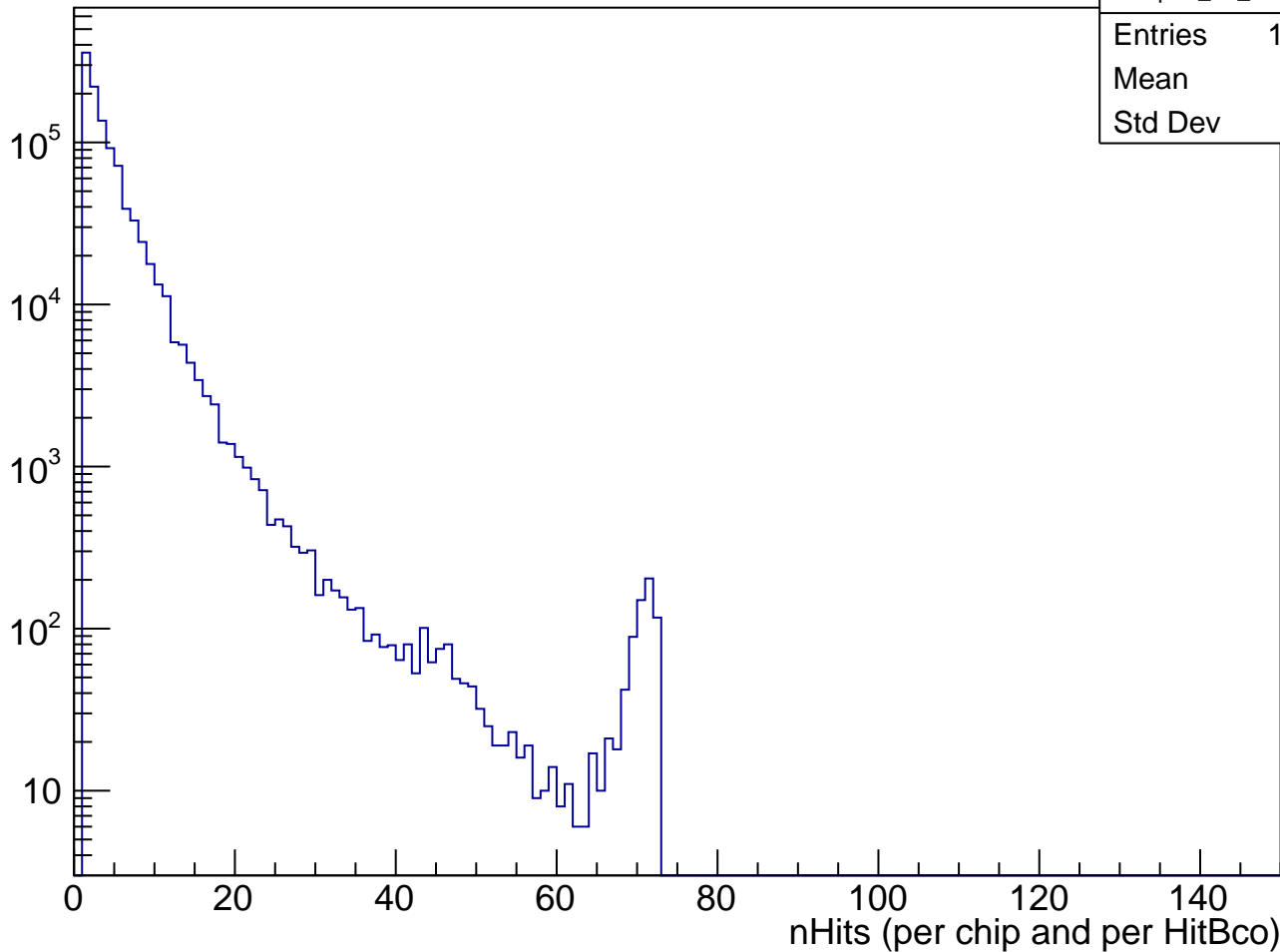


nChipHit_F6_Fch0_Chip15	
Entries	1074799
Mean	3.625
Std Dev	4.159

nHits (per chip and per HitBco)

nChipHit_F6_Fch0_Chip16

Entries

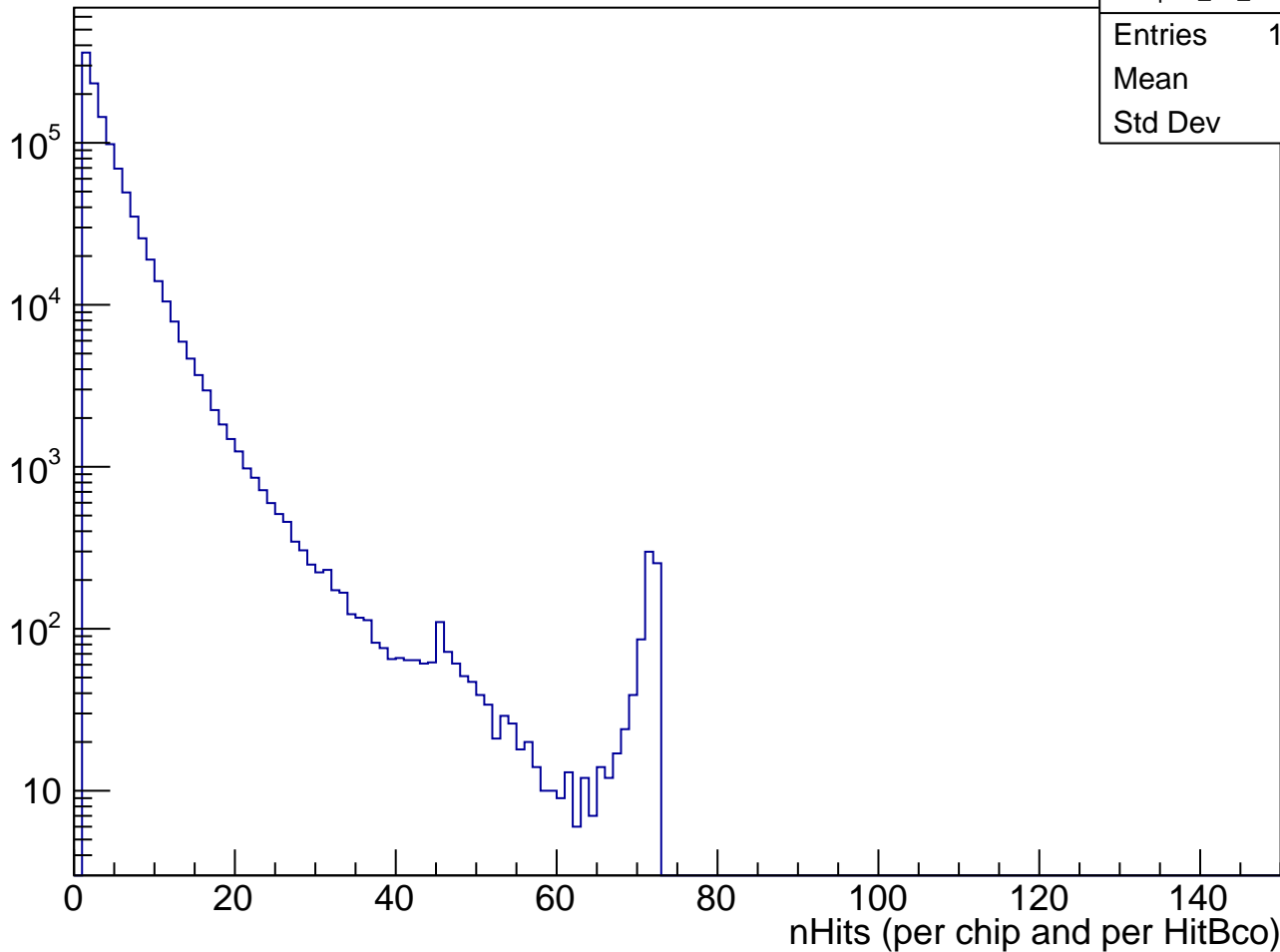


nChipHit_F6_Fch0_Chip16	
Entries	1053468
Mean	3.485
Std Dev	4.054

nHits (per chip and per HitBco)

nChipHit_F6_Fch0_Chip17

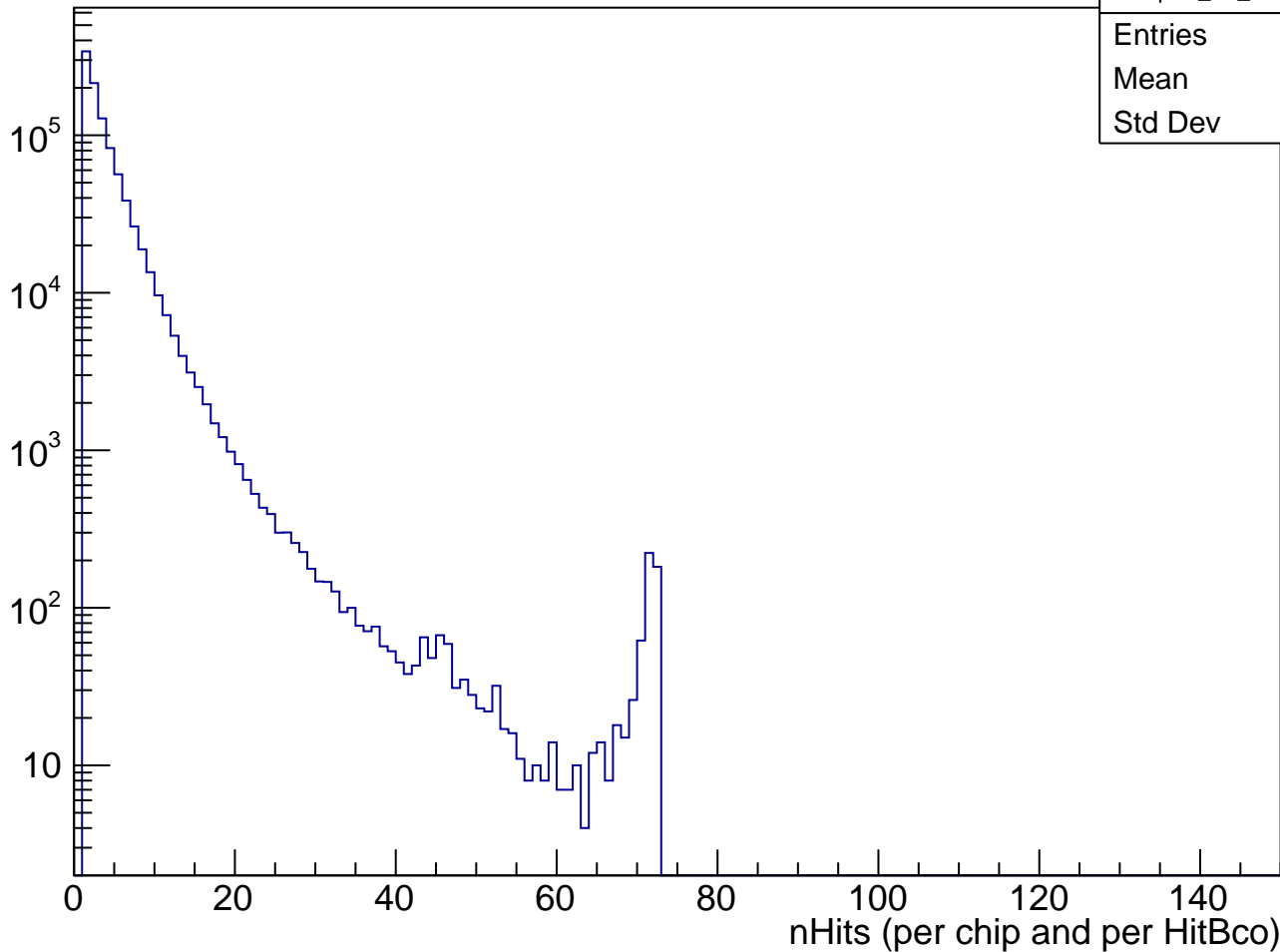
Entries



nChipHit_F6_Fch0_Chip17	
Entries	1098872
Mean	3.547
Std Dev	4.114

nChipHit_F6_Fch0_Chip18

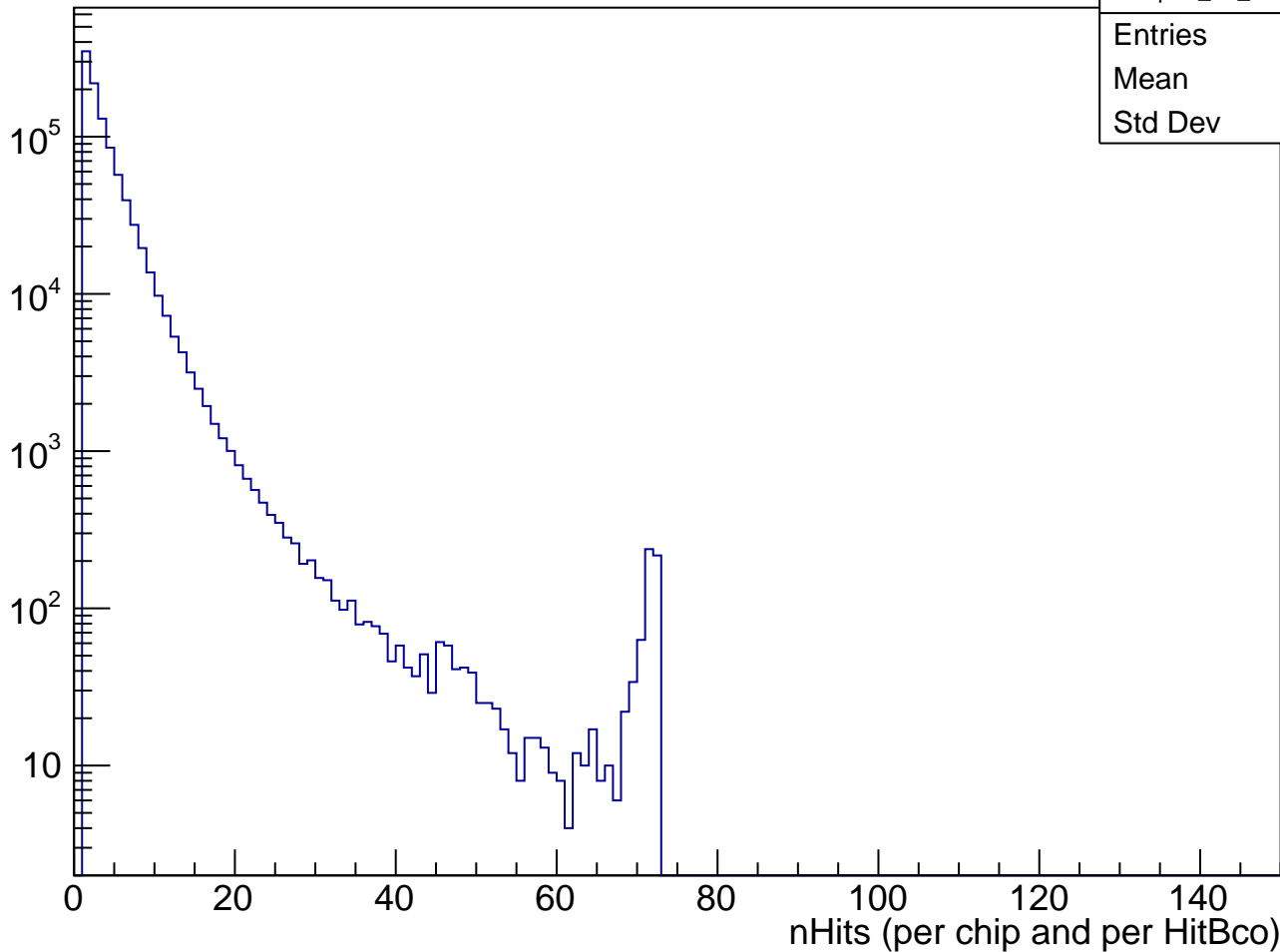
Entries



nChipHit_F6_Fch0_Chip18	
Entries	963407
Mean	3.241
Std Dev	3.763

nChipHit_F6_Fch0_Chip19

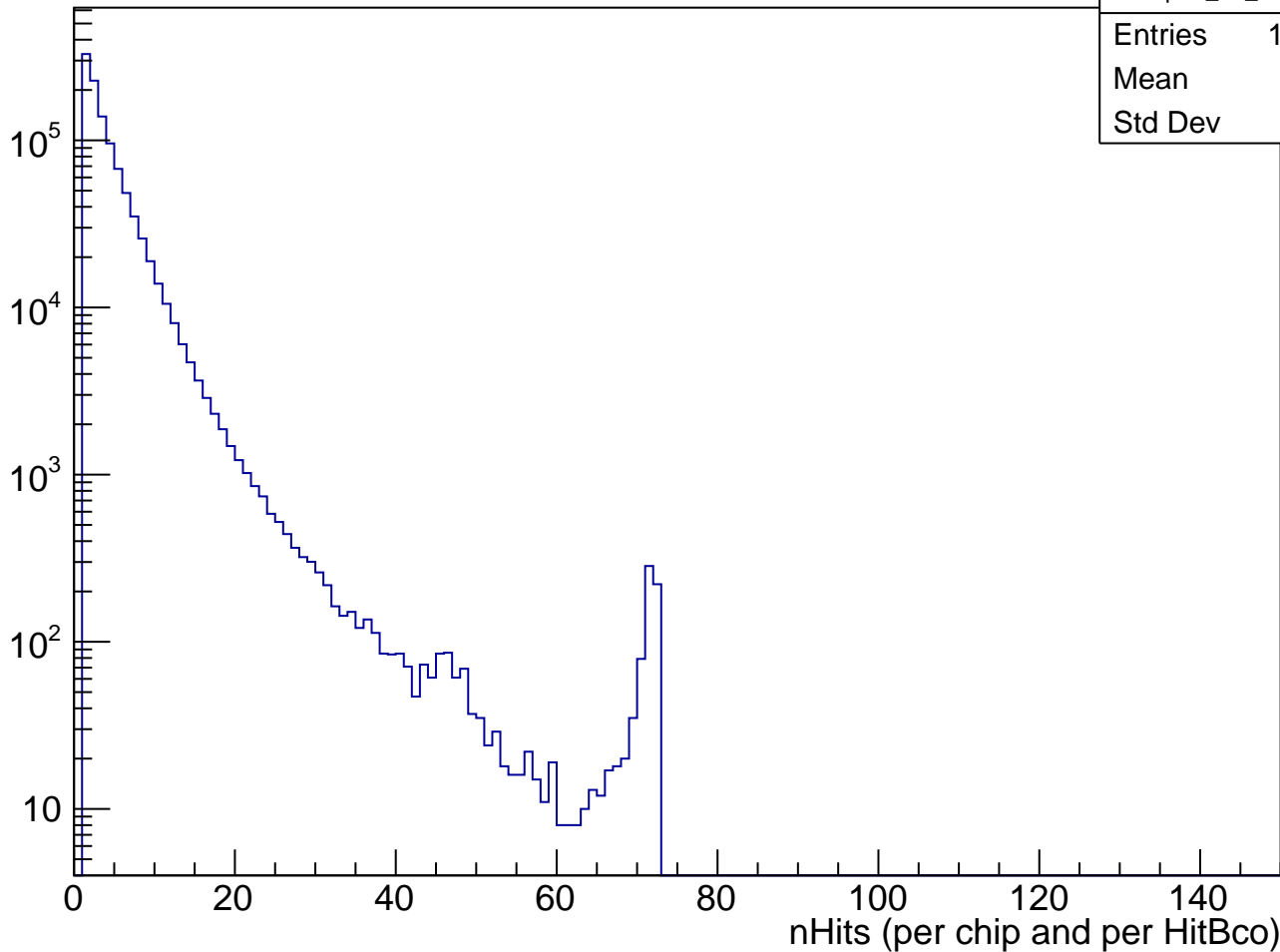
Entries



nChipHit_F6_Fch0_Chip19	
Entries	984900
Mean	3.241
Std Dev	3.789

nChipHit_F6_Fch0_Chip2

Entries



nChipHit_F6_Fch0_Chip2

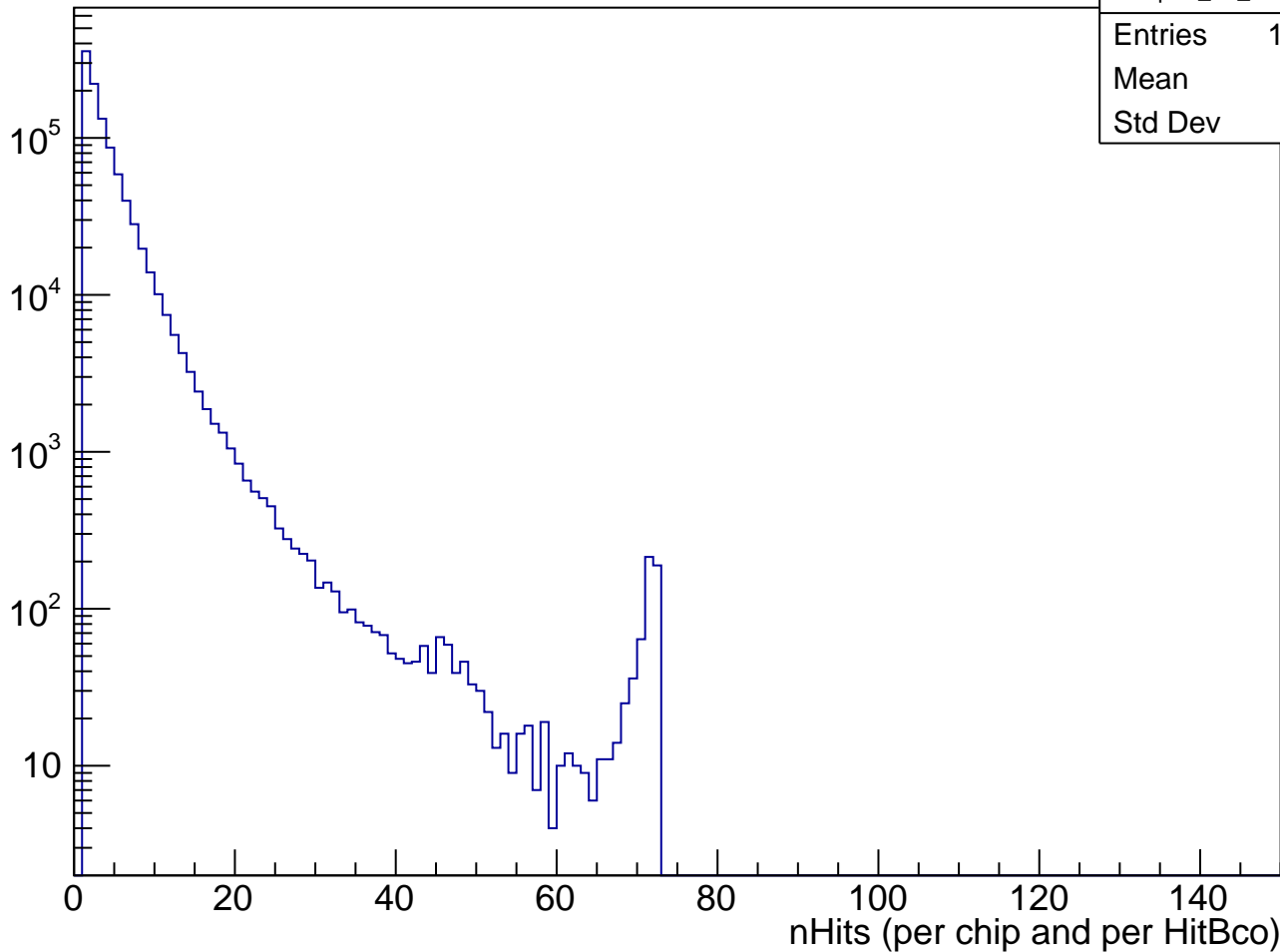
Entries 1051006

Mean 3.635

Std Dev 4.17

nChipHit_F6_Fch0_Chip20

Entries

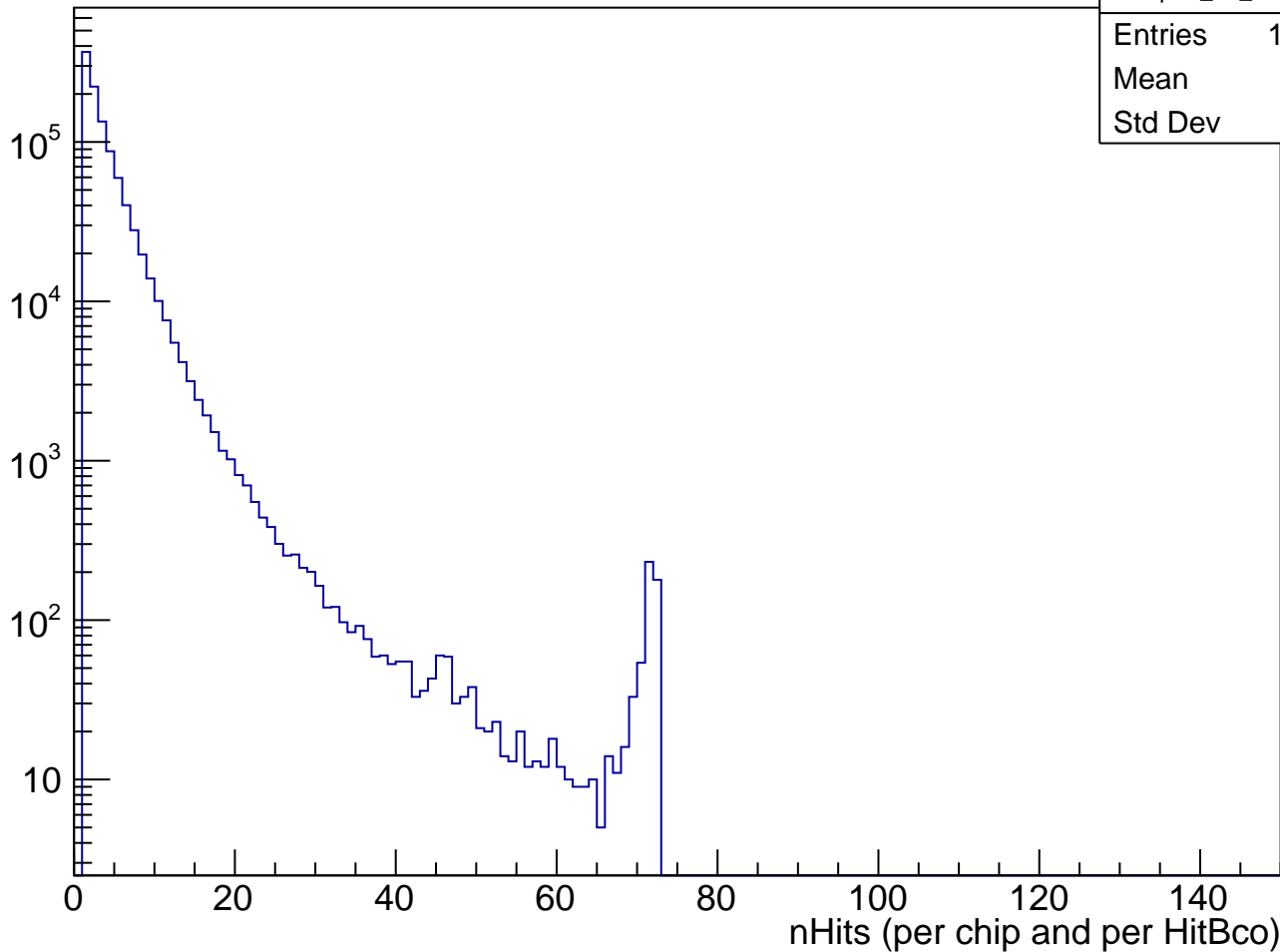


nChipHit_F6_Fch0_Chip20	
Entries	1003087
Mean	3.237
Std Dev	3.753

nHits (per chip and per HitBco)

nChipHit_F6_Fch0_Chip21

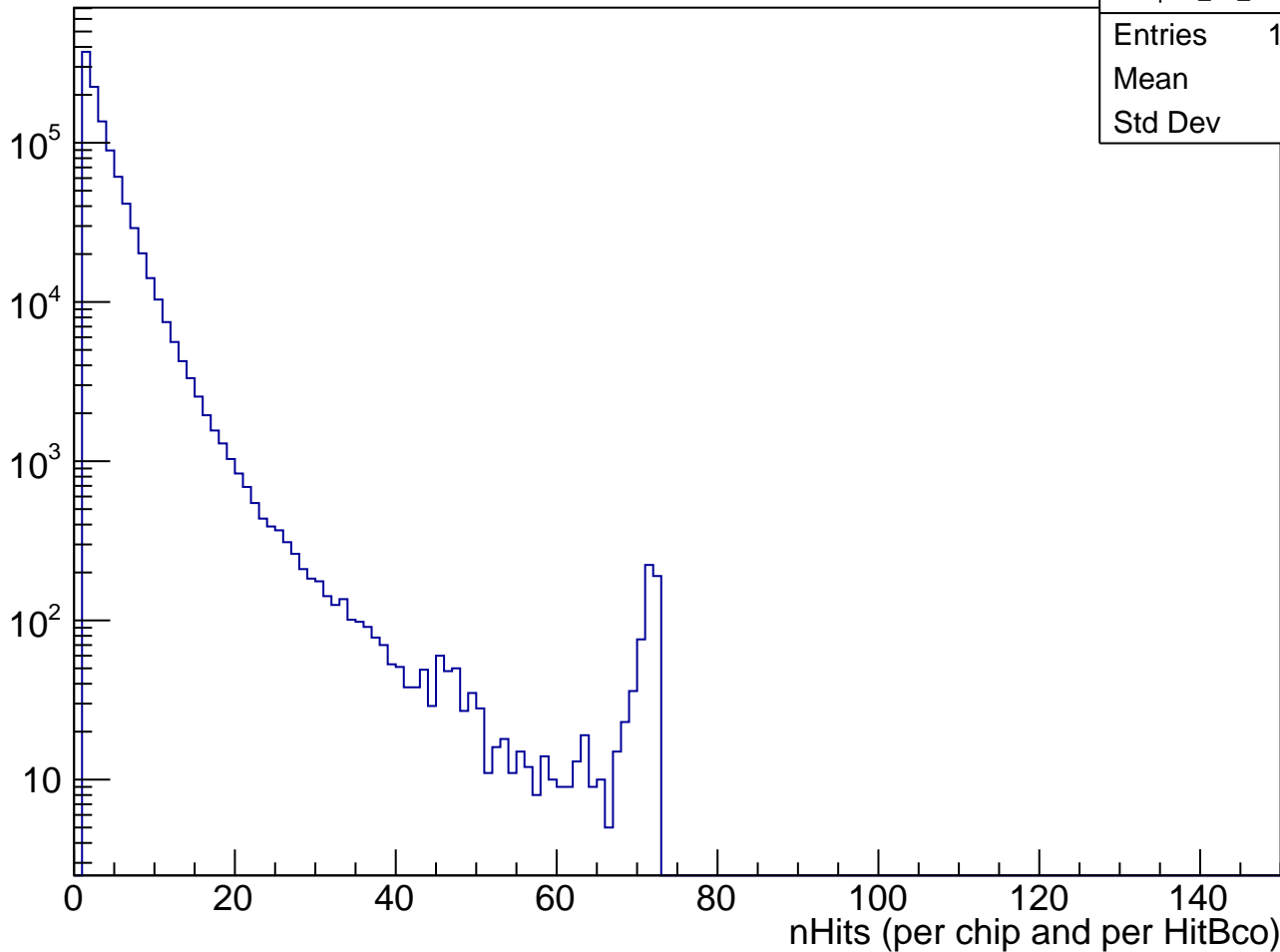
Entries



nChipHit_F6_Fch0_Chip21	
Entries	1017827
Mean	3.203
Std Dev	3.699

nChipHit_F6_Fch0_Chip22

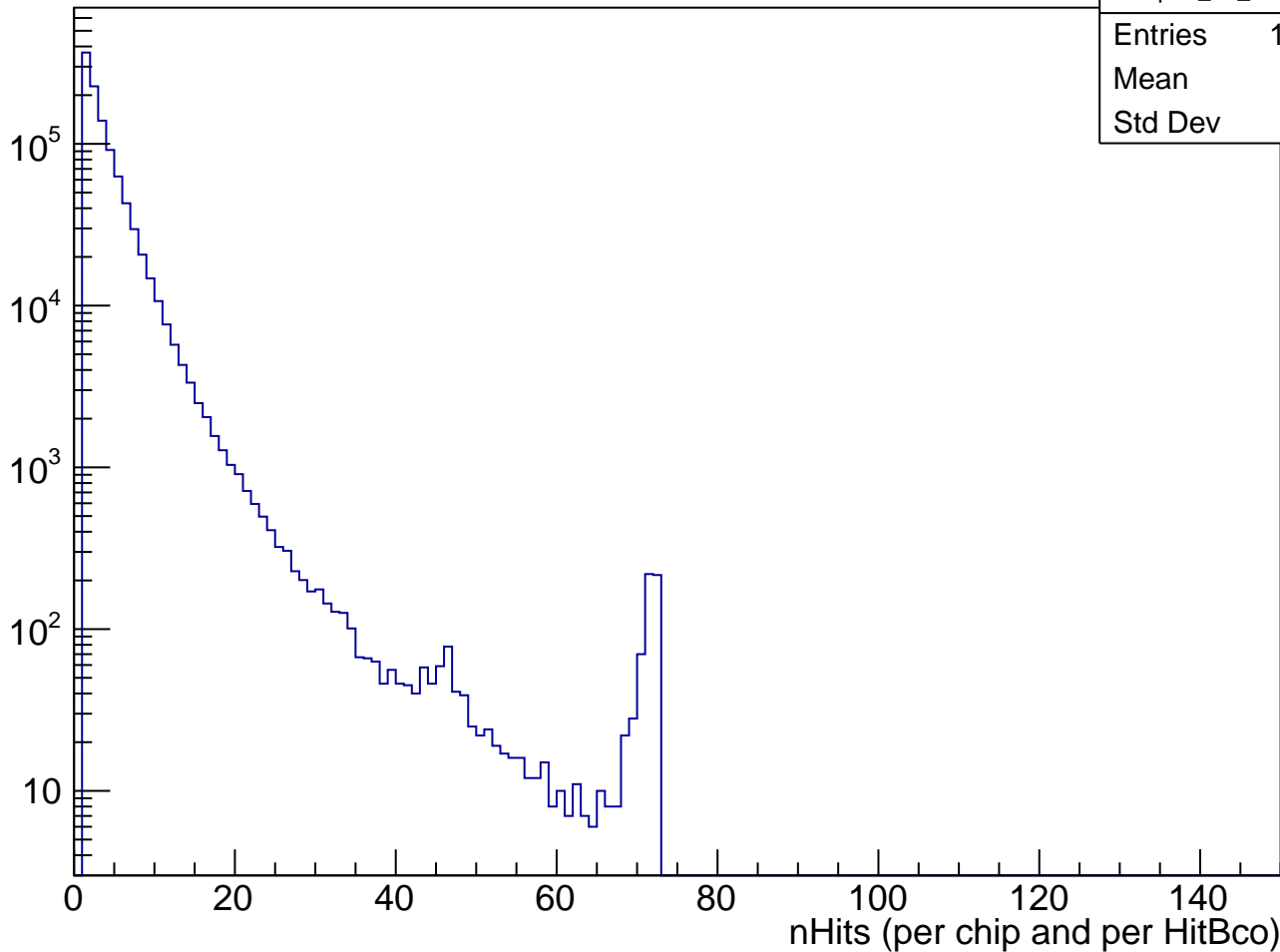
Entries



nChipHit_F6_Fch0_Chip22	
Entries	1035068
Mean	3.222
Std Dev	3.729

nChipHit_F6_Fch0_Chip23

Entries

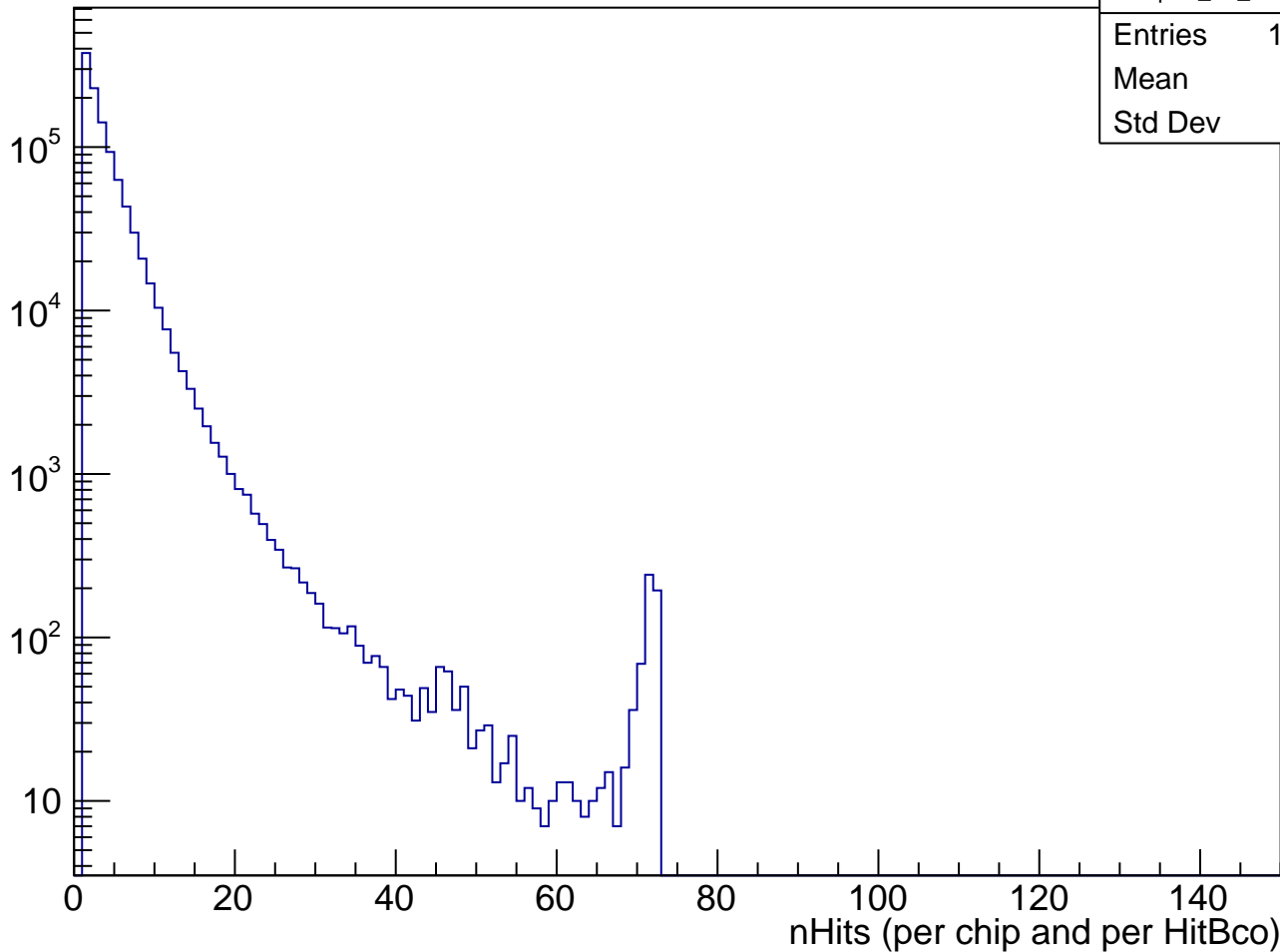


nChipHit_F6_Fch0_Chip23	
Entries	1042130
Mean	3.253
Std Dev	3.728

nHits (per chip and per HitBco)

nChipHit_F6_Fch0_Chip24

Entries

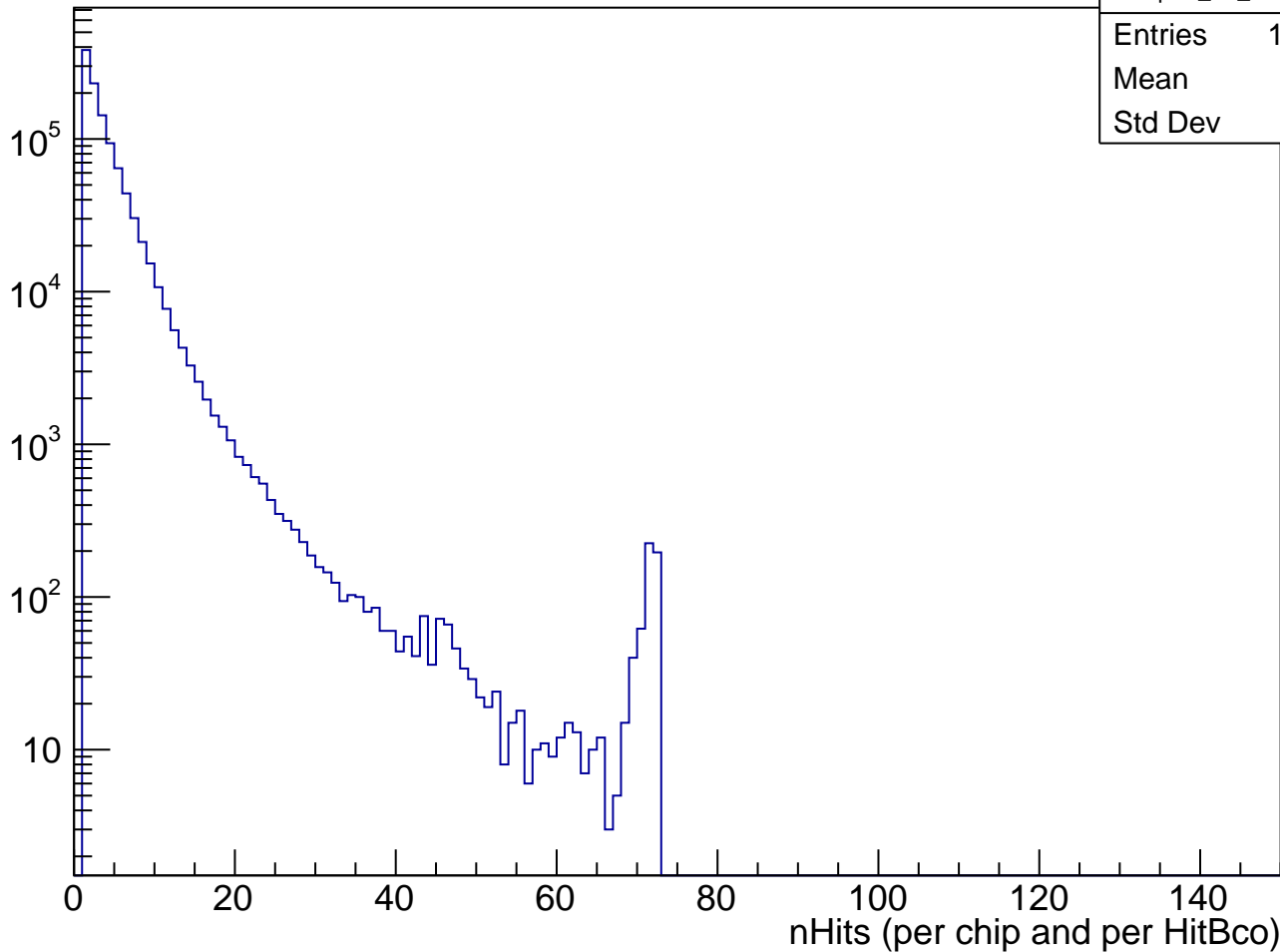


nChipHit_F6_Fch0_Chip24	
Entries	1057844
Mean	3.227
Std Dev	3.702

nHits (per chip and per HitBco)

nChipHit_F6_Fch0_Chip25

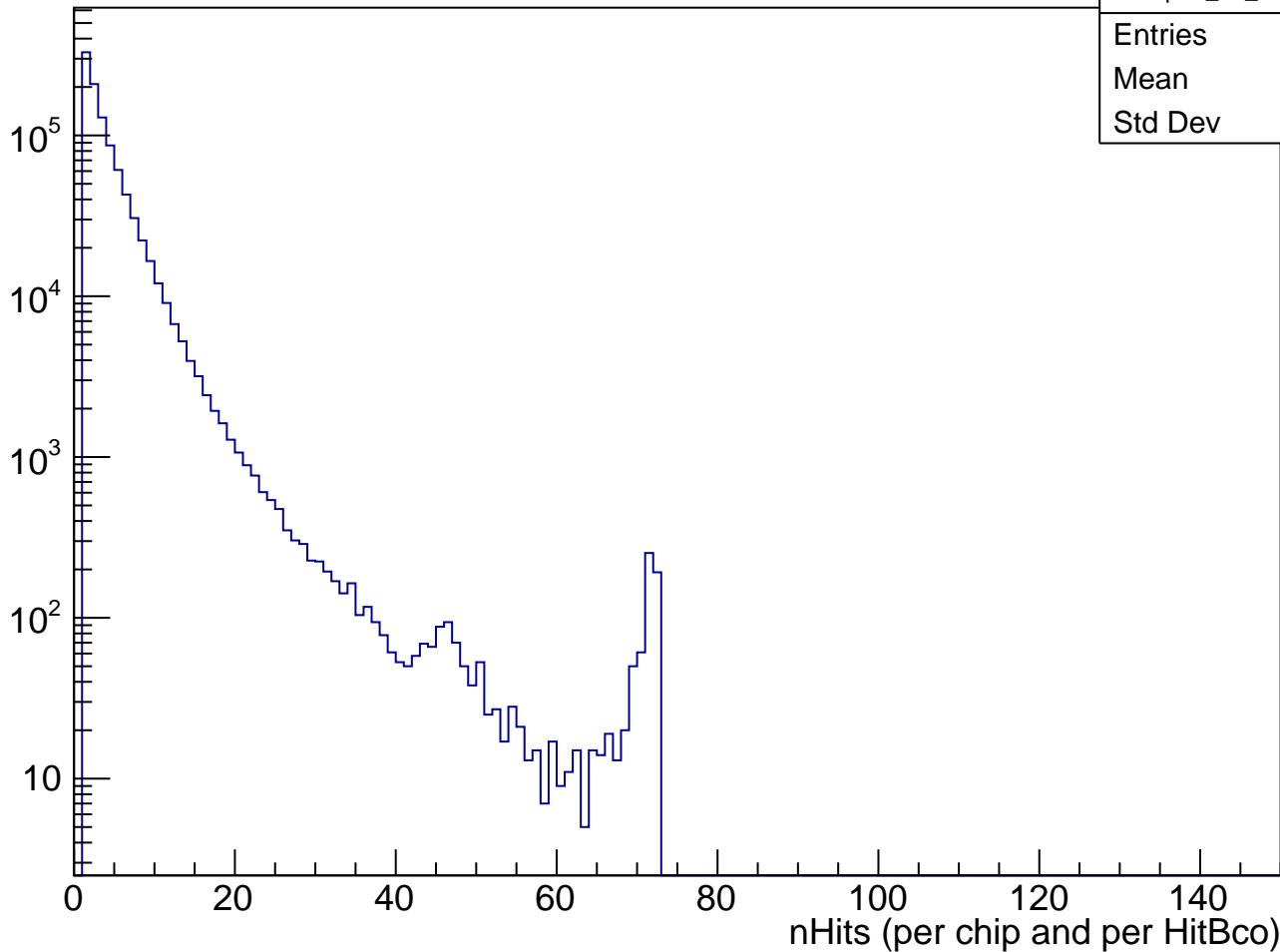
Entries



nChipHit_F6_Fch0_Chip25	
Entries	1072714
Mean	3.231
Std Dev	3.701

nChipHit_F6_Fch0_Chip3

Entries



nChipHit_F6_Fch0_Chip3

Entries 982494

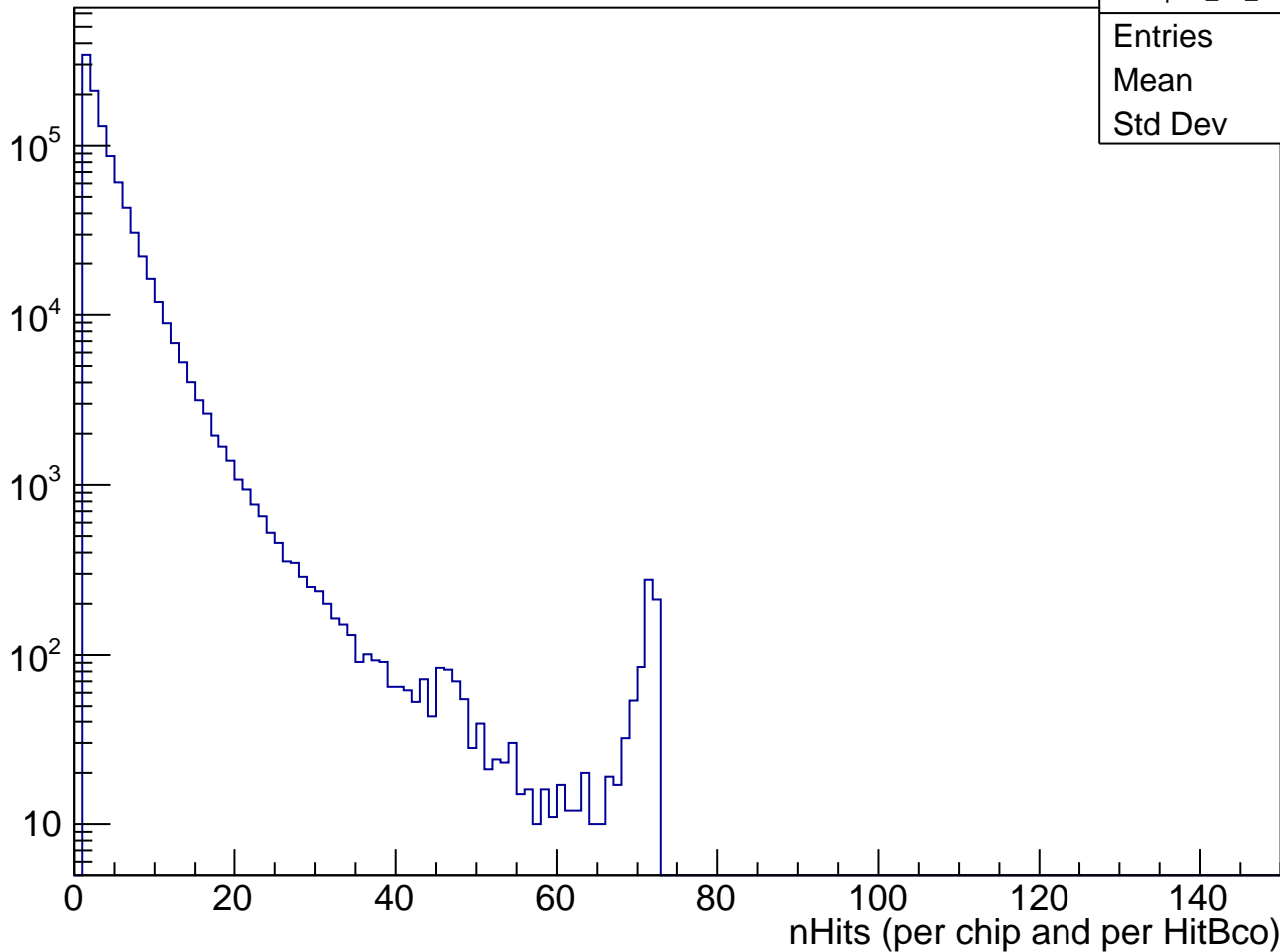
Mean 3.502

Std Dev 4.102

nHits (per chip and per HitBco)

nChipHit_F6_Fch0_Chip4

Entries



nChipHit_F6_Fch0_Chip4

Entries 998811

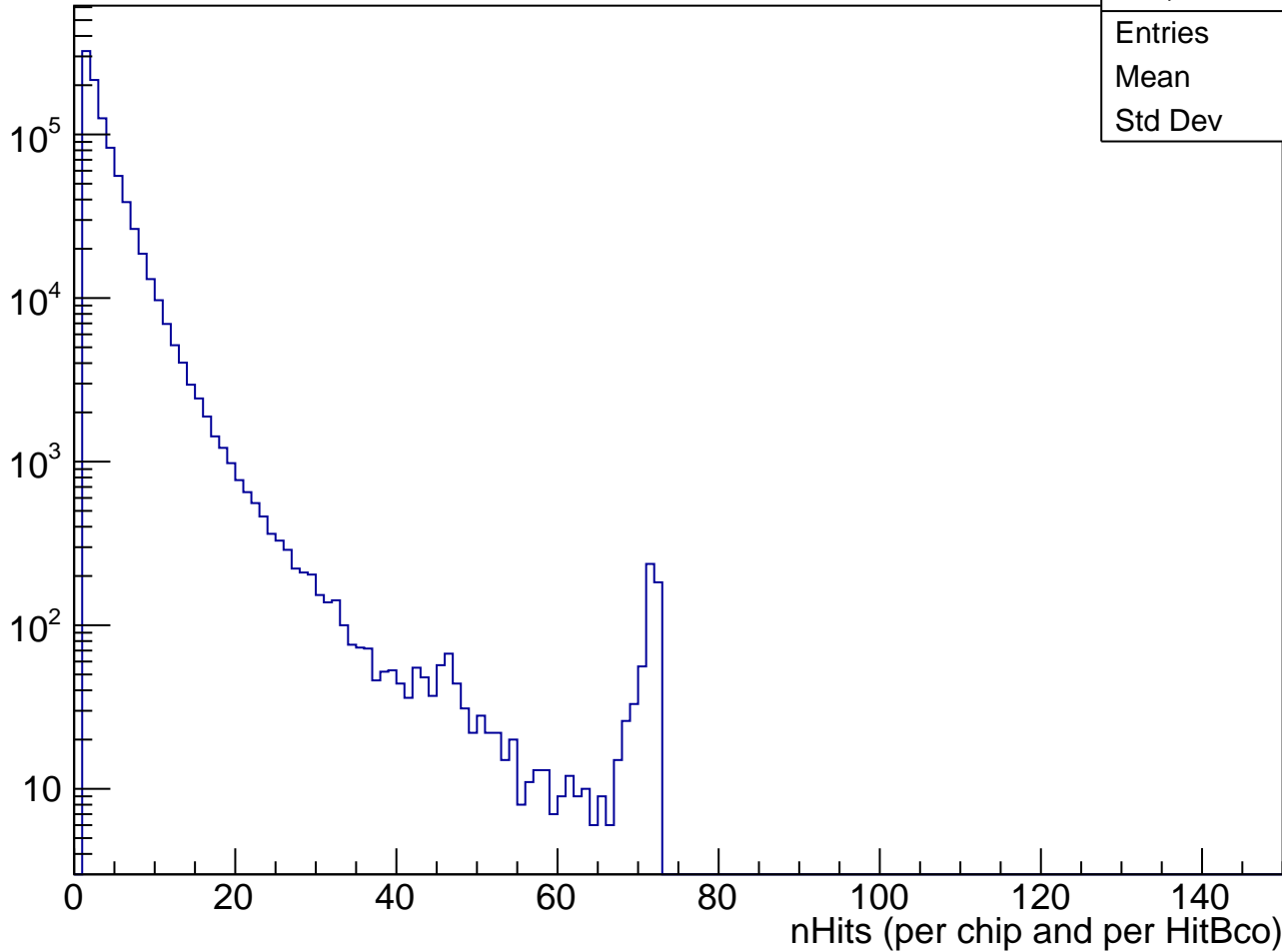
Mean 3.477

Std Dev 4.132

nHits (per chip and per HitBco)

nChipHit_F6_Fch0_Chip5

Entries



nChipHit_F6_Fch0_Chip5

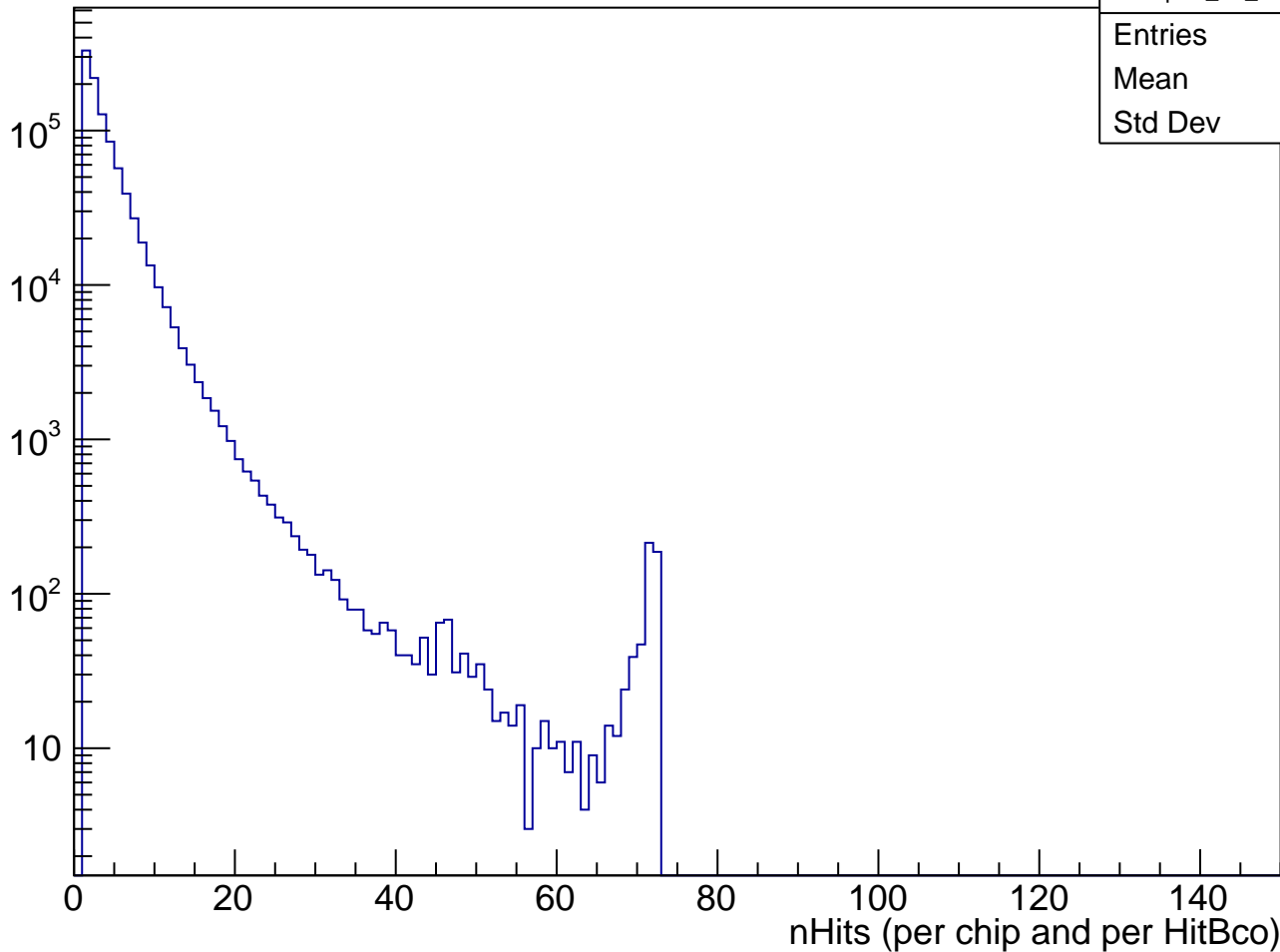
Entries 942486

Mean 3.268

Std Dev 3.78

nChipHit_F6_Fch0_Chip6

Entries

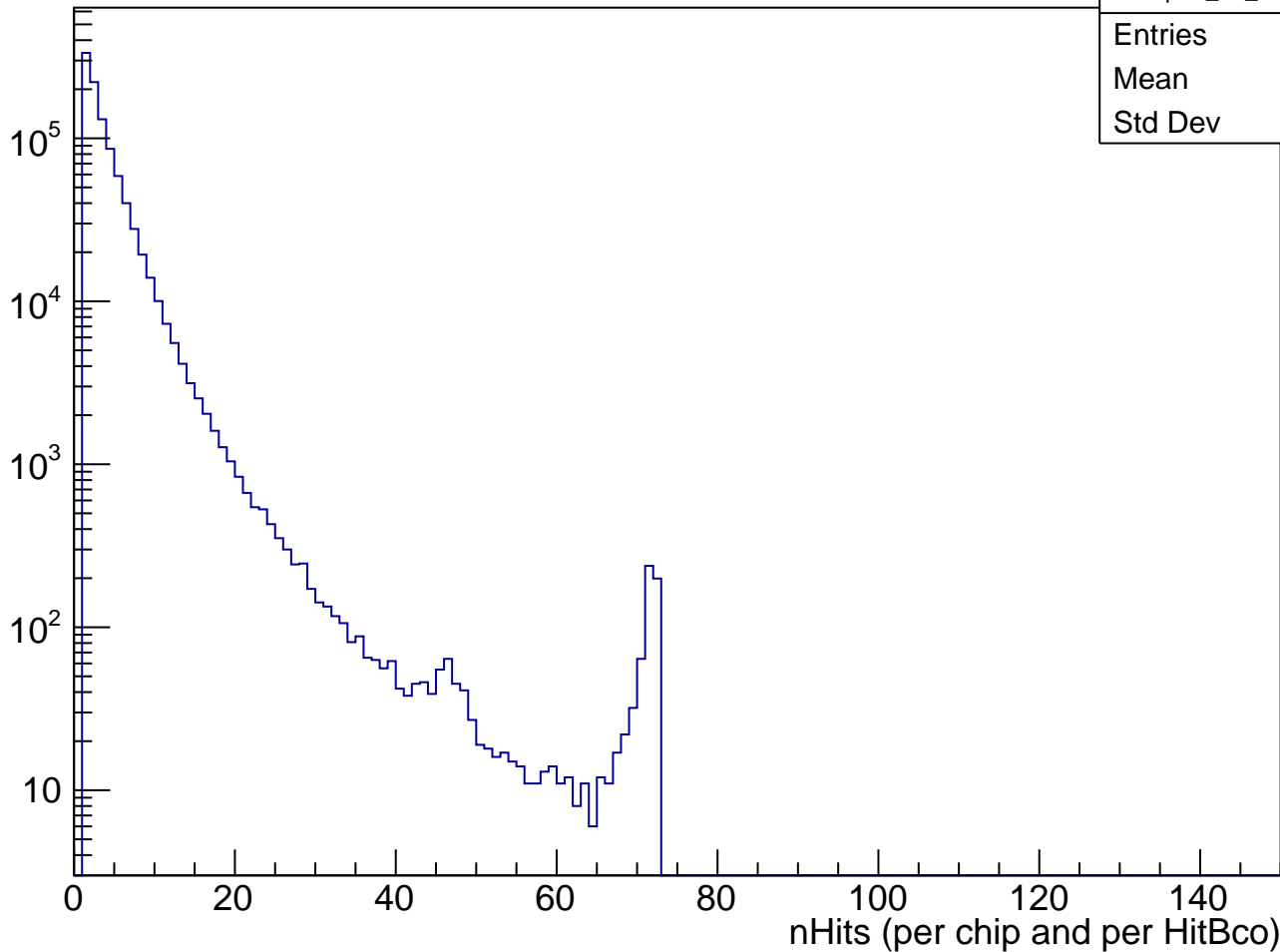


nChipHit_F6_Fch0_Chip6

Entries	959516
Mean	3.254
Std Dev	3.734

nChipHit_F6_Fch0_Chip7

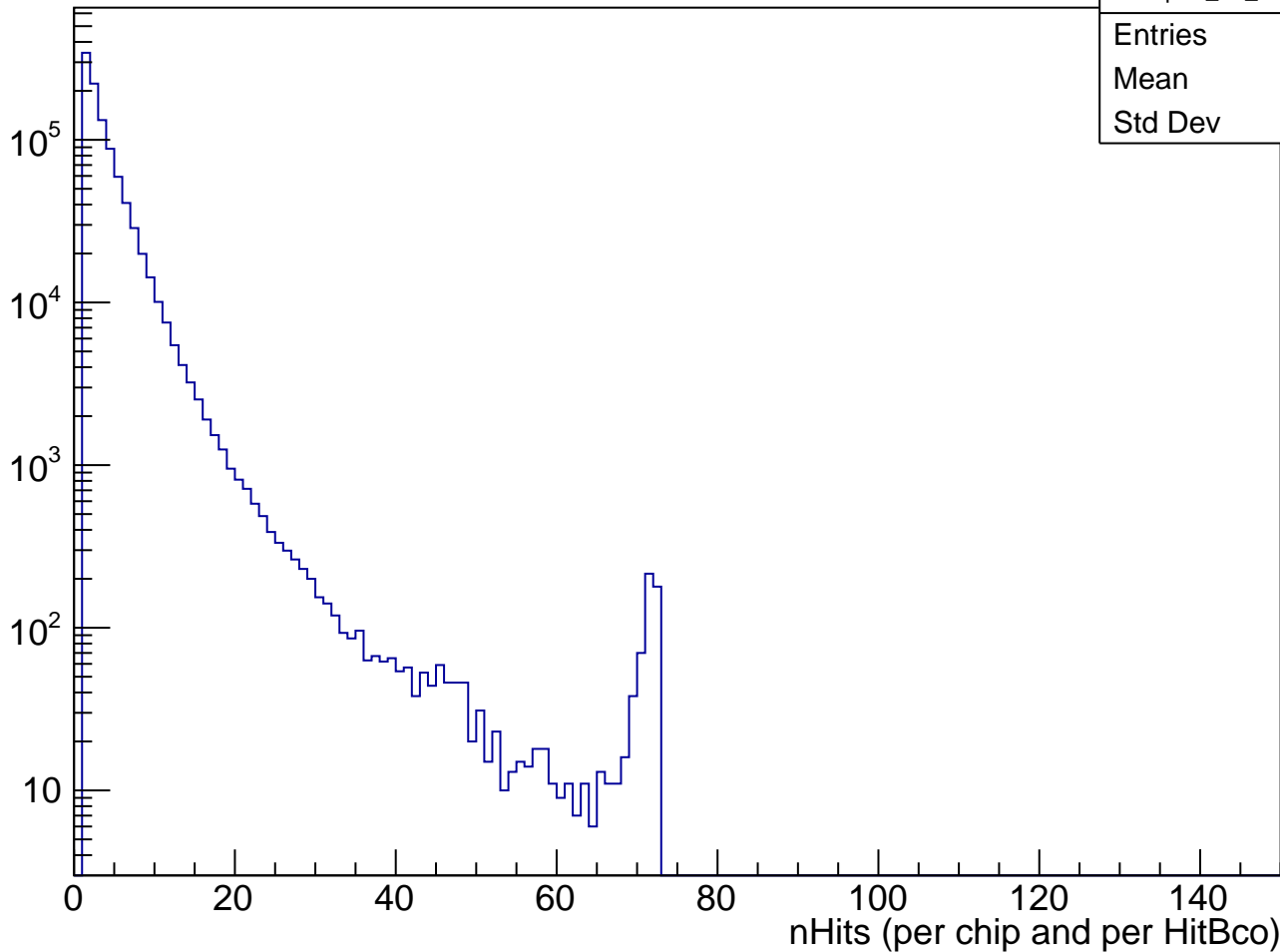
Entries



nChipHit_F6_Fch0_Chip7	
Entries	977367
Mean	3.286
Std Dev	3.794

nChipHit_F6_Fch0_Chip8

Entries



nChipHit_F6_Fch0_Chip8

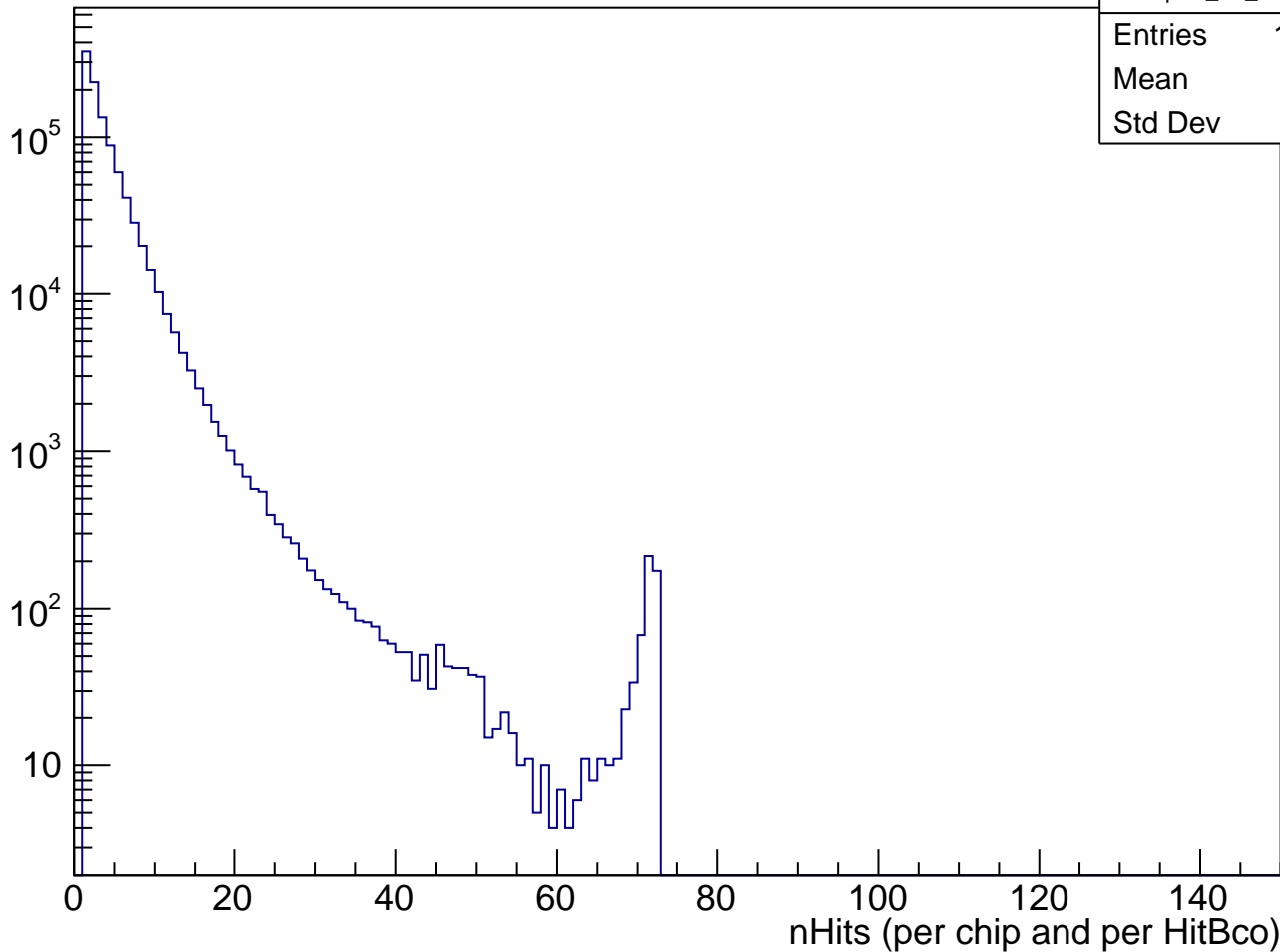
Entries 993105

Mean 3.274

Std Dev 3.756

nChipHit_F6_Fch0_Chip9

Entries



nChipHit_F6_Fch0_Chip9

Entries 1007141

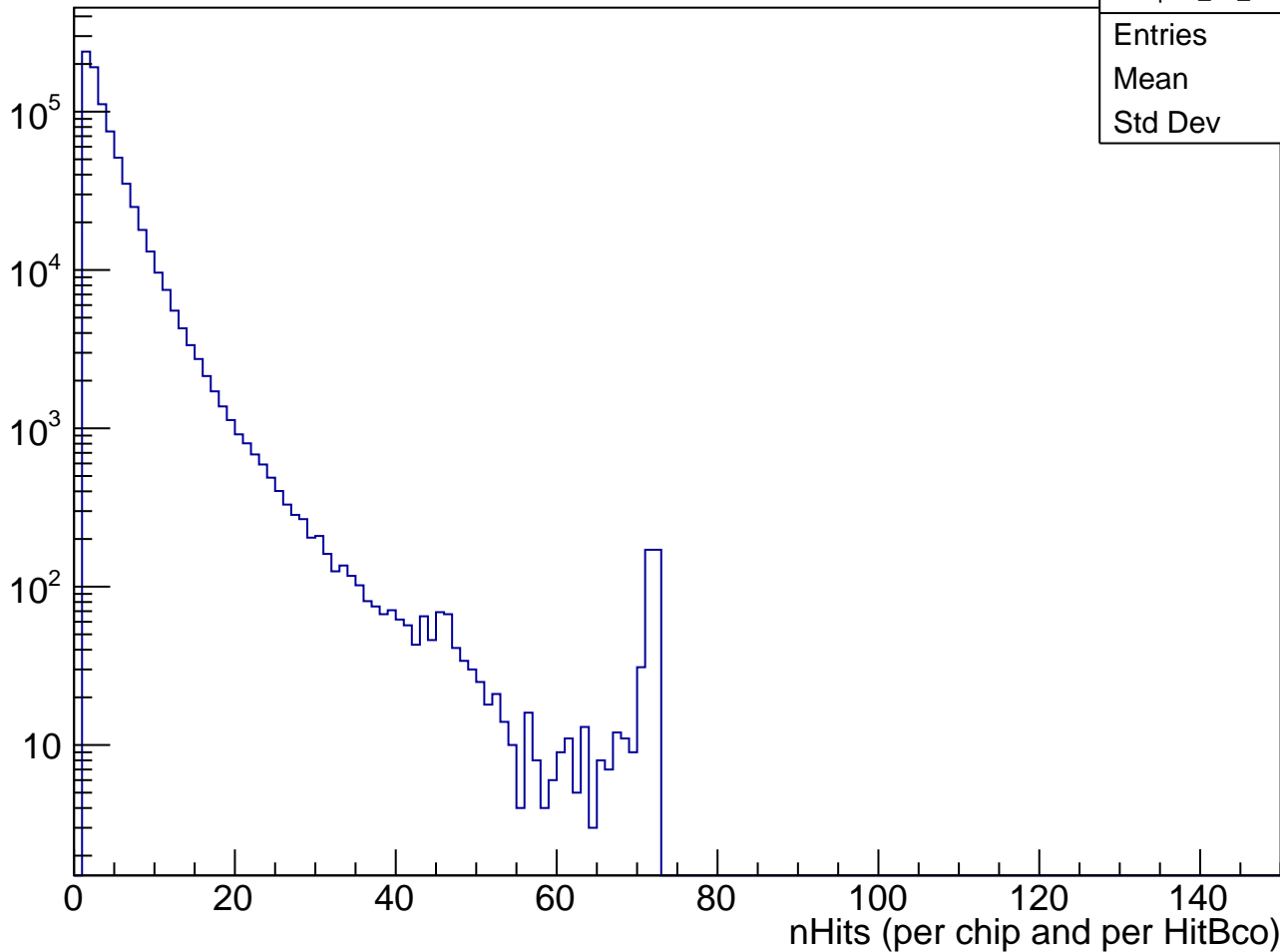
Mean 3.26

Std Dev 3.728

nHits (per chip and per HitBco)

nChipHit_F6_Fch10_Chip0

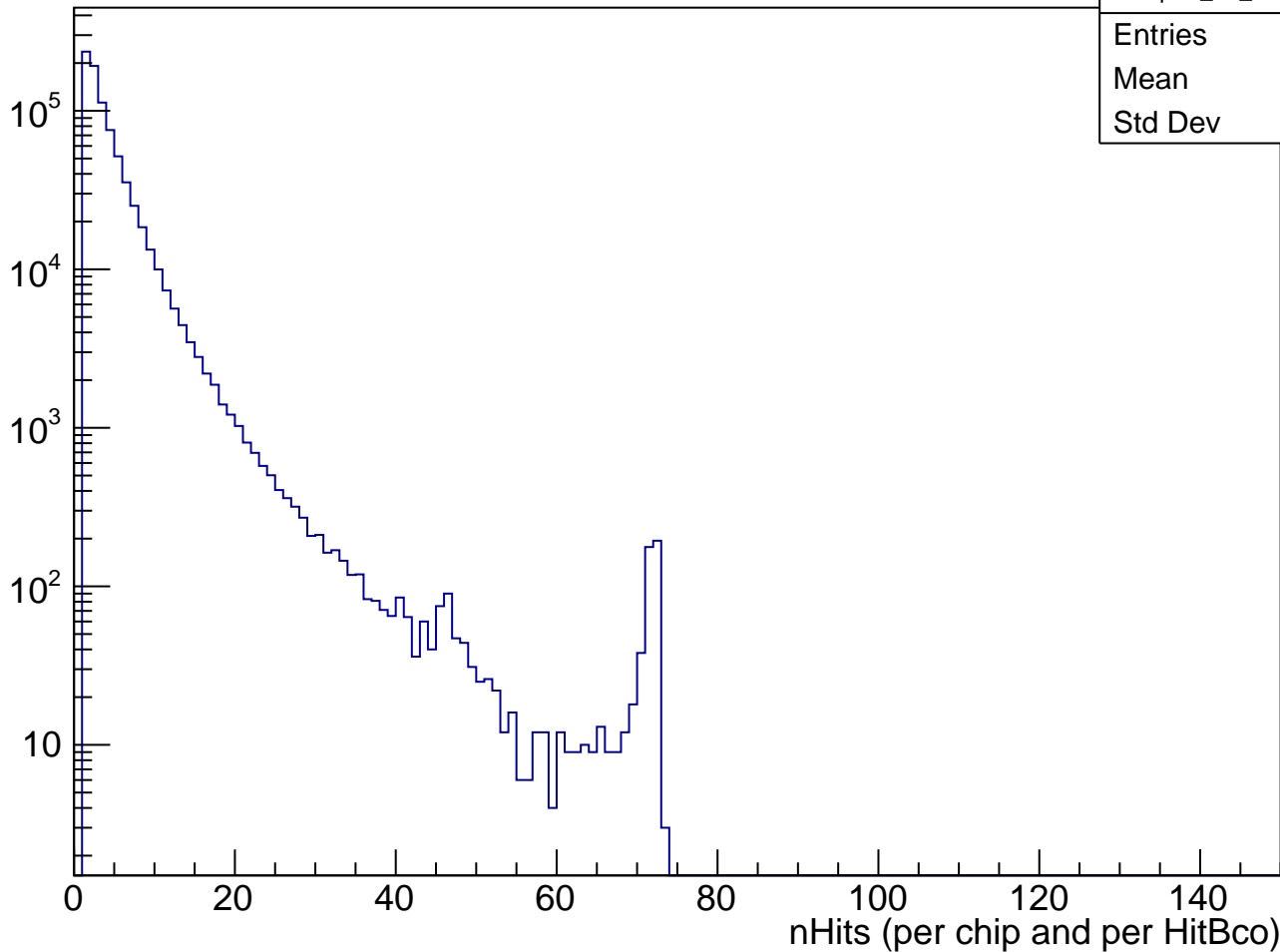
Entries



nChipHit_F6_Fch10_Chip0	
Entries	805664
Mean	3.564
Std Dev	4.037

nChipHit_F6_Fch10_Chip1

Entries

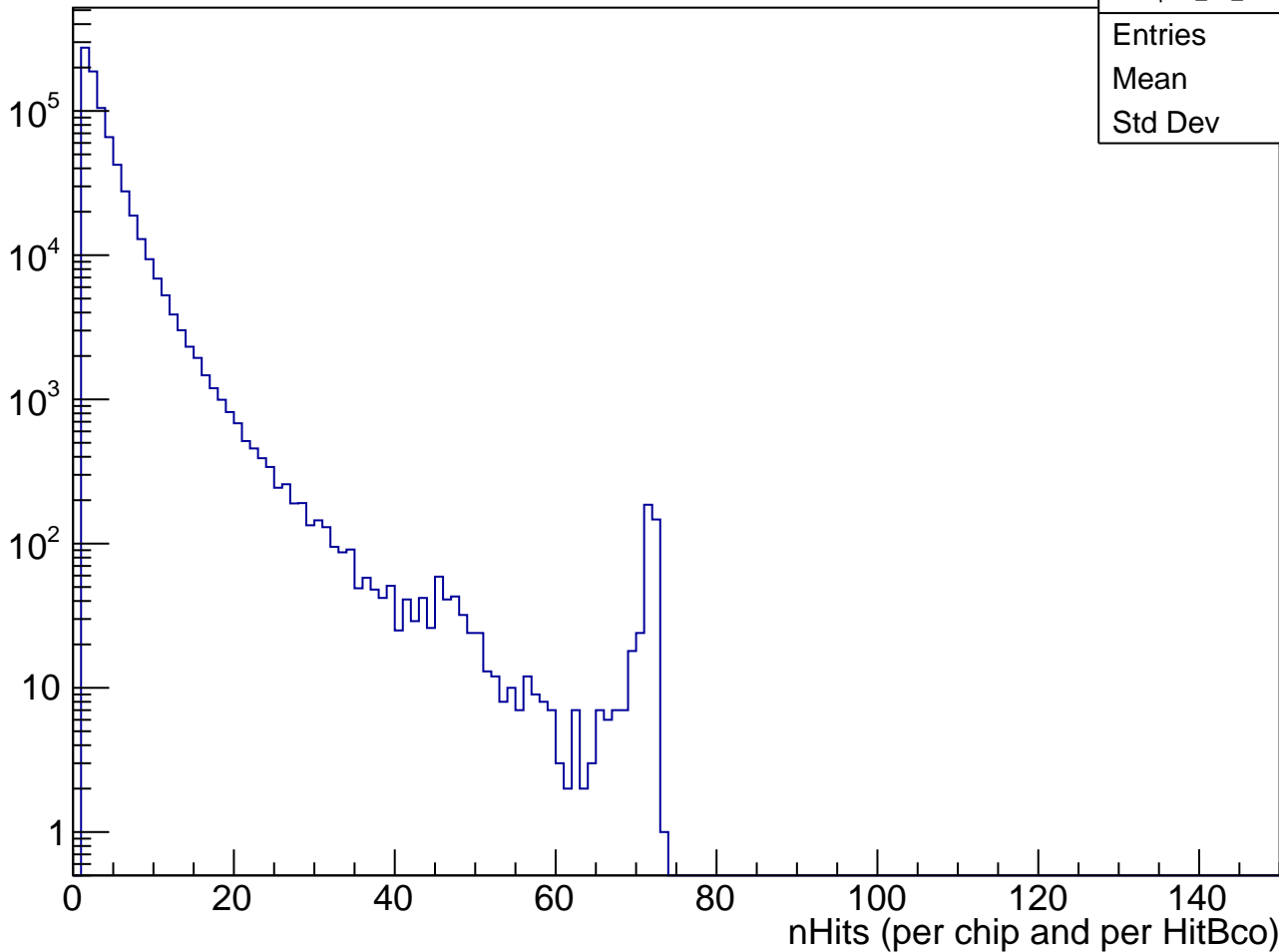


nChipHit_F6_Fch10_Chip1	
Entries	808169
Mean	3.608
Std Dev	4.131

nHits (per chip and per HitBco)

nChipHit_F6_Fch10_Chip10

Entries



nChipHit_F6_Fch10_Chip10

Entries 777390

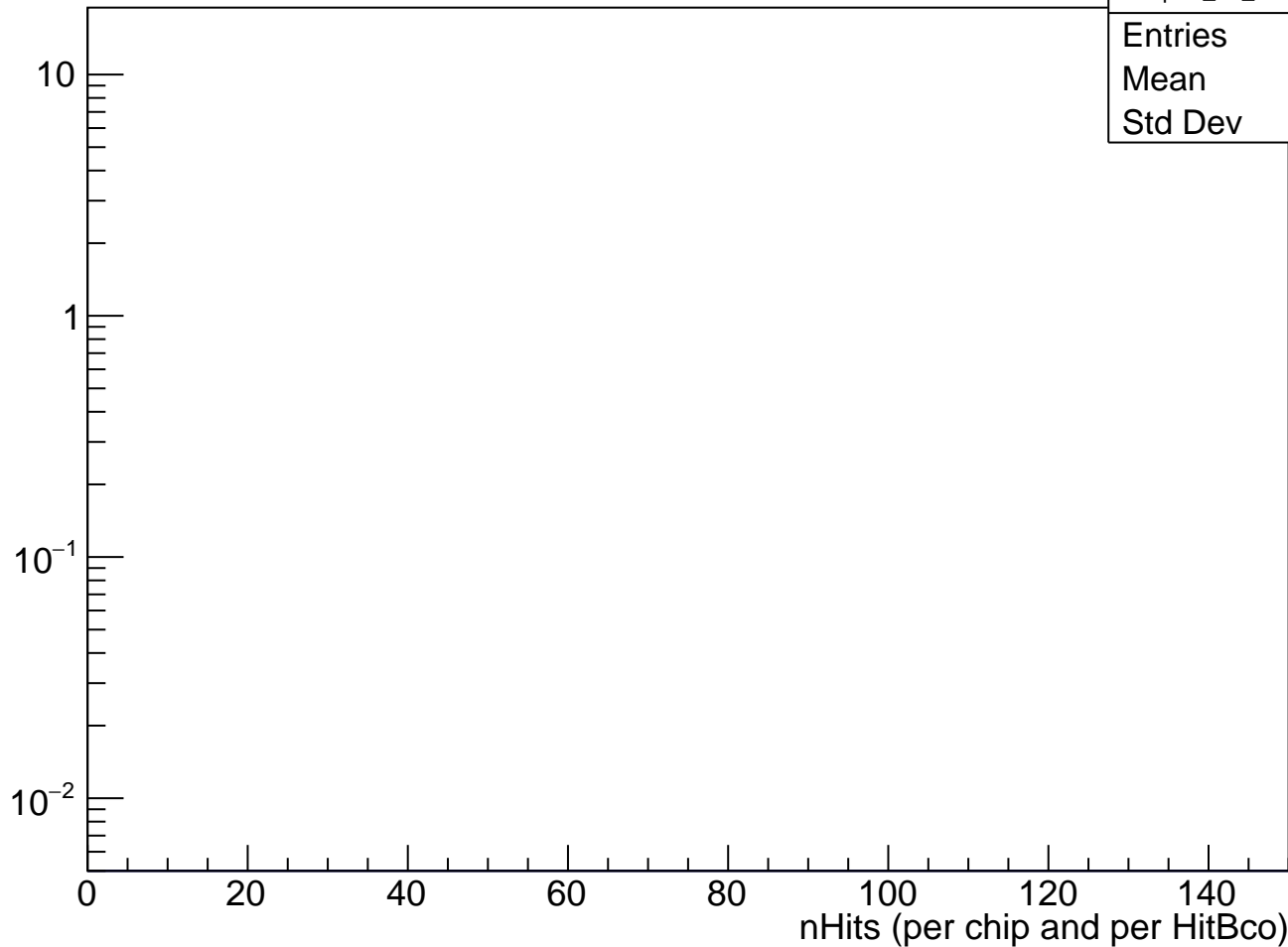
Mean 3.131

Std Dev 3.669

nHits (per chip and per HitBco)

nChipHit_F6_Fch10_Chip11

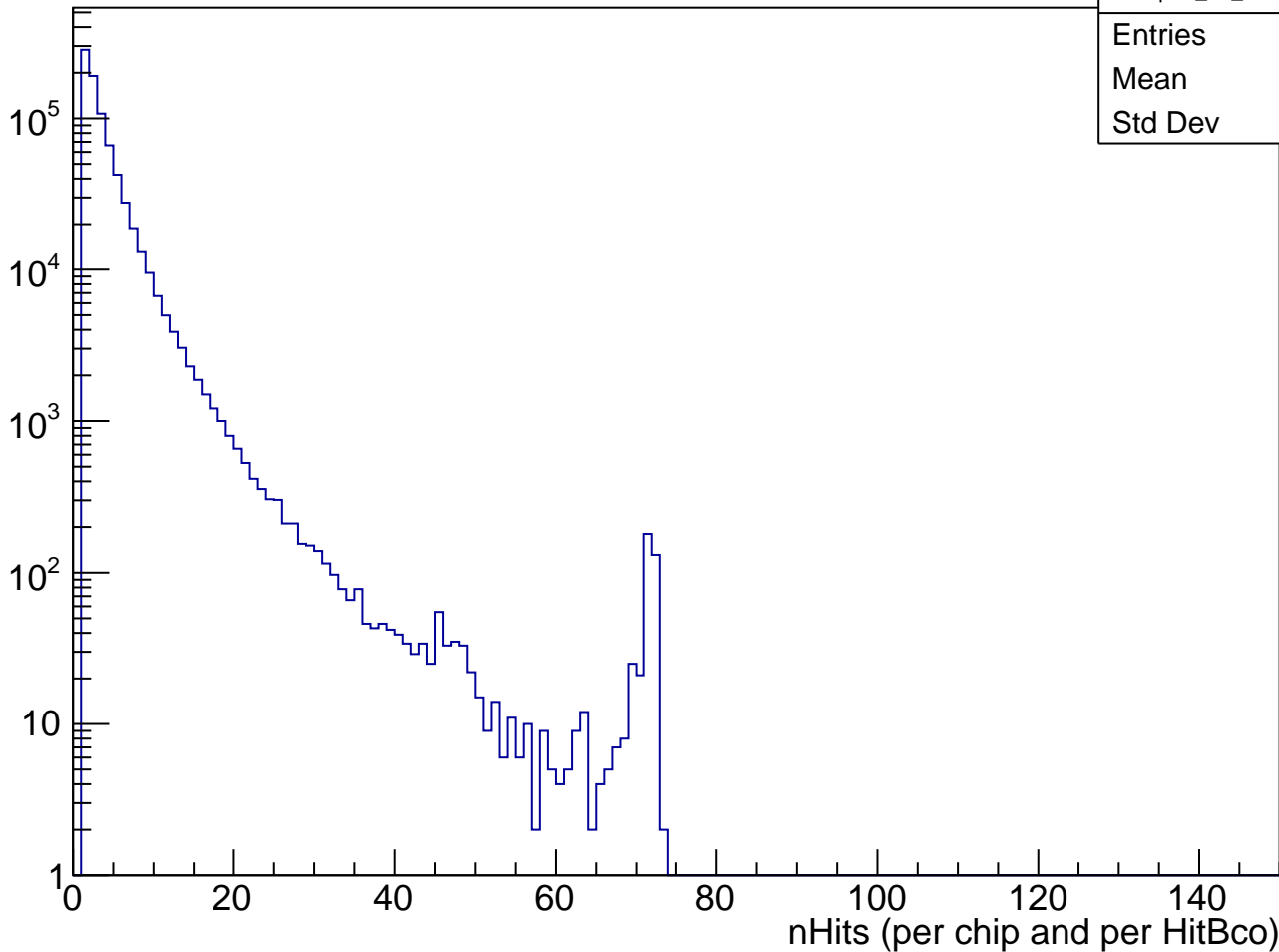
Entries



nChipHit_F6_Fch10_Chip11	
Entries	0
Mean	0
Std Dev	0

nChipHit_F6_Fch10_Chip12

Entries



nChipHit_F6_Fch10_Chip12

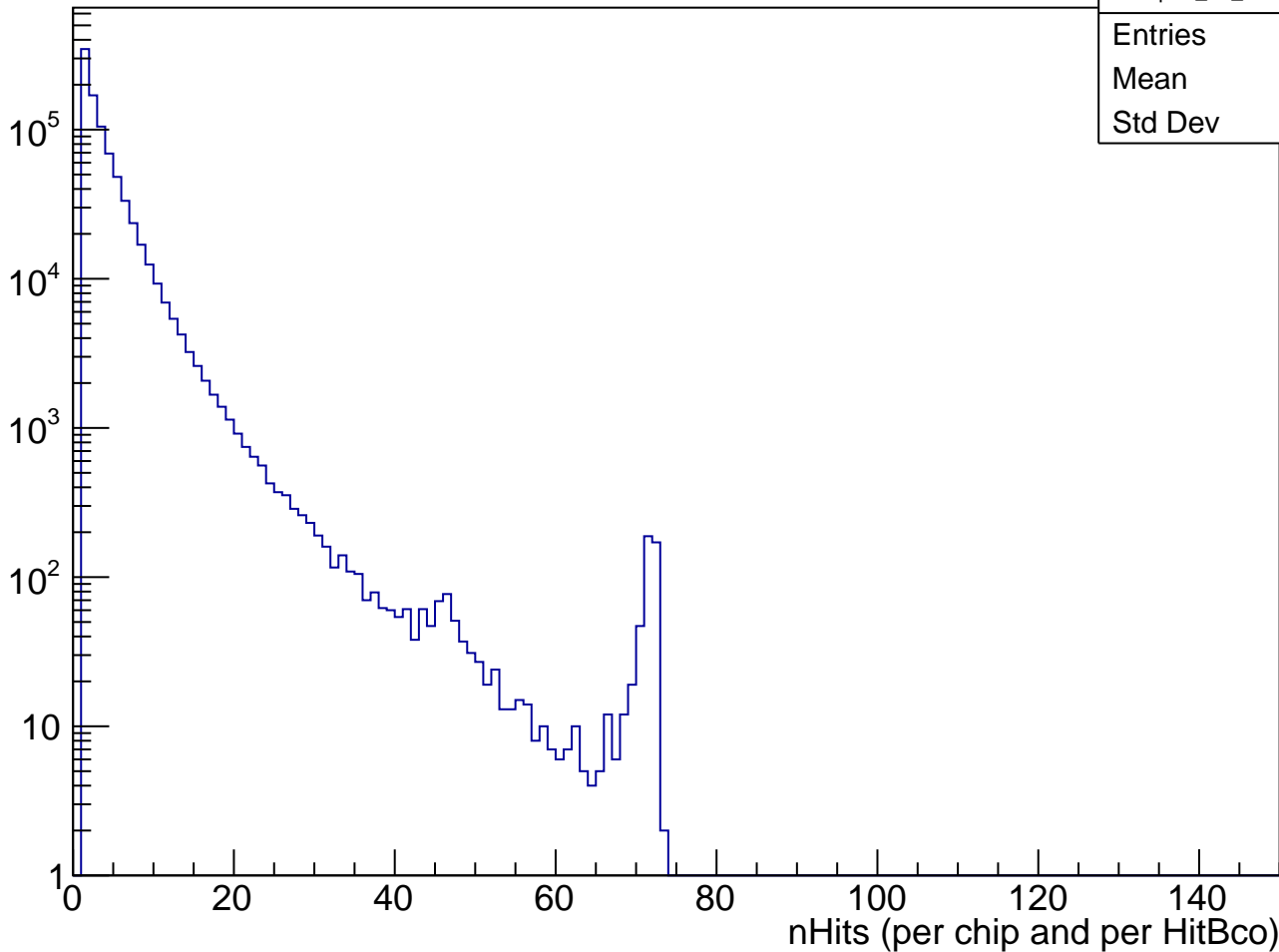
Entries 791700

Mean 3.093

Std Dev 3.599

nChipHit_F6_Fch10_Chip13

Entries



nChipHit_F6_Fch10_Chip13

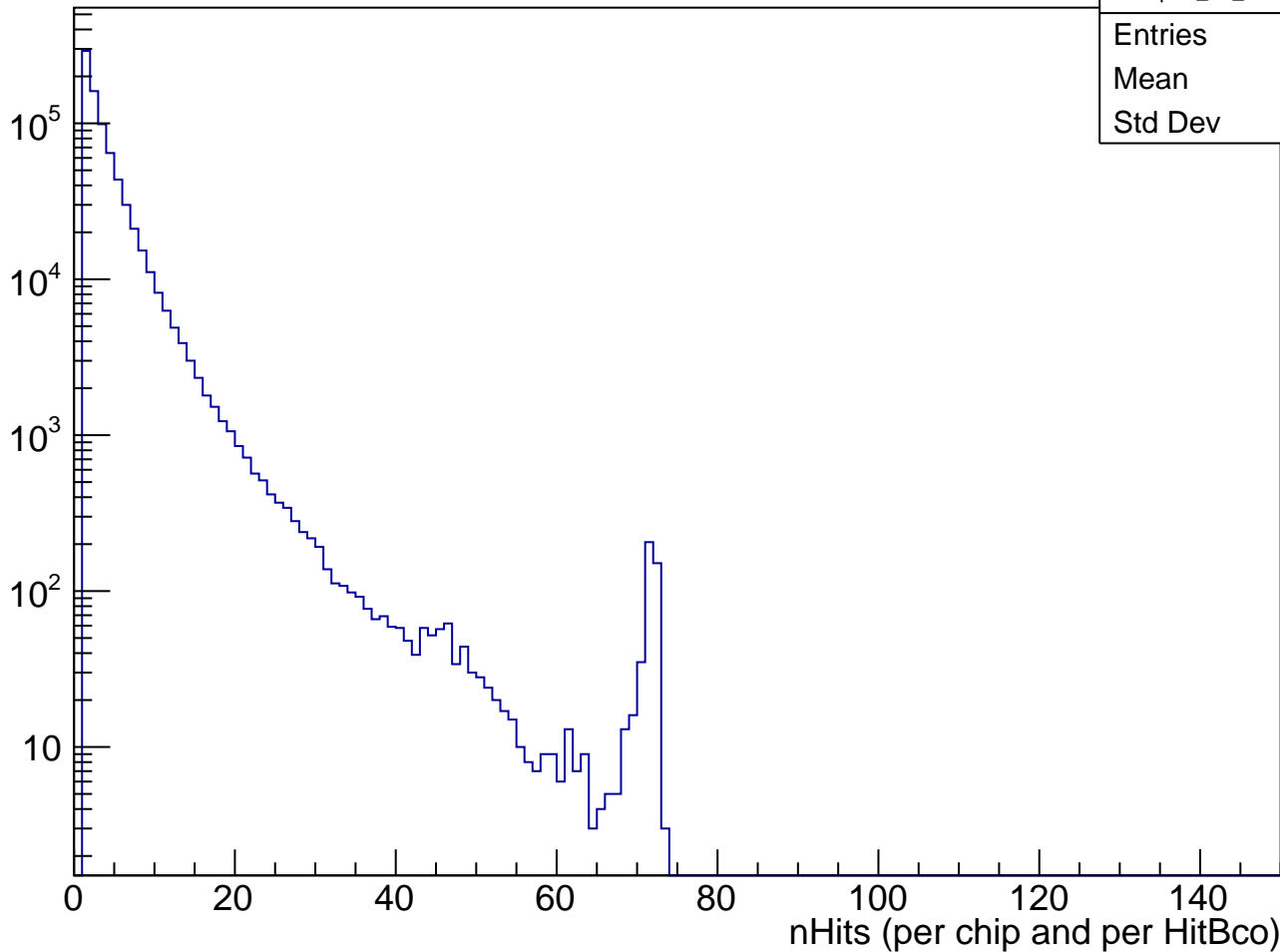
Entries 869913

Mean 3.248

Std Dev 3.963

nChipHit_F6_Fch10_Chip14

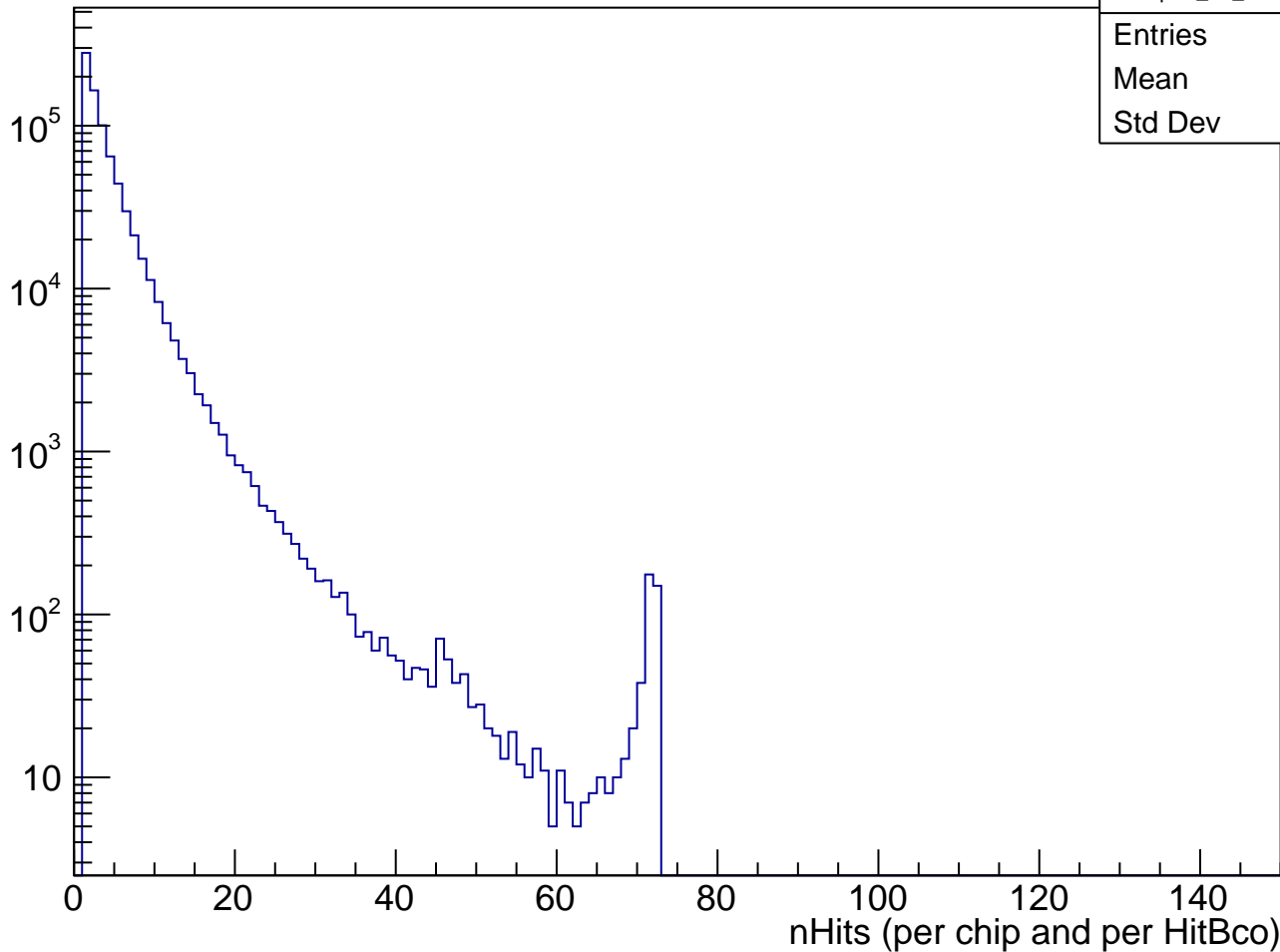
Entries



nChipHit_F6_Fch10_Chip14	
Entries	777425
Mean	3.307
Std Dev	4.011

nChipHit_F6_Fch10_Chip15

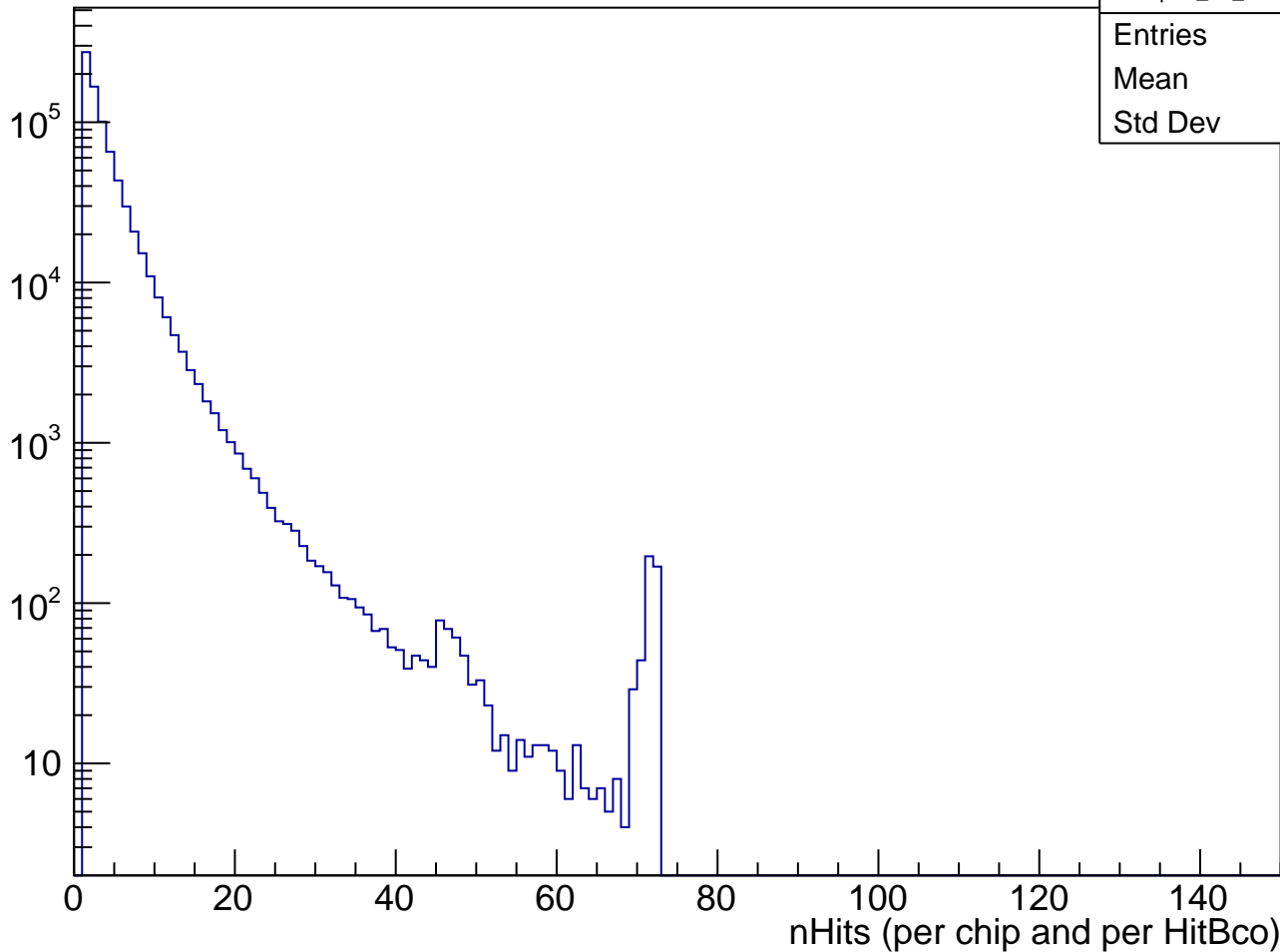
Entries



nChipHit_F6_Fch10_Chip15	
Entries	772157
Mean	3.328
Std Dev	3.988

nChipHit_F6_Fch10_Chip16

Entries



nChipHit_F6_Fch10_Chip16

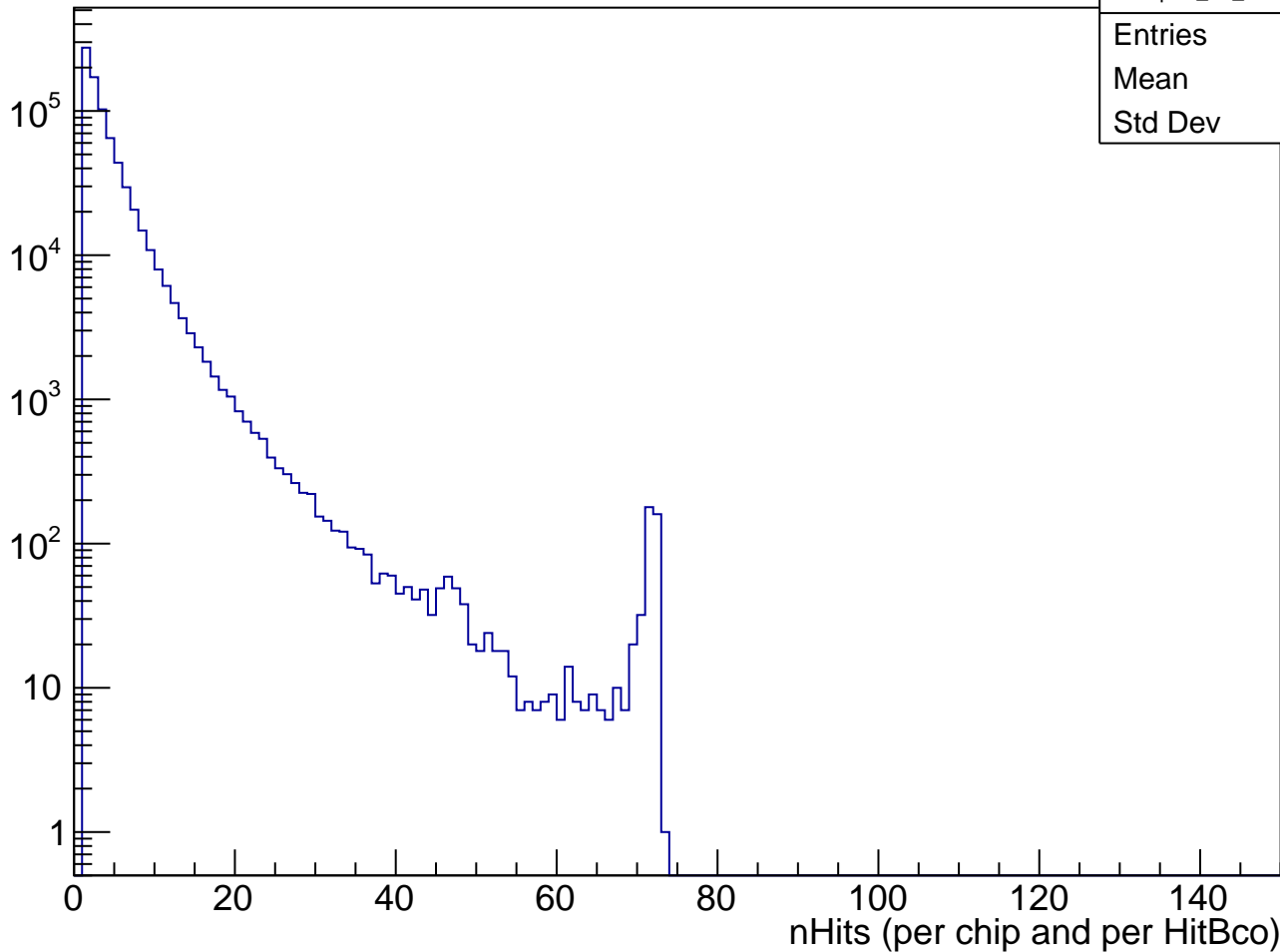
Entries 766679

Mean 3.335

Std Dev 4.033

nChipHit_F6_Fch10_Chip17

Entries



nChipHit_F6_Fch10_Chip17

Entries 772758

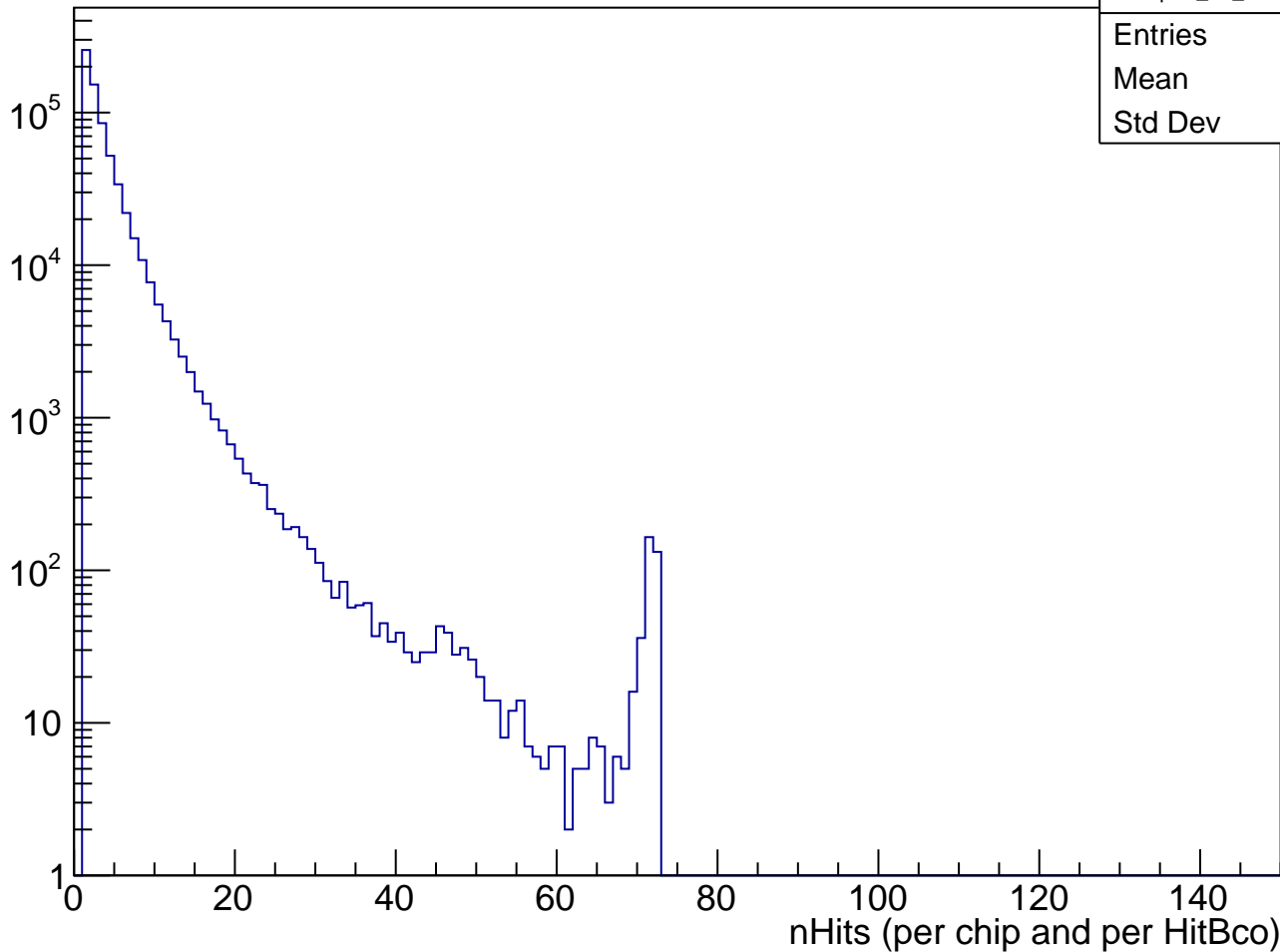
Mean 3.306

Std Dev 3.947

nHits (per chip and per HitBco)

nChipHit_F6_Fch10_Chip18

Entries



nChipHit_F6_Fch10_Chip18

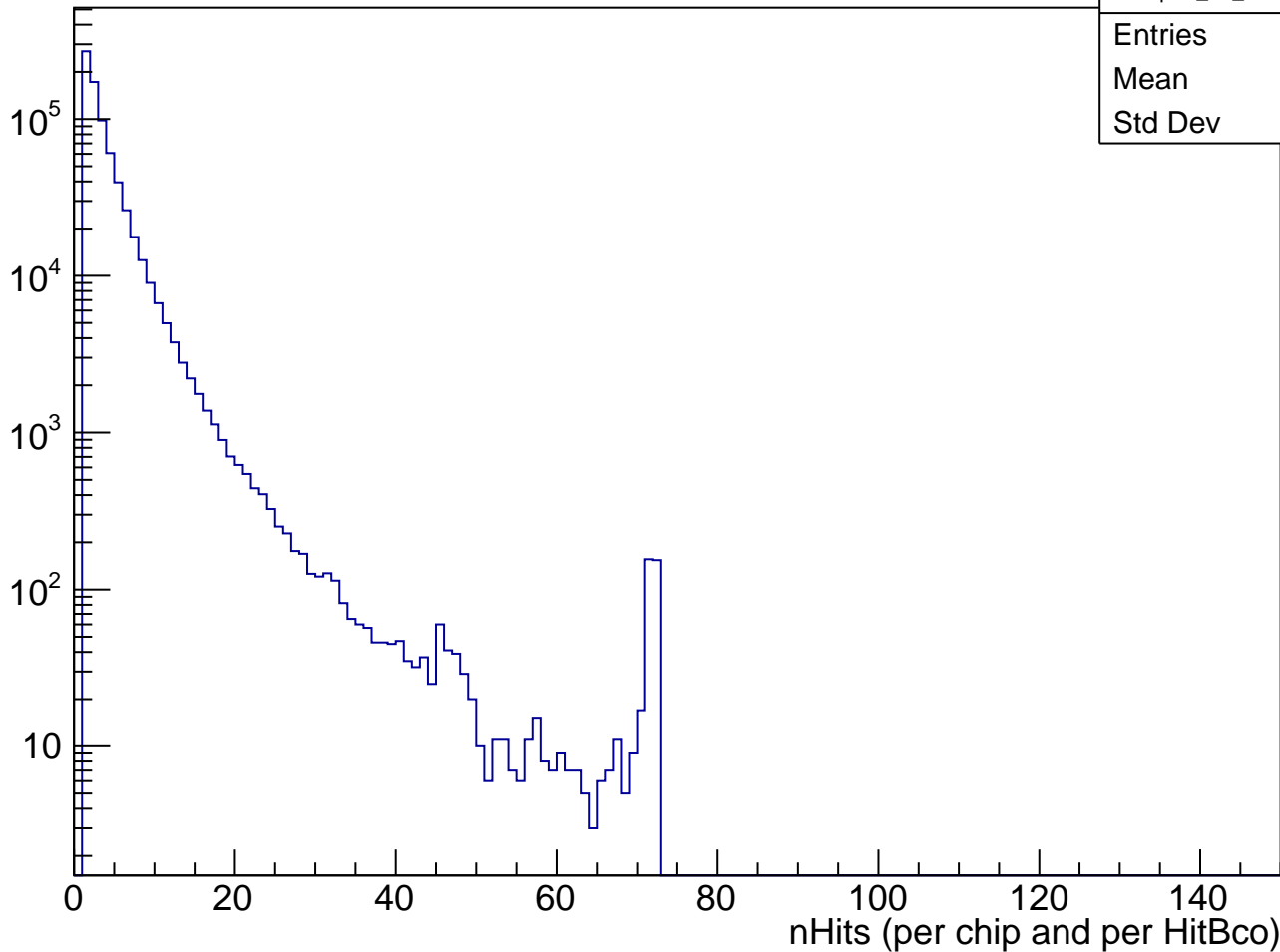
Entries 663684

Mean 3.033

Std Dev 3.694

nChipHit_F6_Fch10_Chip19

Entries



nChipHit_F6_Fch10_Chip19

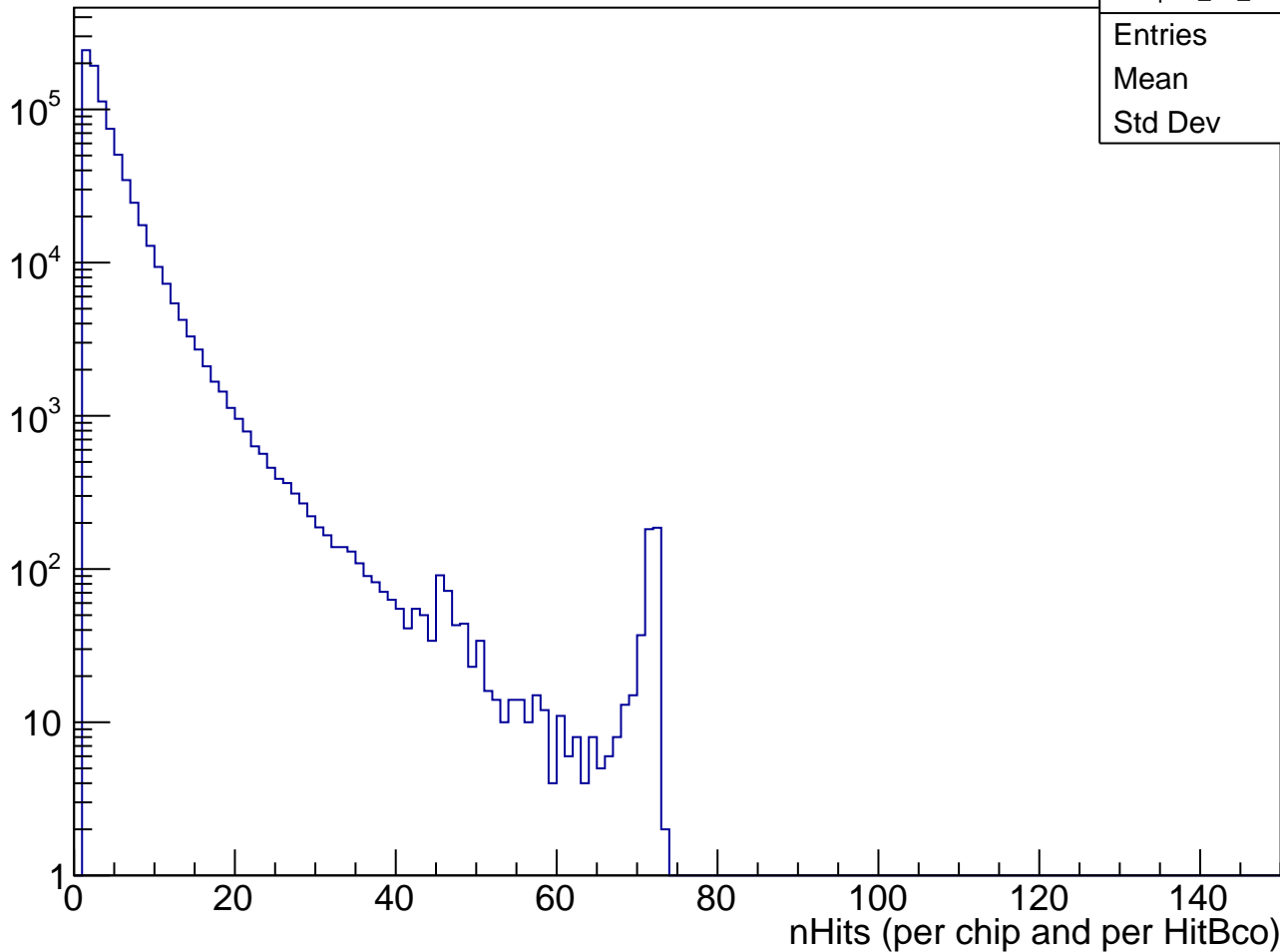
Entries 738077

Mean 3.109

Std Dev 3.67

nChipHit_F6_Fch10_Chip2

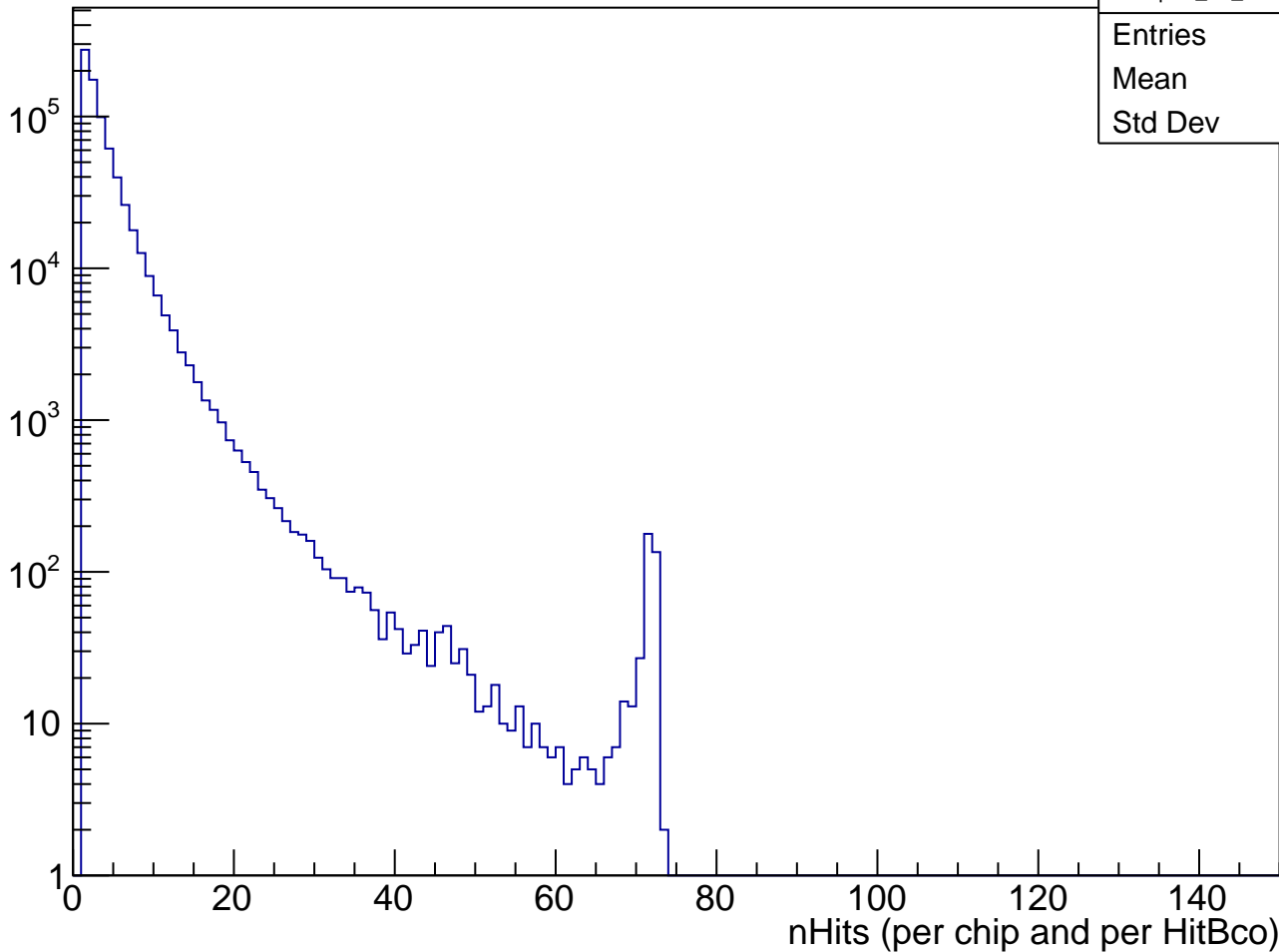
Entries



nChipHit_F6_Fch10_Chip2	
Entries	809858
Mean	3.536
Std Dev	4.056

nChipHit_F6_Fch10_Chip20

Entries



nChipHit_F6_Fch10_Chip20

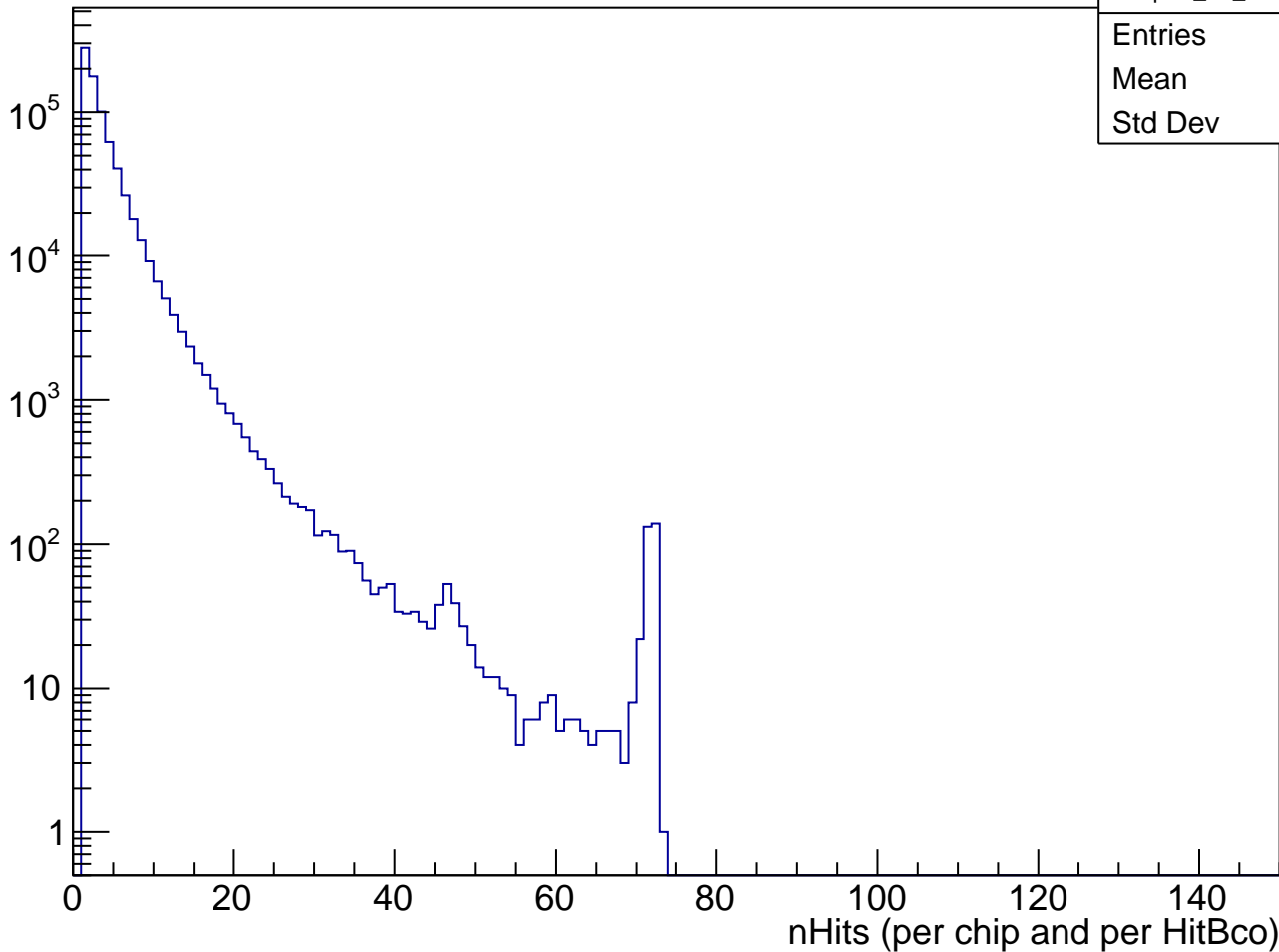
Entries 747069

Mean 3.099

Std Dev 3.673

nChipHit_F6_Fch10_Chip21

Entries

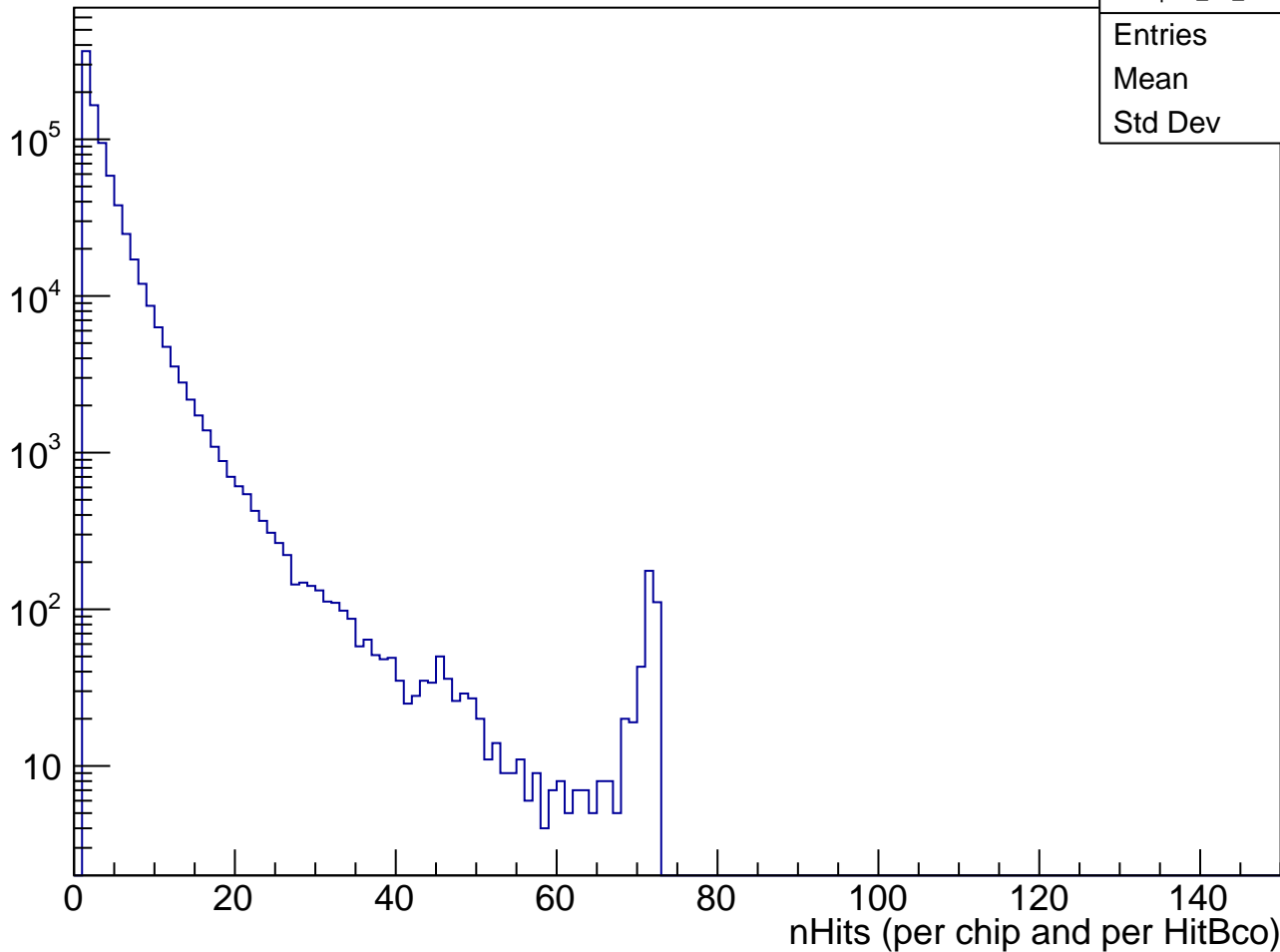


nChipHit_F6_Fch10_Chip21	
Entries	759088
Mean	3.099
Std Dev	3.621

nHits (per chip and per HitBco)

nChipHit_F6_Fch10_Chip22

Entries



nChipHit_F6_Fch10_Chip22

Entries 814672

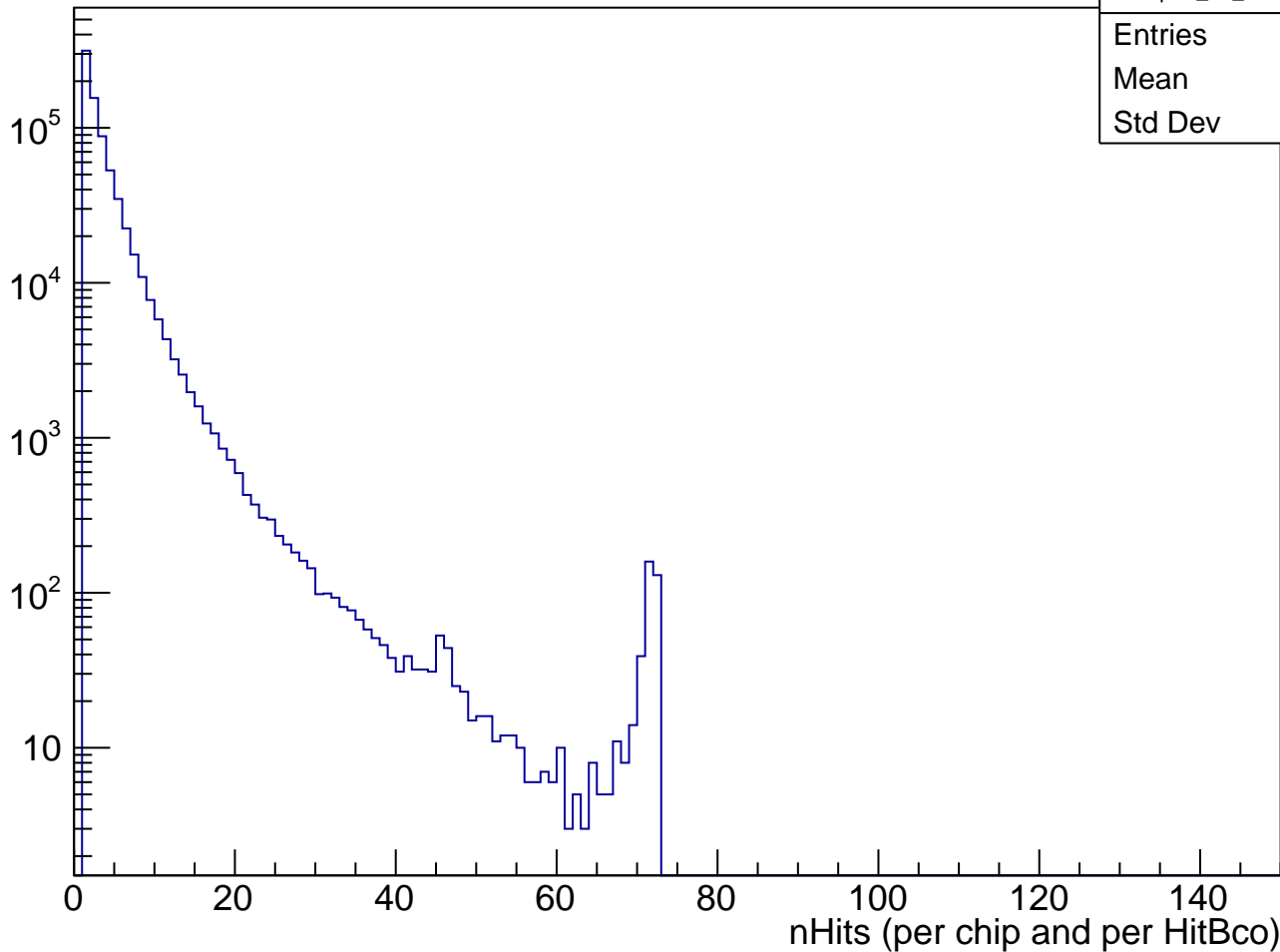
Mean 2.844

Std Dev 3.542

nHits (per chip and per HitBco)

nChipHit_F6_Fch10_Chip23

Entries



nChipHit_F6_Fch10_Chip23

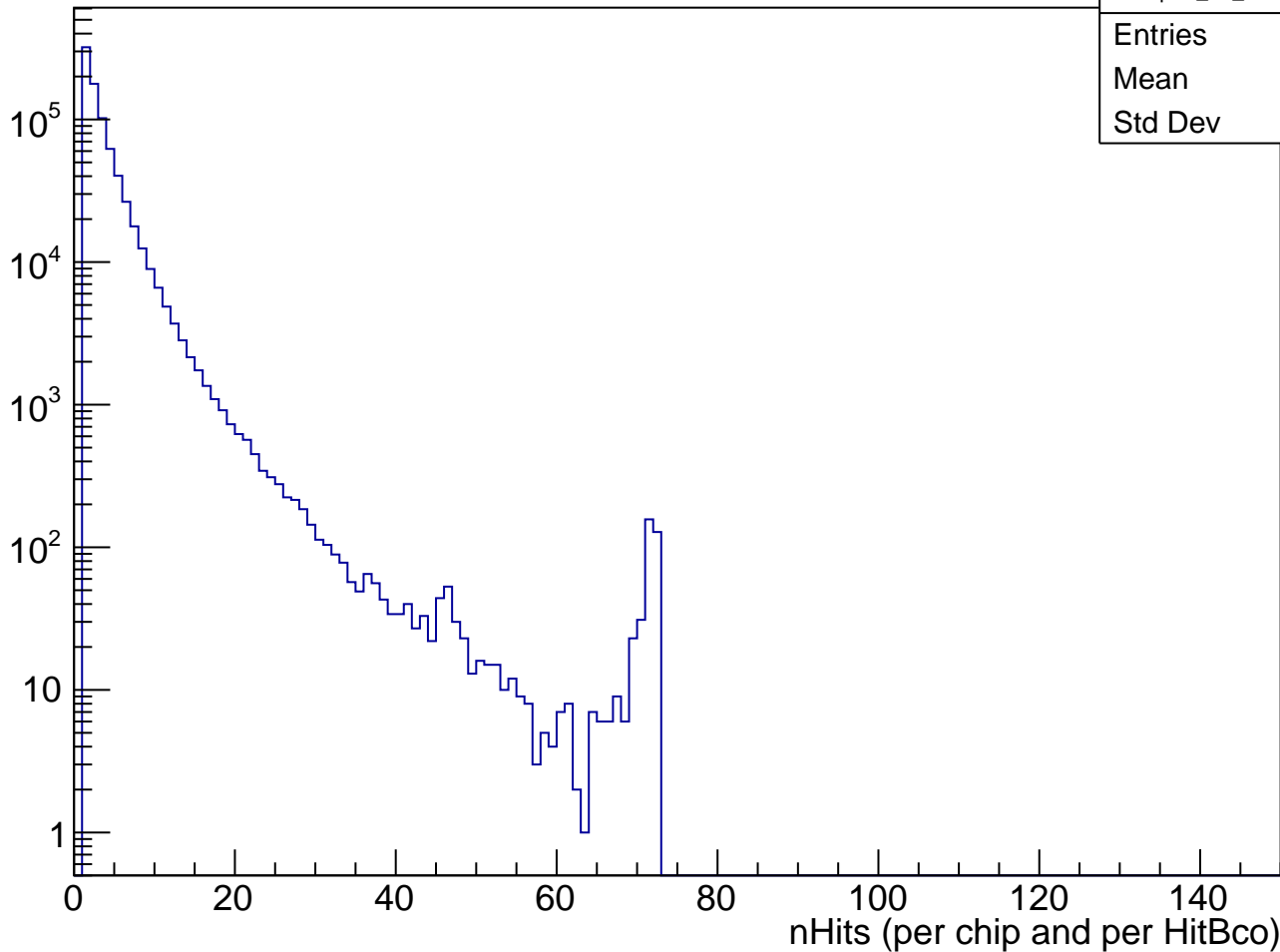
Entries 731025

Mean 2.889

Std Dev 3.595

nChipHit_F6_Fch10_Chip24

Entries

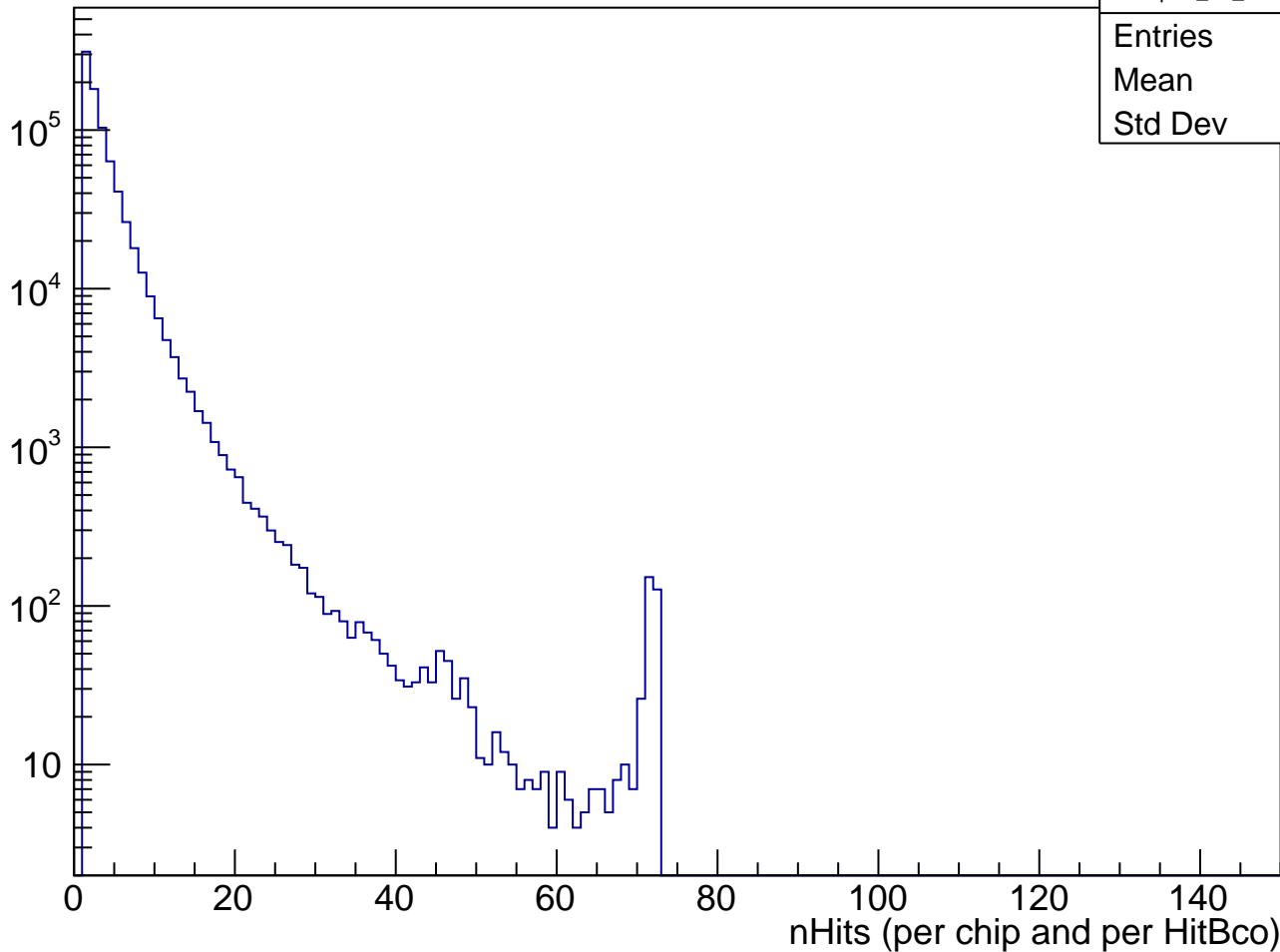


nChipHit_F6_Fch10_Chip24	
Entries	799742
Mean	2.965
Std Dev	3.536

nHits (per chip and per HitBco)

nChipHit_F6_Fch10_Chip25

Entries



nChipHit_F6_Fch10_Chip25

Entries 797621

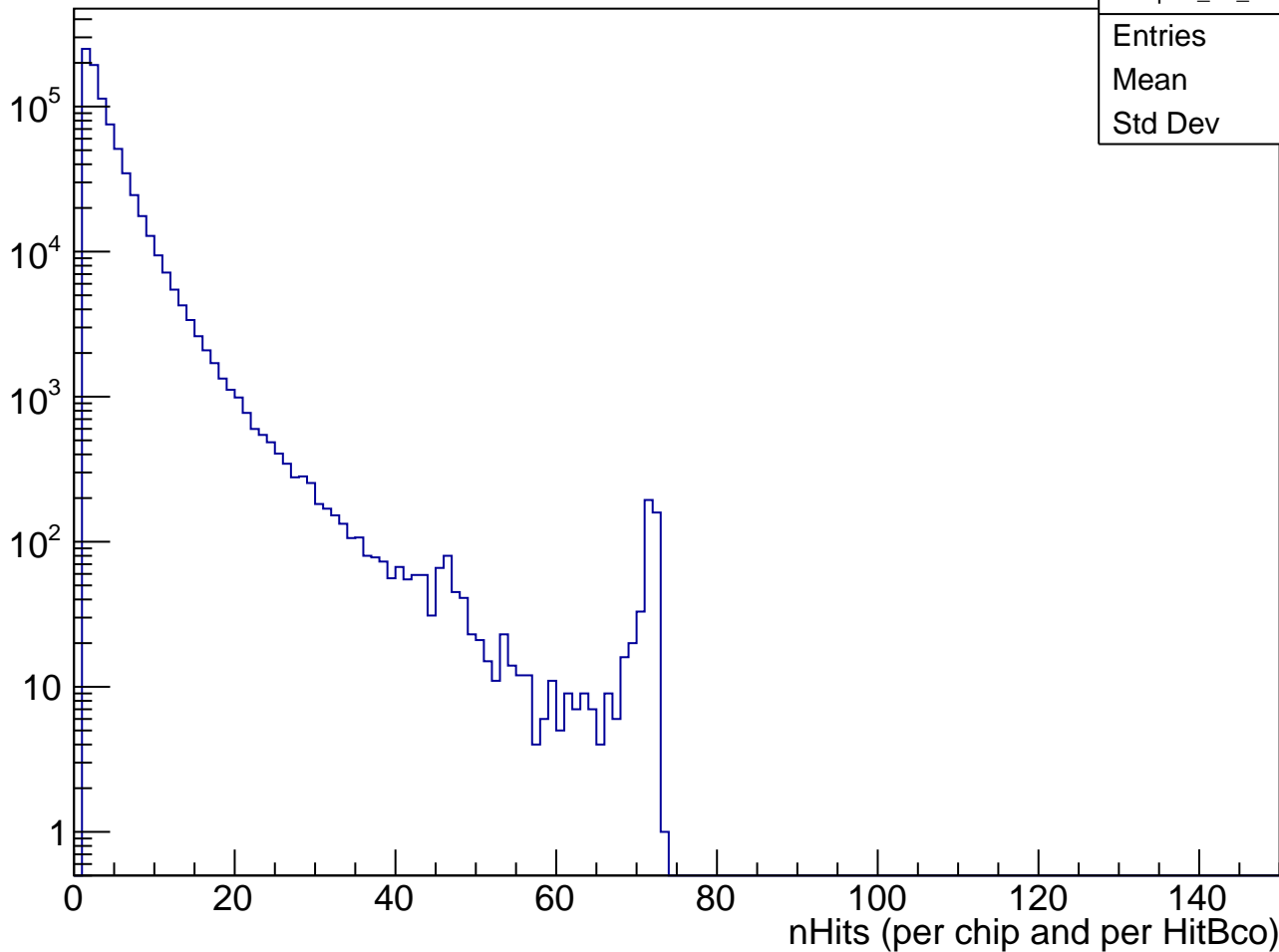
Mean 2.982

Std Dev 3.525

nHits (per chip and per HitBco)

nChipHit_F6_Fch10_Chip3

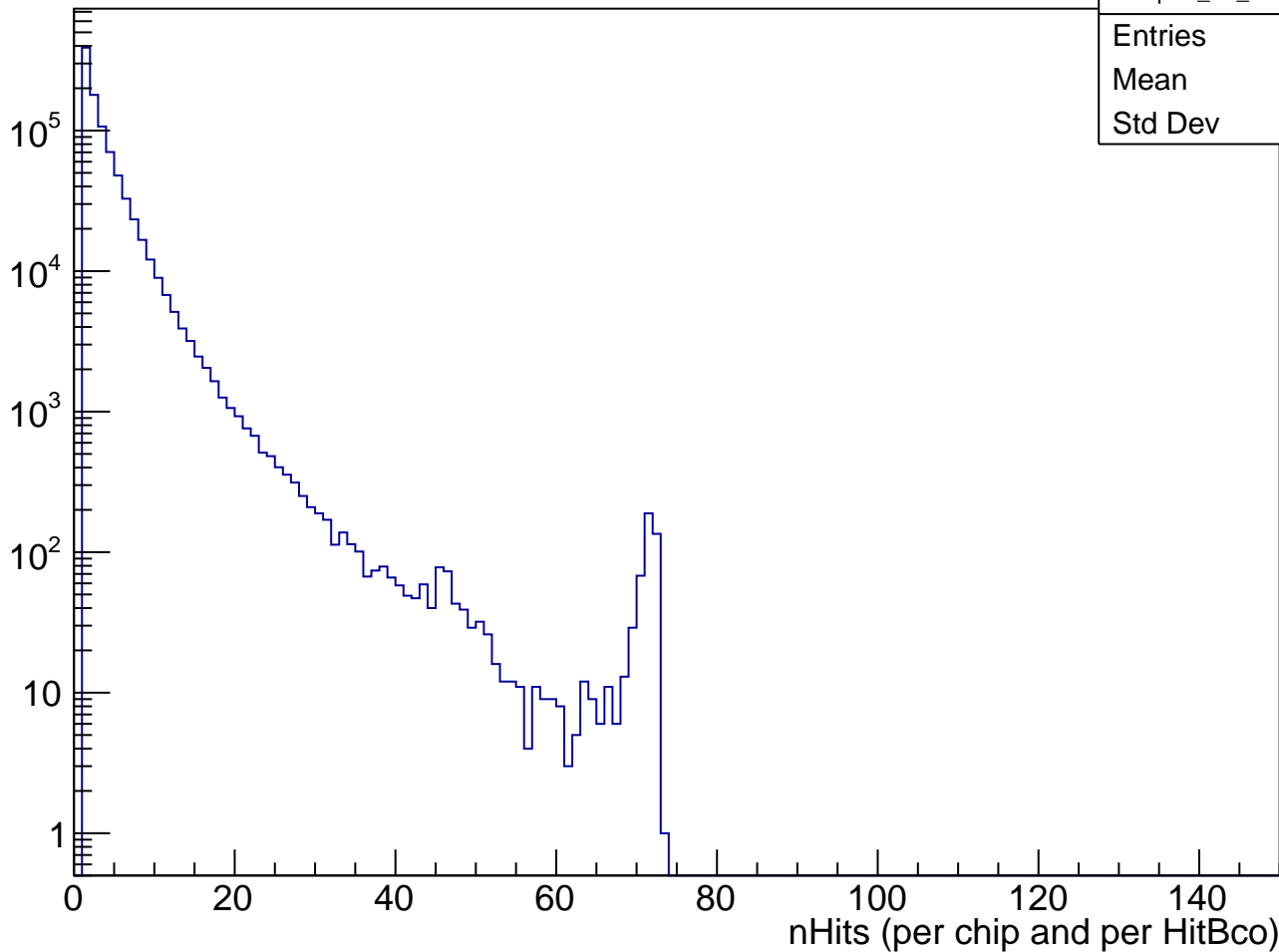
Entries



nChipHit_F6_Fch10_Chip3	
Entries	818205
Mean	3.513
Std Dev	4.021

nChipHit_F6_Fch10_Chip4

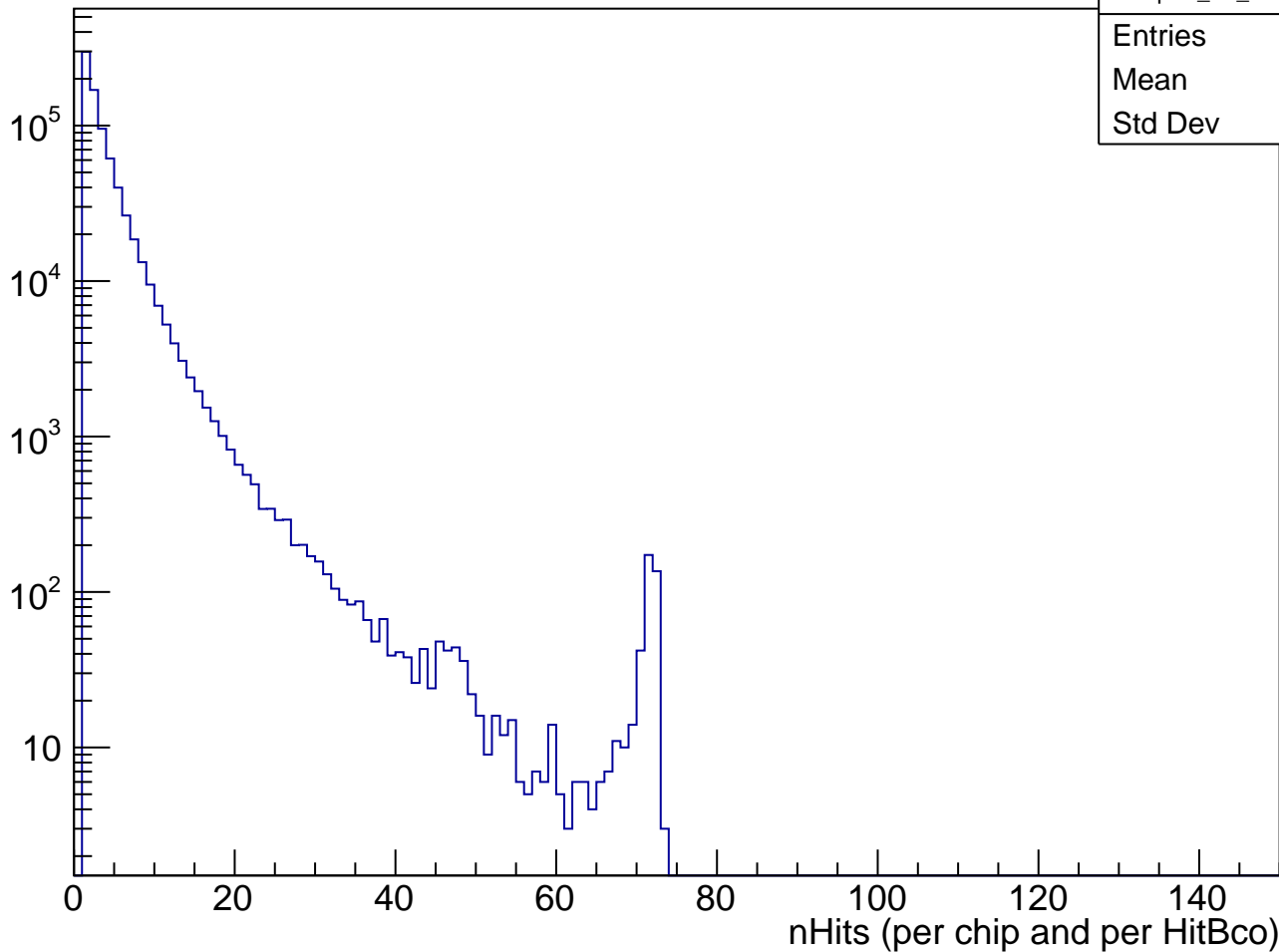
Entries



nChipHit_F6_Fch10_Chip4	
Entries	921906
Mean	3.111
Std Dev	3.854

nChipHit_F6_Fch10_Chip5

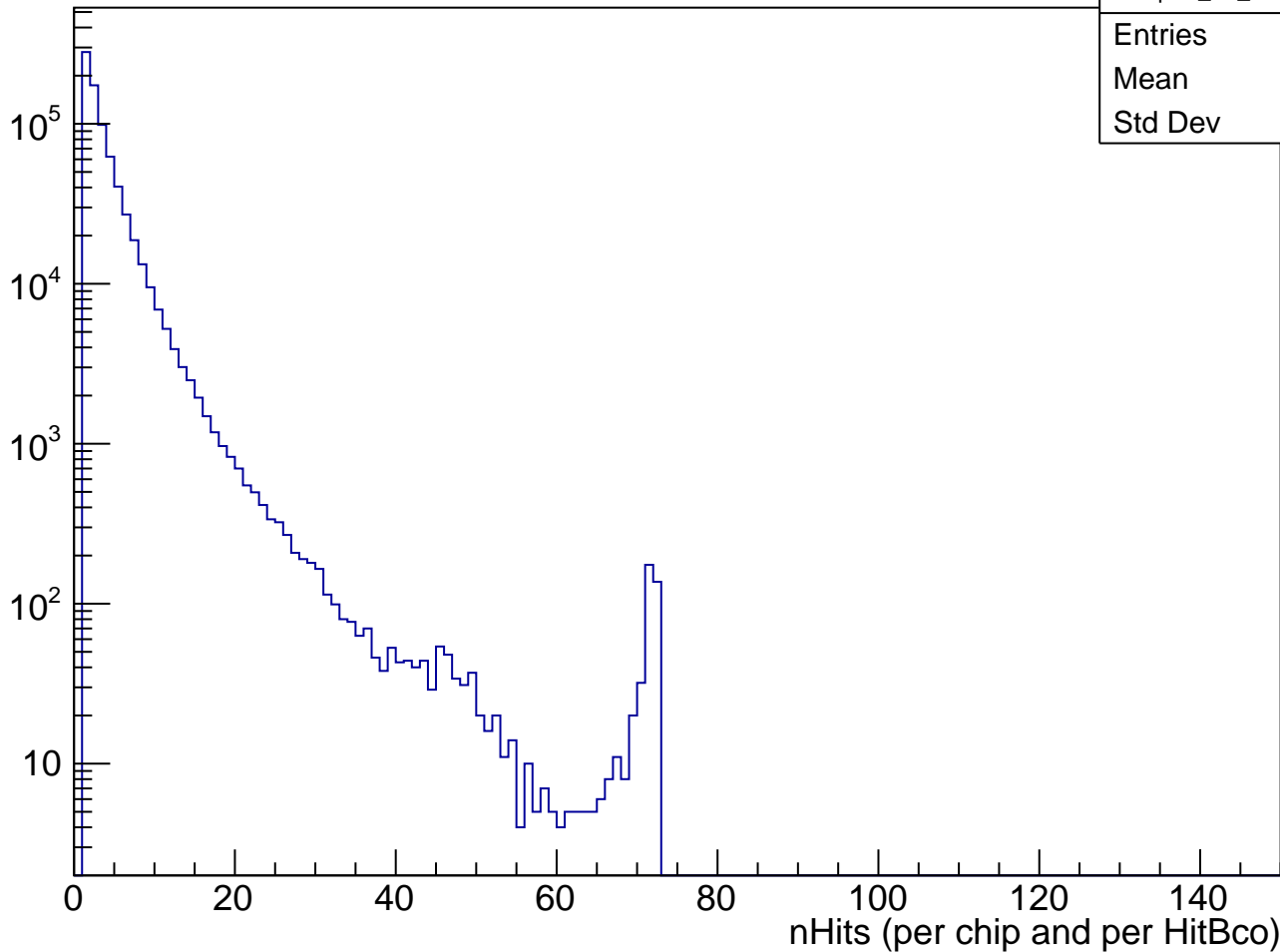
Entries



nChipHit_F6_Fch10_Chip5	
Entries	765838
Mean	3.094
Std Dev	3.746

nChipHit_F6_Fch10_Chip6

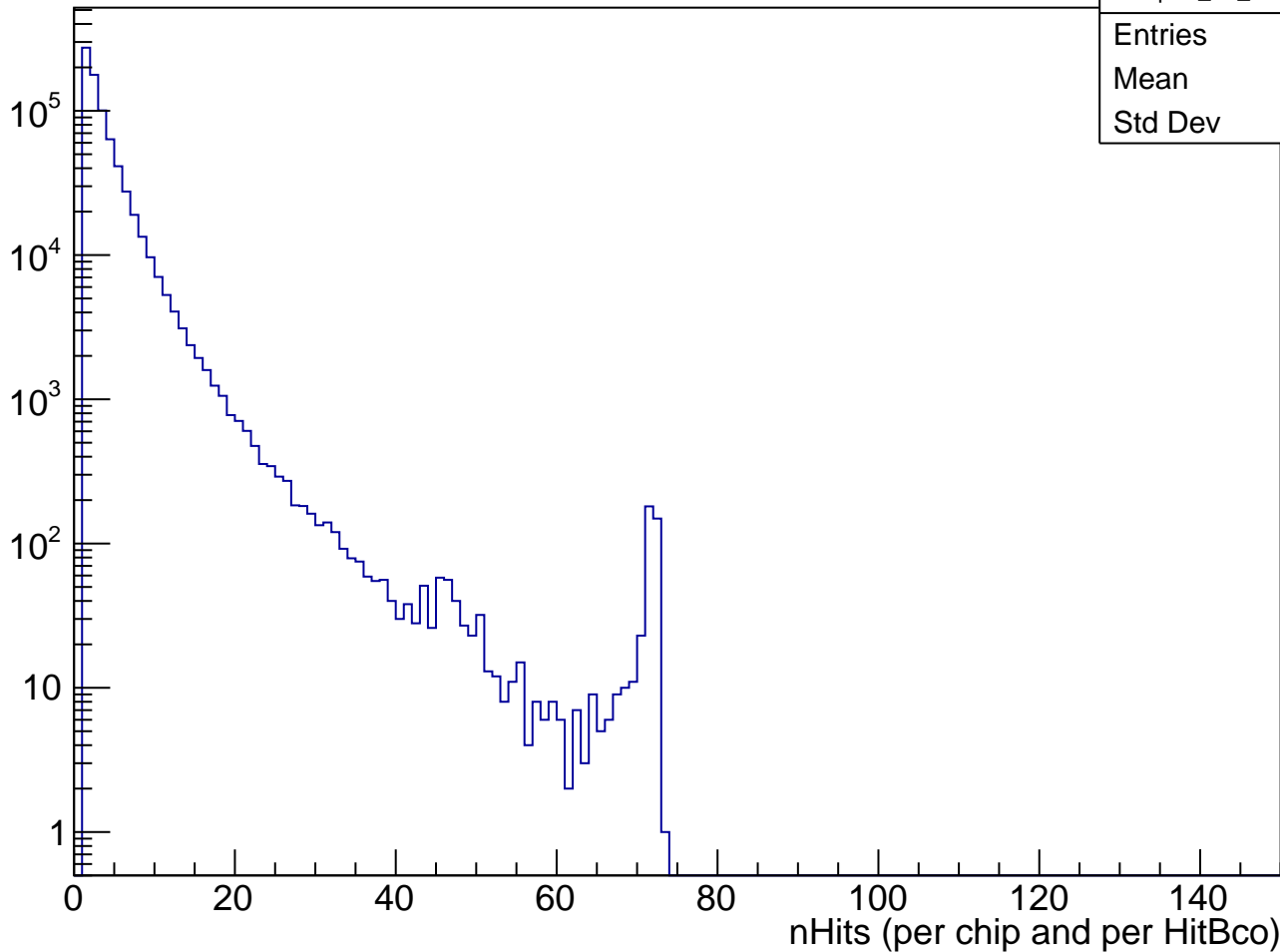
Entries



nChipHit_F6_Fch10_Chip6	
Entries	758355
Mean	3.139
Std Dev	3.752

nChipHit_F6_Fch10_Chip7

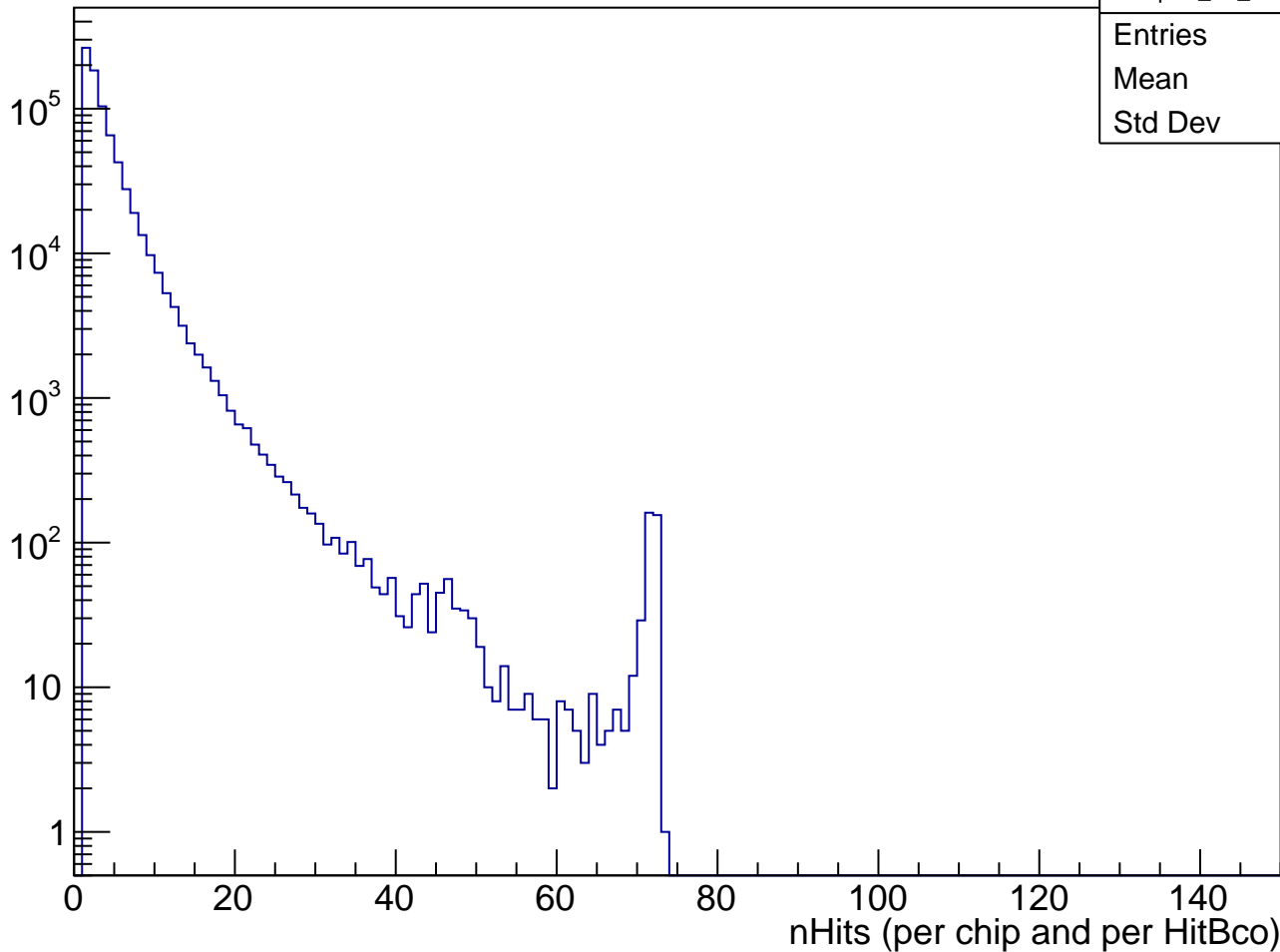
Entries



nChipHit_F6_Fch10_Chip7	
Entries	760530
Mean	3.167
Std Dev	3.749

nChipHit_F6_Fch10_Chip8

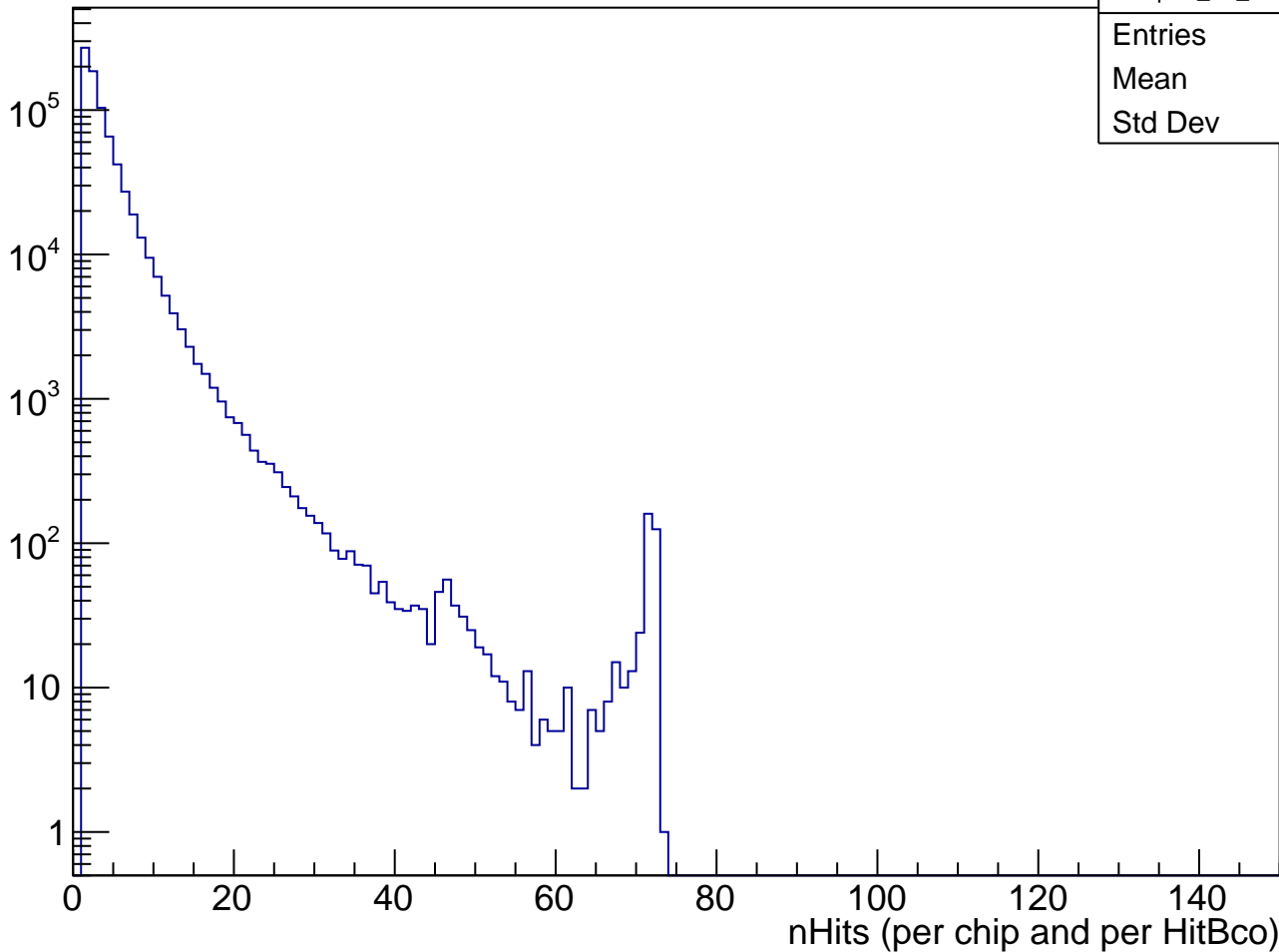
Entries



nChipHit_F6_Fch10_Chip8	
Entries	763714
Mean	3.199
Std Dev	3.727

nChipHit_F6_Fch10_Chip9

Entries

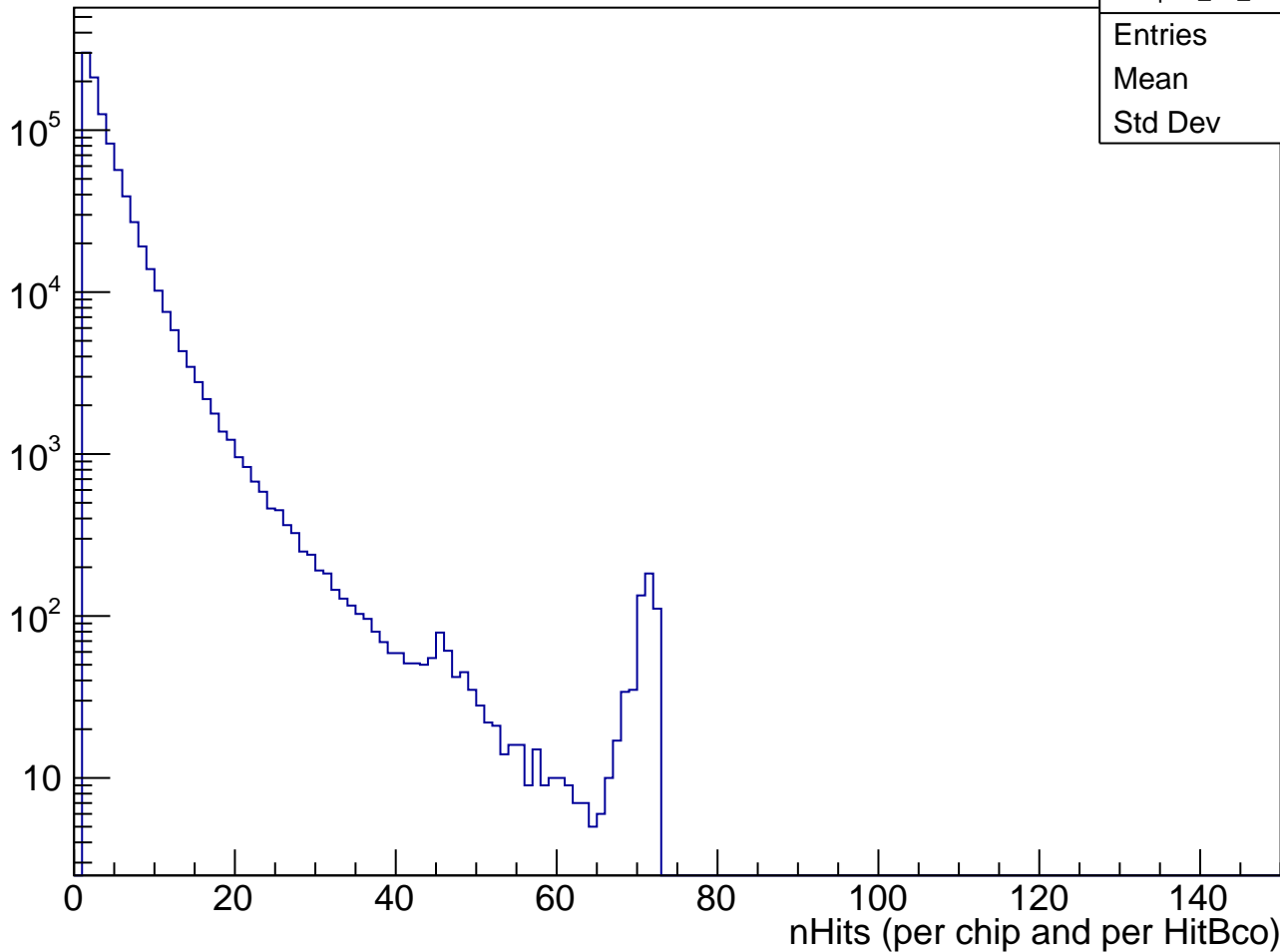


nChipHit_F6_Fch10_Chip9	
Entries	768894
Mean	3.14
Std Dev	3.648

nHits (per chip and per HitBco)

nChipHit_F6_Fch11_Chip0

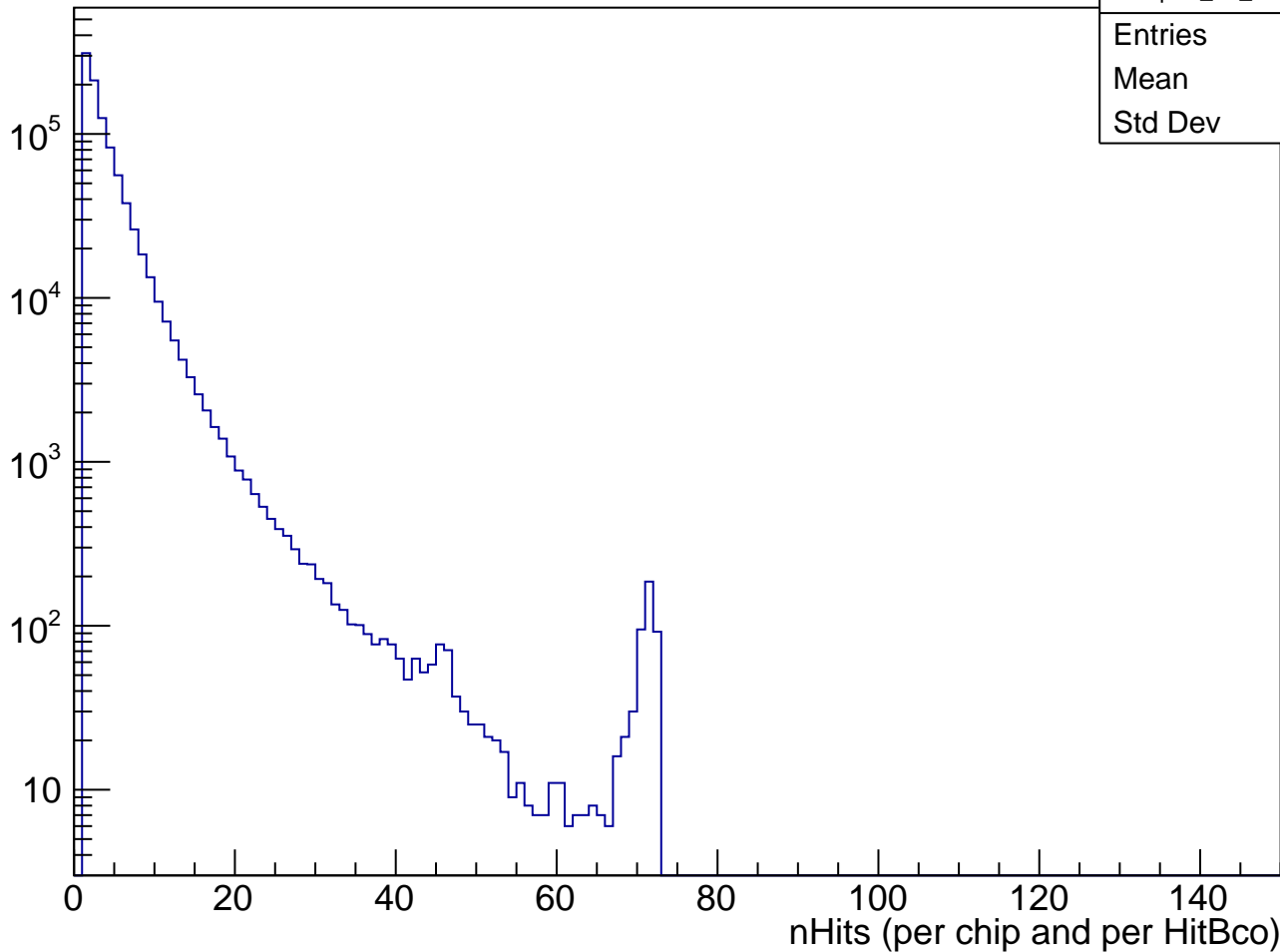
Entries



nChipHit_F6_Fch11_Chip0	
Entries	925030
Mean	3.417
Std Dev	3.954

nChipHit_F6_Fch11_Chip1

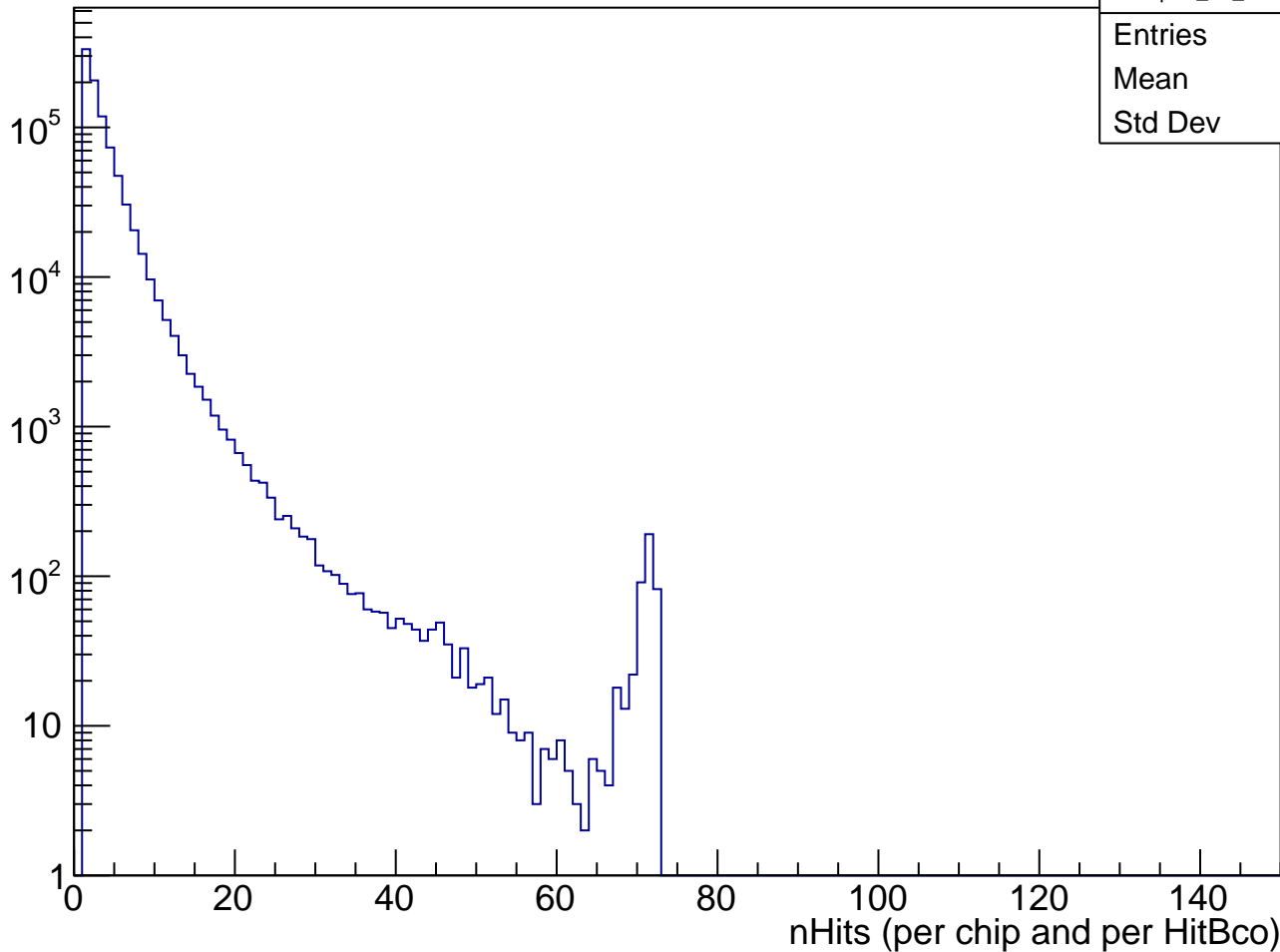
Entries



nChipHit_F6_Fch11_Chip1	
Entries	928244
Mean	3.339
Std Dev	3.841

nChipHit_F6_Fch11_Chip10

Entries

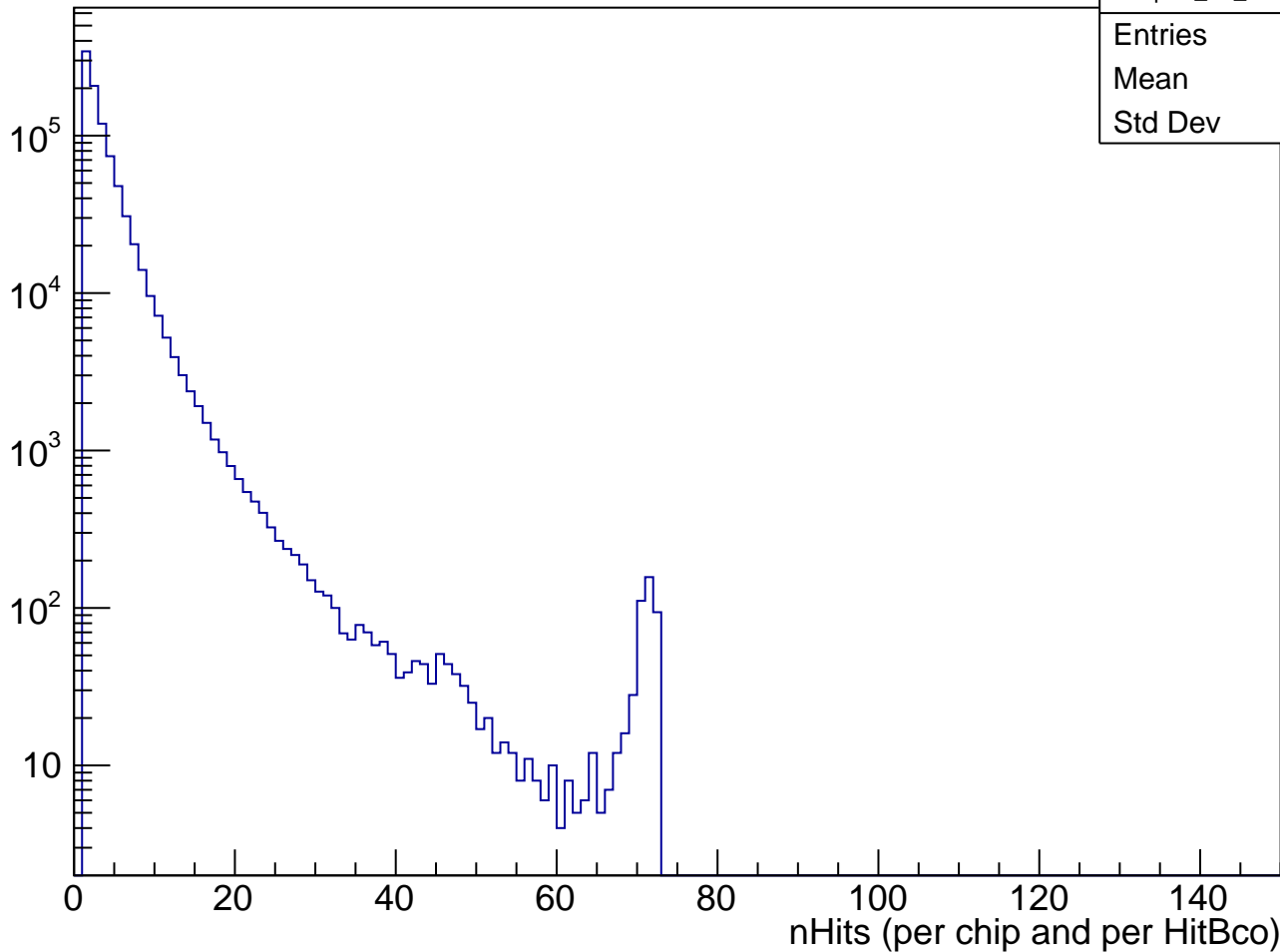


nChipHit_F6_Fch11_Chip10	
Entries	886339
Mean	3.013
Std Dev	3.522

nHits (per chip and per HitBco)

nChipHit_F6_Fch11_Chip11

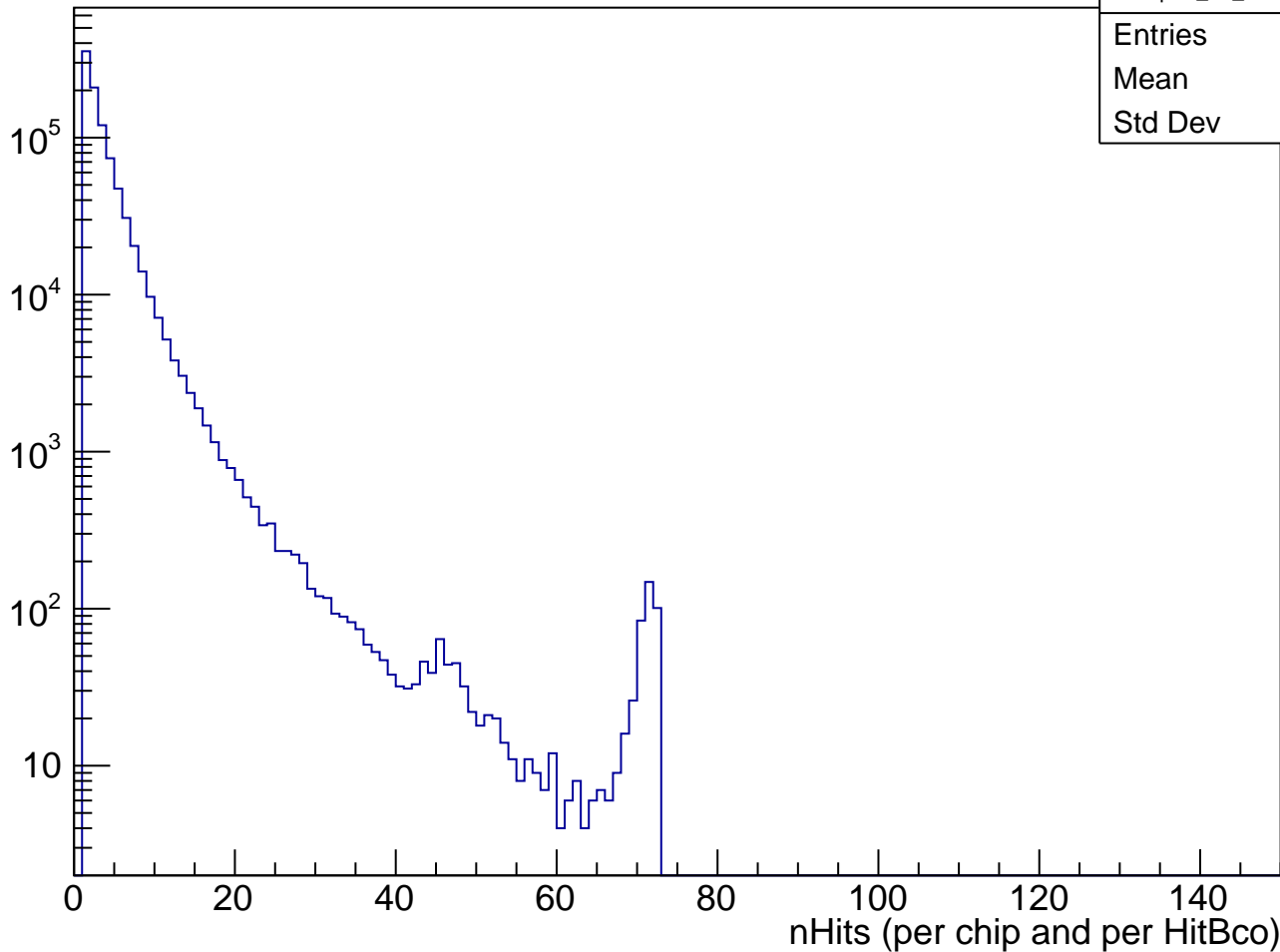
Entries



nChipHit_F6_Fch11_Chip11	
Entries	899176
Mean	2.995
Std Dev	3.524

nChipHit_F6_Fch11_Chip12

Entries



nChipHit_F6_Fch11_Chip12

Entries 912606

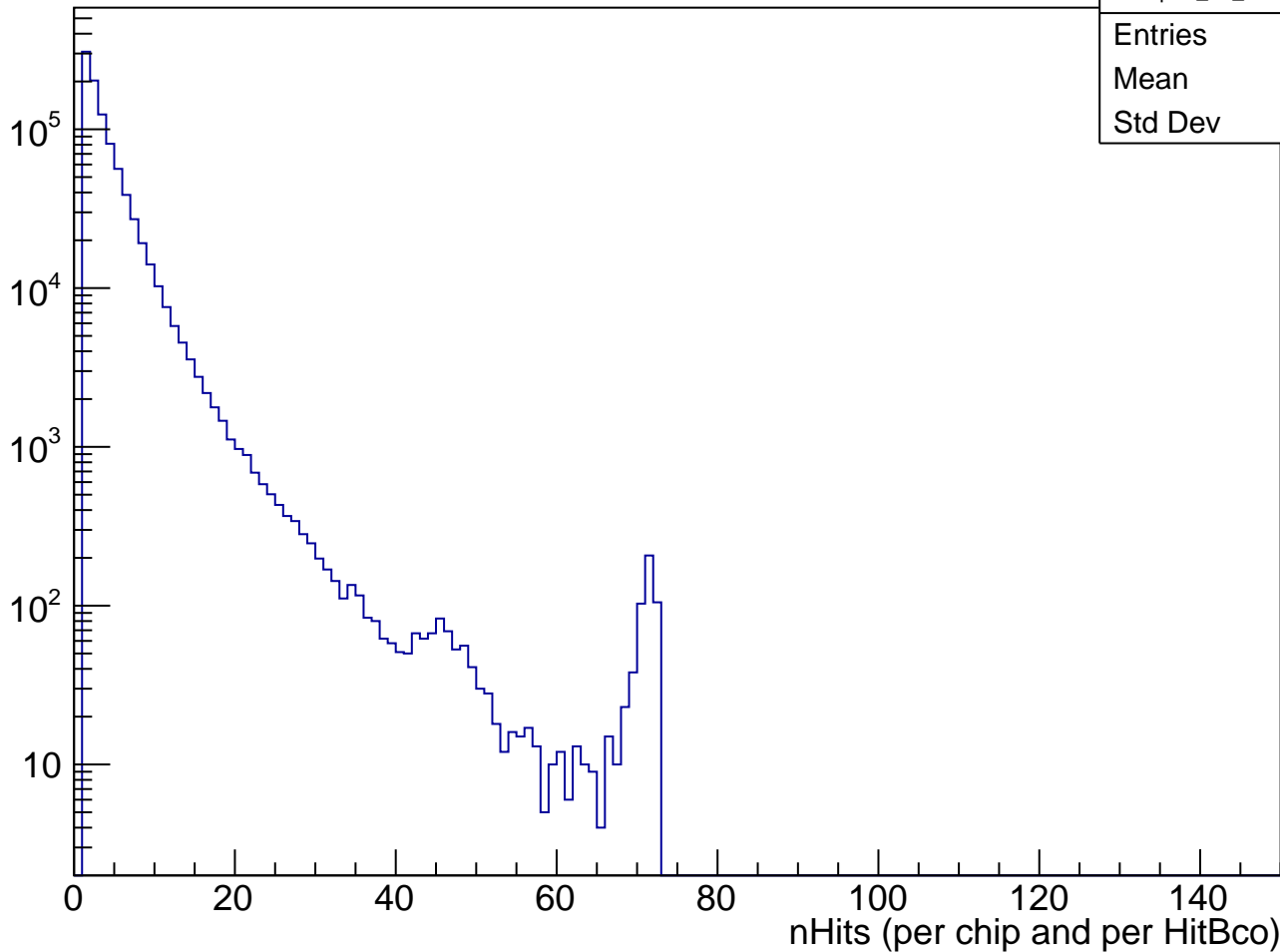
Mean 2.956

Std Dev 3.463

nHits (per chip and per HitBco)

nChipHit_F6_Fch11_Chip13

Entries



nChipHit_F6_Fch11_Chip13

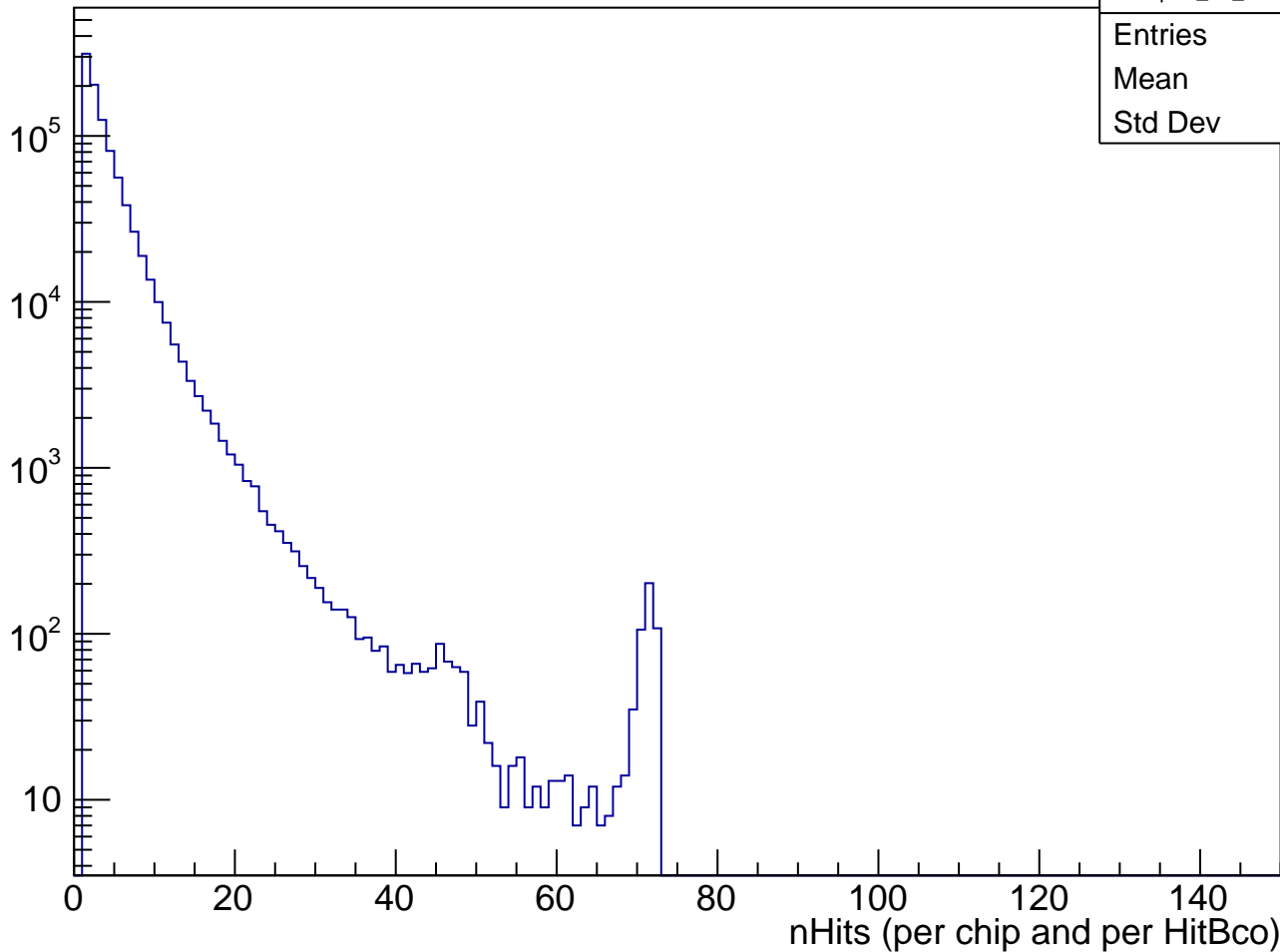
Entries 919907

Mean 3.421

Std Dev 3.988

nChipHit_F6_Fch11_Chip14

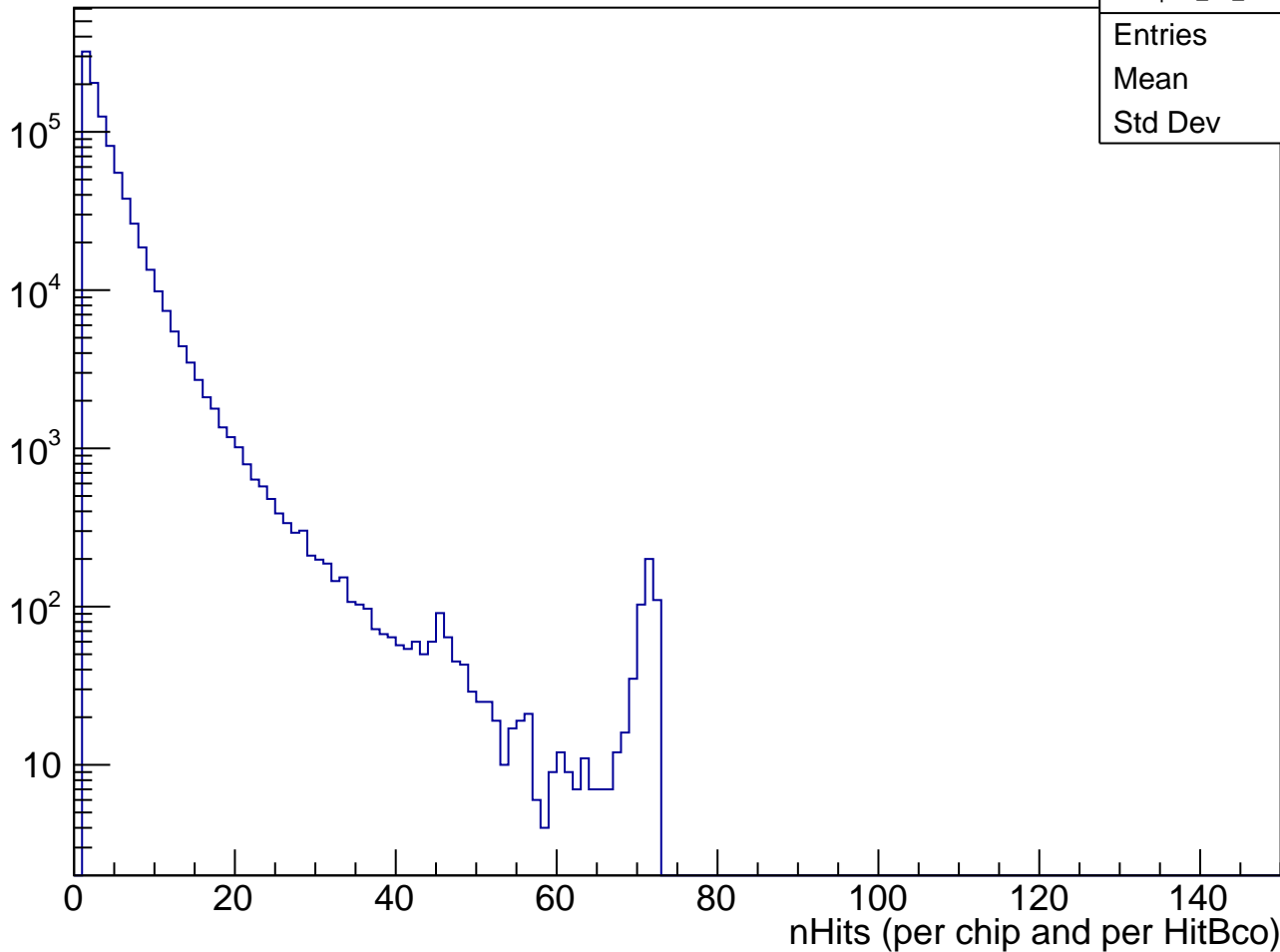
Entries



nChipHit_F6_Fch11_Chip14	
Entries	923904
Mean	3.389
Std Dev	3.963

nChipHit_F6_Fch11_Chip15

Entries



nChipHit_F6_Fch11_Chip15

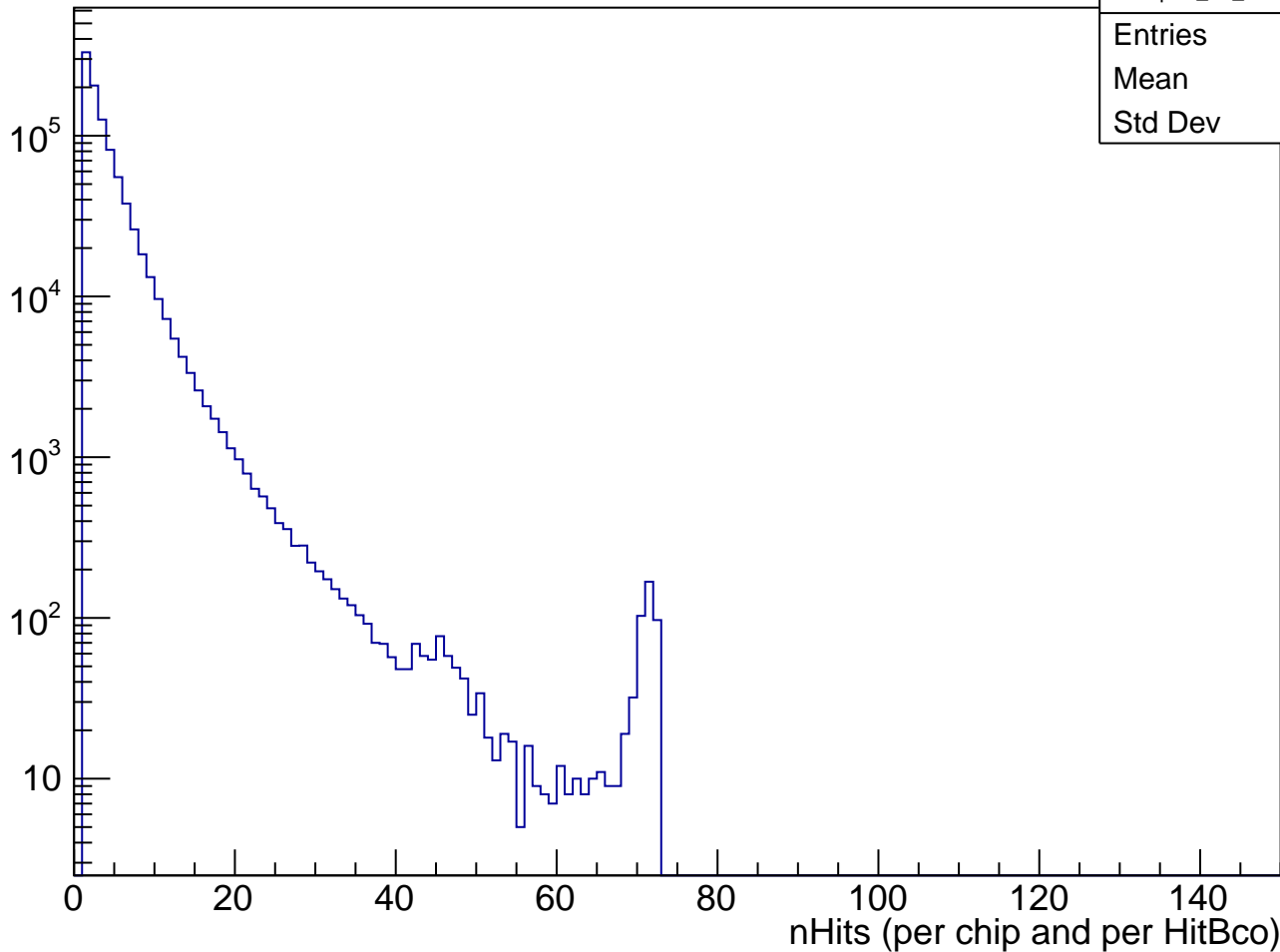
Entries 930674

Mean 3.349

Std Dev 3.918

nChipHit_F6_Fch11_Chip16

Entries



nChipHit_F6_Fch11_Chip16

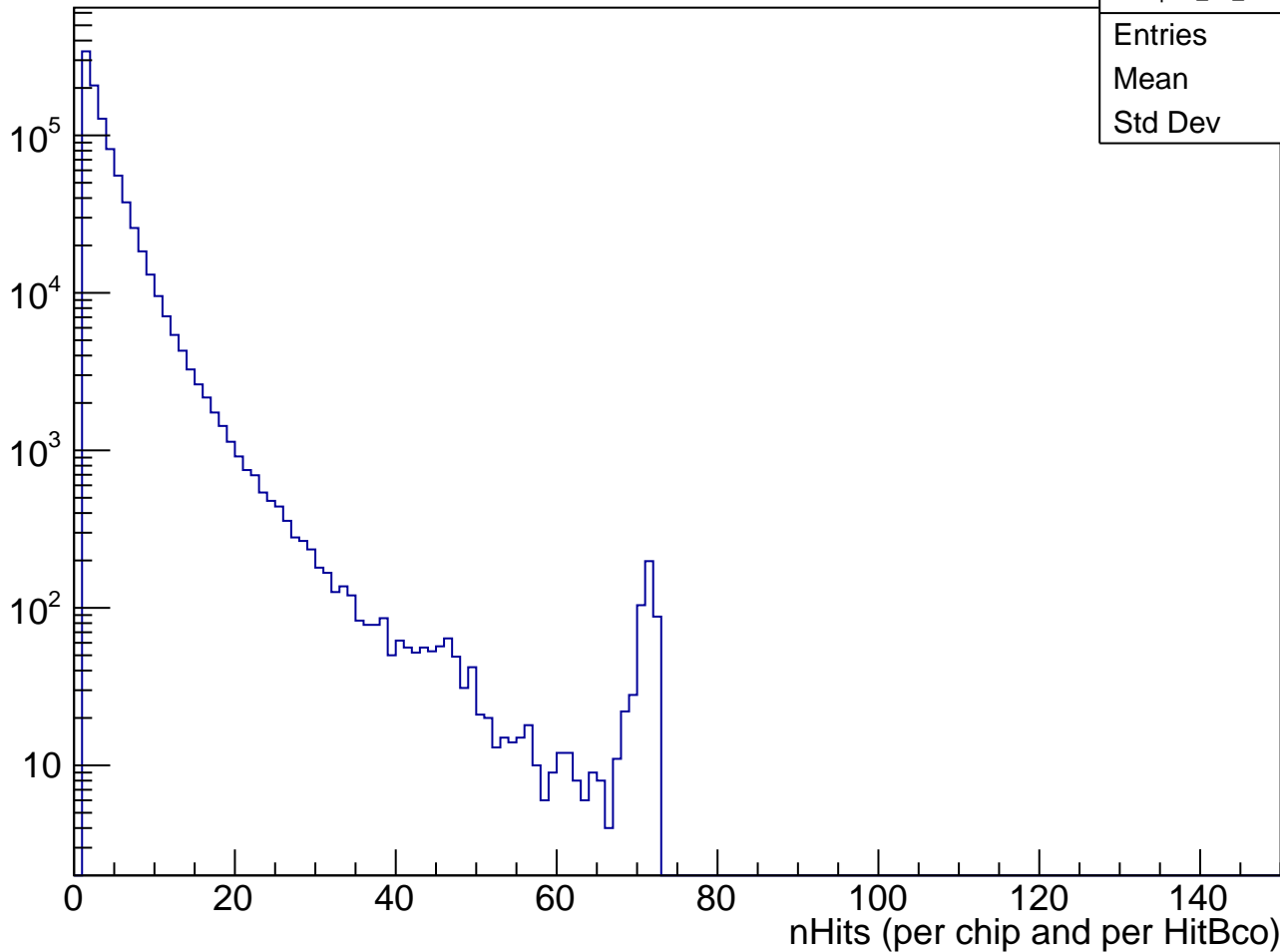
Entries 940315

Mean 3.307

Std Dev 3.851

nChipHit_F6_Fch11_Chip17

Entries



nChipHit_F6_Fch11_Chip17

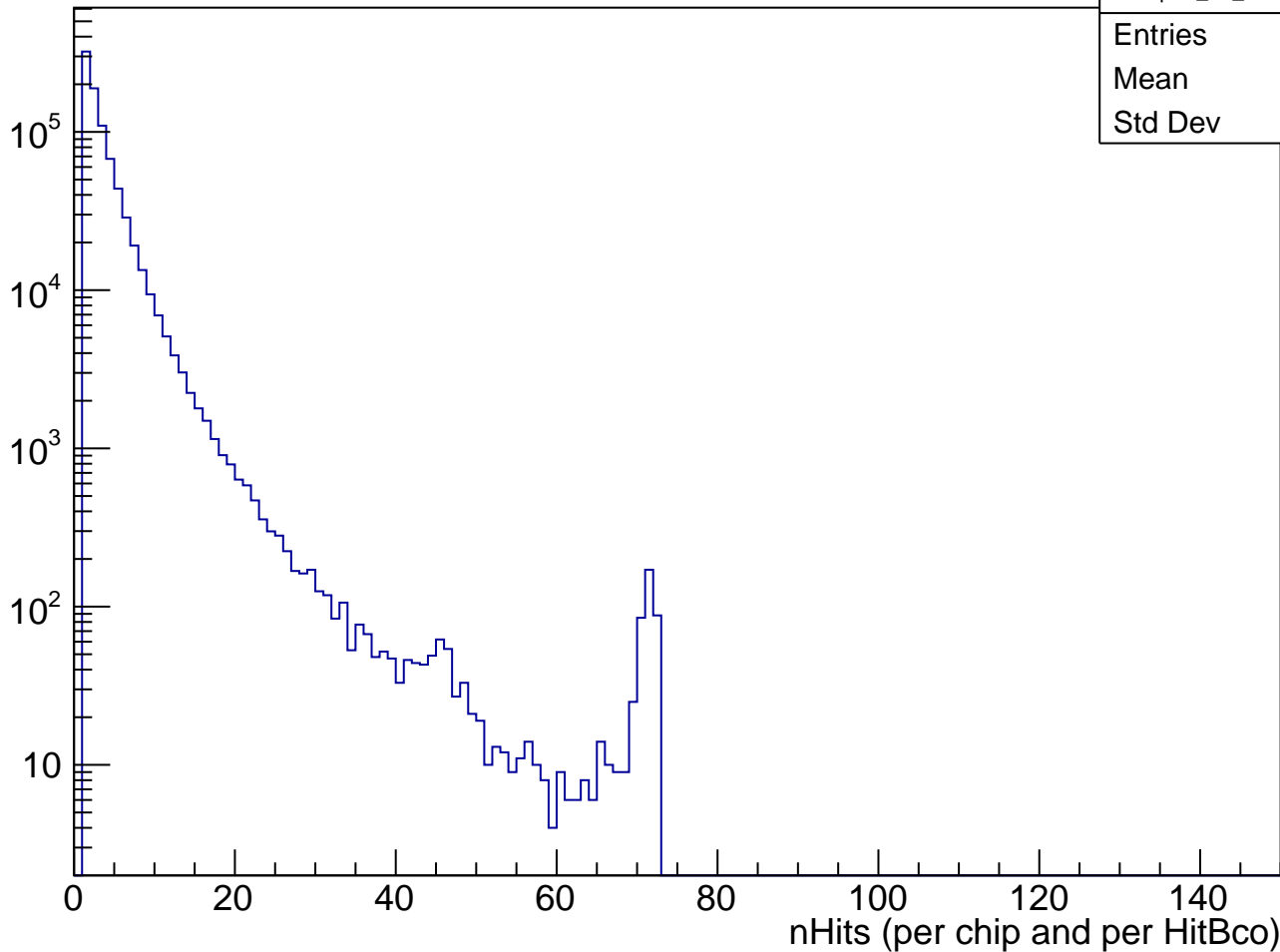
Entries 954618

Mean 3.272

Std Dev 3.83

nChipHit_F6_Fch11_Chip18

Entries



nChipHit_F6_Fch11_Chip18

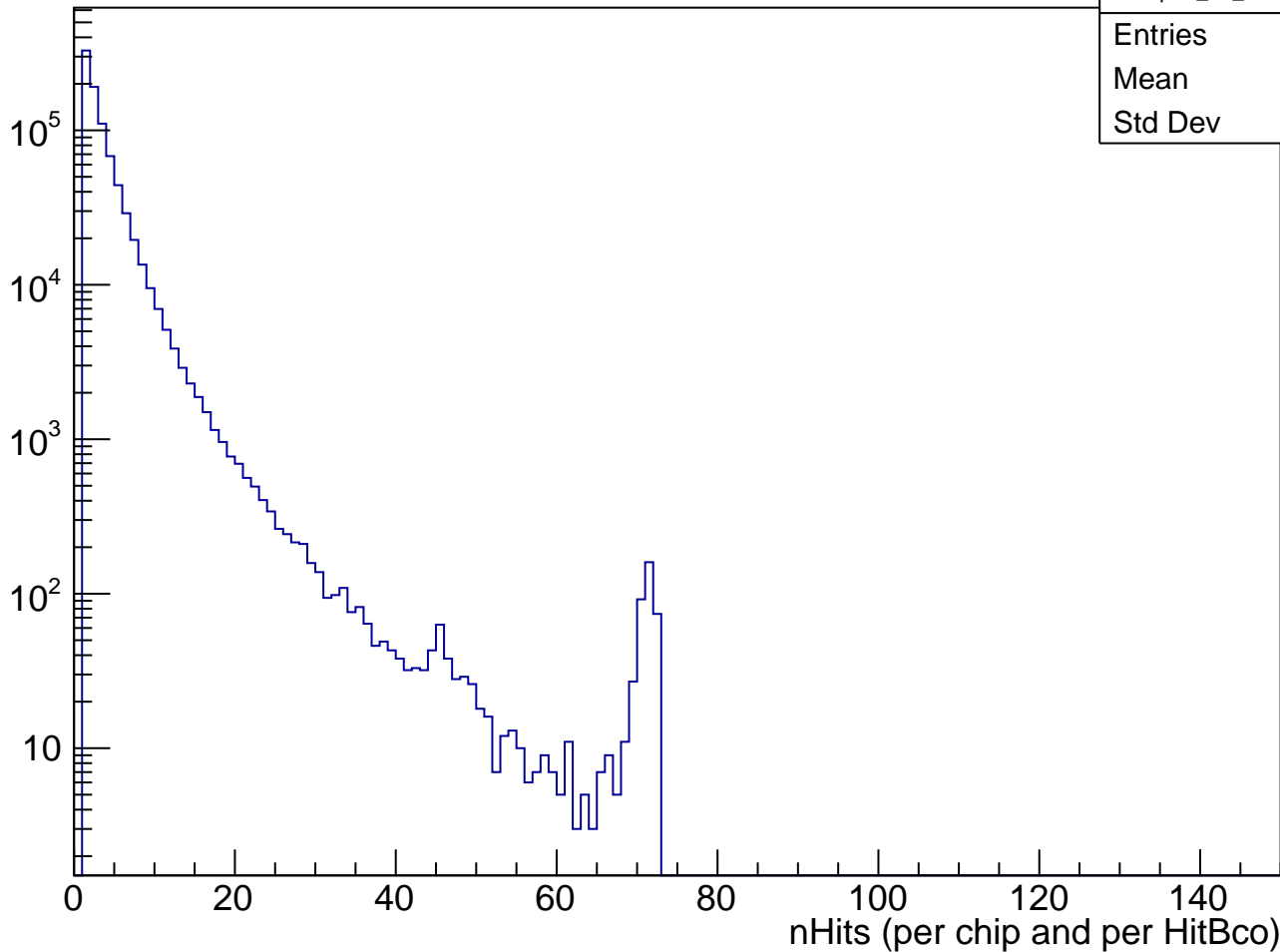
Entries 834496

Mean 3.018

Std Dev 3.588

nChipHit_F6_Fch11_Chip19

Entries



nChipHit_F6_Fch11_Chip19

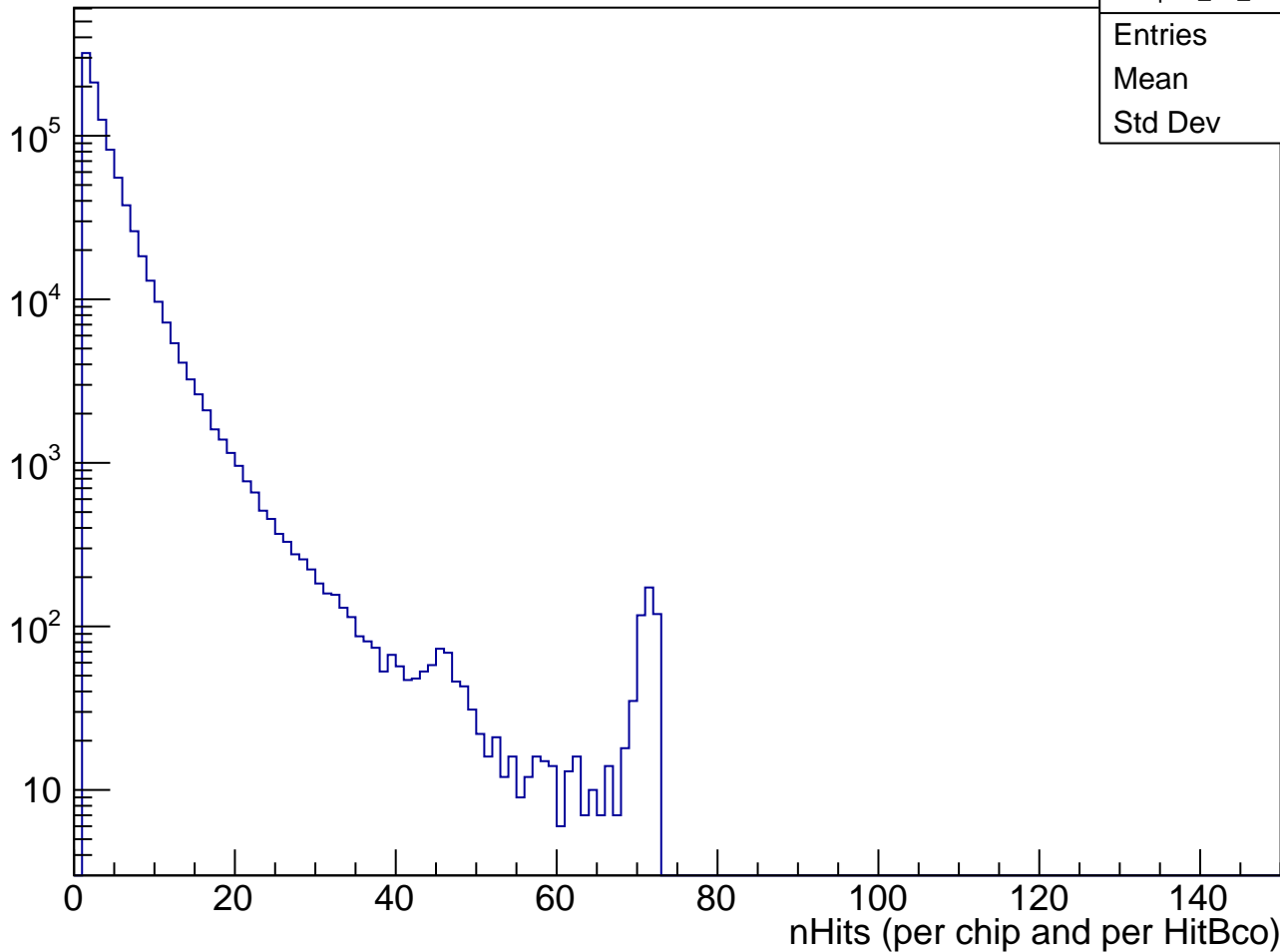
Entries 846487

Mean 3.007

Std Dev 3.554

nChipHit_F6_Fch11_Chip2

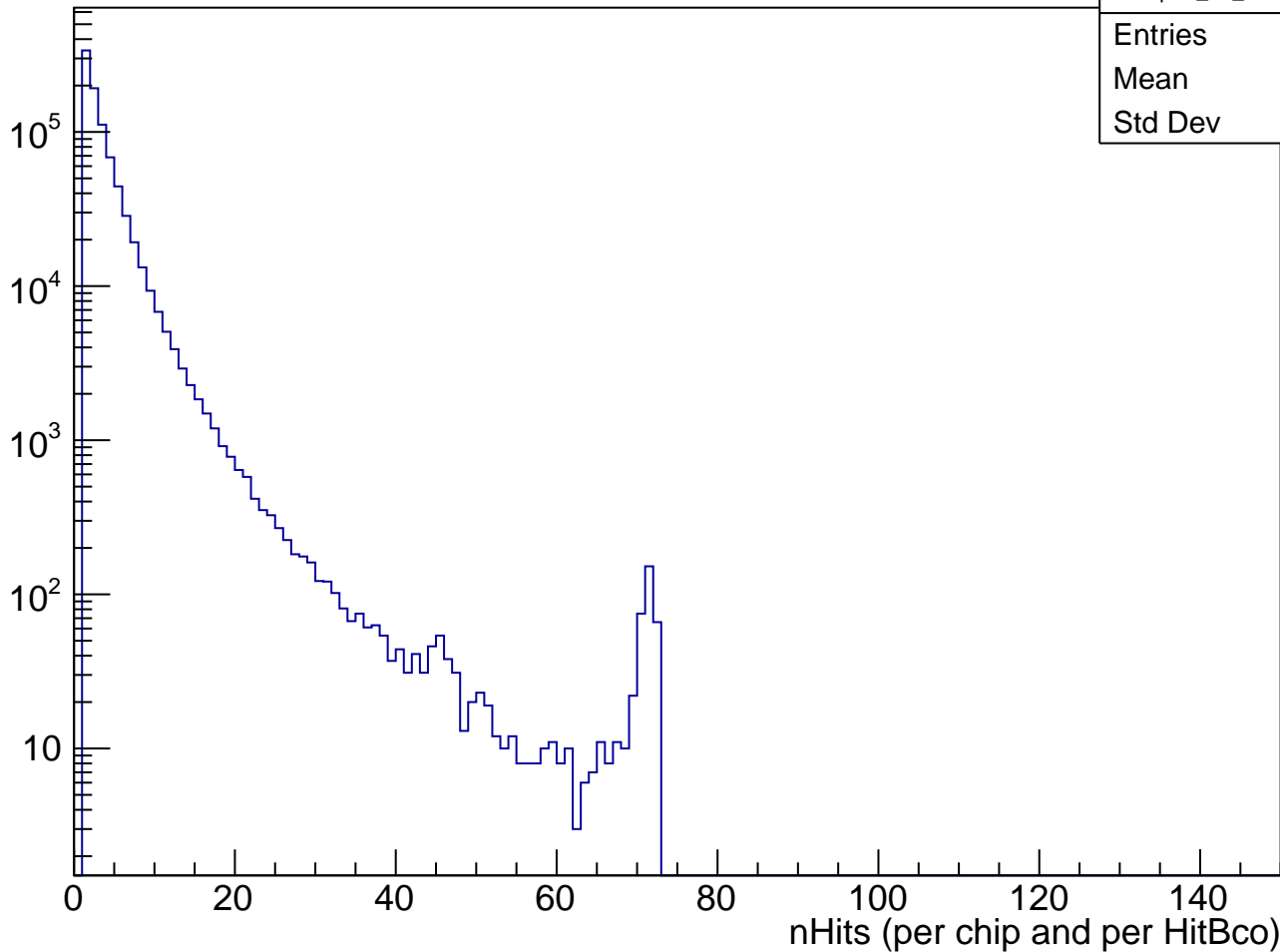
Entries



nChipHit_F6_Fch11_Chip2	
Entries	936027
Mean	3.314
Std Dev	3.86

nChipHit_F6_Fch11_Chip20

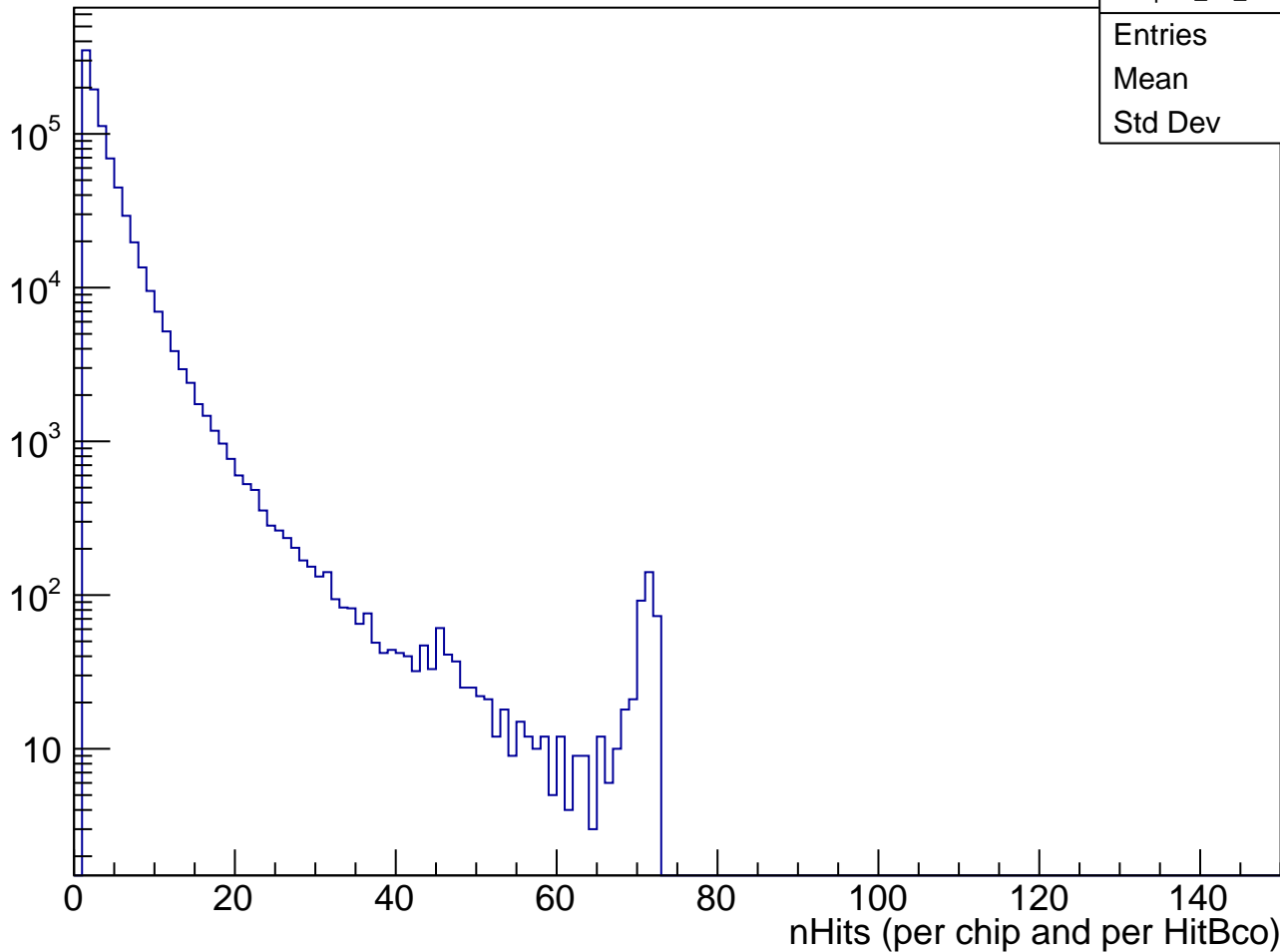
Entries



nChipHit_F6_Fch11_Chip20	
Entries	857025
Mean	2.968
Std Dev	3.494

nChipHit_F6_Fch11_Chip21

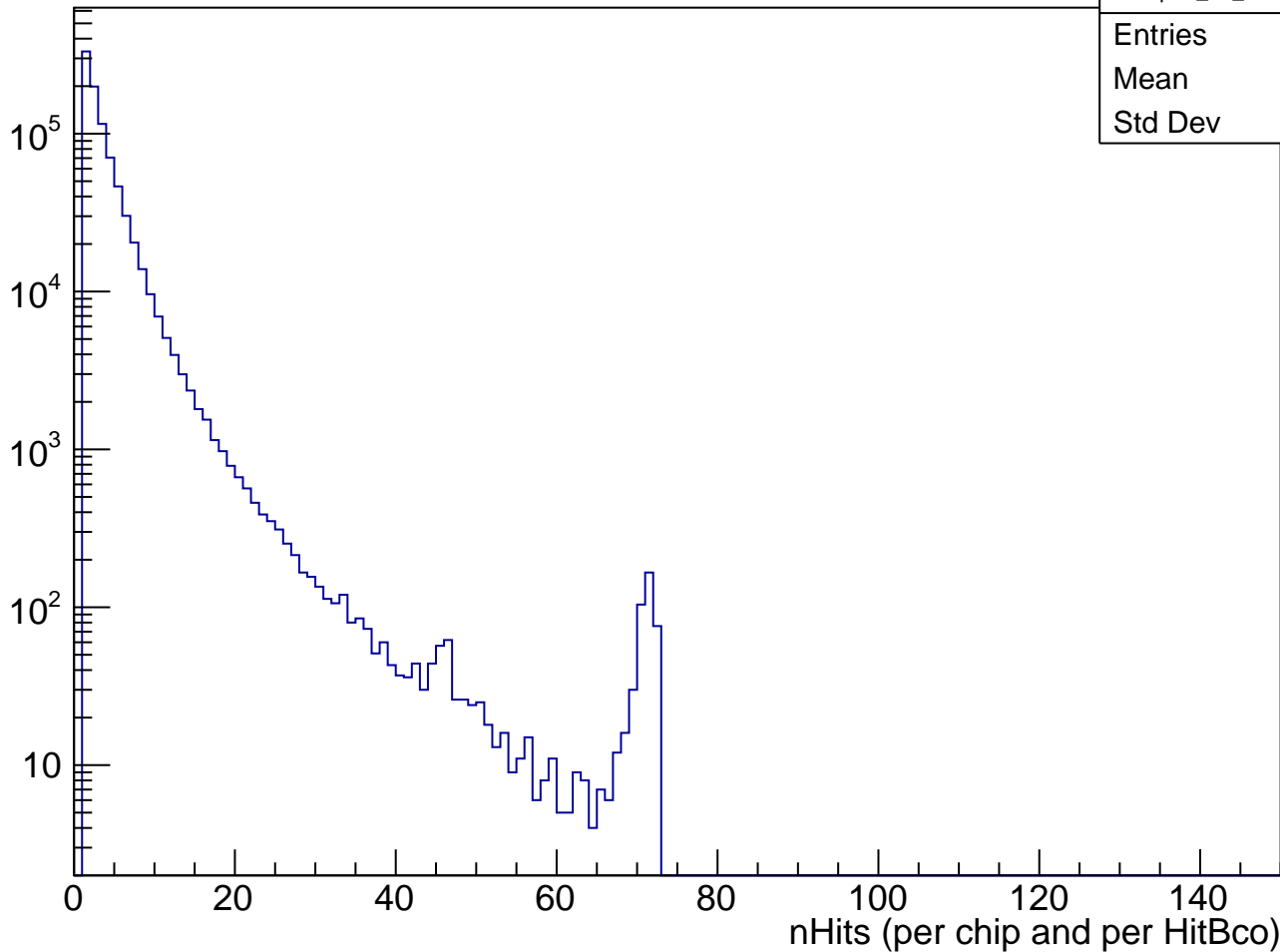
Entries



nChipHit_F6_Fch11_Chip21	
Entries	875566
Mean	2.955
Std Dev	3.504

nChipHit_F6_Fch11_Chip22

Entries



nChipHit_F6_Fch11_Chip22

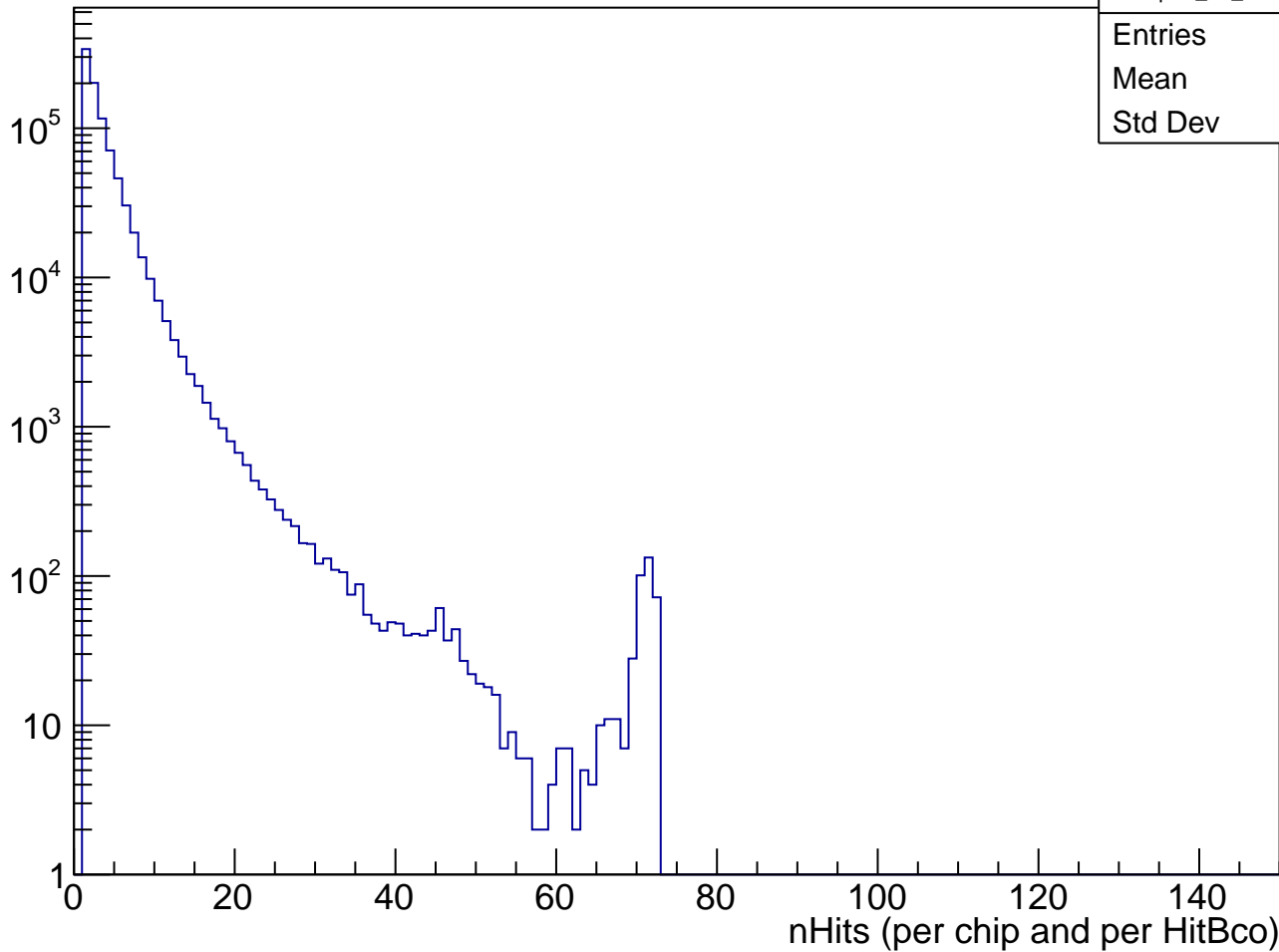
Entries 869949

Mean 3.018

Std Dev 3.571

nChipHit_F6_Fch11_Chip23

Entries



nChipHit_F6_Fch11_Chip23

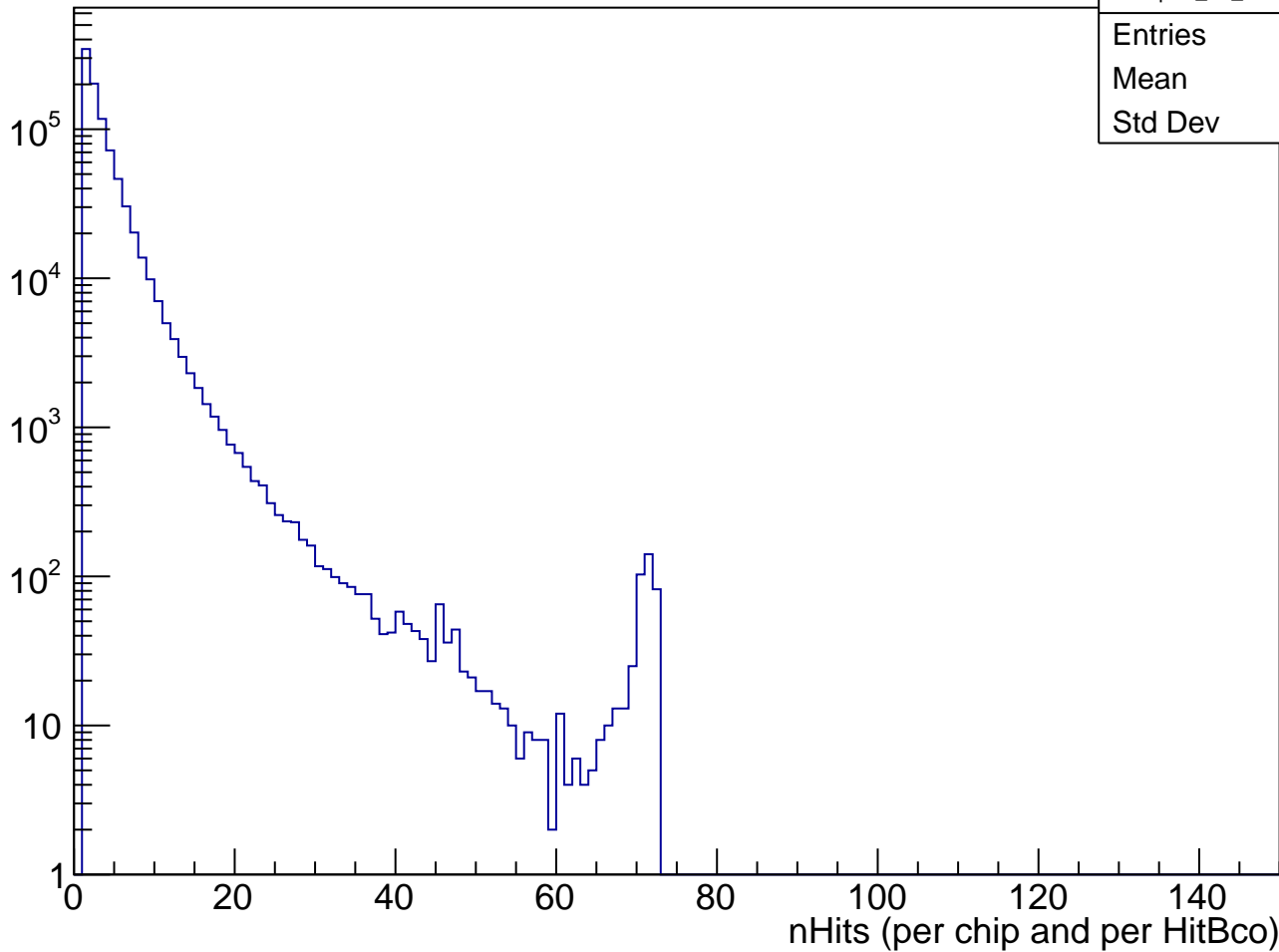
Entries 879982

Mean 2.985

Std Dev 3.485

nChipHit_F6_Fch11_Chip24

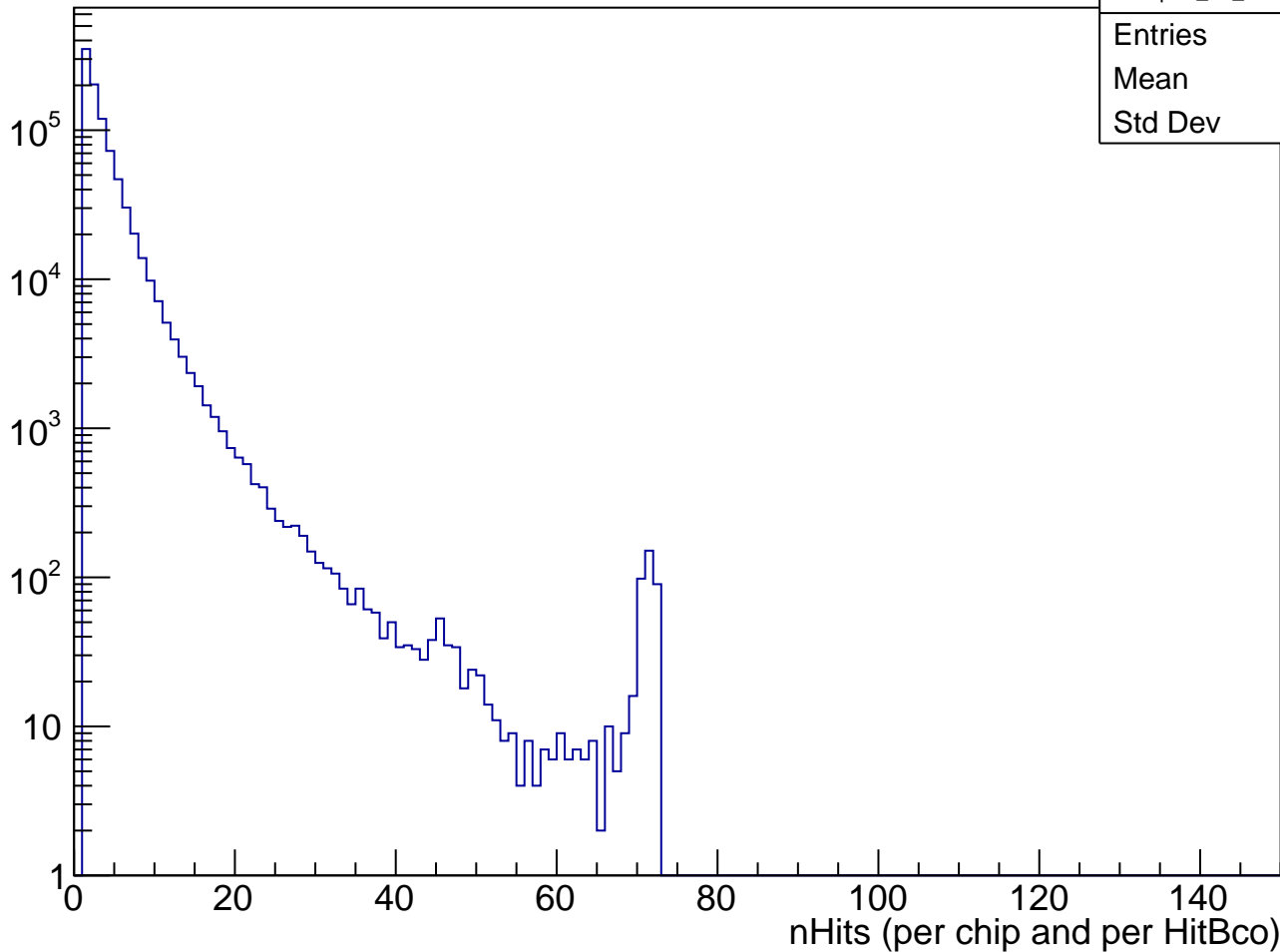
Entries



nChipHit_F6_Fch11_Chip24	
Entries	890232
Mean	2.976
Std Dev	3.492

nChipHit_F6_Fch11_Chip25

Entries



nChipHit_F6_Fch11_Chip25

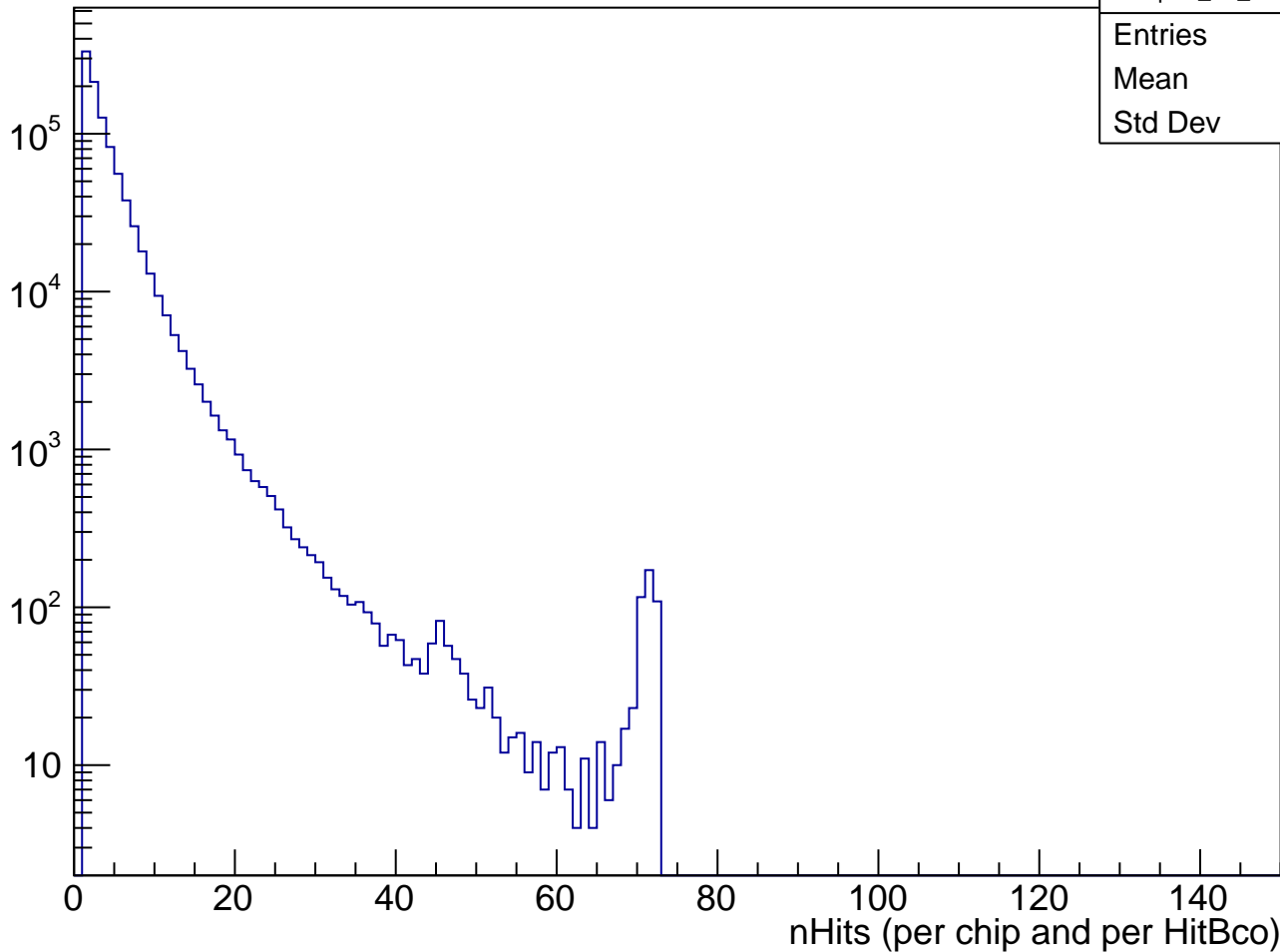
Entries 898792

Mean 2.963

Std Dev 3.45

nChipHit_F6_Fch11_Chip3

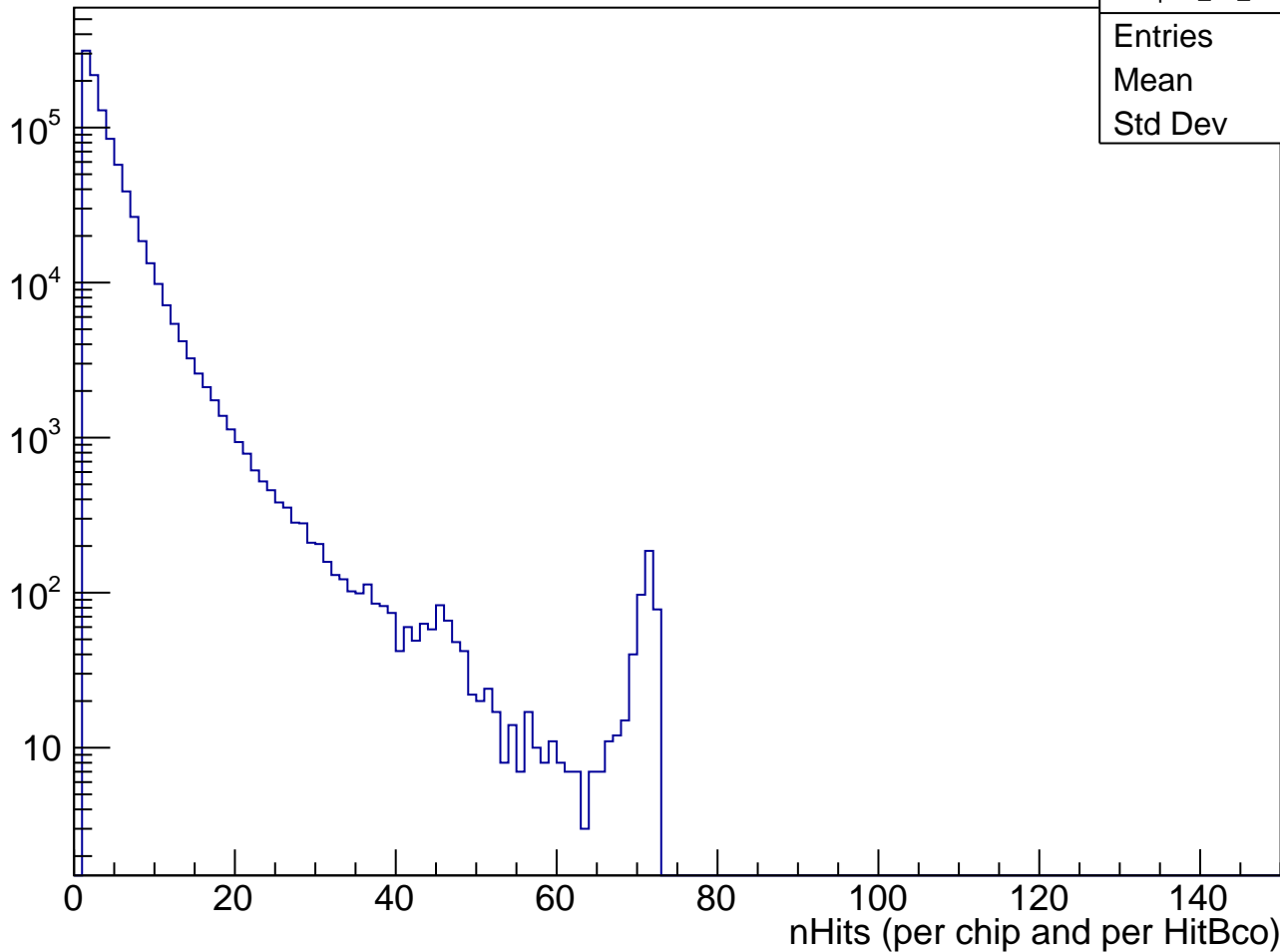
Entries



nChipHit_F6_Fch11_Chip3	
Entries	949943
Mean	3.276
Std Dev	3.811

nChipHit_F6_Fch11_Chip4

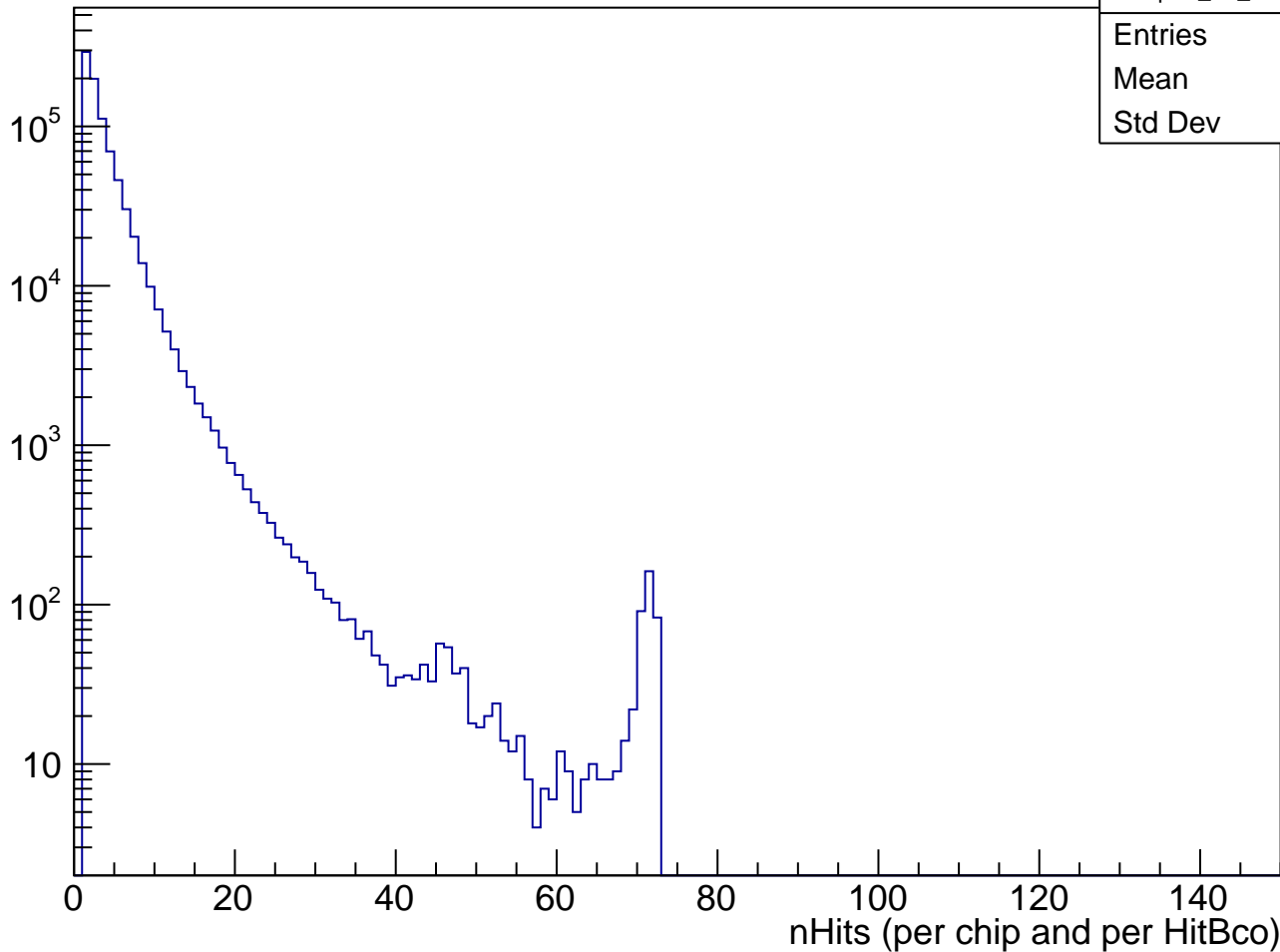
Entries



nChipHit_F6_Fch11_Chip4	
Entries	945611
Mean	3.337
Std Dev	3.816

nChipHit_F6_Fch11_Chip5

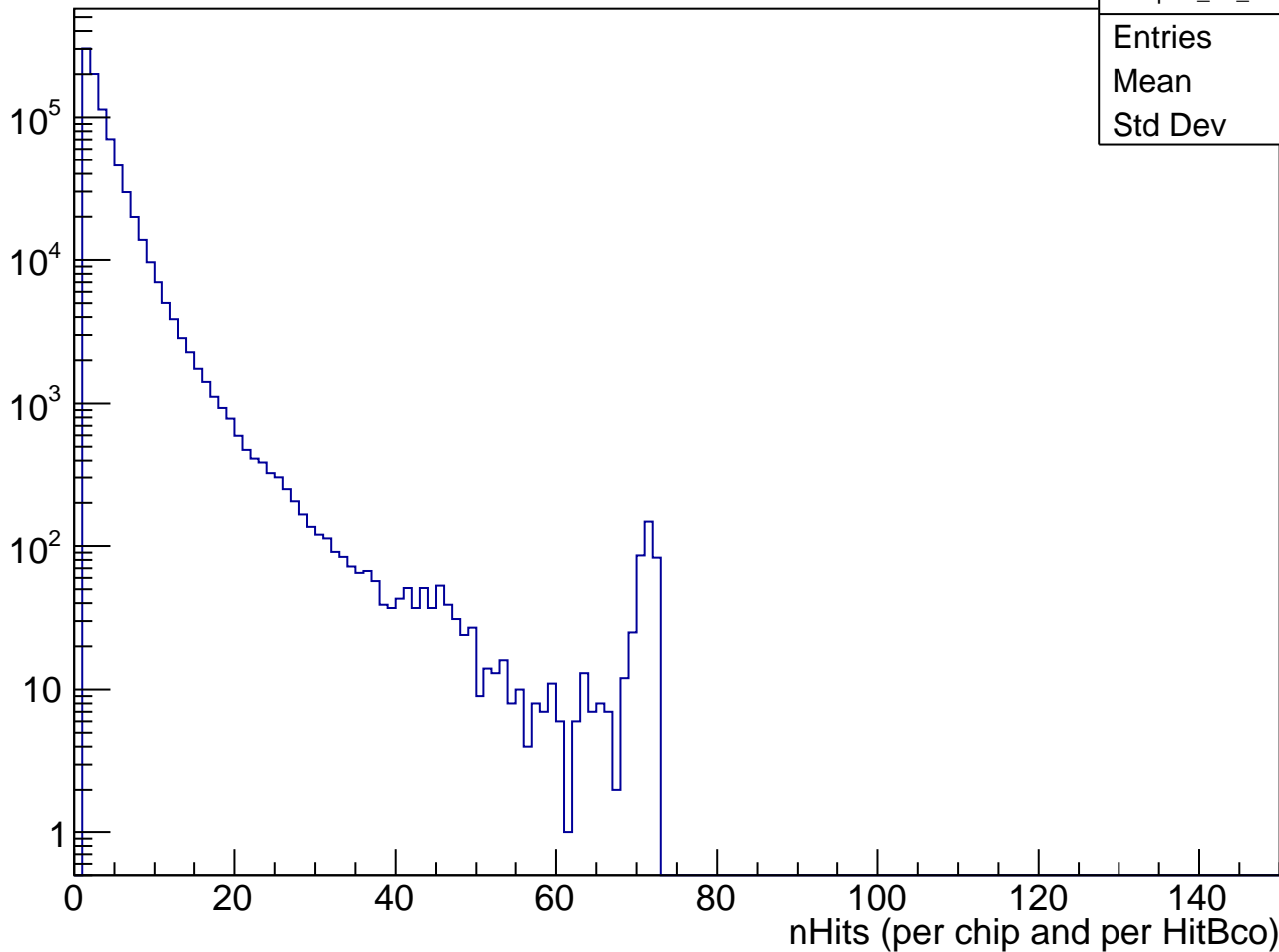
Entries



nChipHit_F6_Fch11_Chip5	
Entries	827072
Mean	3.104
Std Dev	3.599

nChipHit_F6_Fch11_Chip6

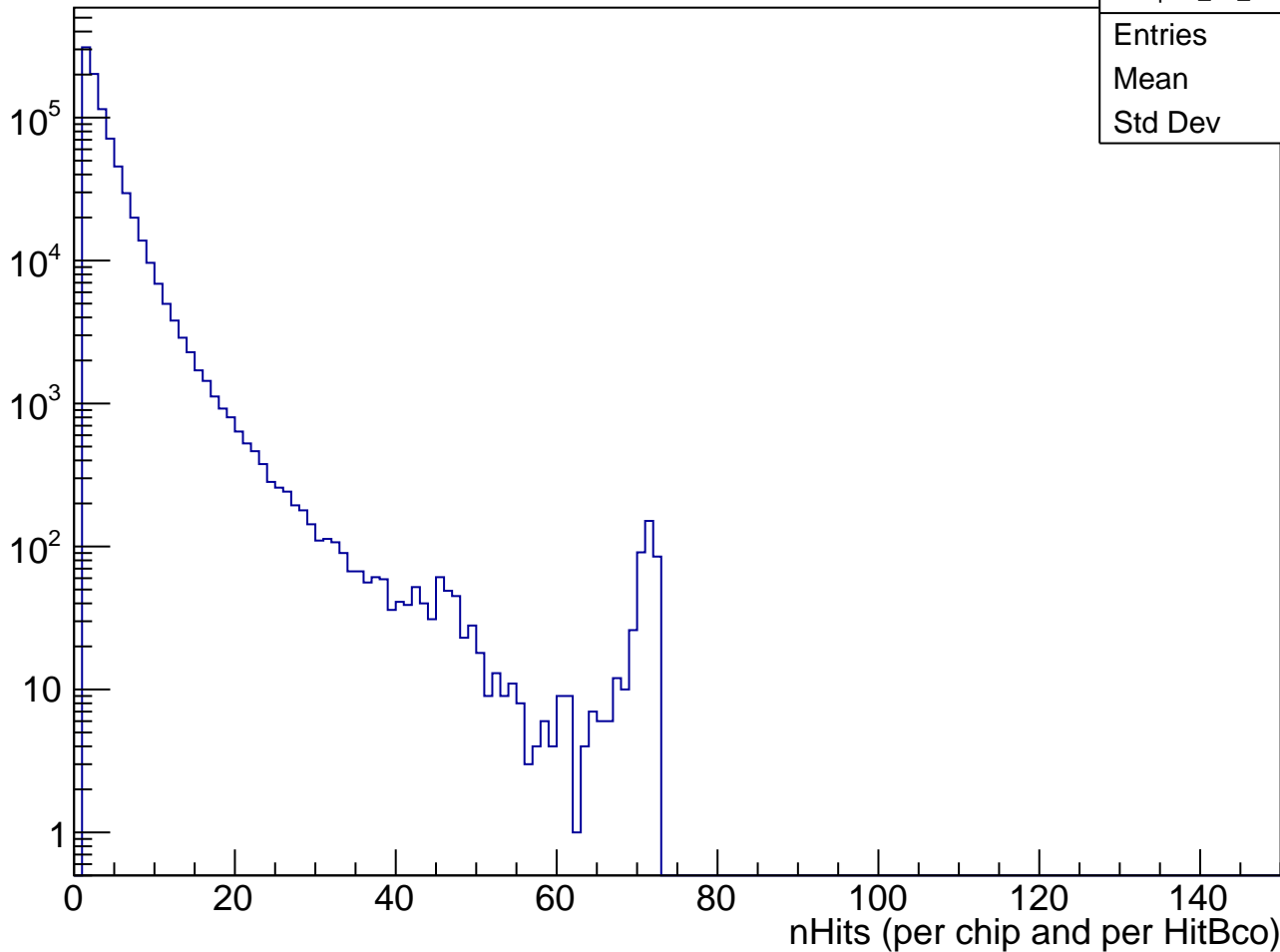
Entries



nChipHit_F6_Fch11_Chip6	
Entries	837431
Mean	3.058
Std Dev	3.523

nChipHit_F6_Fch11_Chip7

Entries

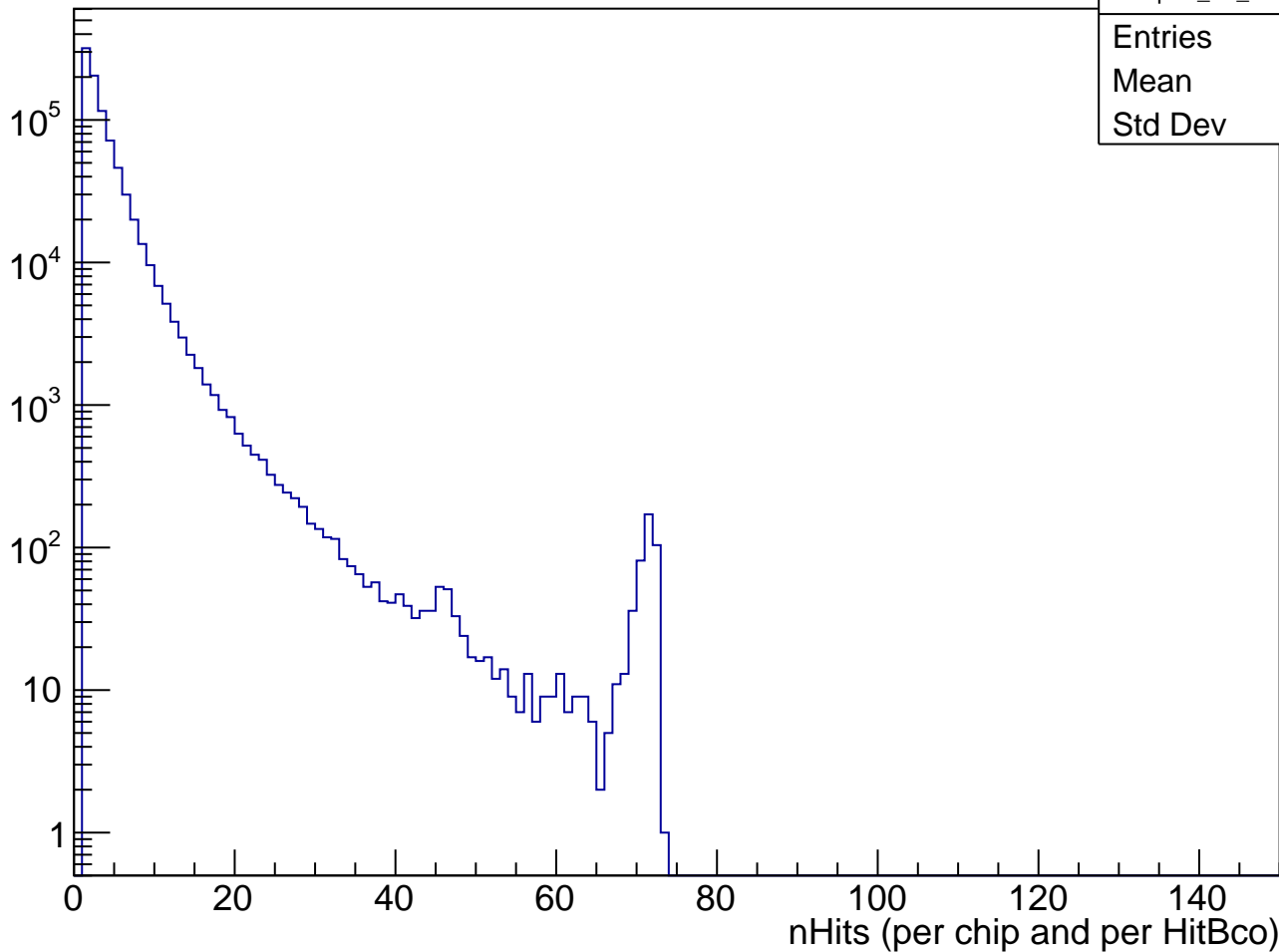


nChipHit_F6_Fch11_Chip7	
Entries	848298
Mean	3.038
Std Dev	3.518

nHits (per chip and per HitBco)

nChipHit_F6_Fch11_Chip8

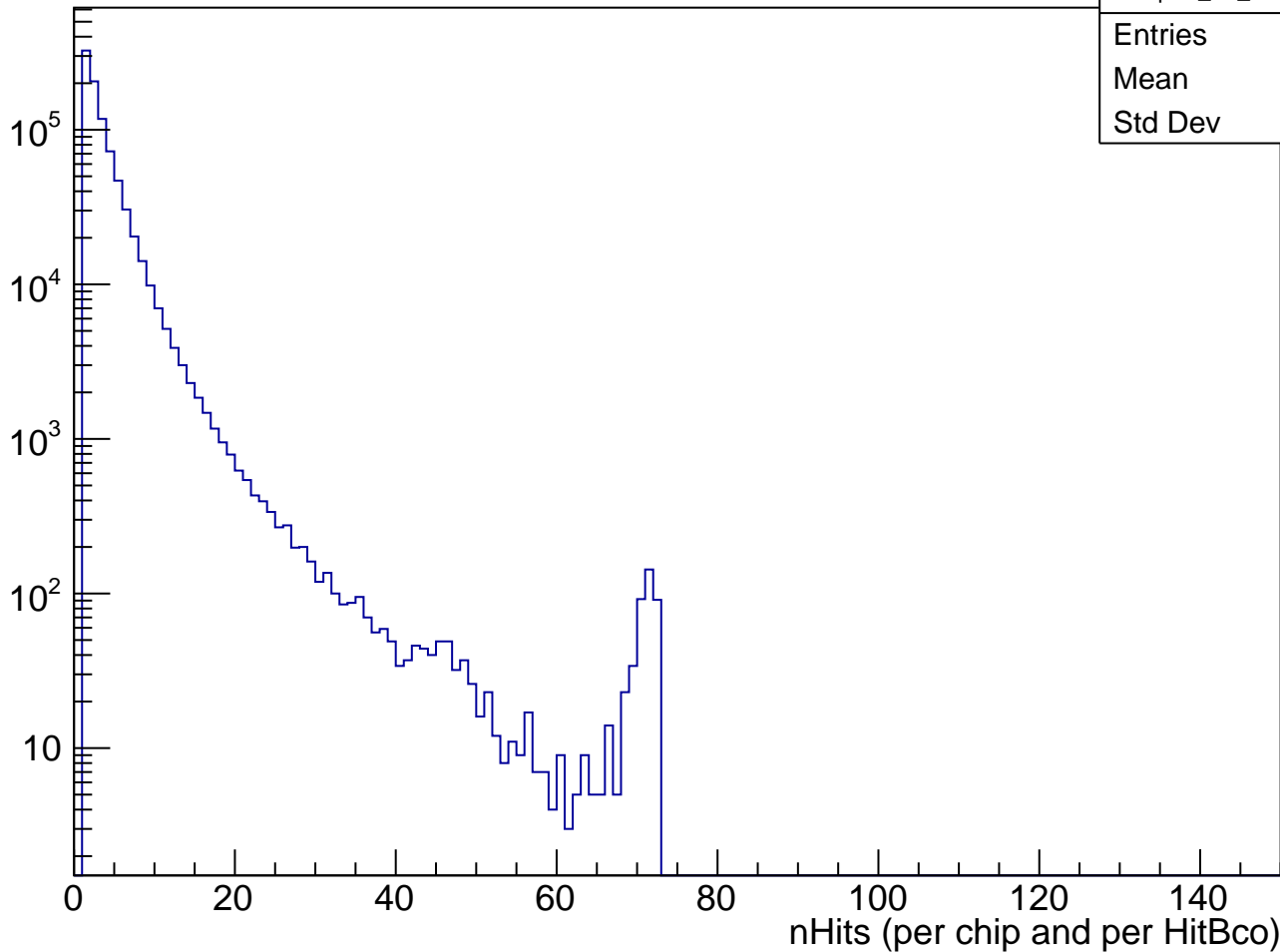
Entries



nChipHit_F6_Fch11_Chip8	
Entries	862111
Mean	3.027
Std Dev	3.549

nChipHit_F6_Fch11_Chip9

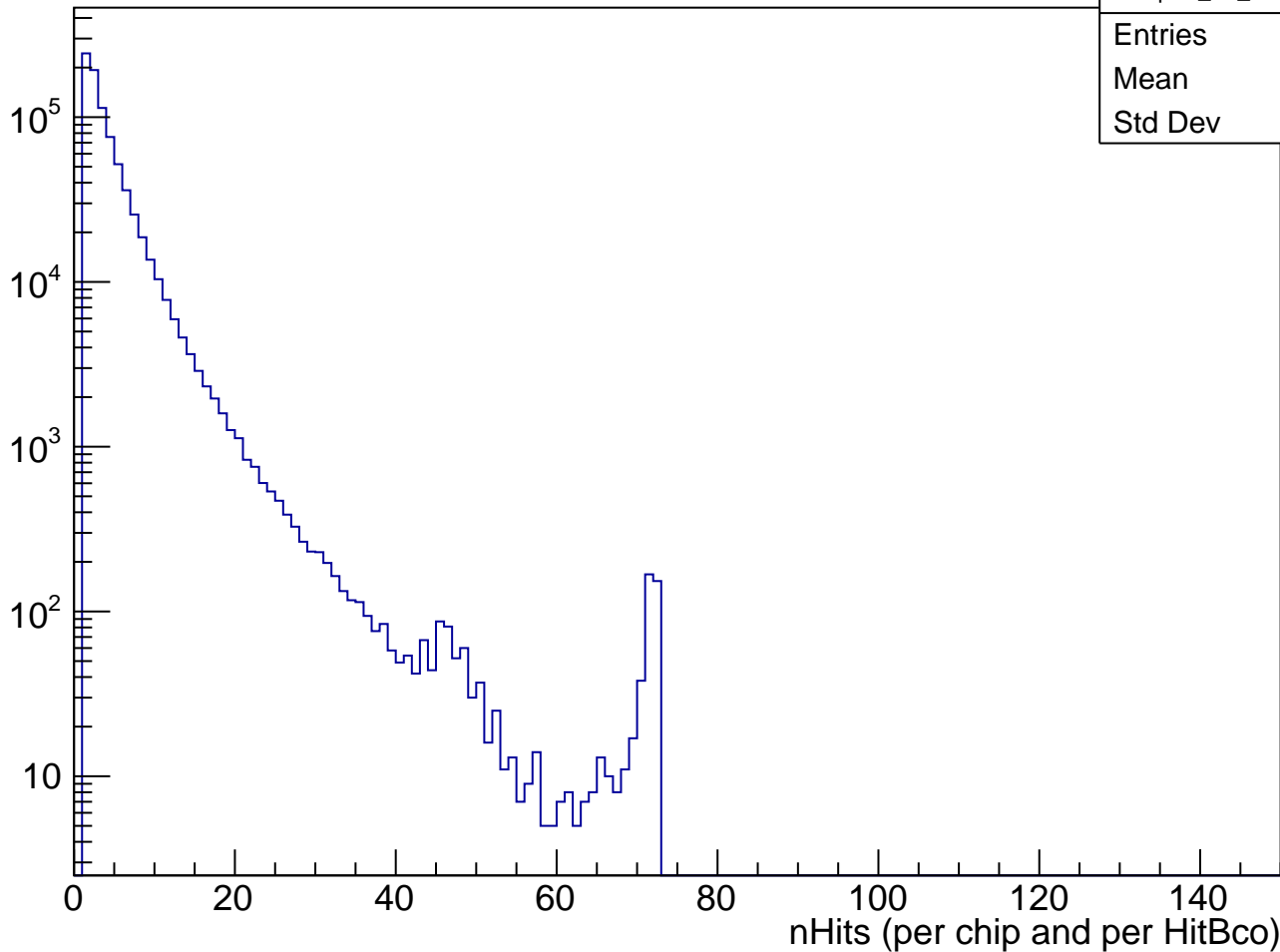
Entries



nChipHit_F6_Fch11_Chip9	
Entries	875568
Mean	3.028
Std Dev	3.541

nChipHit_F6_Fch12_Chip0

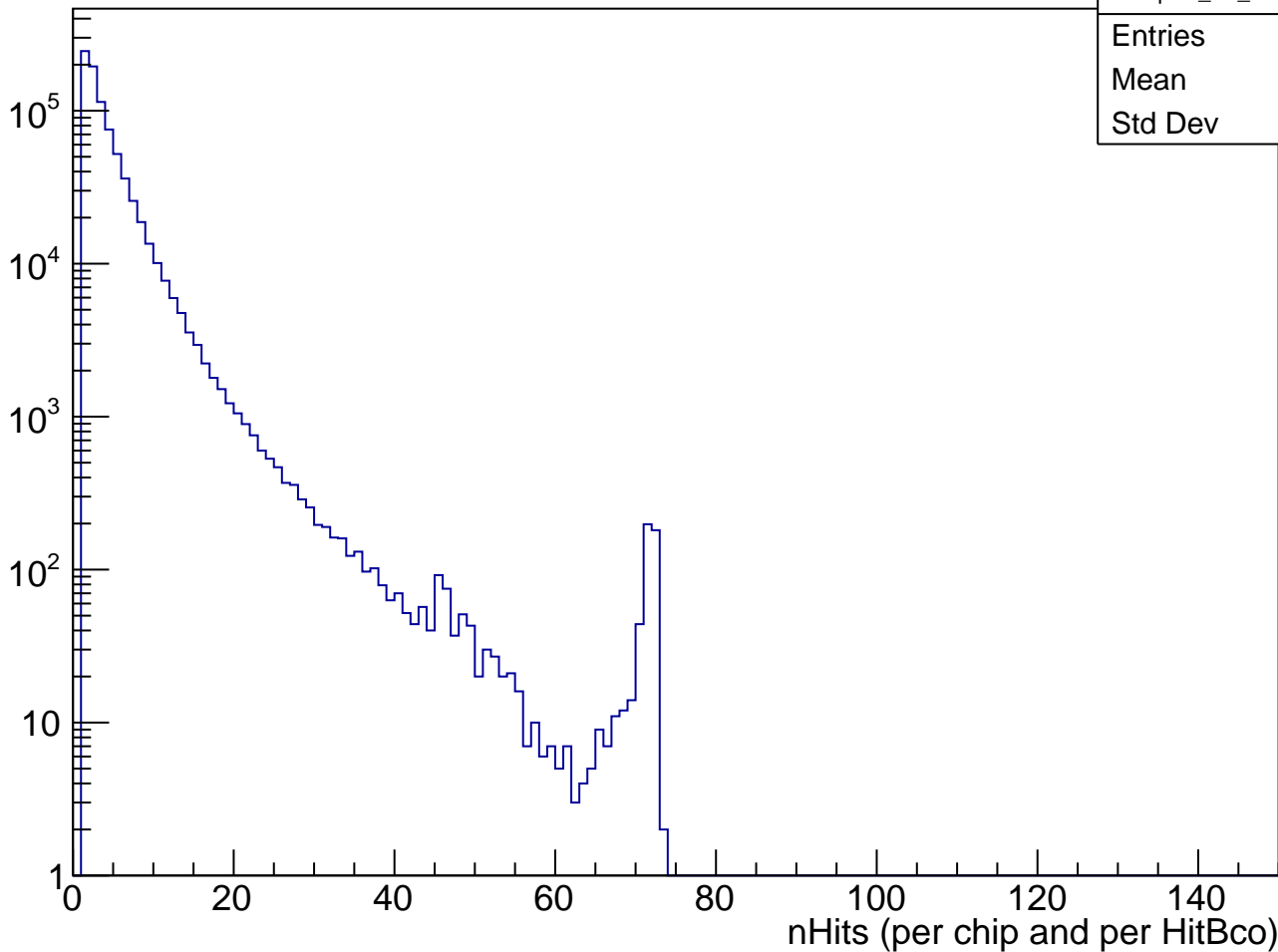
Entries



nChipHit_F6_Fch12_Chip0	
Entries	822634
Mean	3.615
Std Dev	4.116

nChipHit_F6_Fch12_Chip1

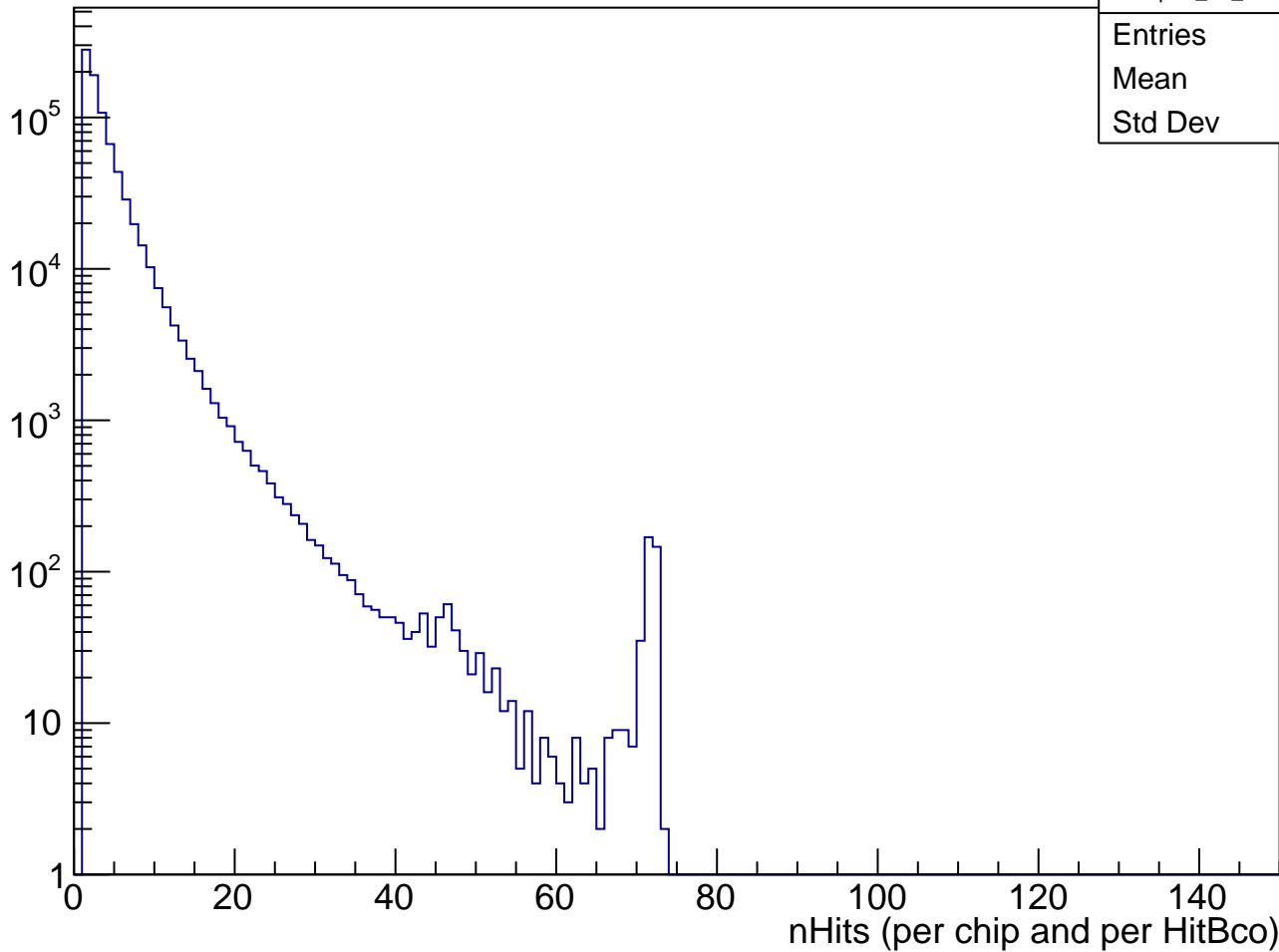
Entries



nChipHit_F6_Fch12_Chip1	
Entries	825706
Mean	3.608
Std Dev	4.153

nChipHit_F6_Fch12_Chip10

Entries



nChipHit_F6_Fch12_Chip10

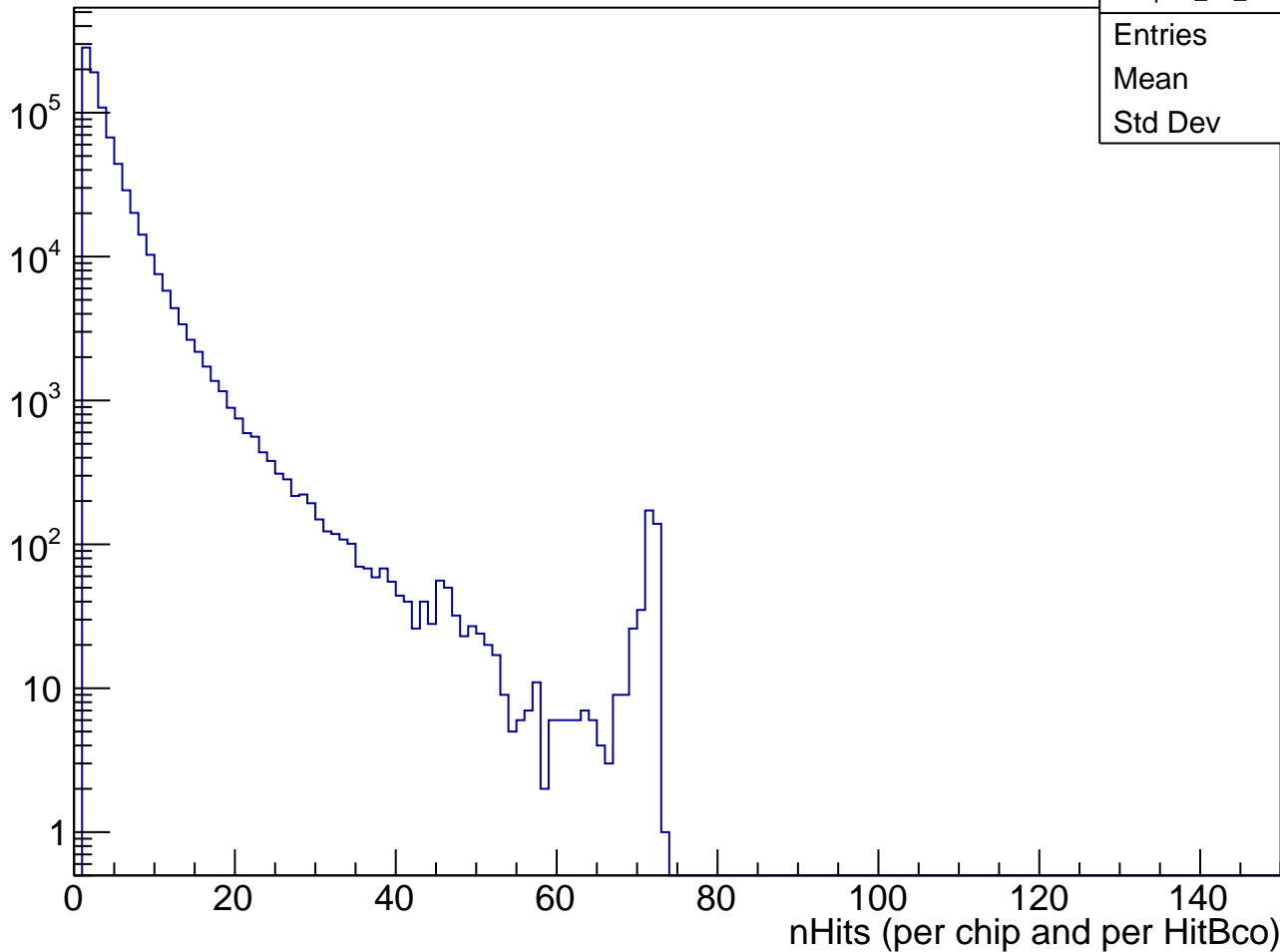
Entries 797504

Mean 3.186

Std Dev 3.739

nChipHit_F6_Fch12_Chip11

Entries

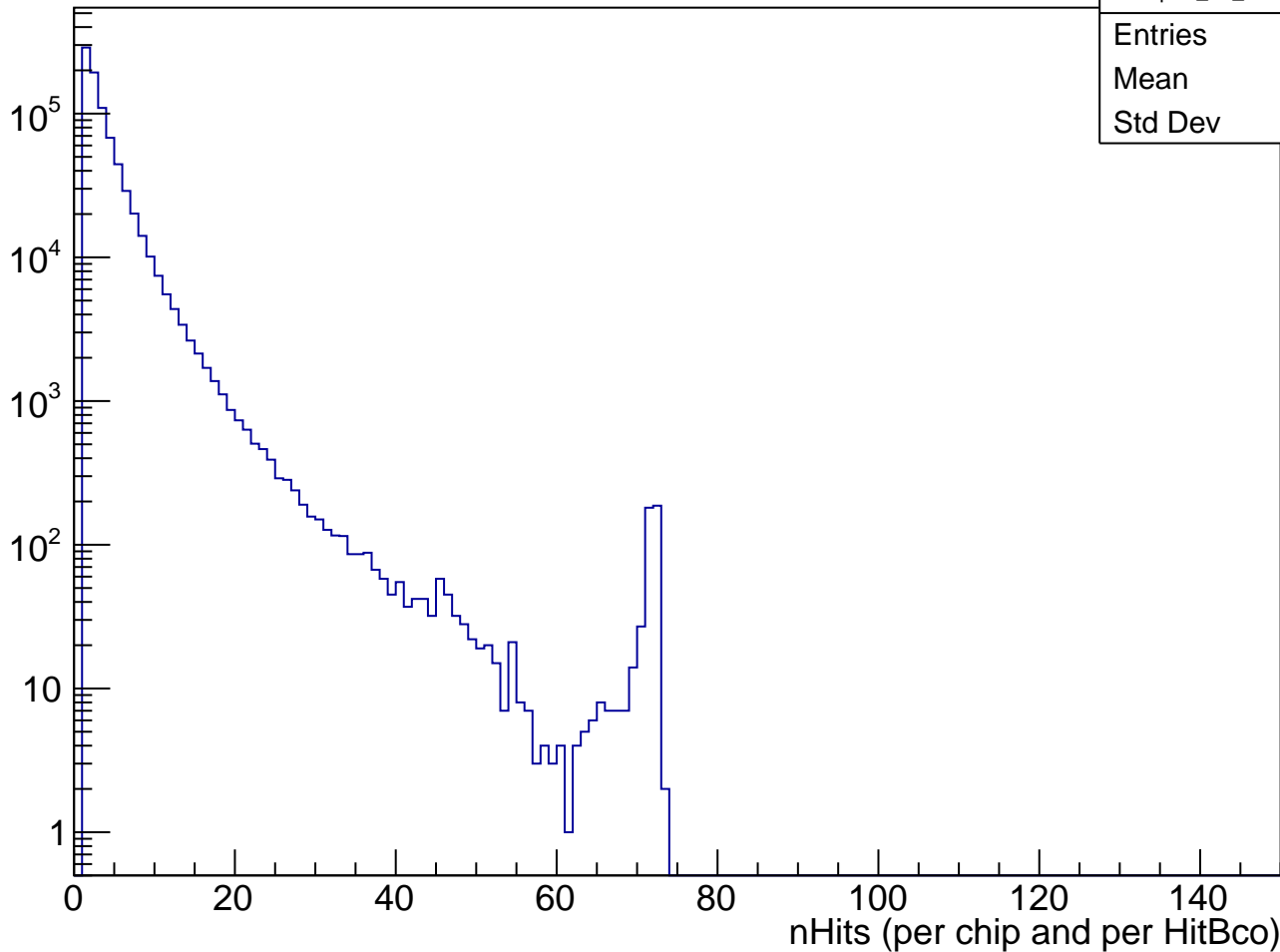


nChipHit_F6_Fch12_Chip11	
Entries	804460
Mean	3.193
Std Dev	3.749

nHits (per chip and per HitBco)

nChipHit_F6_Fch12_Chip12

Entries



nChipHit_F6_Fch12_Chip12

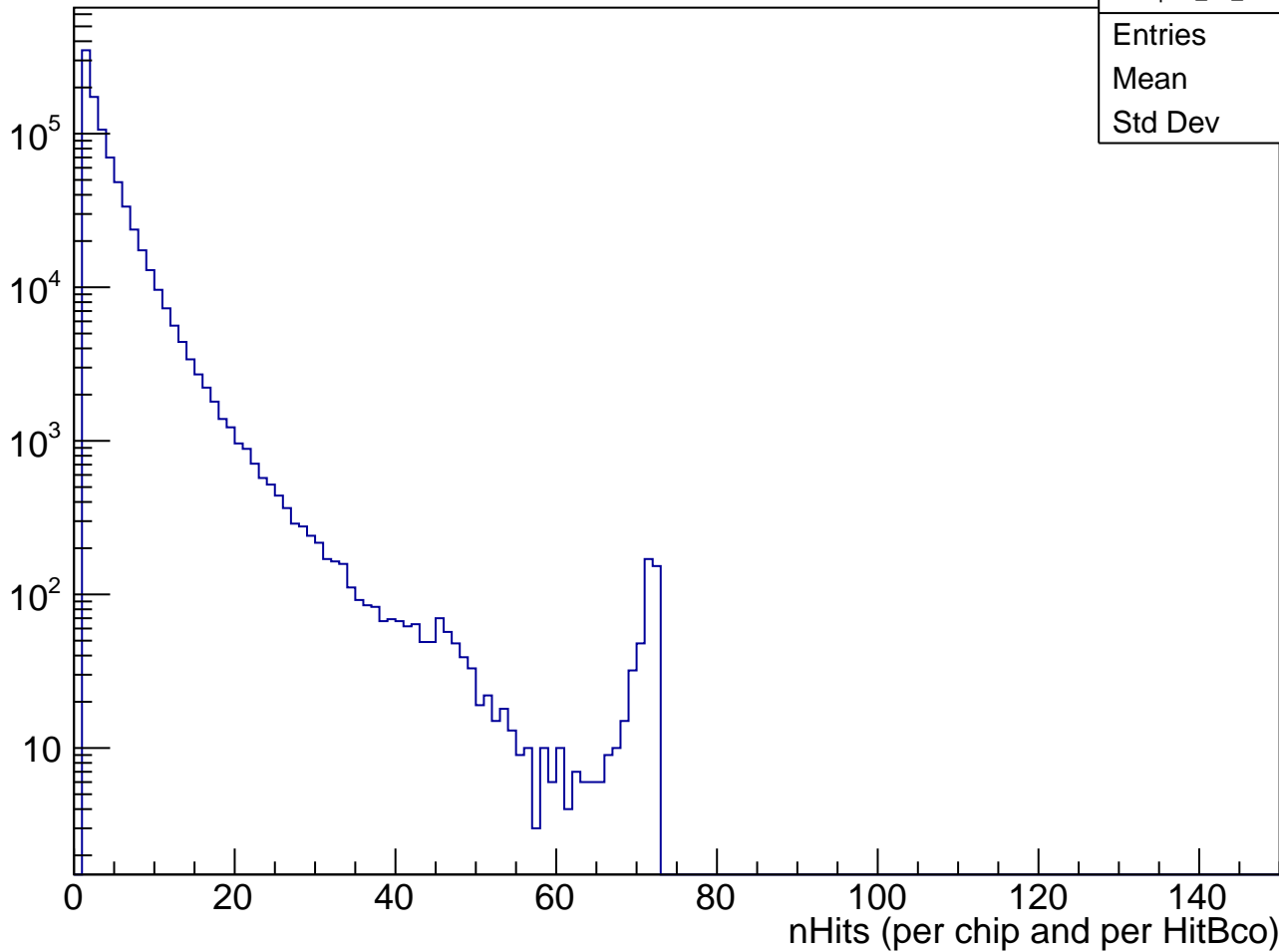
Entries 813160

Mean 3.174

Std Dev 3.755

nChipHit_F6_Fch12_Chip13

Entries



nChipHit_F6_Fch12_Chip13

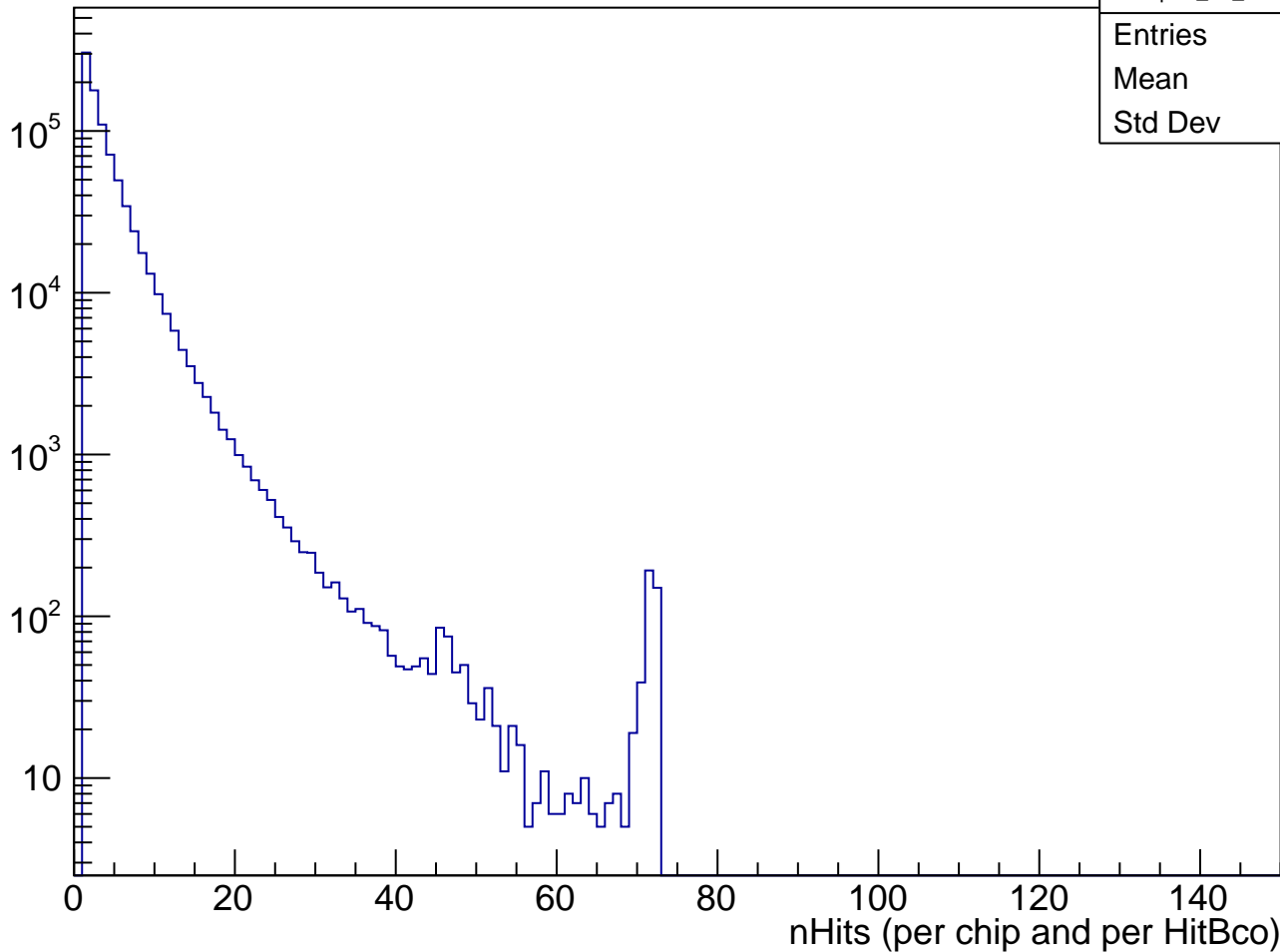
Entries 882427

Mean 3.275

Std Dev 3.988

nChipHit_F6_Fch12_Chip14

Entries



nChipHit_F6_Fch12_Chip14

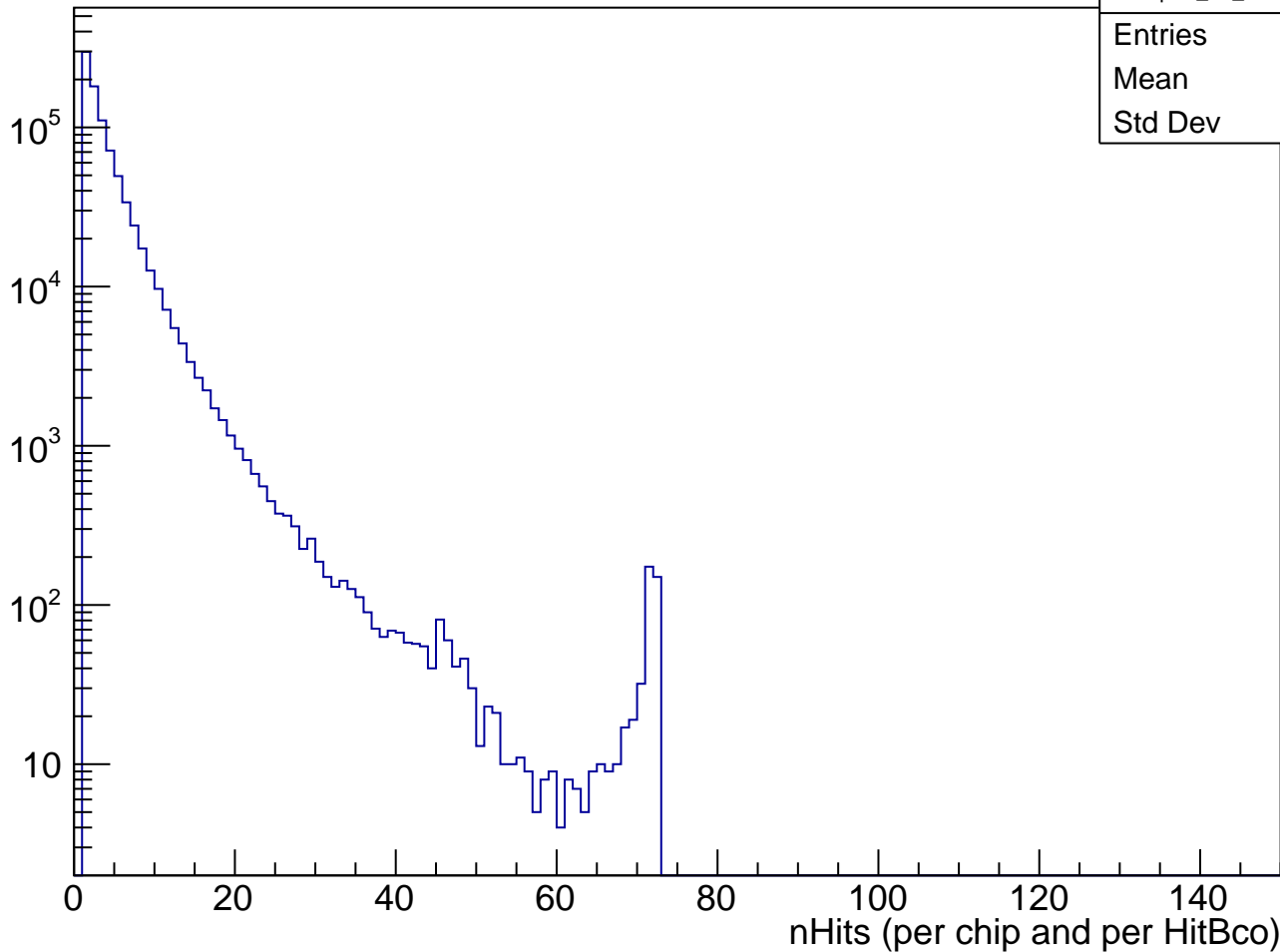
Entries 851206

Mean 3.401

Std Dev 4.035

nChipHit_F6_Fch12_Chip15

Entries



nChipHit_F6_Fch12_Chip15

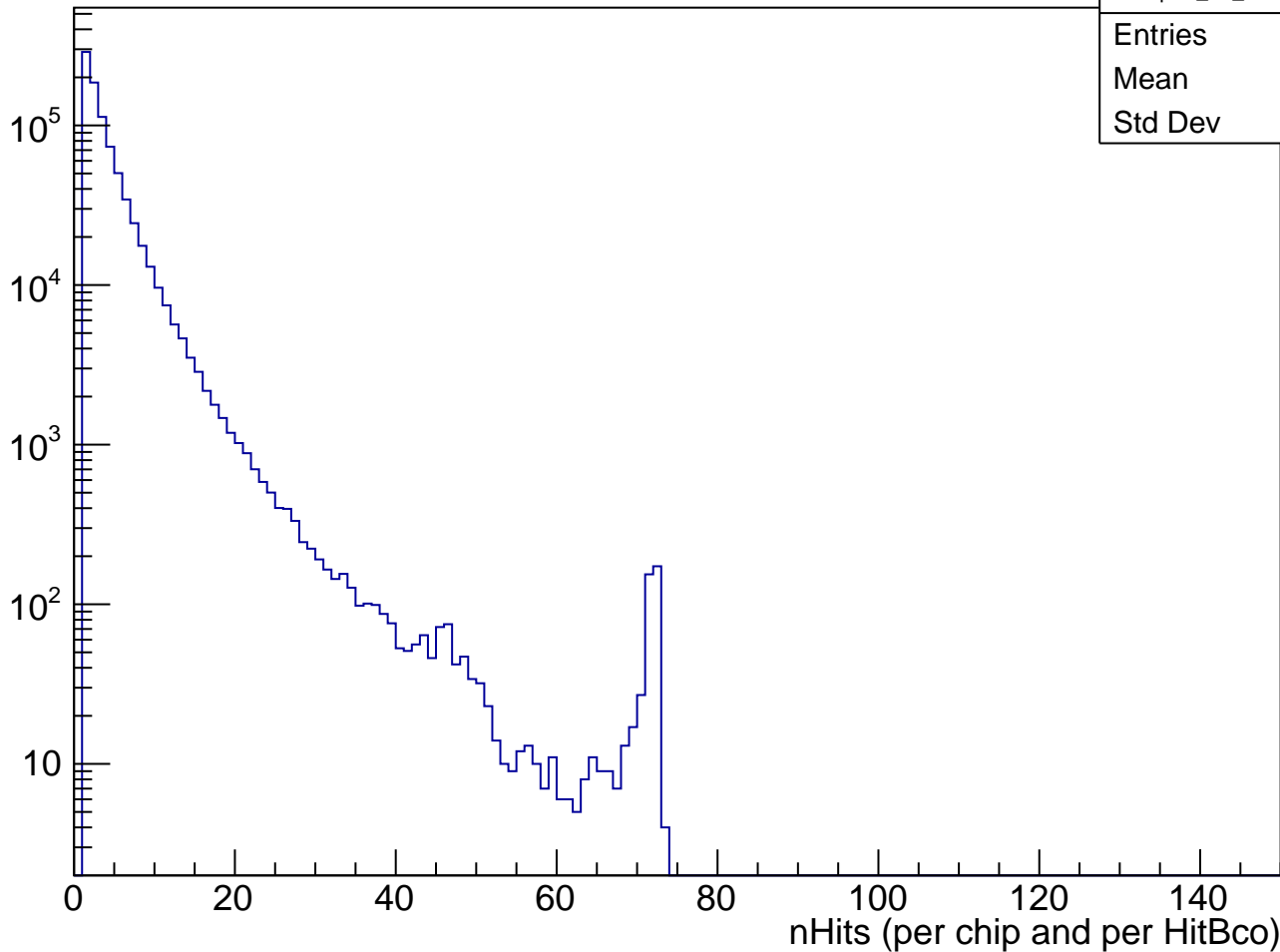
Entries 845405

Mean 3.388

Std Dev 3.988

nChipHit_F6_Fch12_Chip16

Entries



nChipHit_F6_Fch12_Chip16

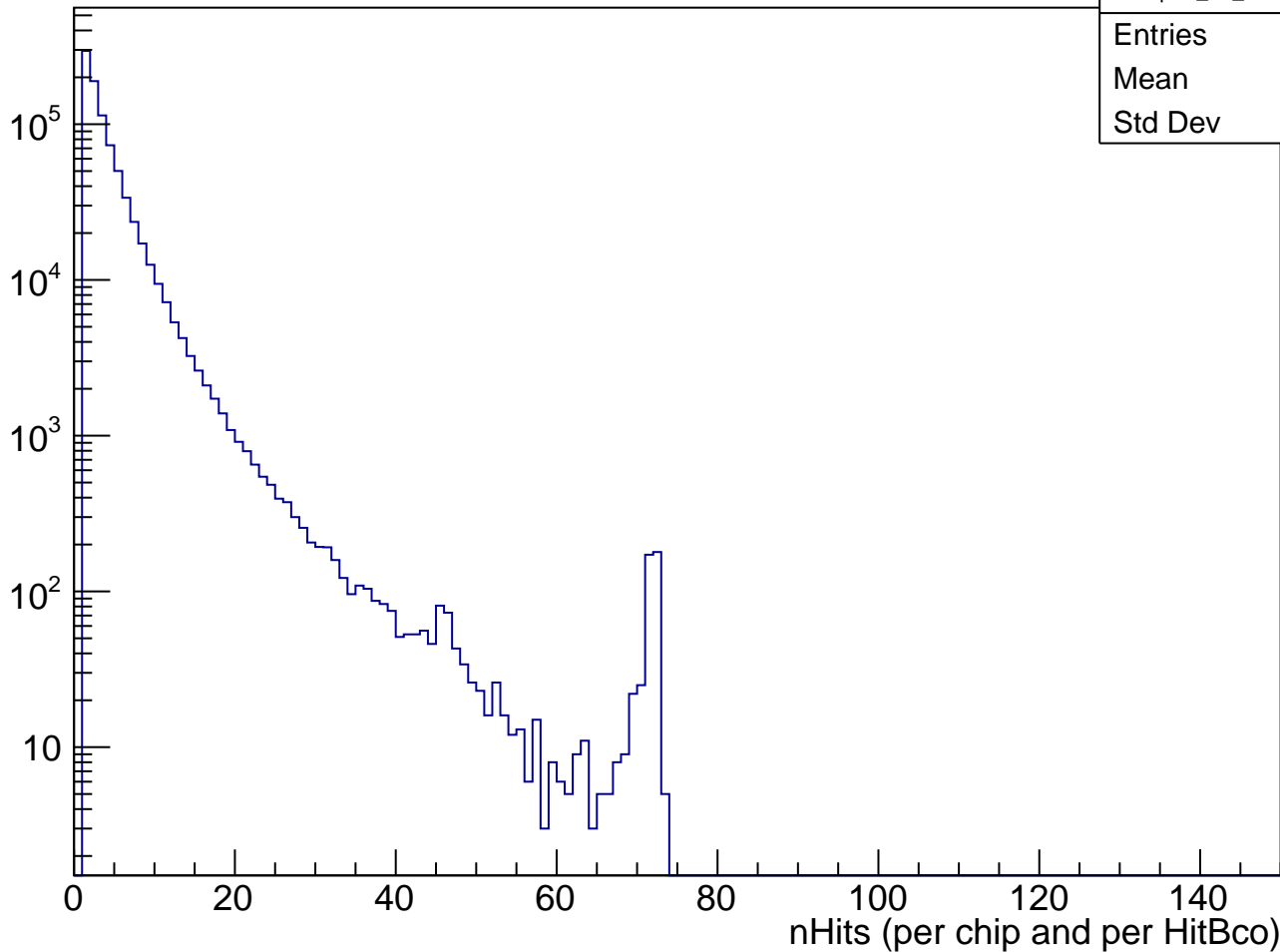
Entries 848409

Mean 3.441

Std Dev 4.033

nChipHit_F6_Fch12_Chip17

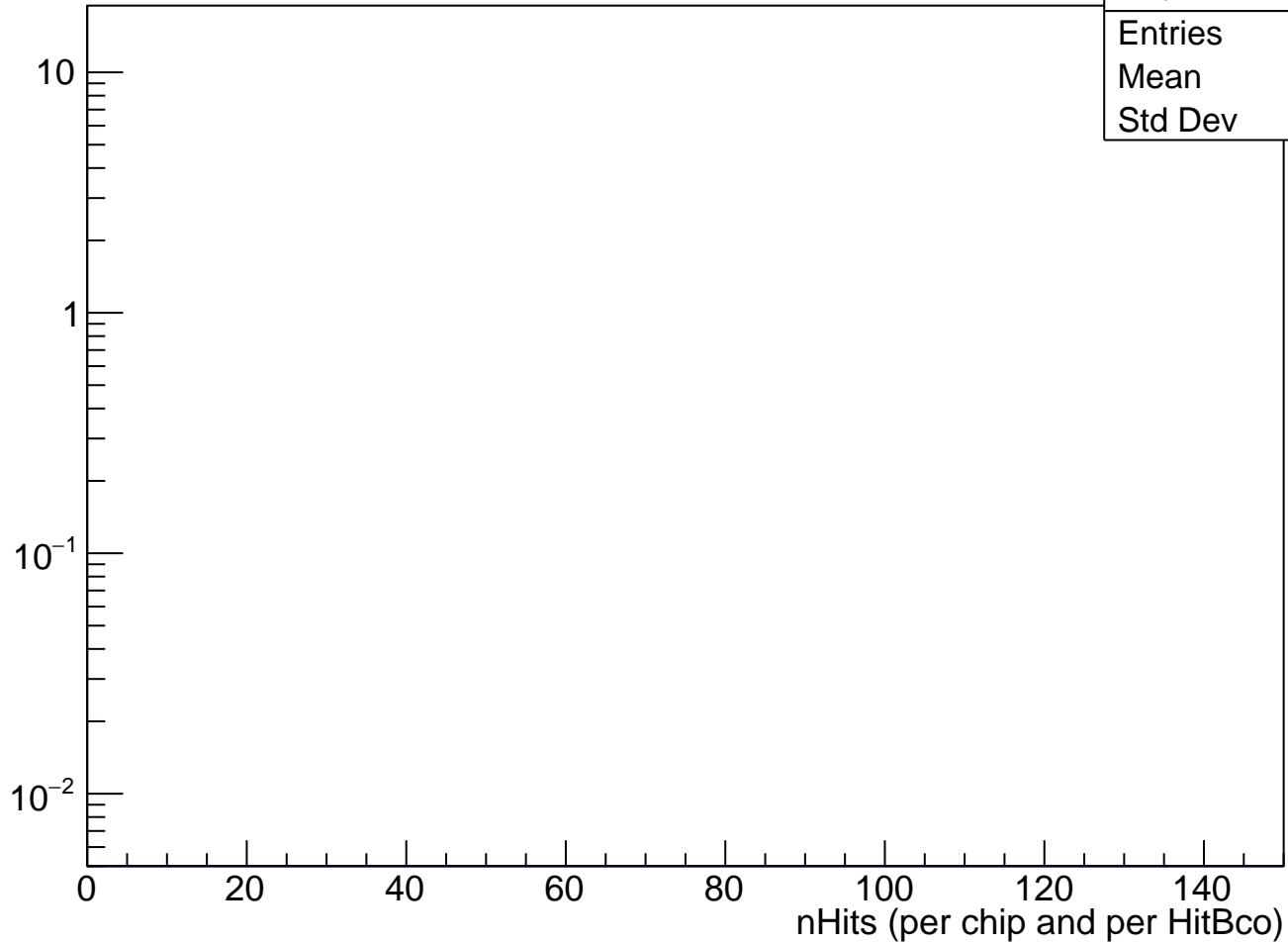
Entries



nChipHit_F6_Fch12_Chip17	
Entries	854596
Mean	3.37
Std Dev	3.969

nChipHit_F6_Fch12_Chip18

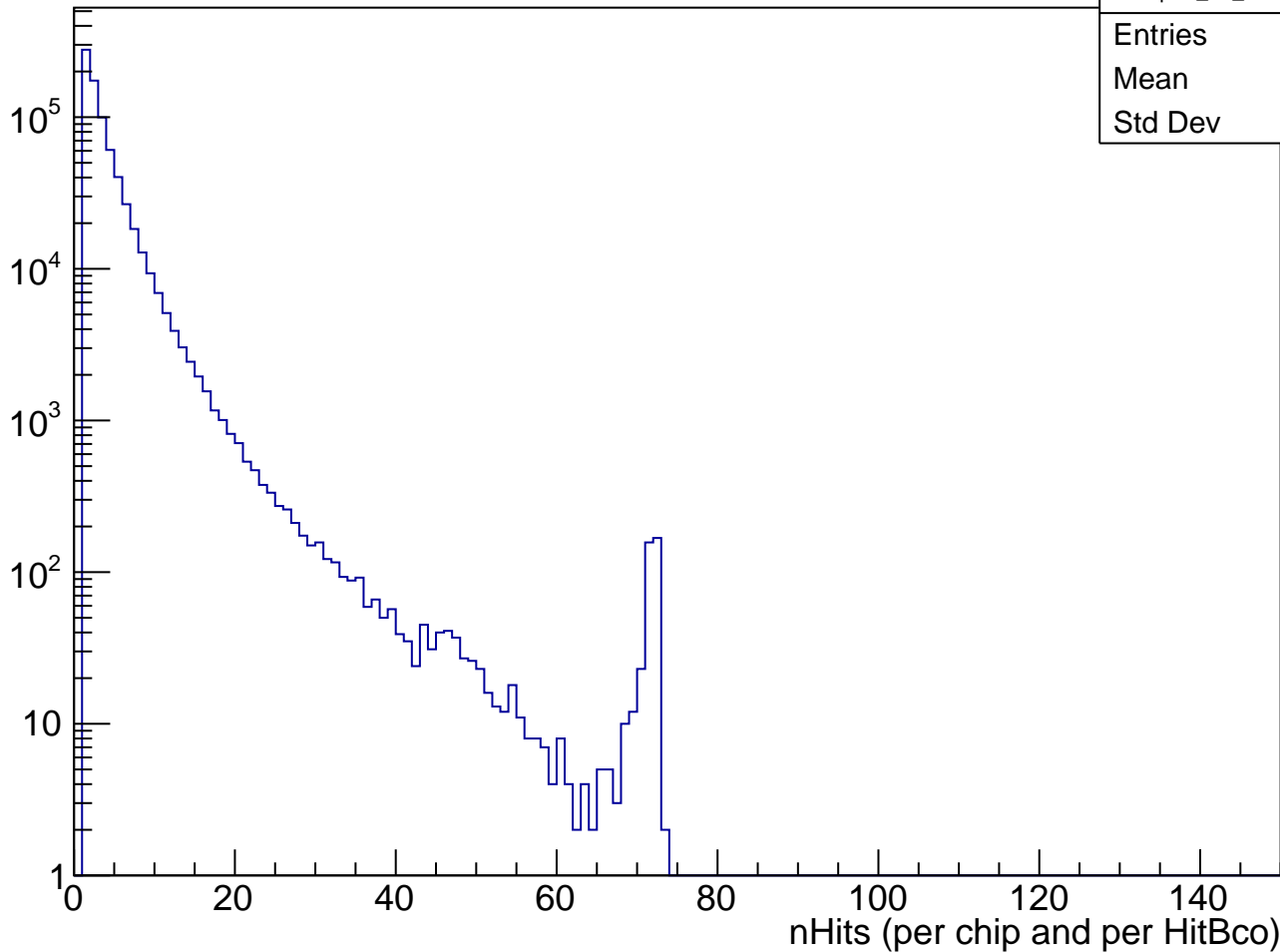
Entries



nChipHit_F6_Fch12_Chip18	
Entries	0
Mean	0
Std Dev	0

nChipHit_F6_Fch12_Chip19

Entries



nChipHit_F6_Fch12_Chip19

Entries 753692

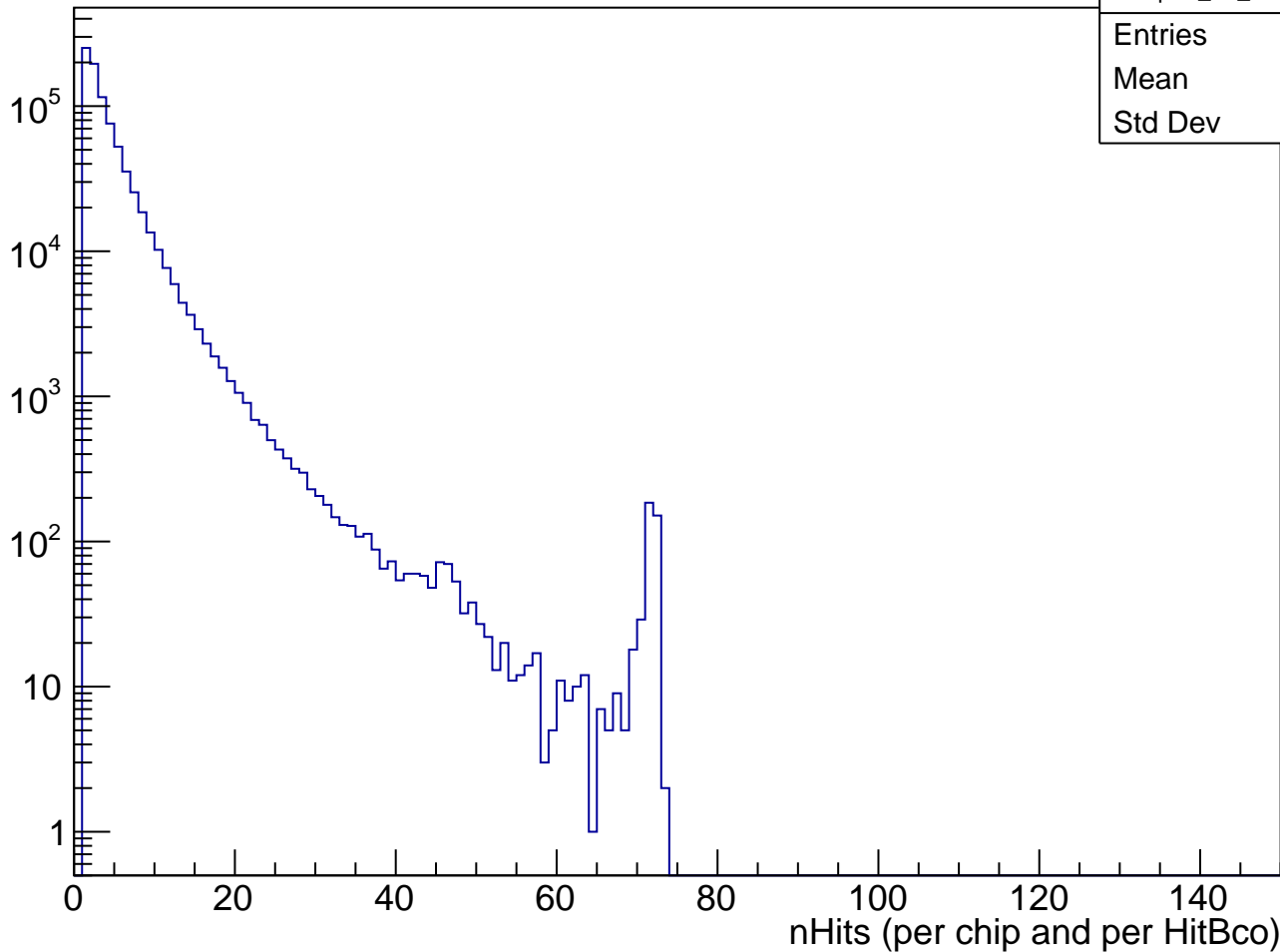
Mean 3.132

Std Dev 3.736

nHits (per chip and per HitBco)

nChipHit_F6_Fch12_Chip2

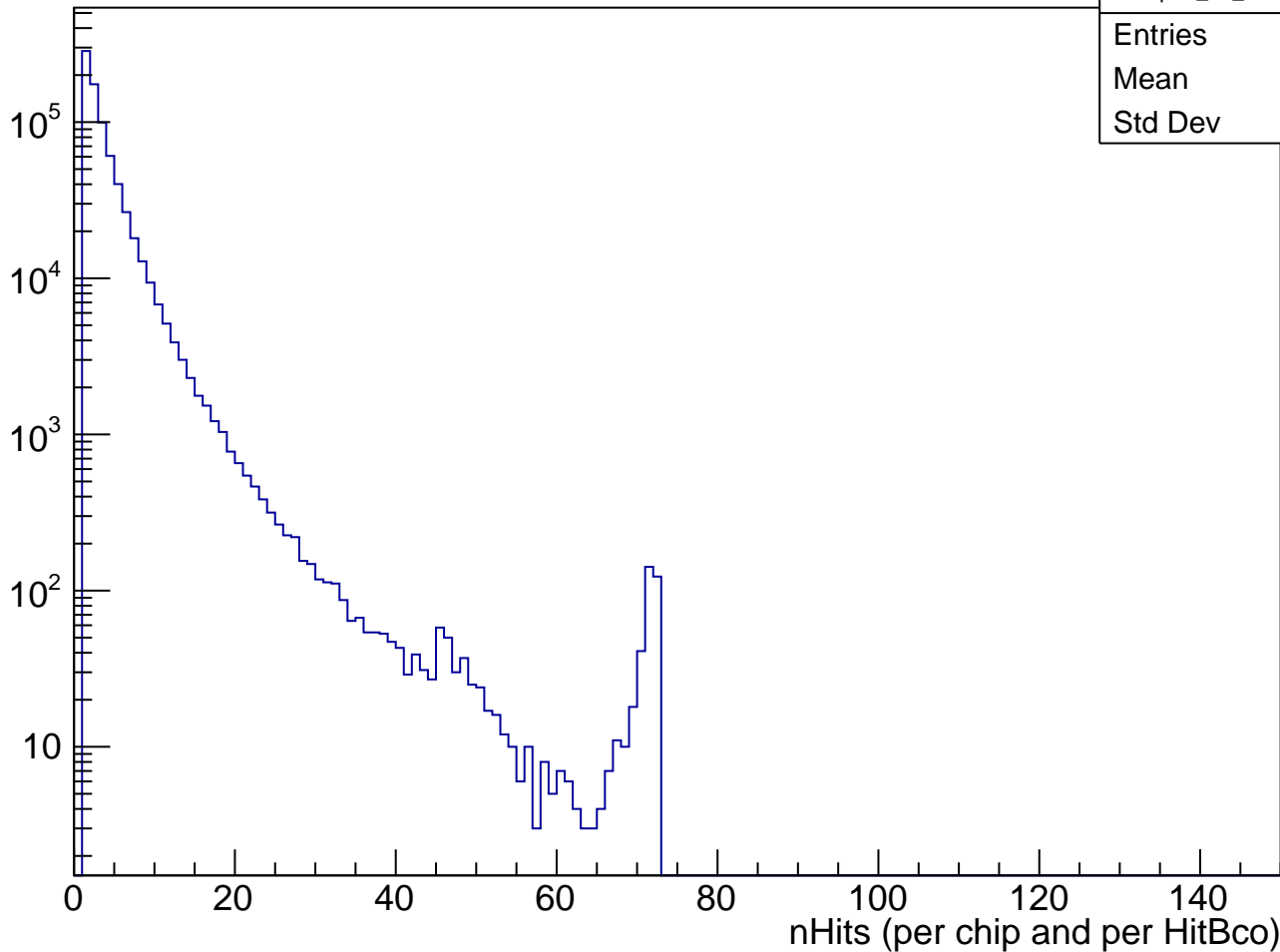
Entries



nChipHit_F6_Fch12_Chip2	
Entries	833733
Mean	3.572
Std Dev	4.077

nChipHit_F6_Fch12_Chip20

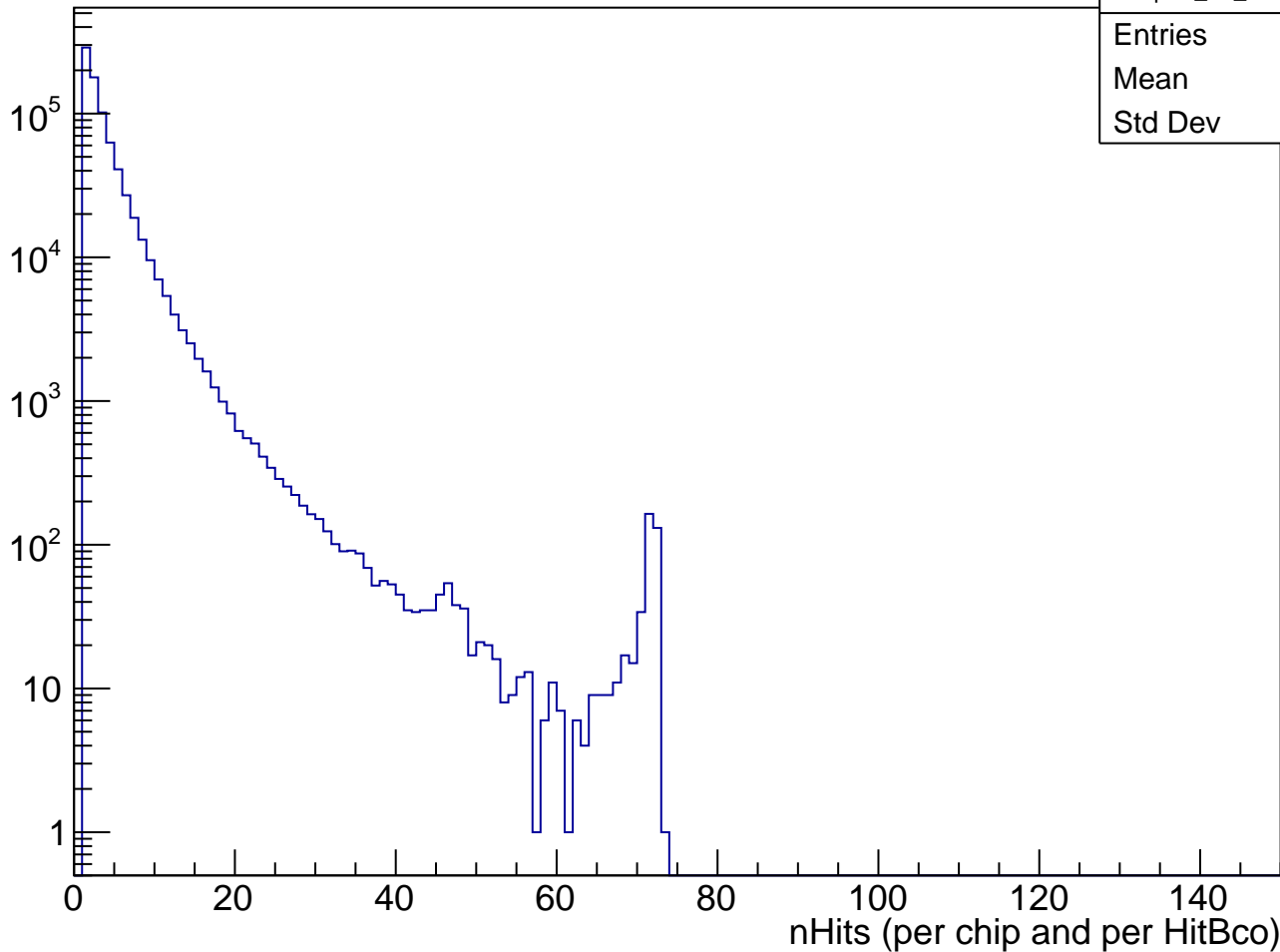
Entries



nChipHit_F6_Fch12_Chip20	
Entries	759760
Mean	3.093
Std Dev	3.665

nChipHit_F6_Fch12_Chip21

Entries

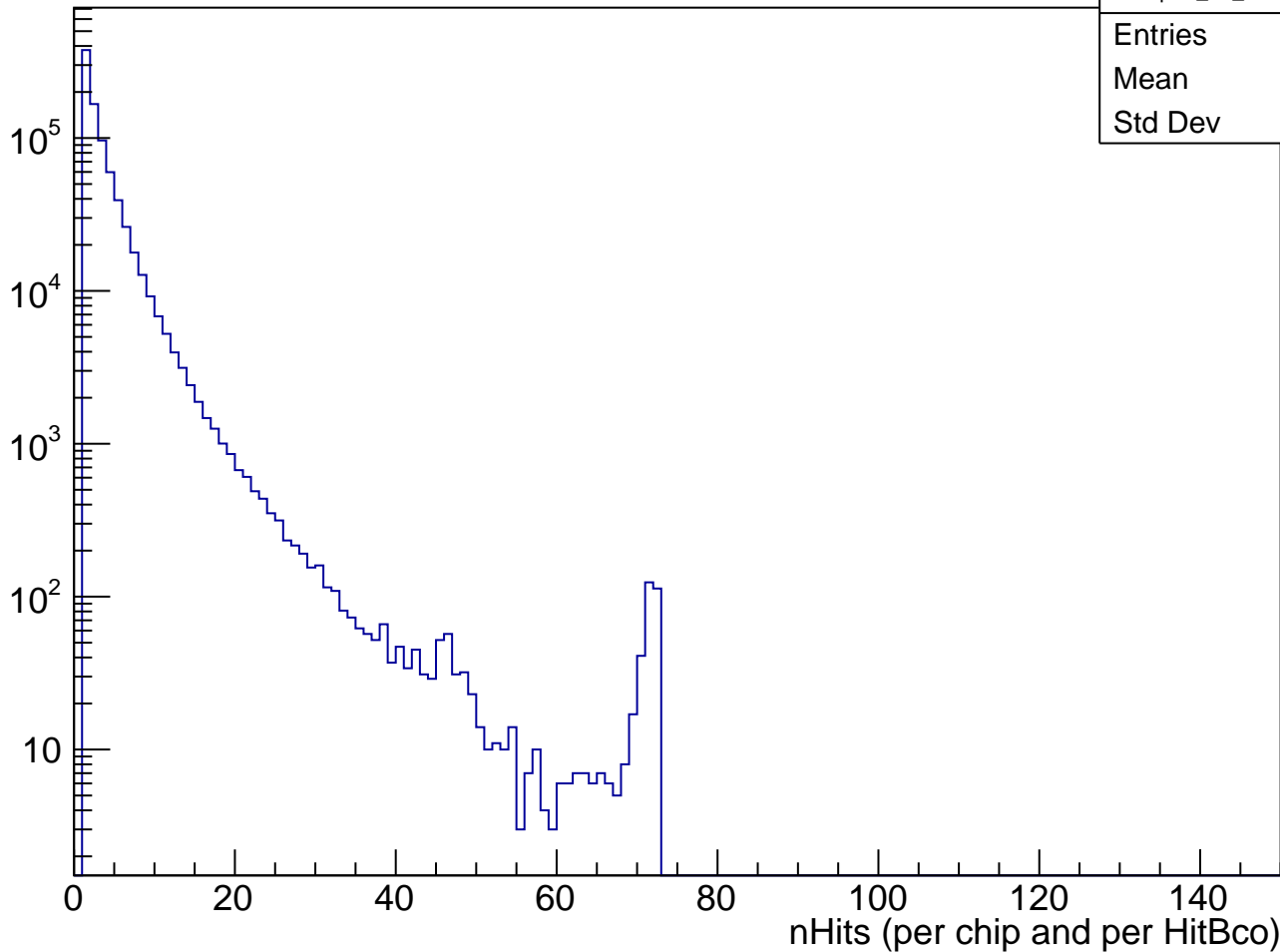


nChipHit_F6_Fch12_Chip21	
Entries	774988
Mean	3.122
Std Dev	3.725

nHits (per chip and per HitBco)

nChipHit_F6_Fch12_Chip22

Entries



nChipHit_F6_Fch12_Chip22

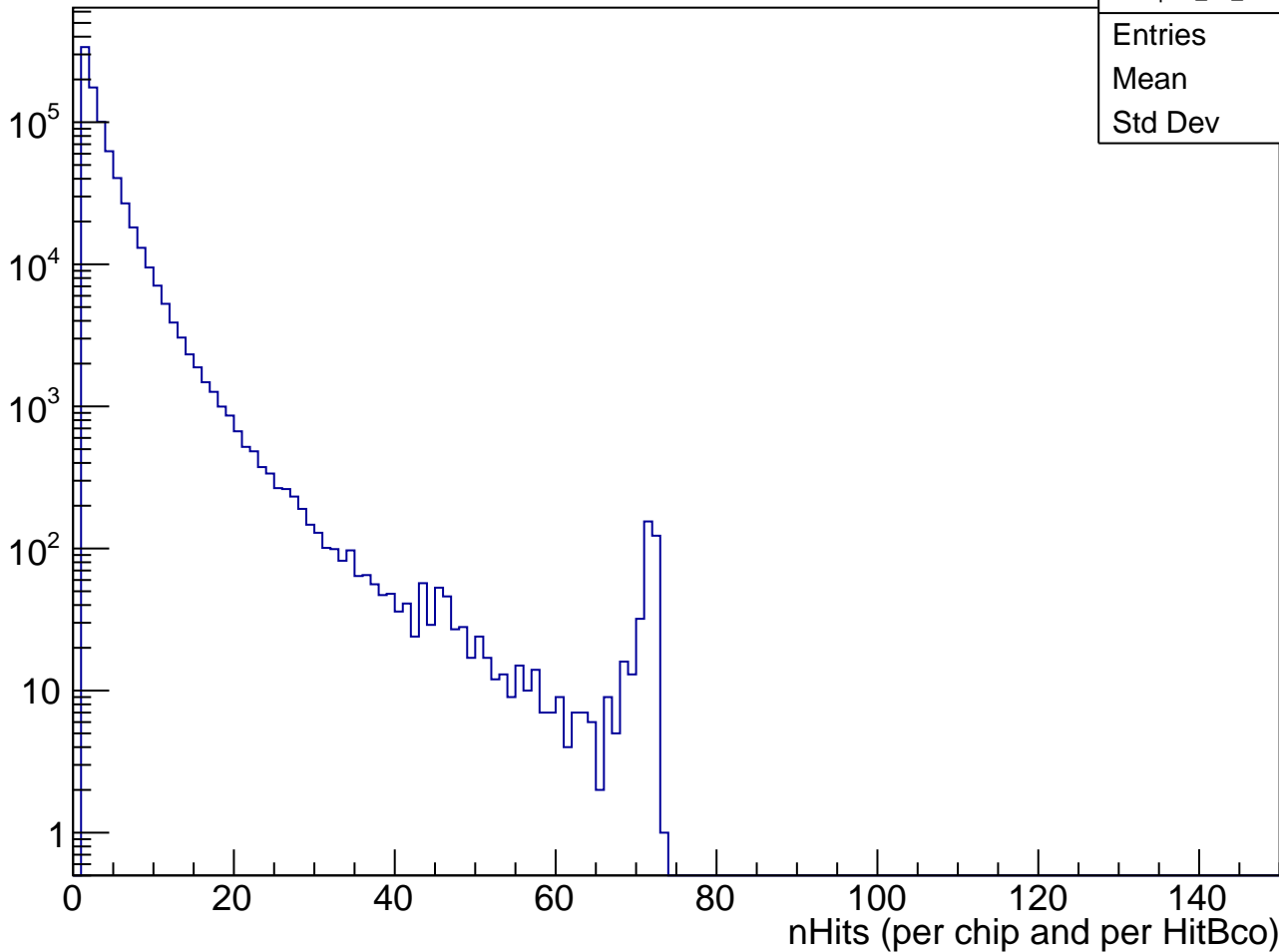
Entries 837645

Mean 2.882

Std Dev 3.555

nChipHit_F6_Fch12_Chip23

Entries



nChipHit_F6_Fch12_Chip23

Entries 818099

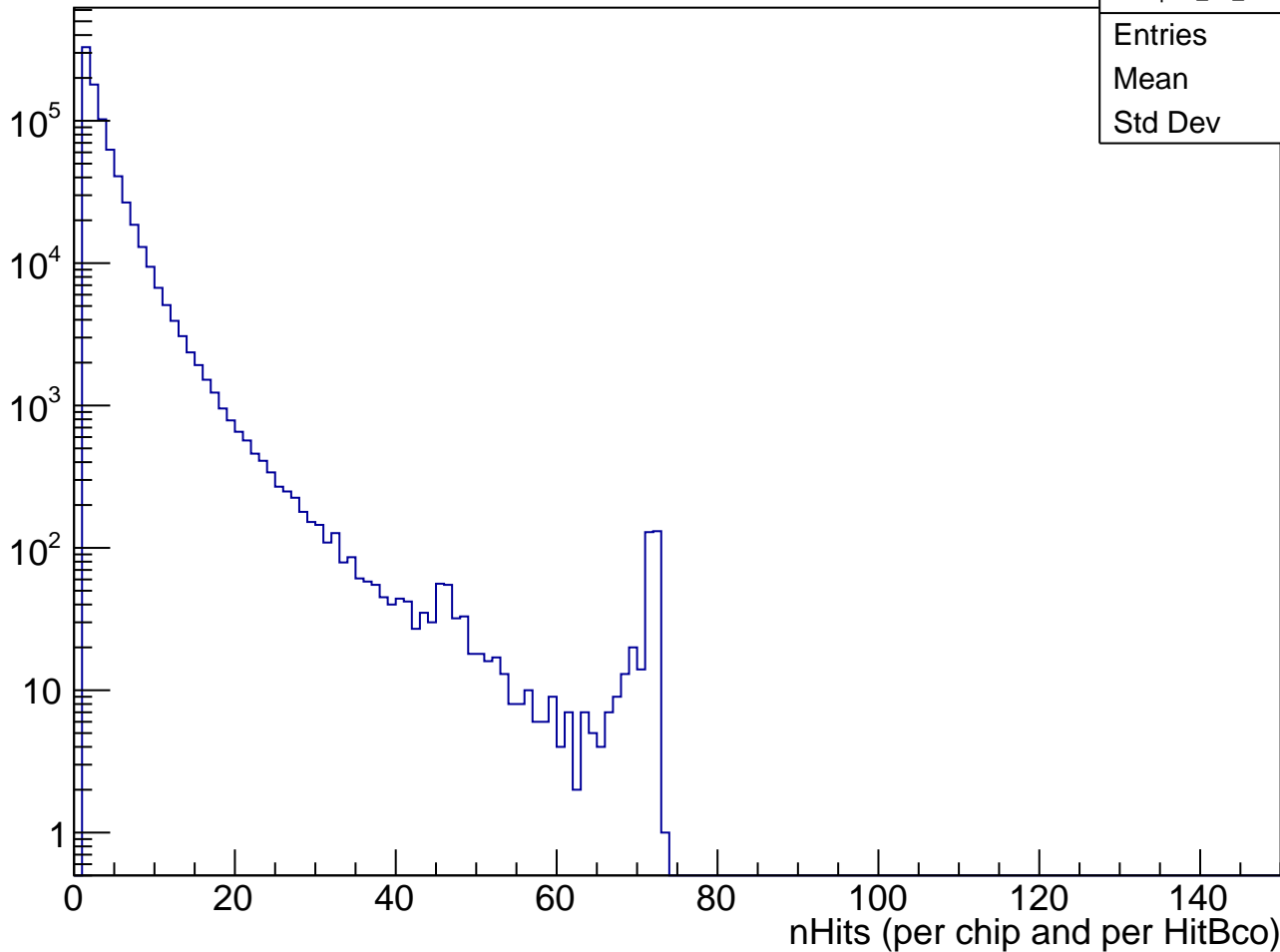
Mean 2.976

Std Dev 3.606

nHits (per chip and per HitBco)

nChipHit_F6_Fch12_Chip24

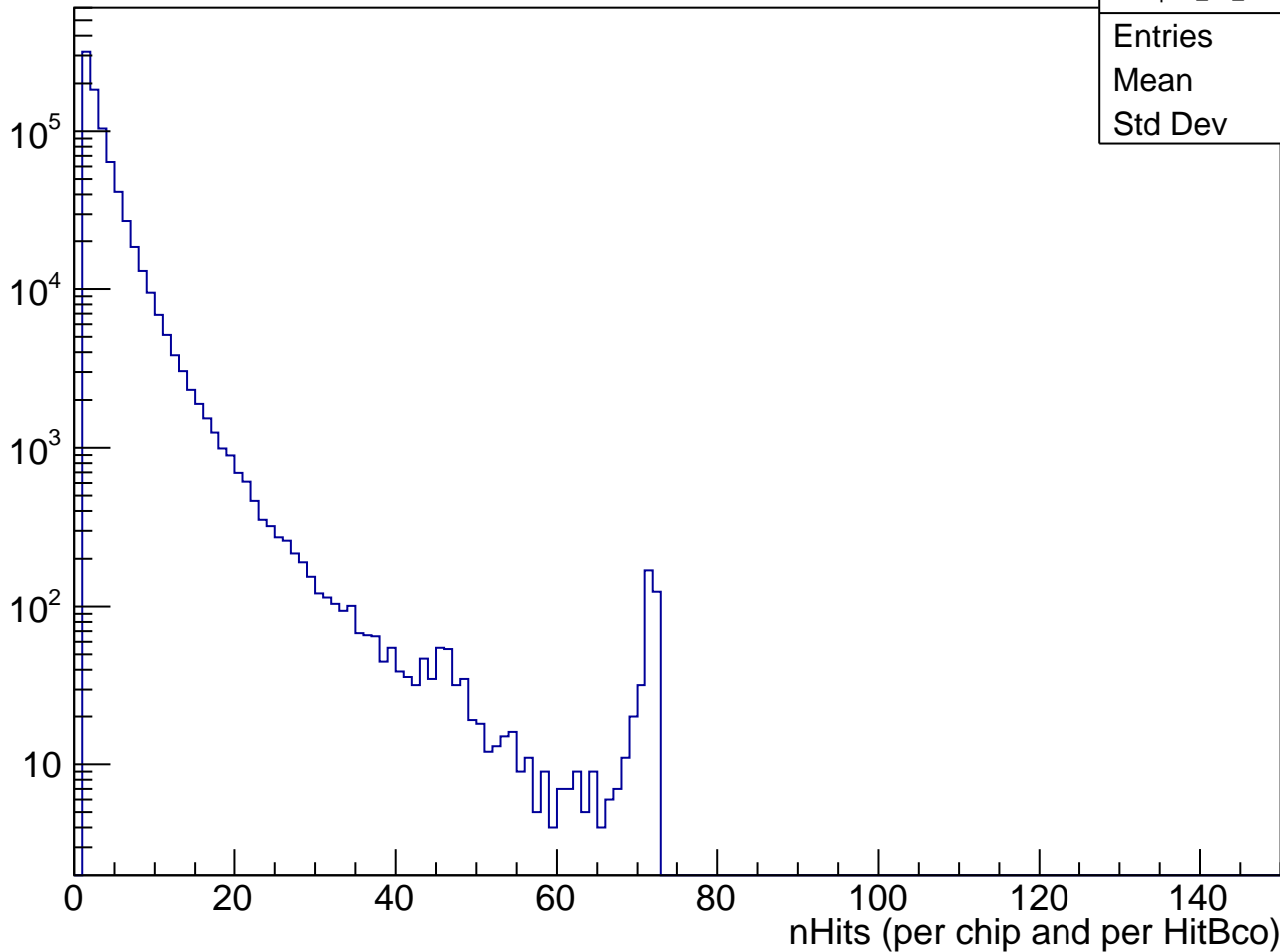
Entries



nChipHit_F6_Fch12_Chip24	
Entries	814874
Mean	2.986
Std Dev	3.573

nChipHit_F6_Fch12_Chip25

Entries



nChipHit_F6_Fch12_Chip25

Entries 810431

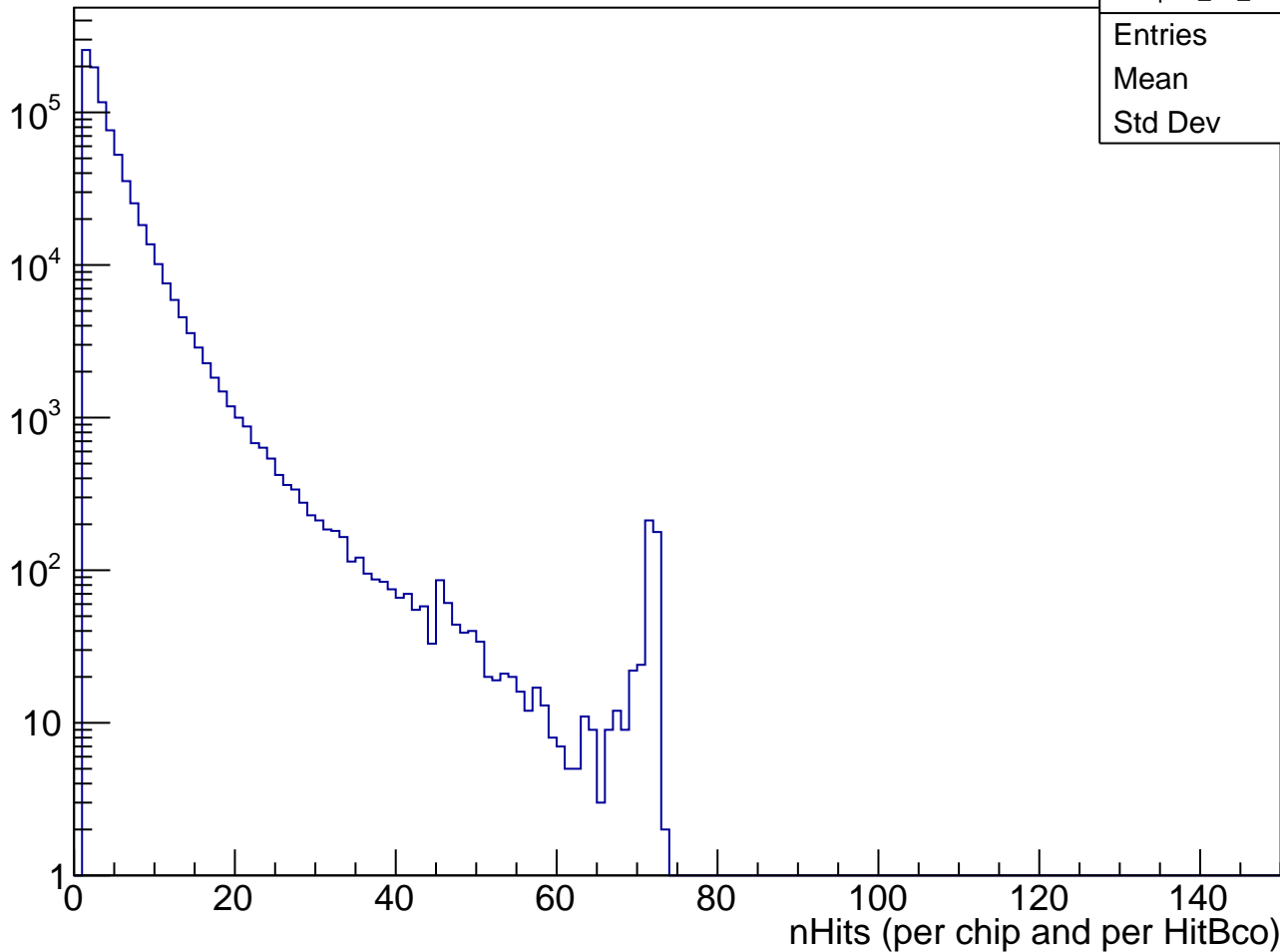
Mean 3.028

Std Dev 3.637

nHits (per chip and per HitBco)

nChipHit_F6_Fch12_Chip3

Entries



nChipHit_F6_Fch12_Chip3

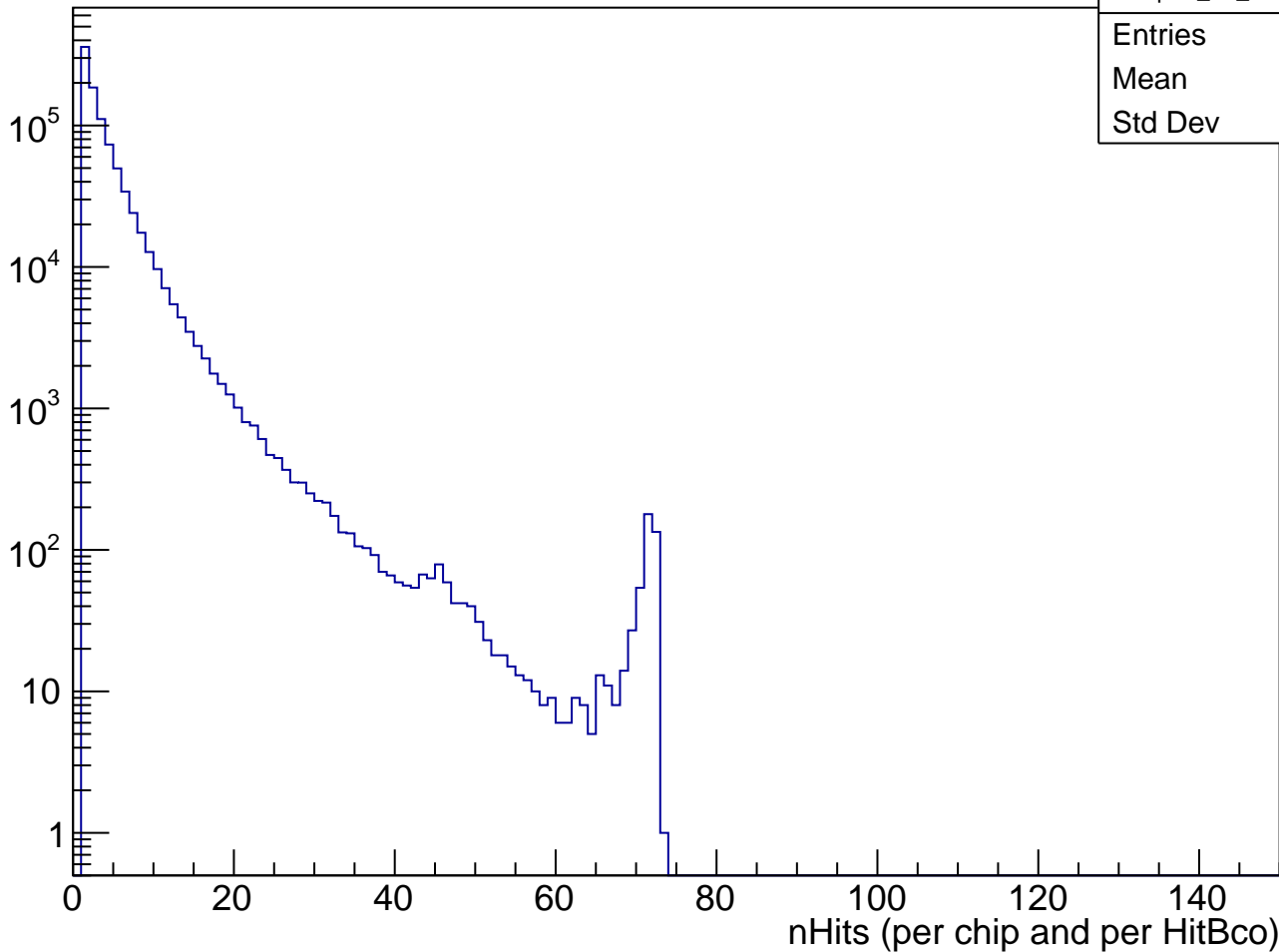
Entries 841290

Mean 3.556

Std Dev 4.113

nChipHit_F6_Fch12_Chip4

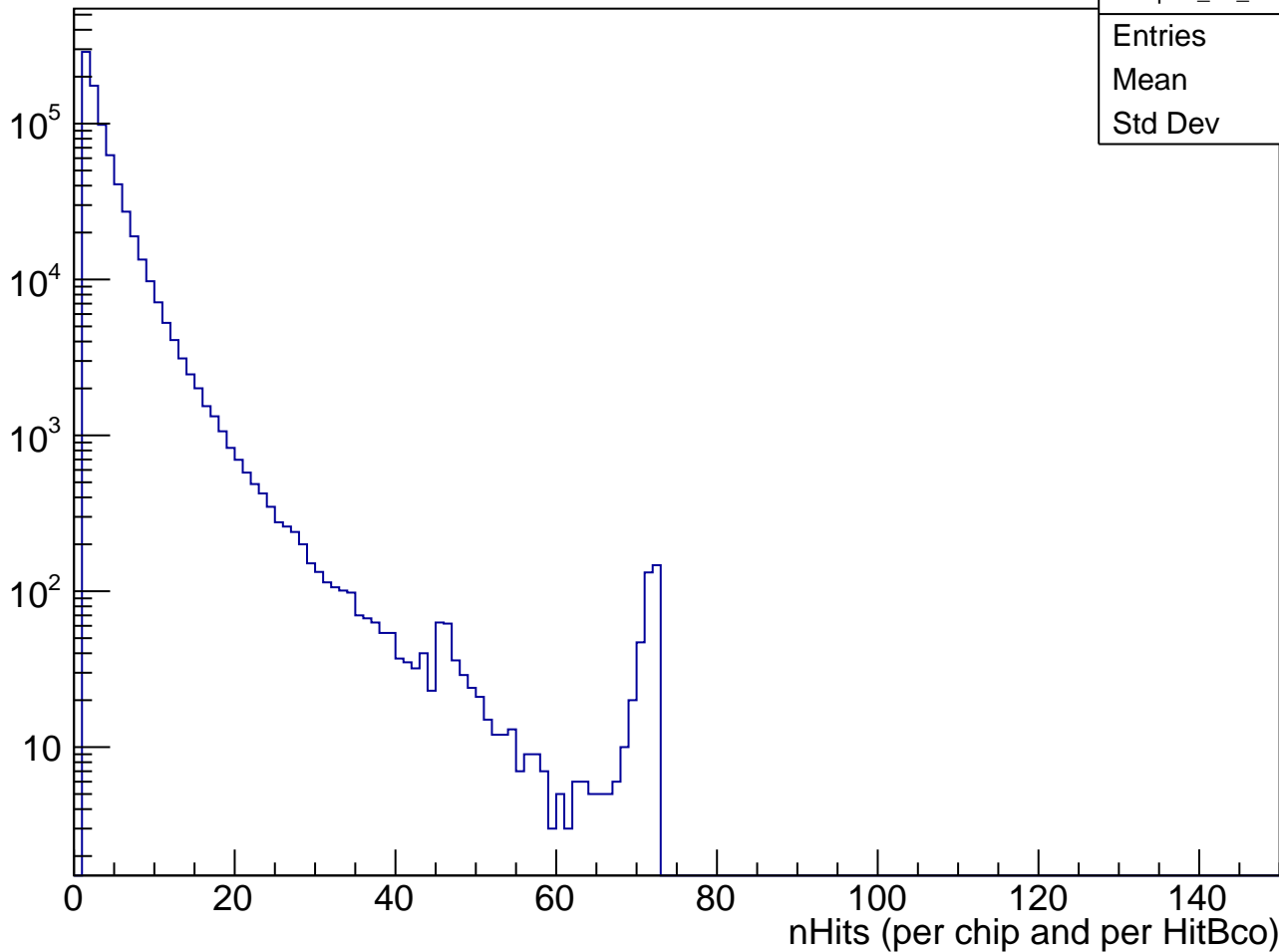
Entries



nChipHit_F6_Fch12_Chip4	
Entries	914737
Mean	3.248
Std Dev	3.963

nChipHit_F6_Fch12_Chip5

Entries



nChipHit_F6_Fch12_Chip5

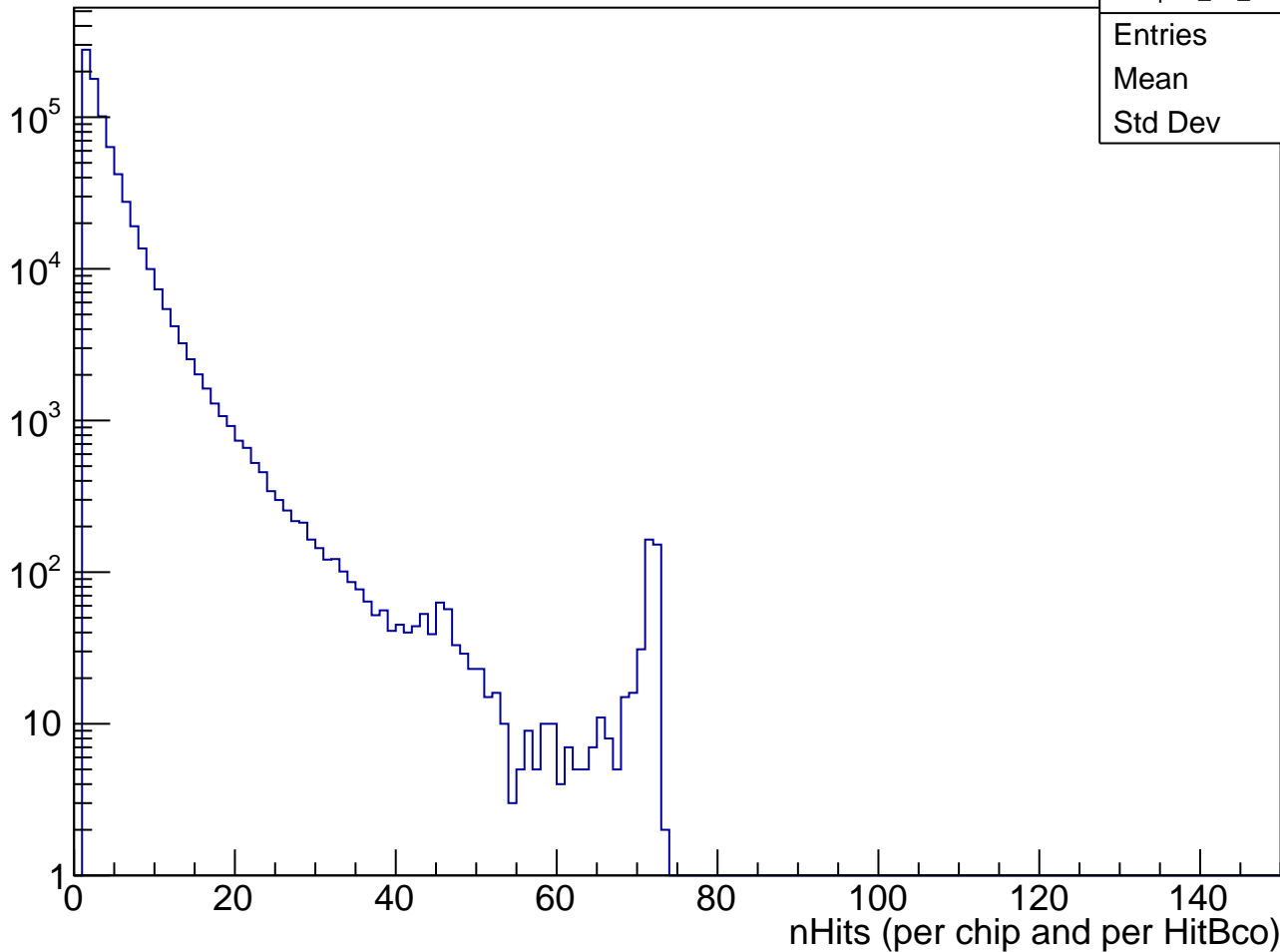
Entries 768814

Mean 3.133

Std Dev 3.737

nChipHit_F6_Fch12_Chip6

Entries

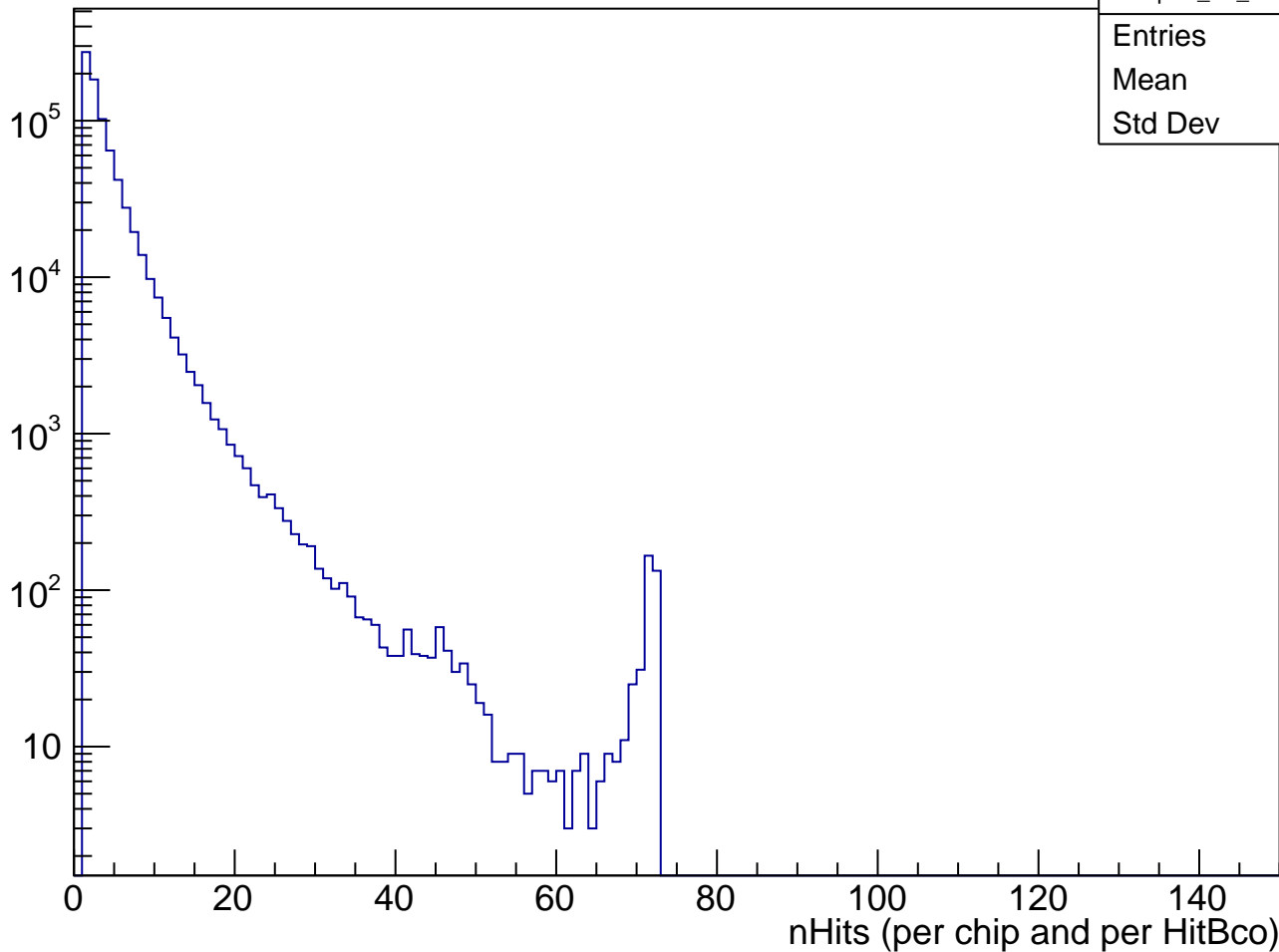


nChipHit_F6_Fch12_Chip6	
Entries	770003
Mean	3.184
Std Dev	3.793

nHits (per chip and per HitBco)

nChipHit_F6_Fch12_Chip7

Entries

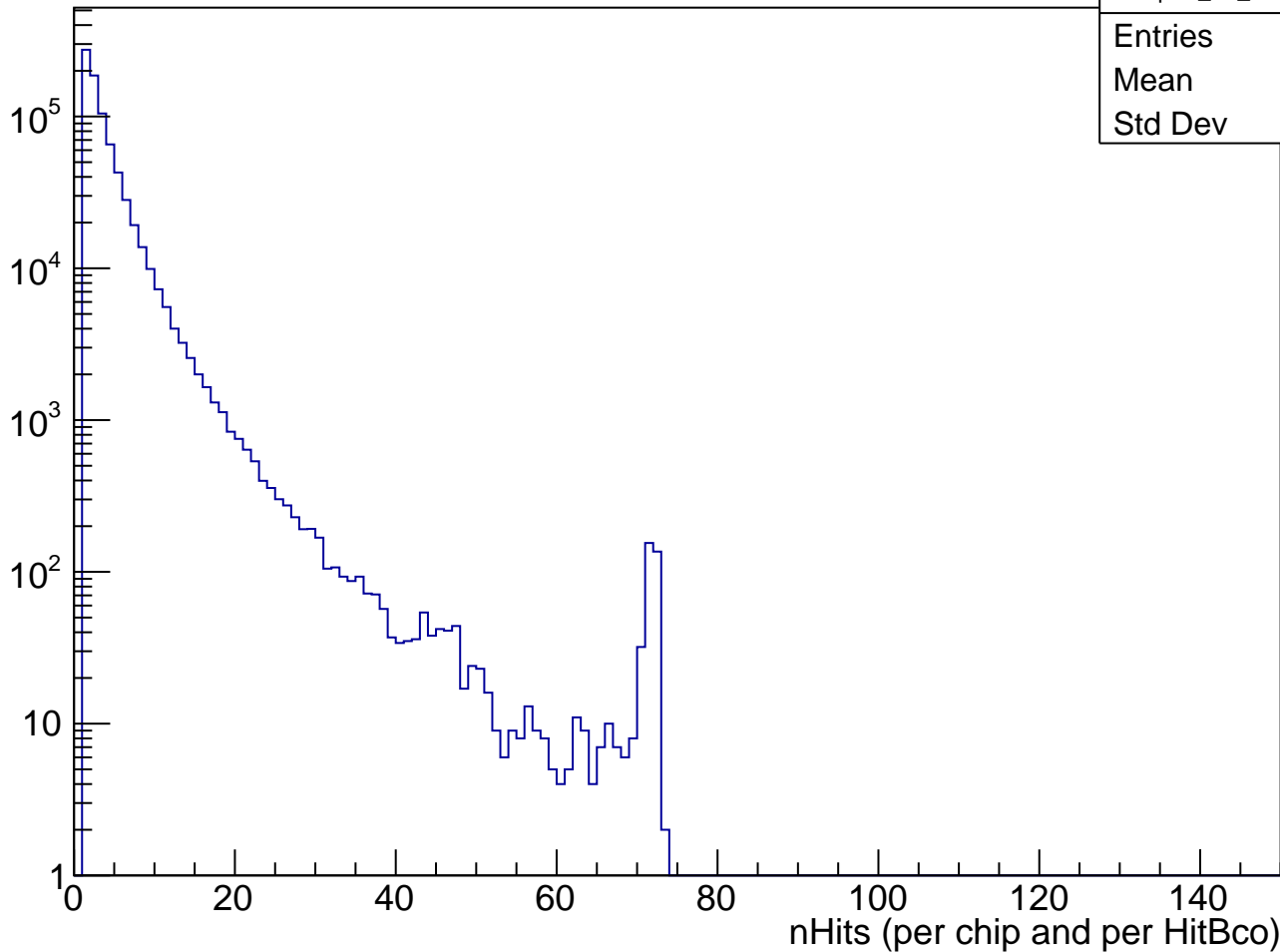


nChipHit_F6_Fch12_Chip7	
Entries	772687
Mean	3.182
Std Dev	3.748

nHits (per chip and per HitBco)

nChipHit_F6_Fch12_Chip8

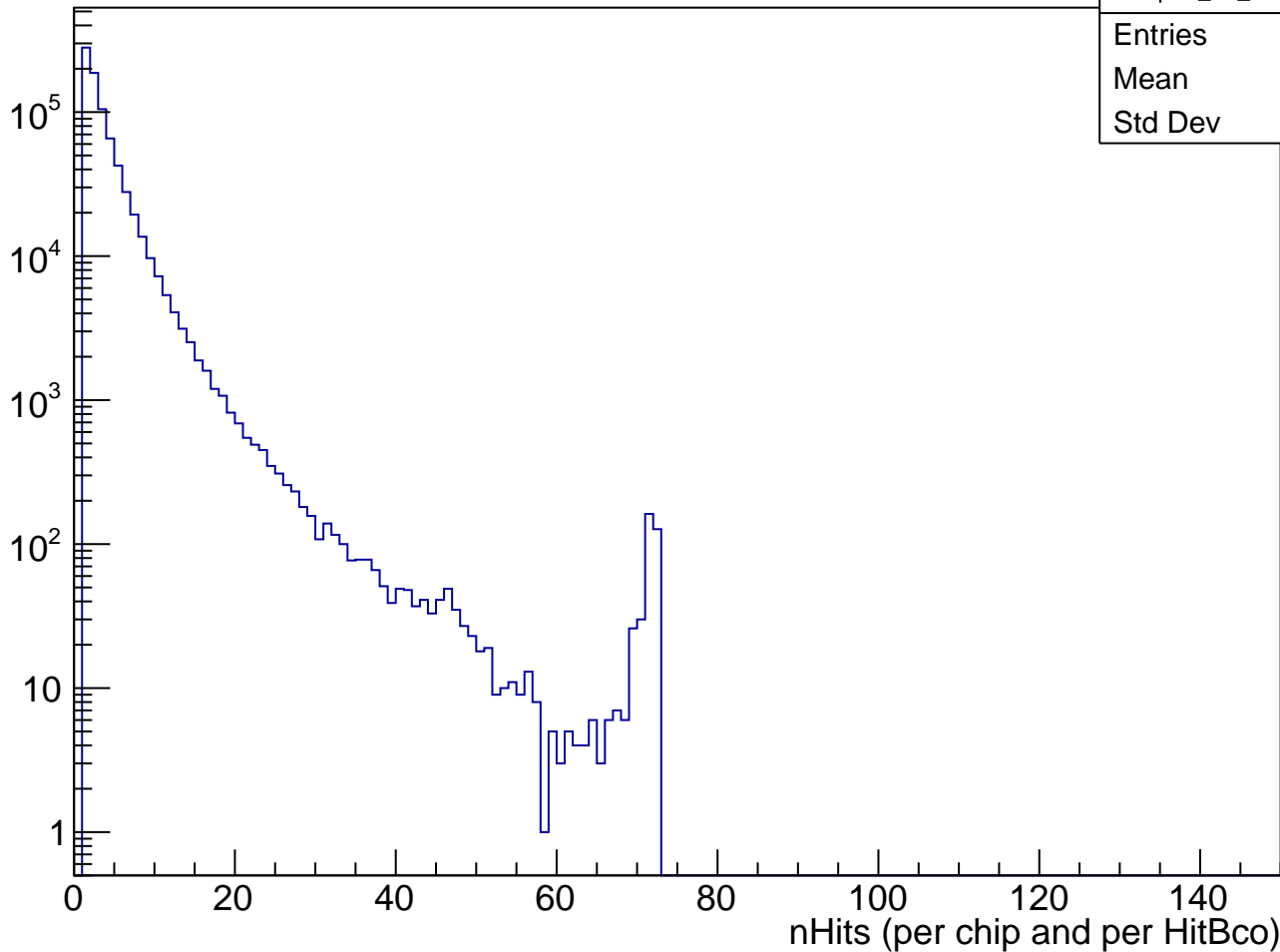
Entries



nChipHit_F6_Fch12_Chip8	
Entries	780511
Mean	3.183
Std Dev	3.727

nChipHit_F6_Fch12_Chip9

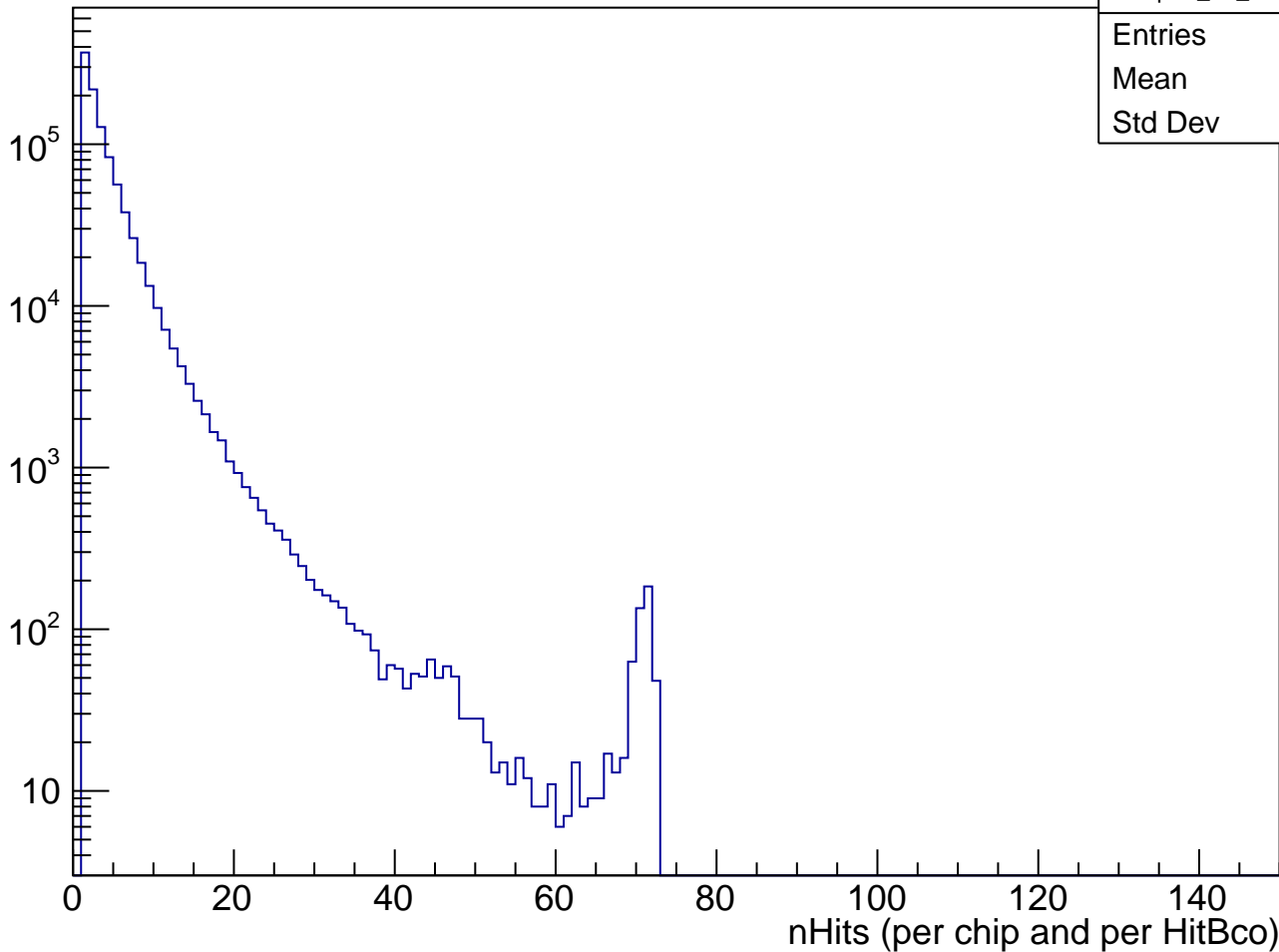
Entries



nChipHit_F6_Fch12_Chip9	
Entries	785493
Mean	3.147
Std Dev	3.683

nChipHit_F6_Fch13_Chip0

Entries

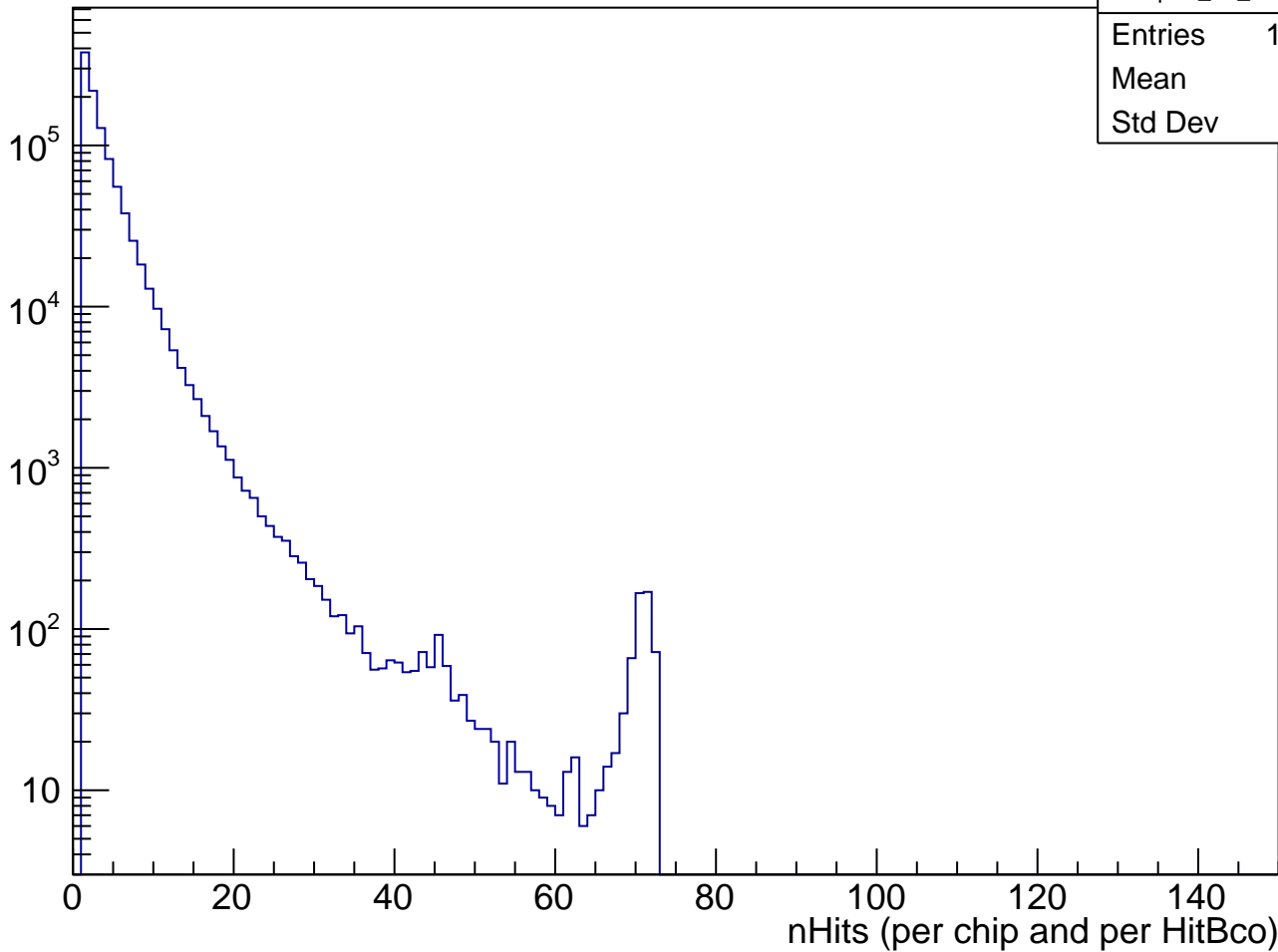


nChipHit_F6_Fch13_Chip0	
Entries	996600
Mean	3.198
Std Dev	3.761

nHits (per chip and per HitBco)

nChipHit_F6_Fch13_Chip1

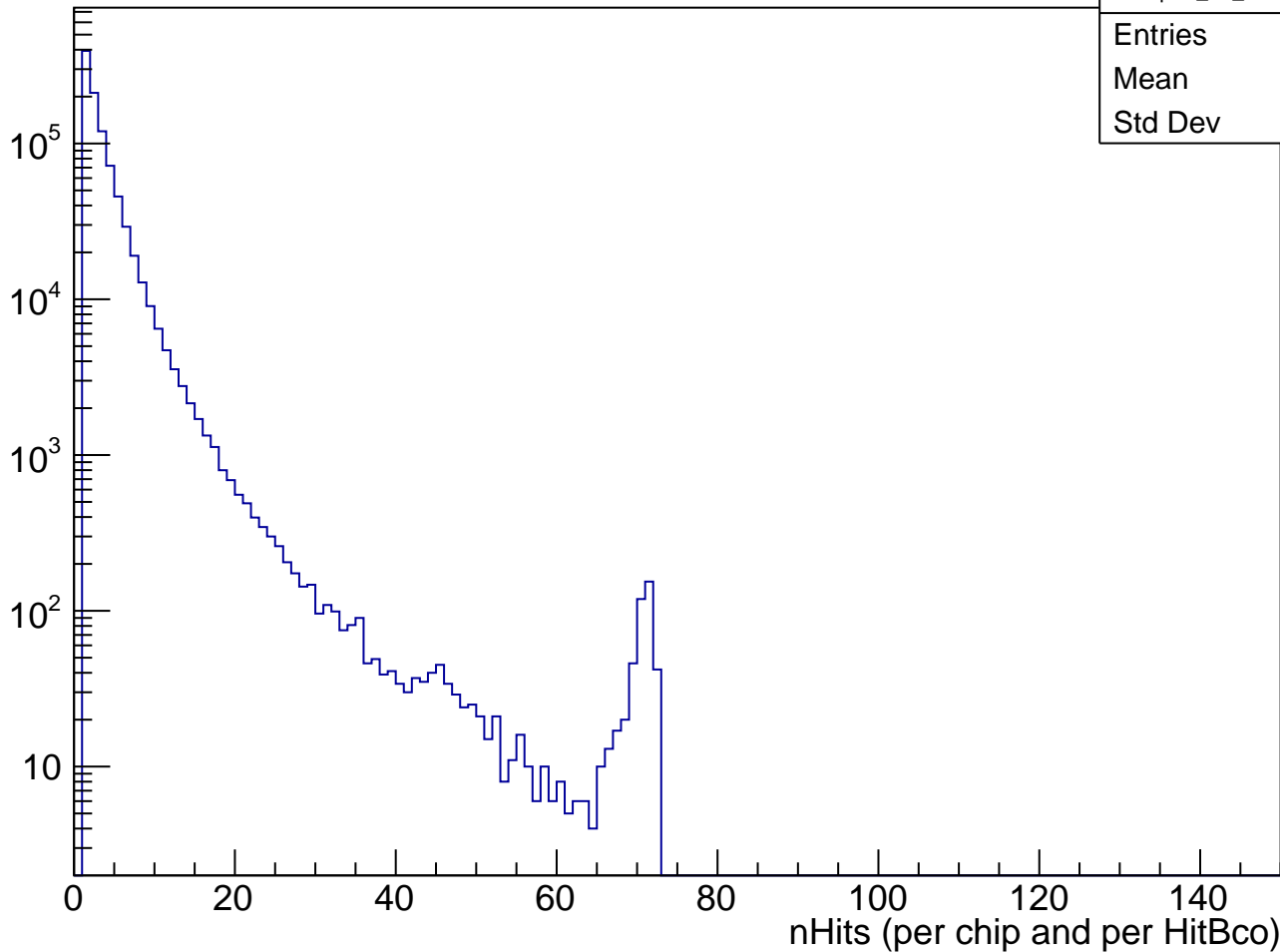
Entries



nChipHit_F6_Fch13_Chip1	
Entries	1002389
Mean	3.172
Std Dev	3.787

nChipHit_F6_Fch13_Chip10

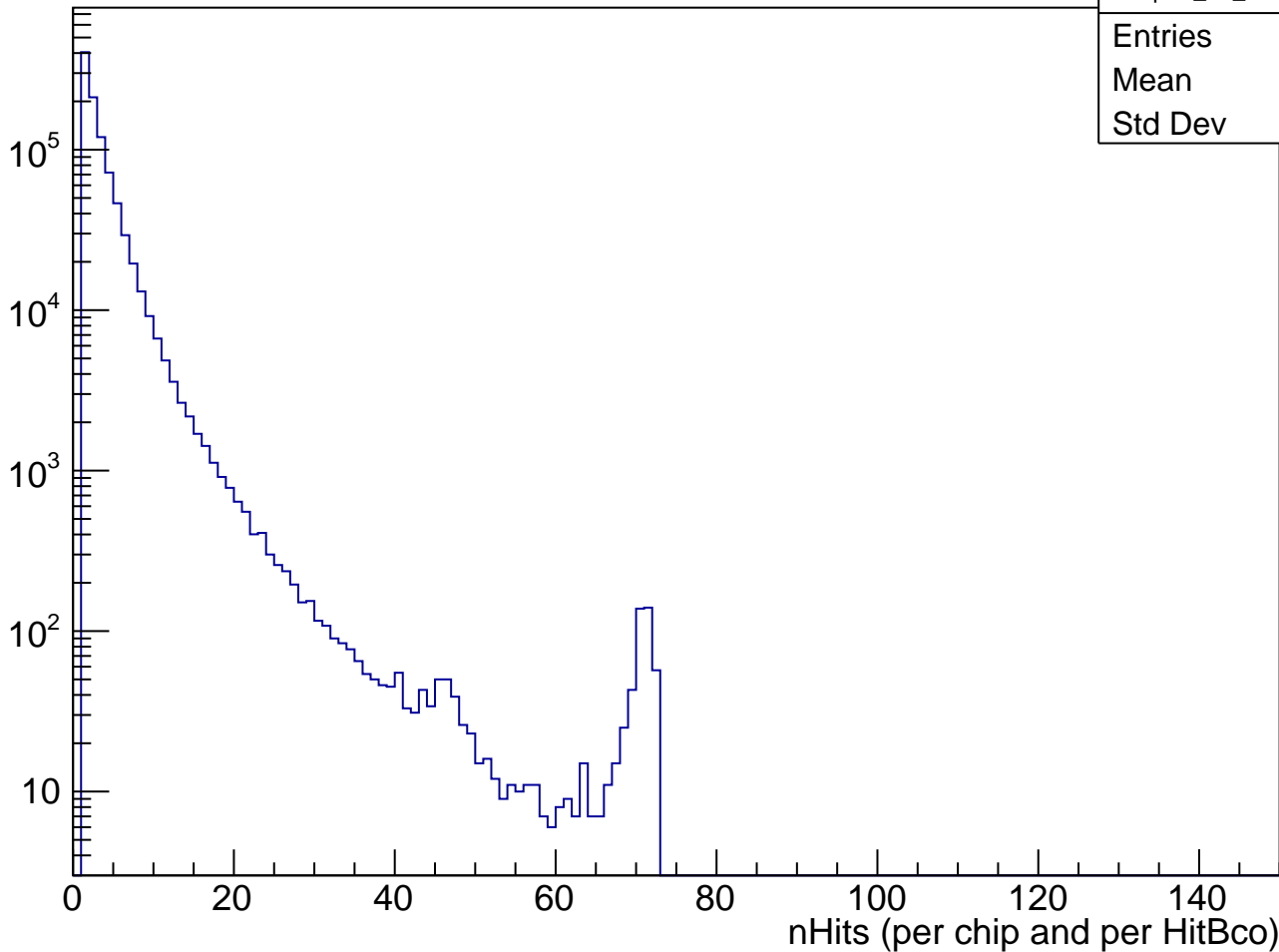
Entries



nChipHit_F6_Fch13_Chip10	
Entries	942136
Mean	2.813
Std Dev	3.343

nChipHit_F6_Fch13_Chip11

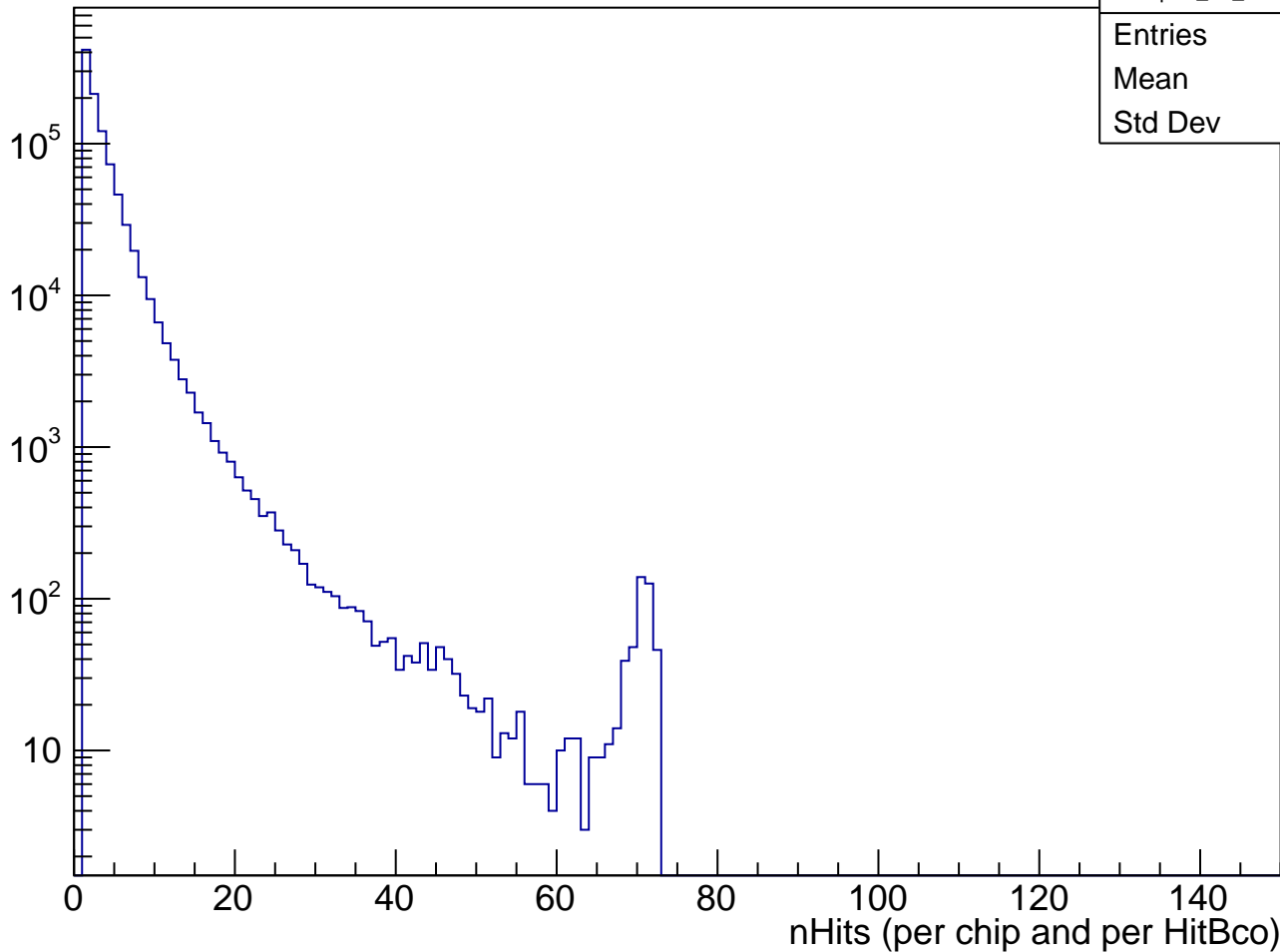
Entries



nChipHit_F6_Fch13_Chip11	
Entries	957201
Mean	2.812
Std Dev	3.385

nChipHit_F6_Fch13_Chip12

Entries



nChipHit_F6_Fch13_Chip12

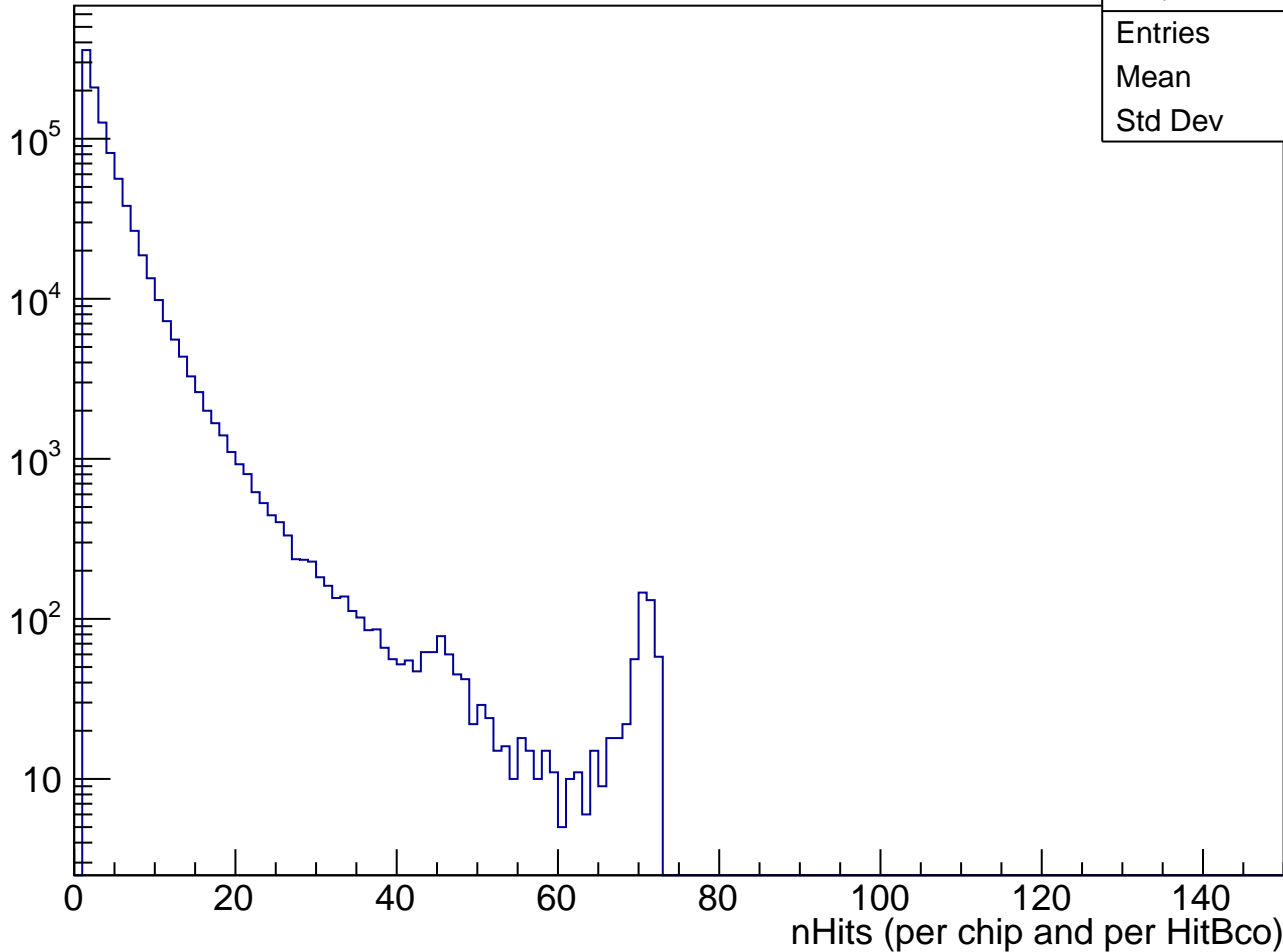
Entries 971750

Mean 2.801

Std Dev 3.381

nChipHit_F6_Fch13_Chip13

Entries

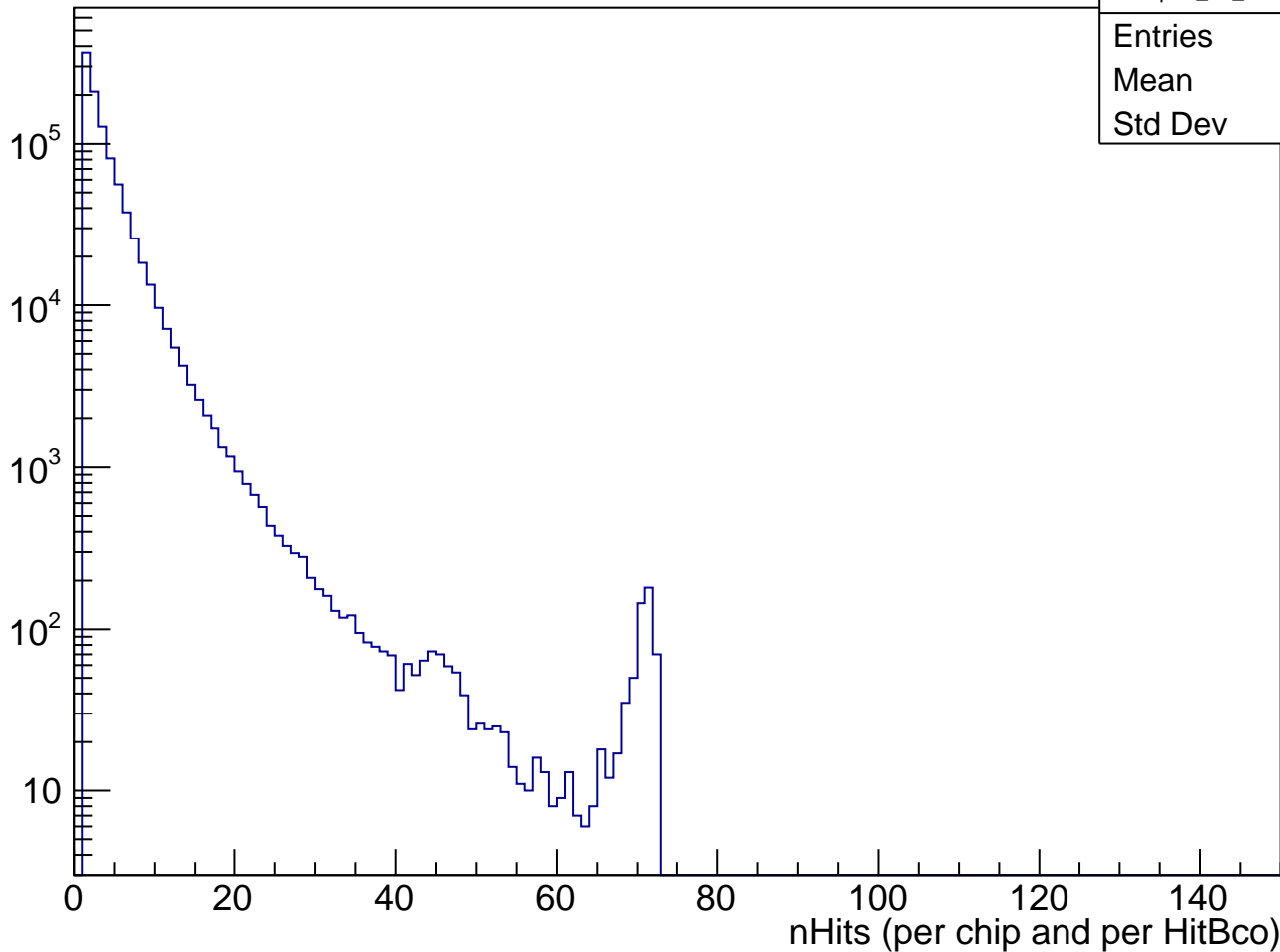


nChipHit_F6_Fch13_Chip13	
Entries	974049
Mean	3.239
Std Dev	3.795

nHits (per chip and per HitBco)

nChipHit_F6_Fch13_Chip14

Entries



nChipHit_F6_Fch13_Chip14

Entries 981361

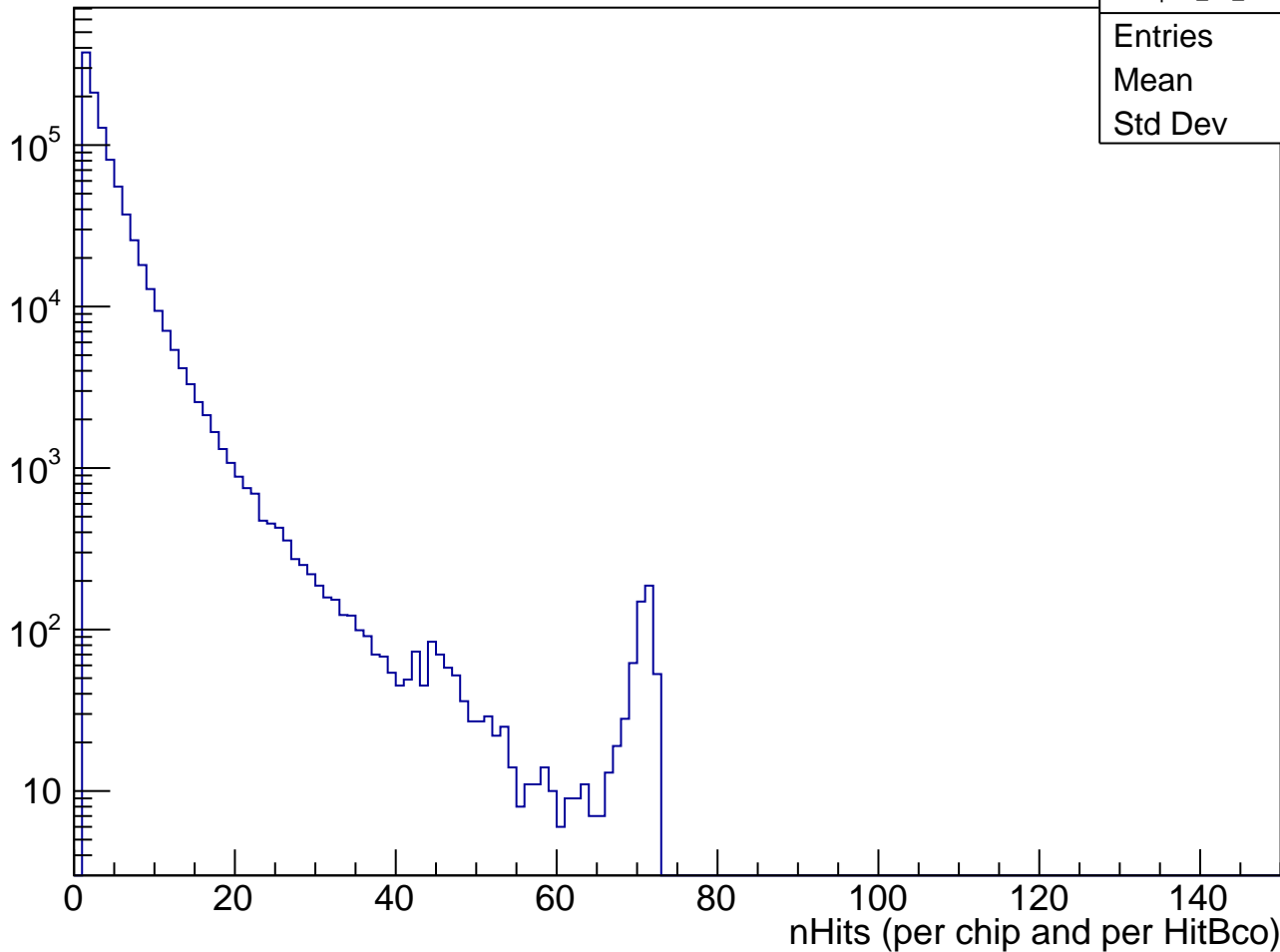
Mean 3.22

Std Dev 3.833

nHits (per chip and per HitBco)

nChipHit_F6_Fch13_Chip15

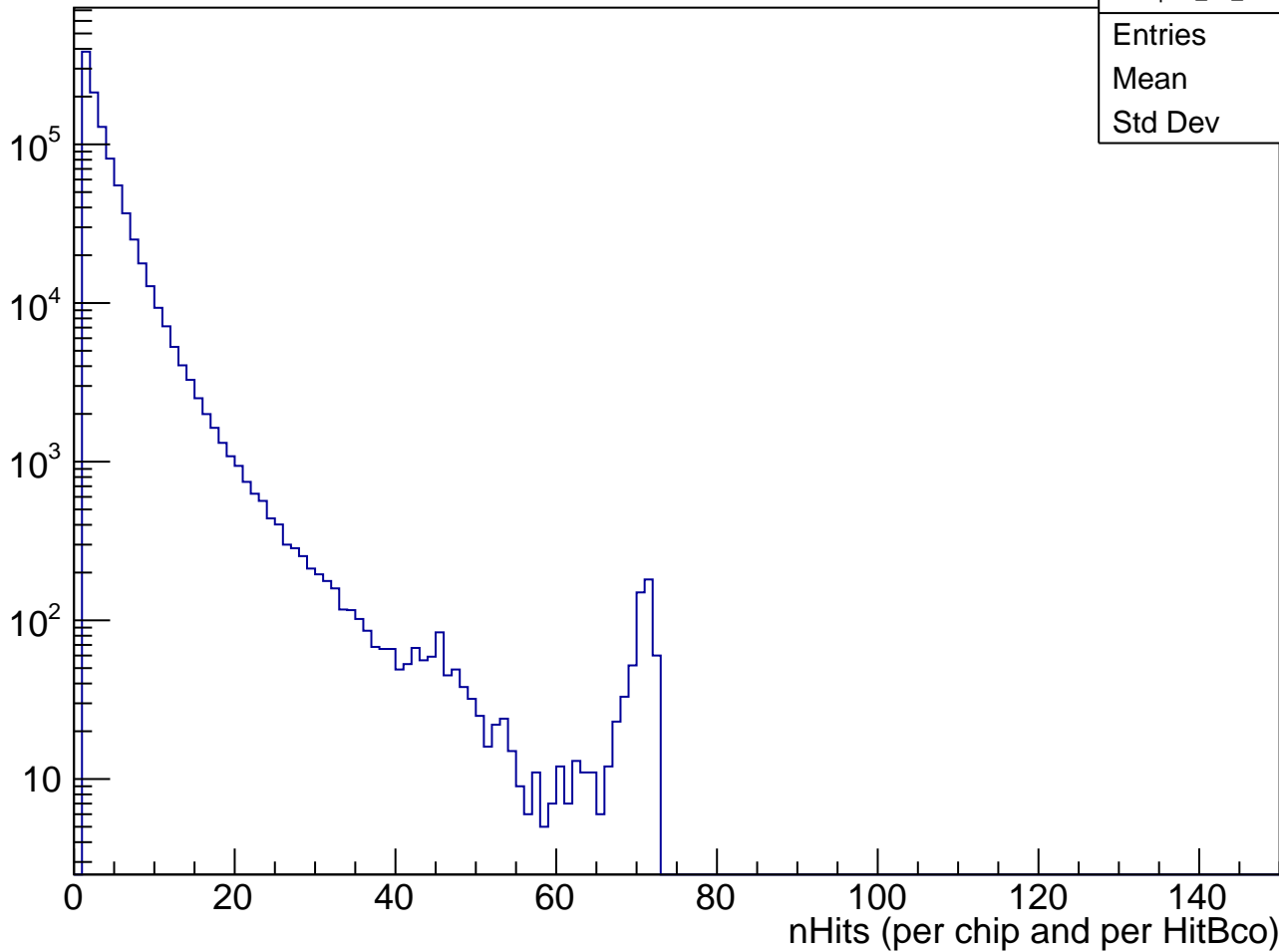
Entries



nChipHit_F6_Fch13_Chip15	
Entries	989231
Mean	3.182
Std Dev	3.803

nChipHit_F6_Fch13_Chip16

Entries



nChipHit_F6_Fch13_Chip16

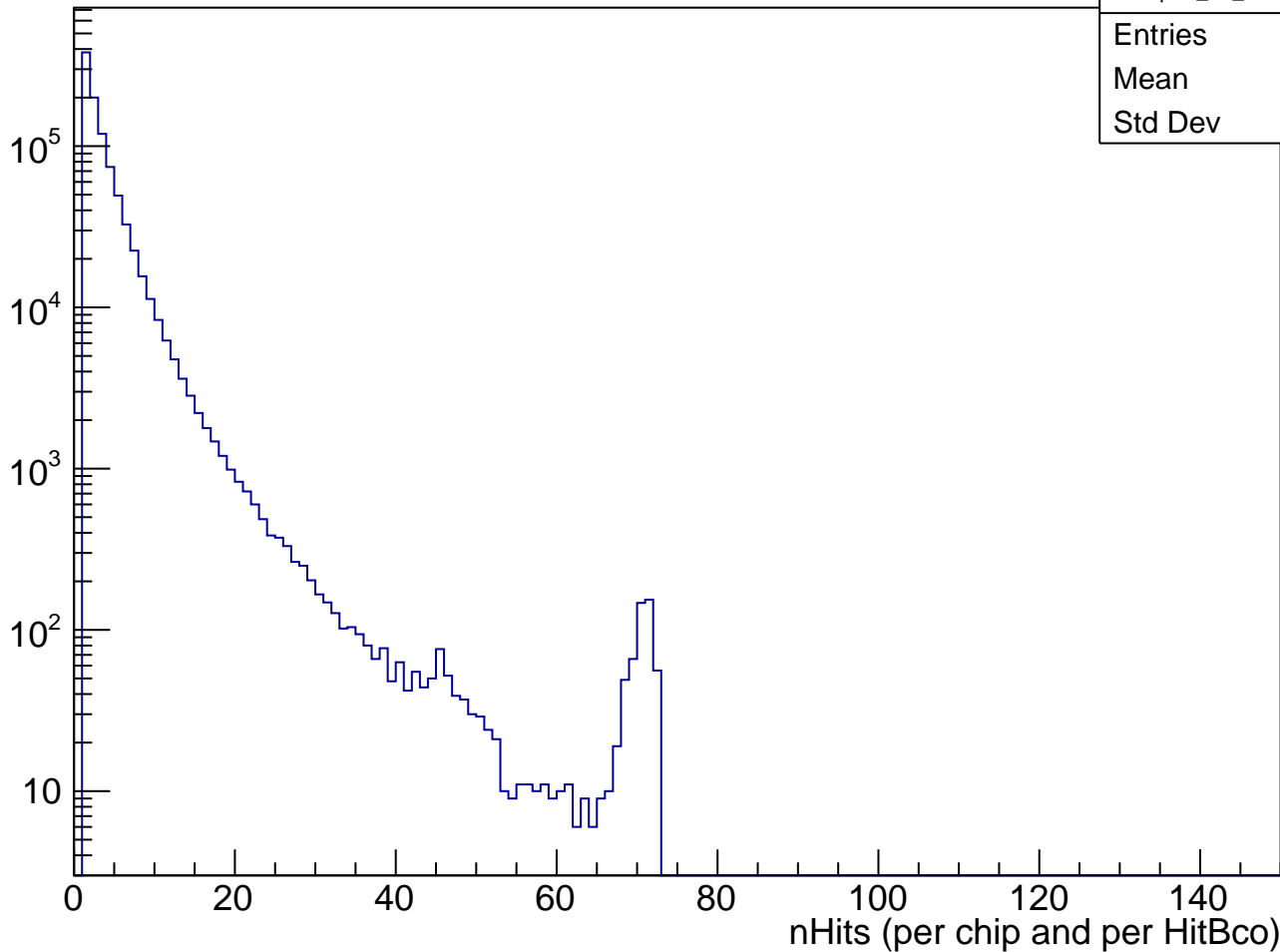
Entries 998593

Mean 3.149

Std Dev 3.775

nChipHit_F6_Fch13_Chip17

Entries

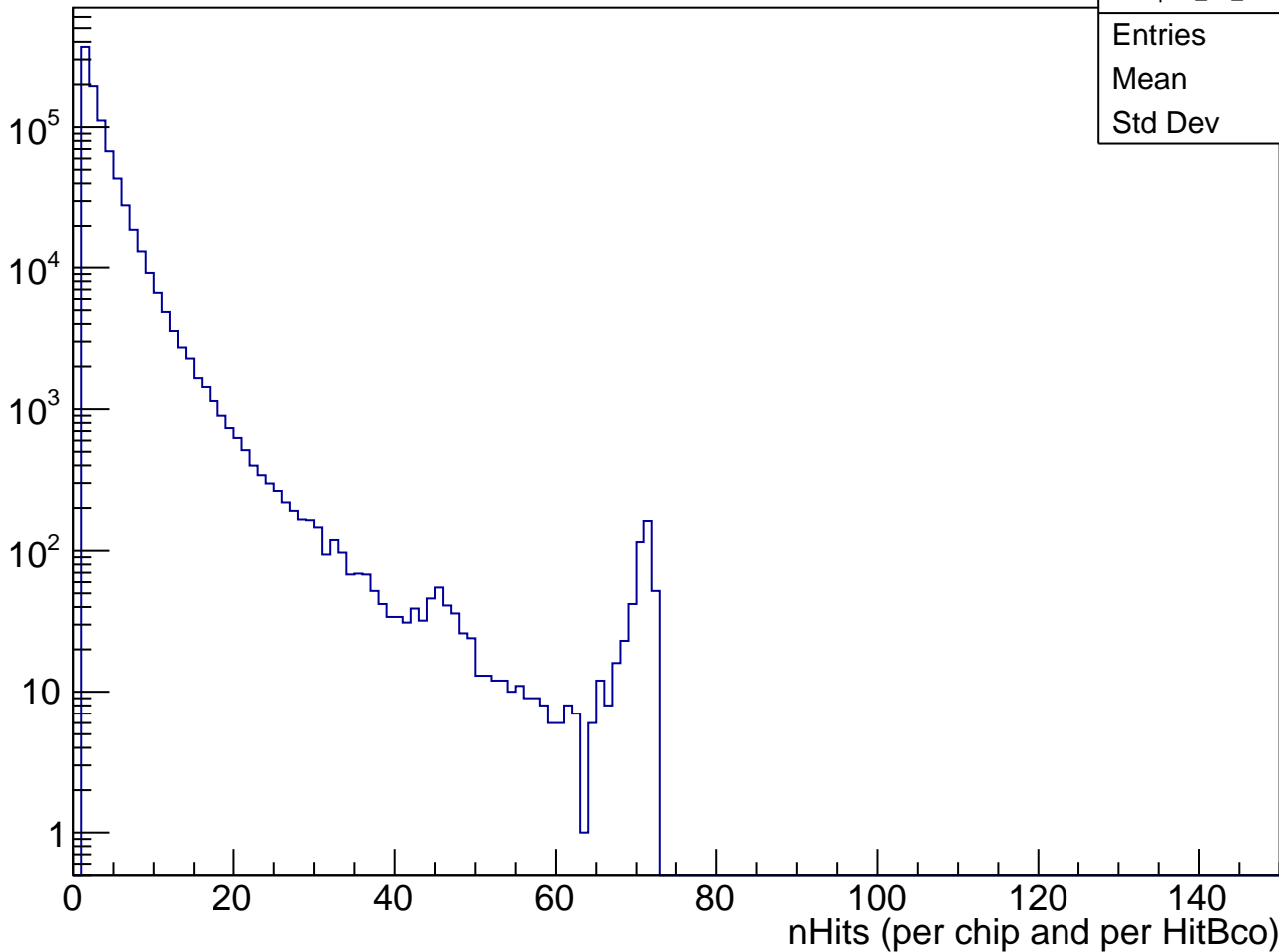


nChipHit_F6_Fch13_Chip17	
Entries	946534
Mean	3.054
Std Dev	3.736

nHits (per chip and per HitBco)

nChipHit_F6_Fch13_Chip18

Entries



nChipHit_F6_Fch13_Chip18

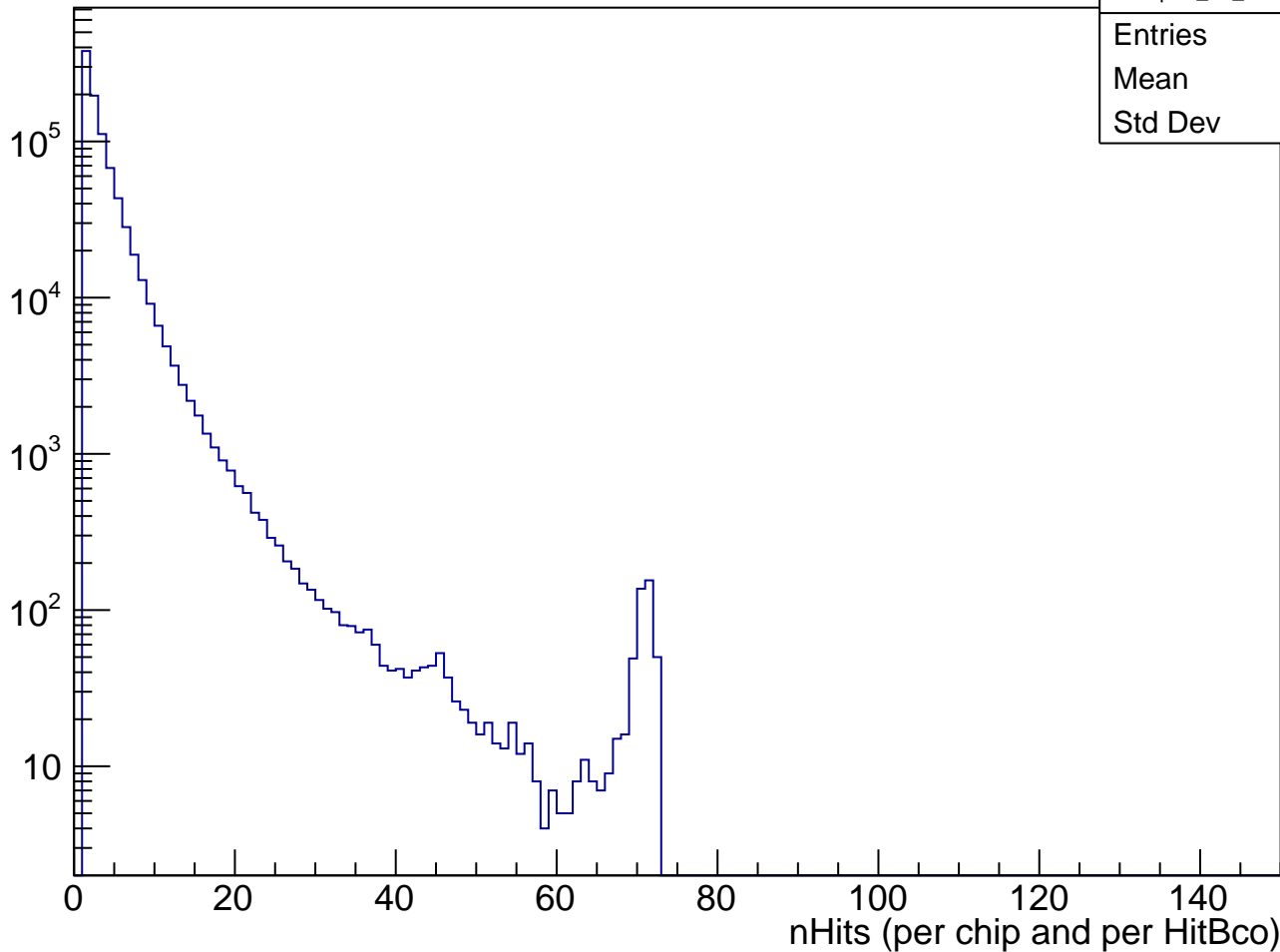
Entries 885033

Mean 2.876

Std Dev 3.47

nChipHit_F6_Fch13_Chip19

Entries



nChipHit_F6_Fch13_Chip19

Entries 898462

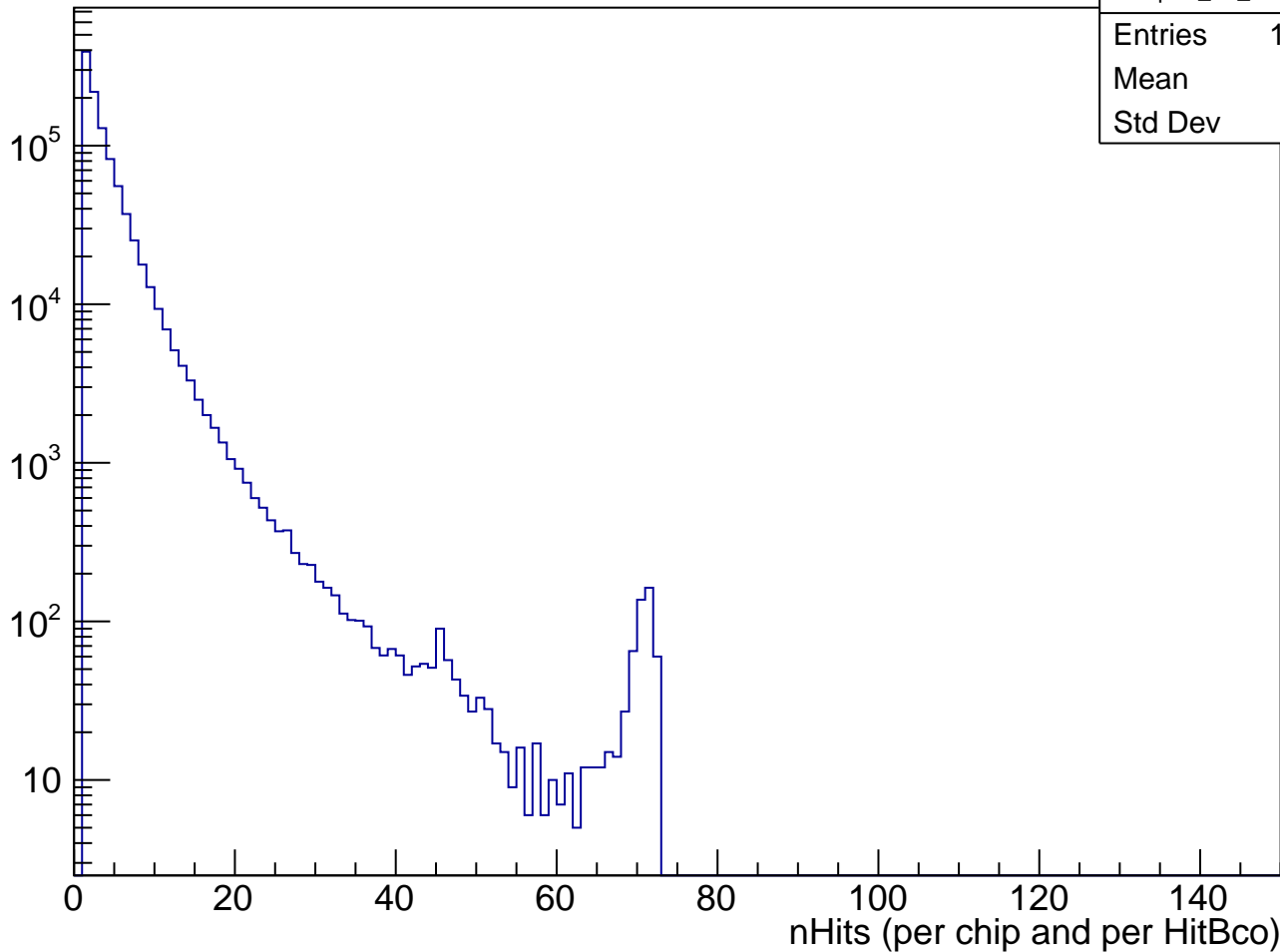
Mean 2.855

Std Dev 3.468

nHits (per chip and per HitBco)

nChipHit_F6_Fch13_Chip2

Entries

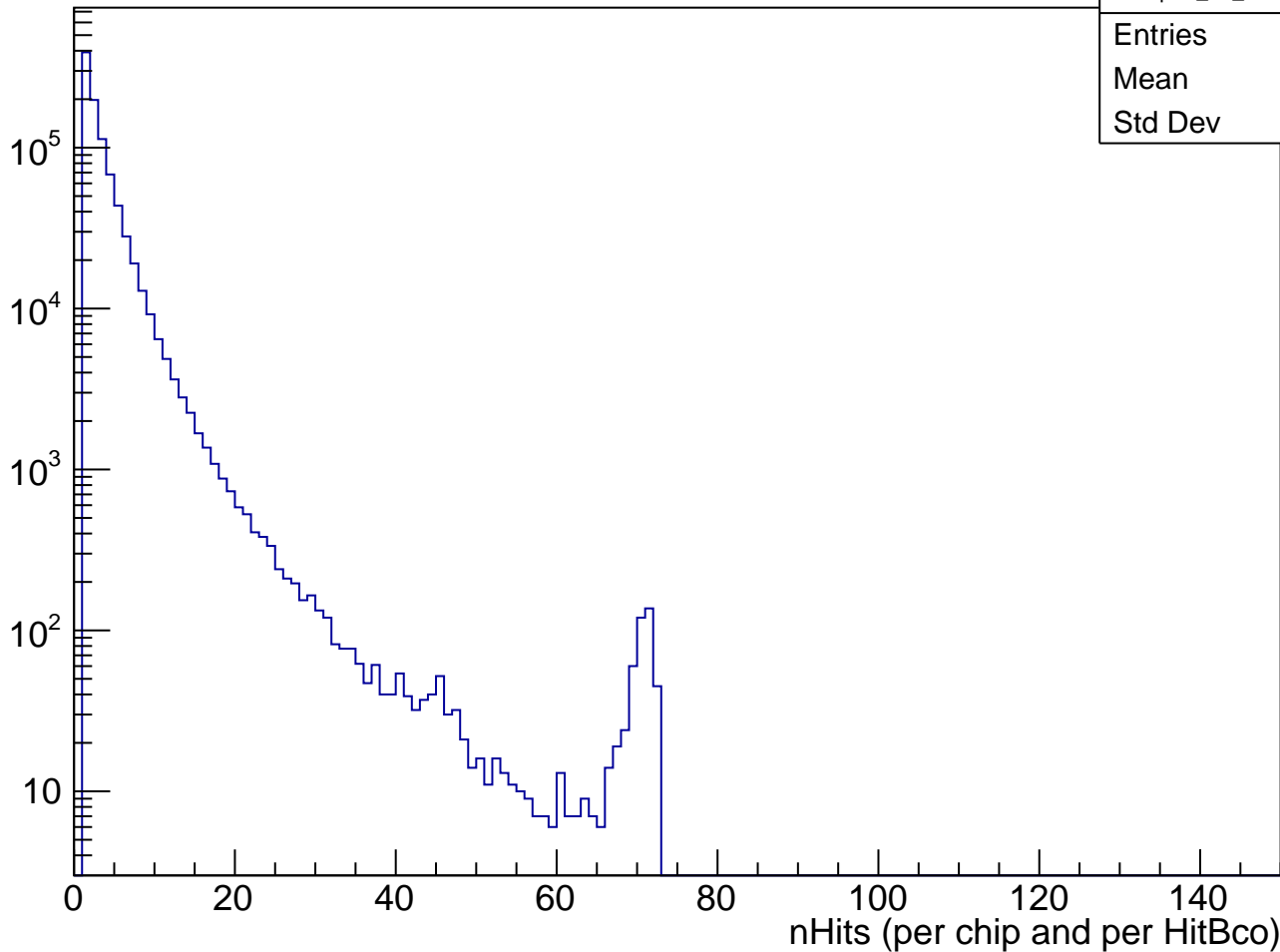


nChipHit_F6_Fch13_Chip2	
Entries	1013793
Mean	3.122
Std Dev	3.725

nHits (per chip and per HitBco)

nChipHit_F6_Fch13_Chip20

Entries

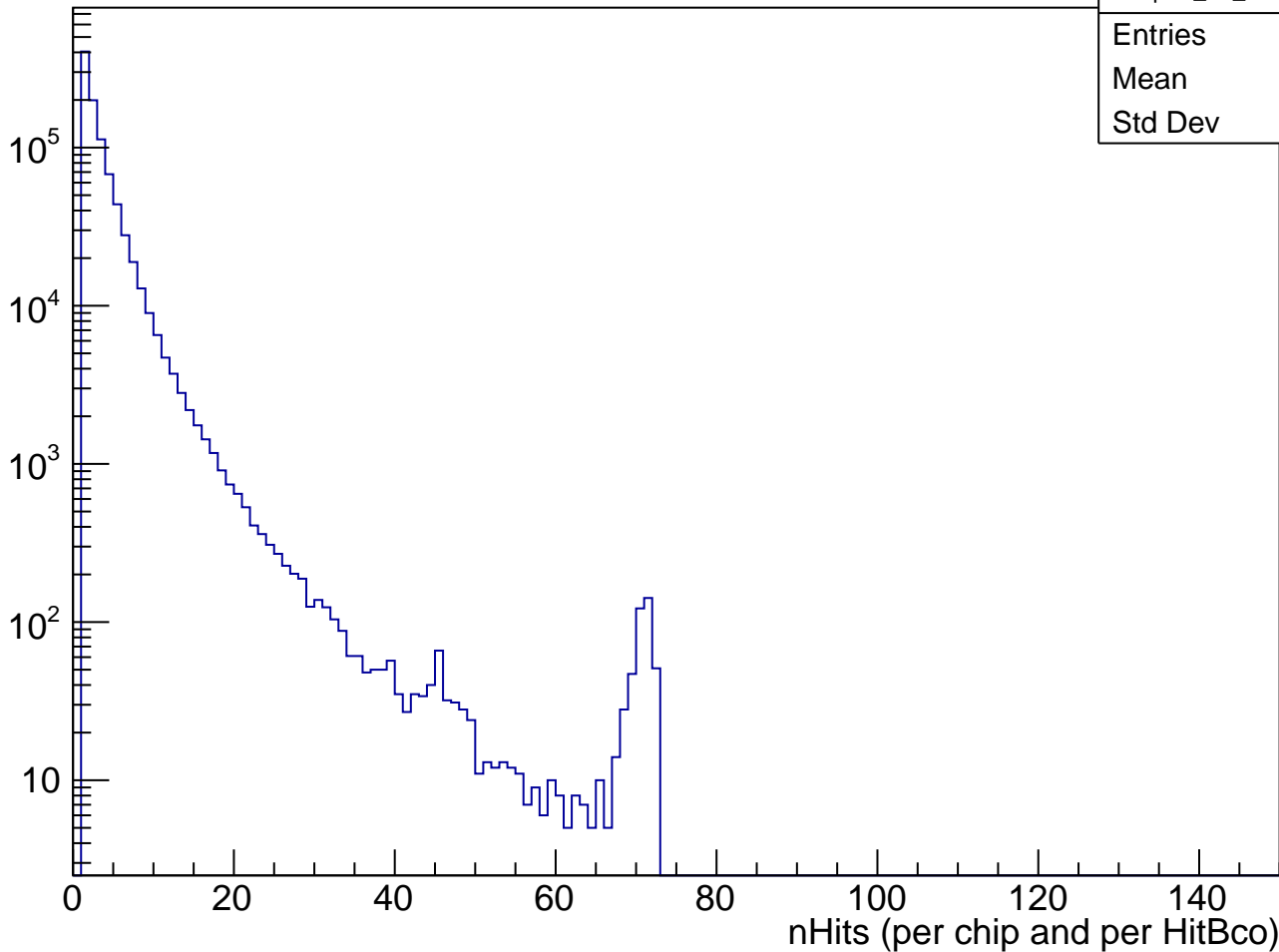


nChipHit_F6_Fch13_Chip20	
Entries	913627
Mean	2.826
Std Dev	3.417

nHits (per chip and per HitBco)

nChipHit_F6_Fch13_Chip21

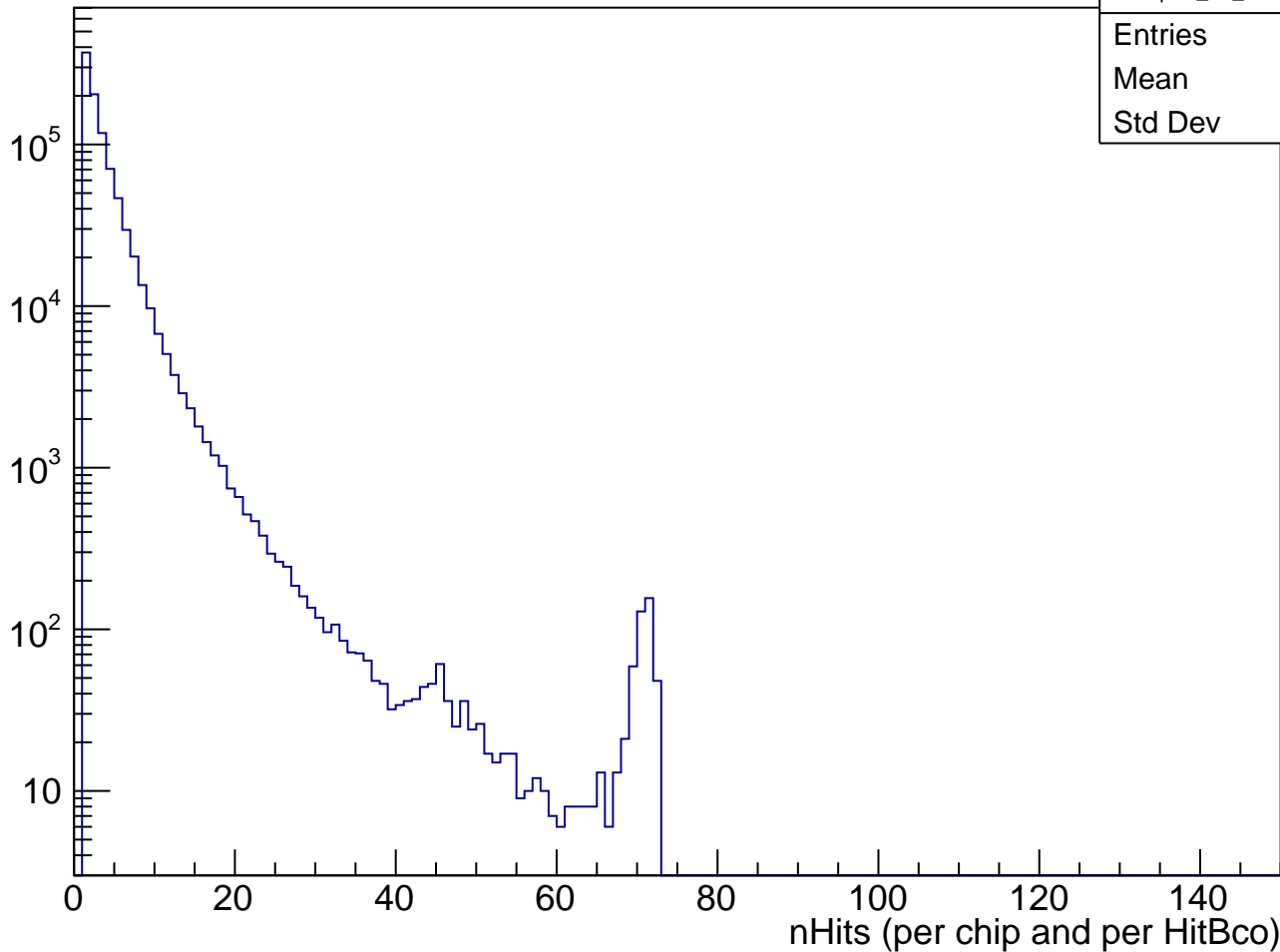
Entries



nChipHit_F6_Fch13_Chip21	
Entries	927839
Mean	2.8
Std Dev	3.407

nChipHit_F6_Fch13_Chip22

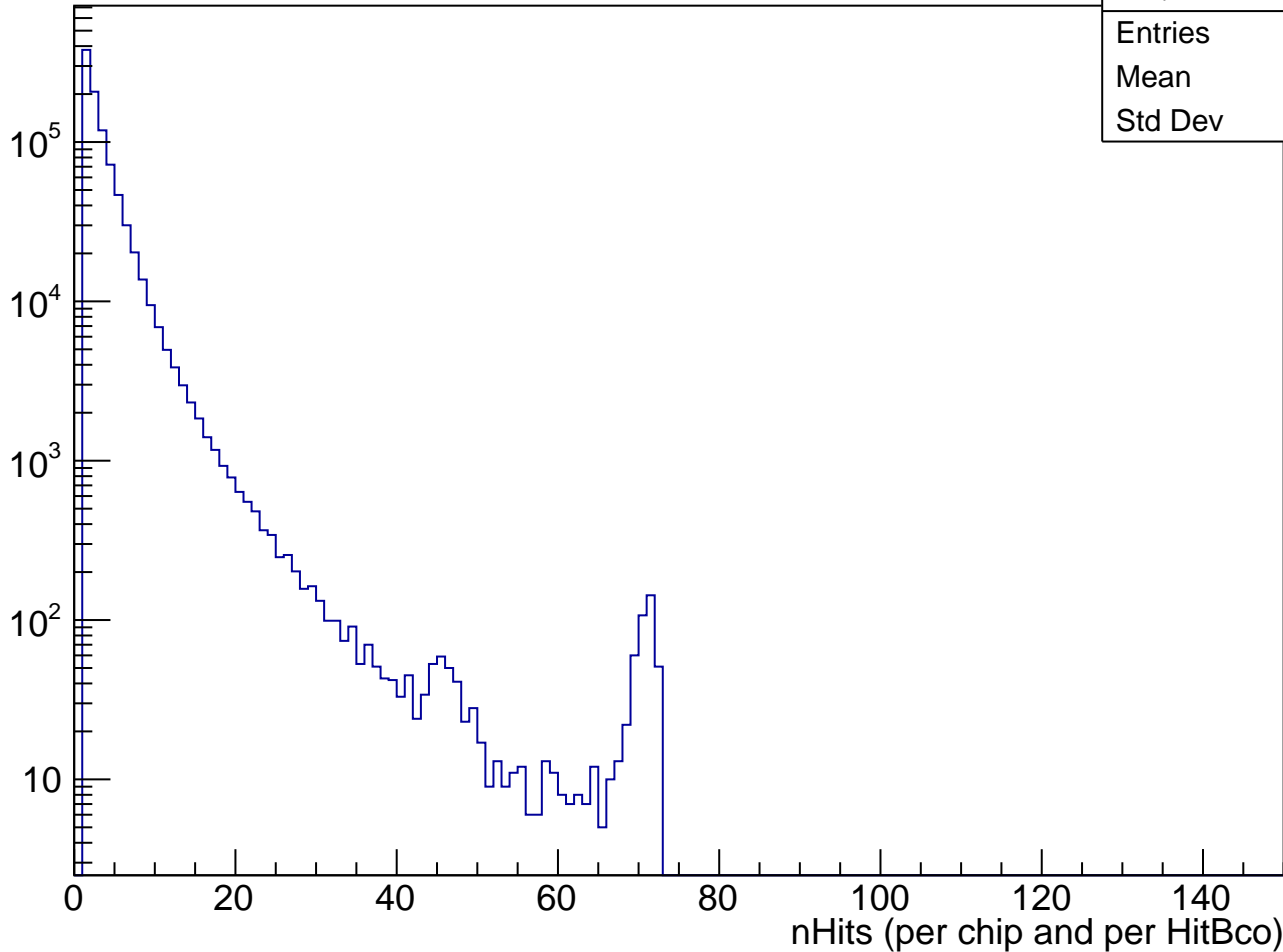
Entries



nChipHit_F6_Fch13_Chip22	
Entries	915107
Mean	2.91
Std Dev	3.482

nChipHit_F6_Fch13_Chip23

Entries

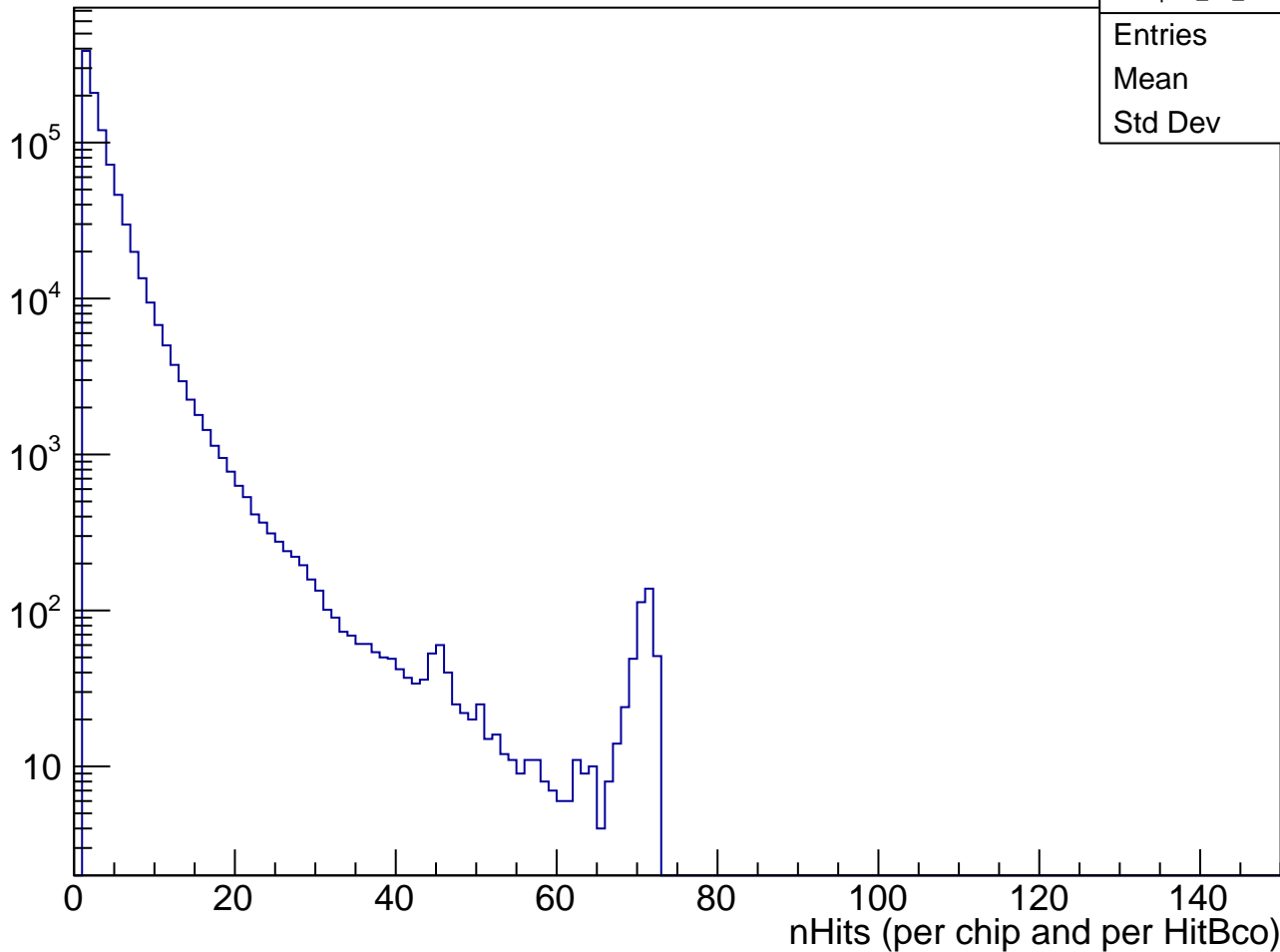


nChipHit_F6_Fch13_Chip23	
Entries	928528
Mean	2.894
Std Dev	3.443

nHits (per chip and per HitBco)

nChipHit_F6_Fch13_Chip24

Entries



nChipHit_F6_Fch13_Chip24

Entries 937880

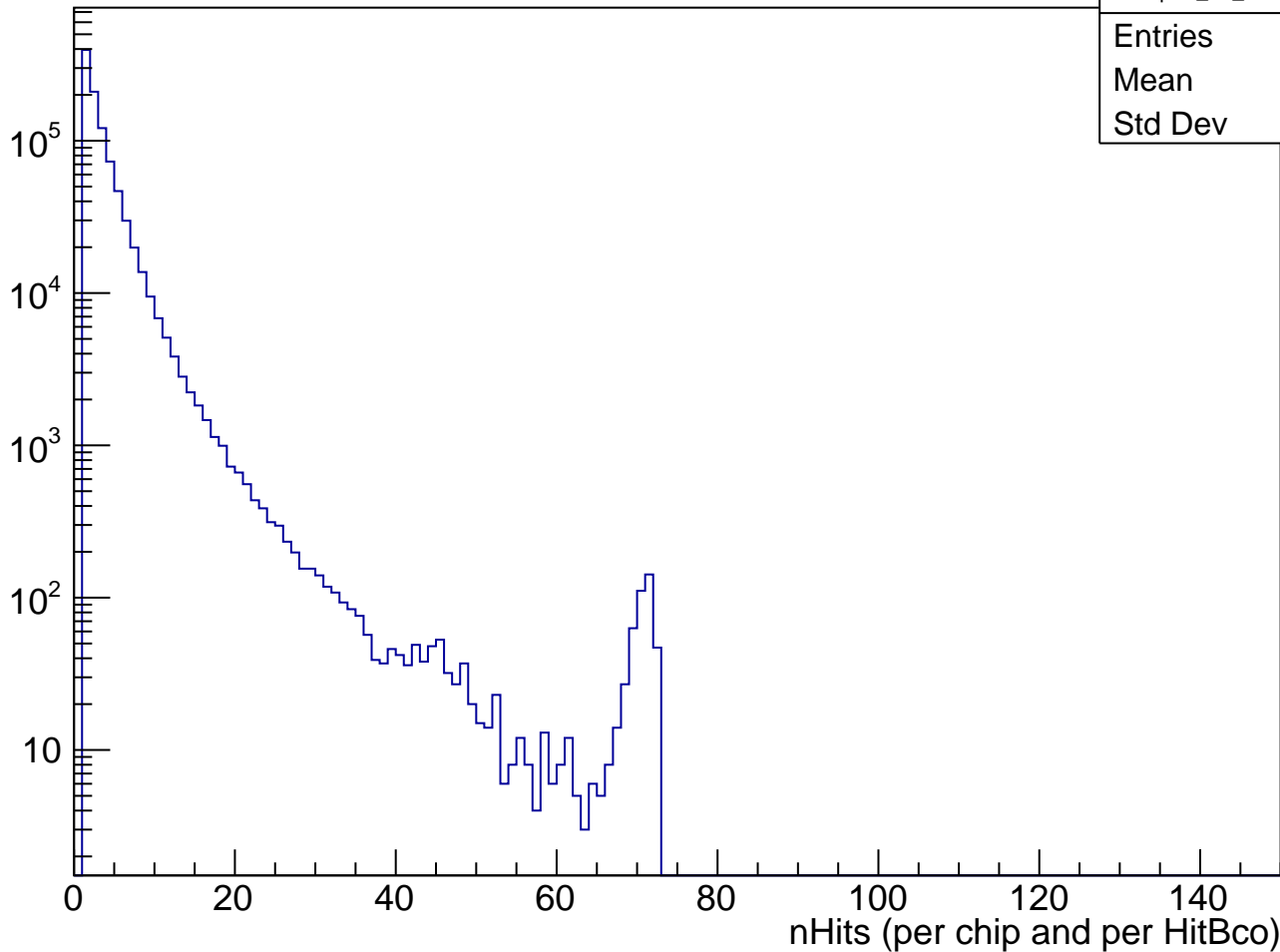
Mean 2.867

Std Dev 3.413

nHits (per chip and per HitBco)

nChipHit_F6_Fch13_Chip25

Entries

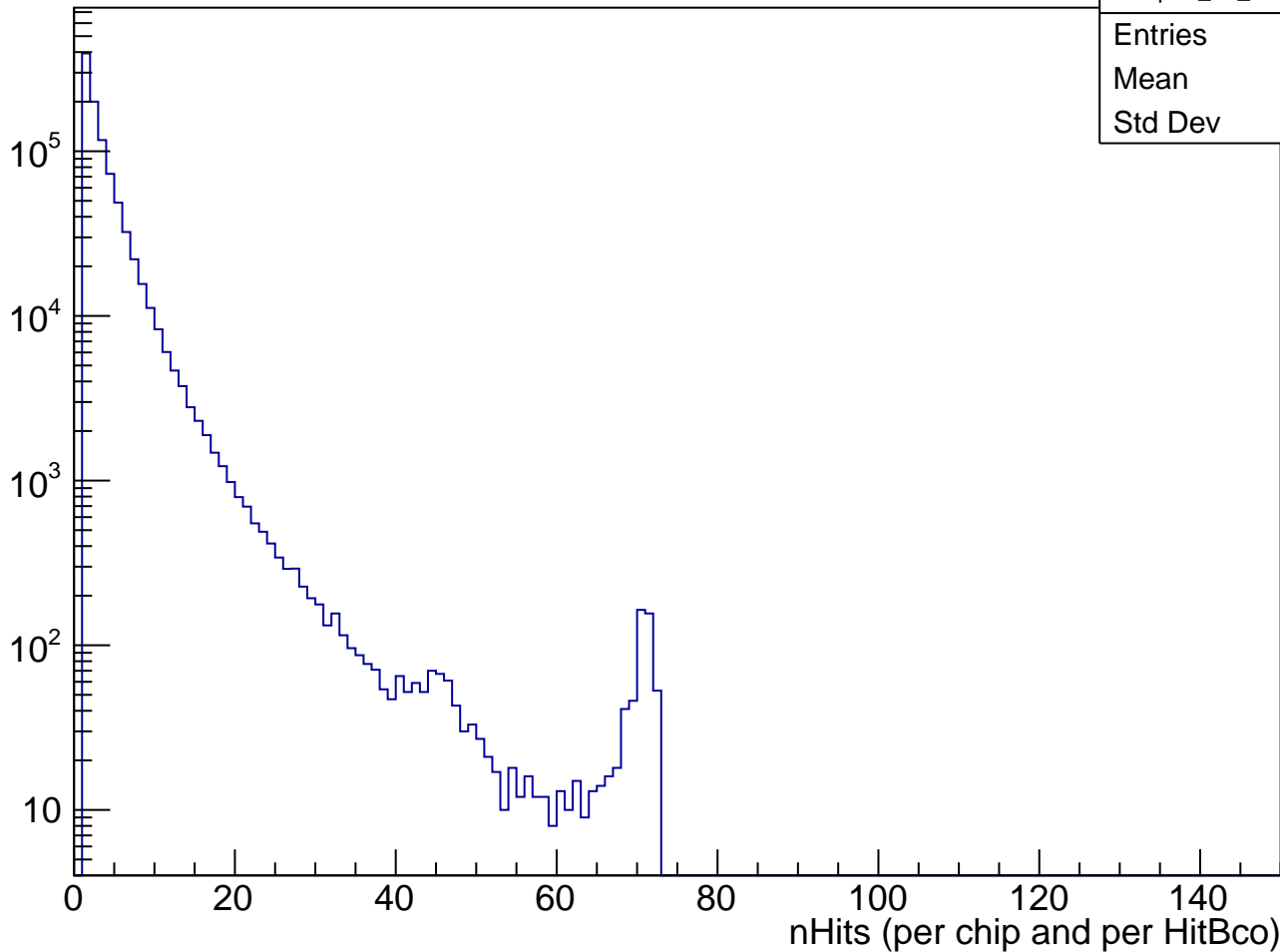


nChipHit_F6_Fch13_Chip25	
Entries	950251
Mean	2.858
Std Dev	3.407

nHits (per chip and per HitBco)

nChipHit_F6_Fch13_Chip3

Entries



nChipHit_F6_Fch13_Chip3

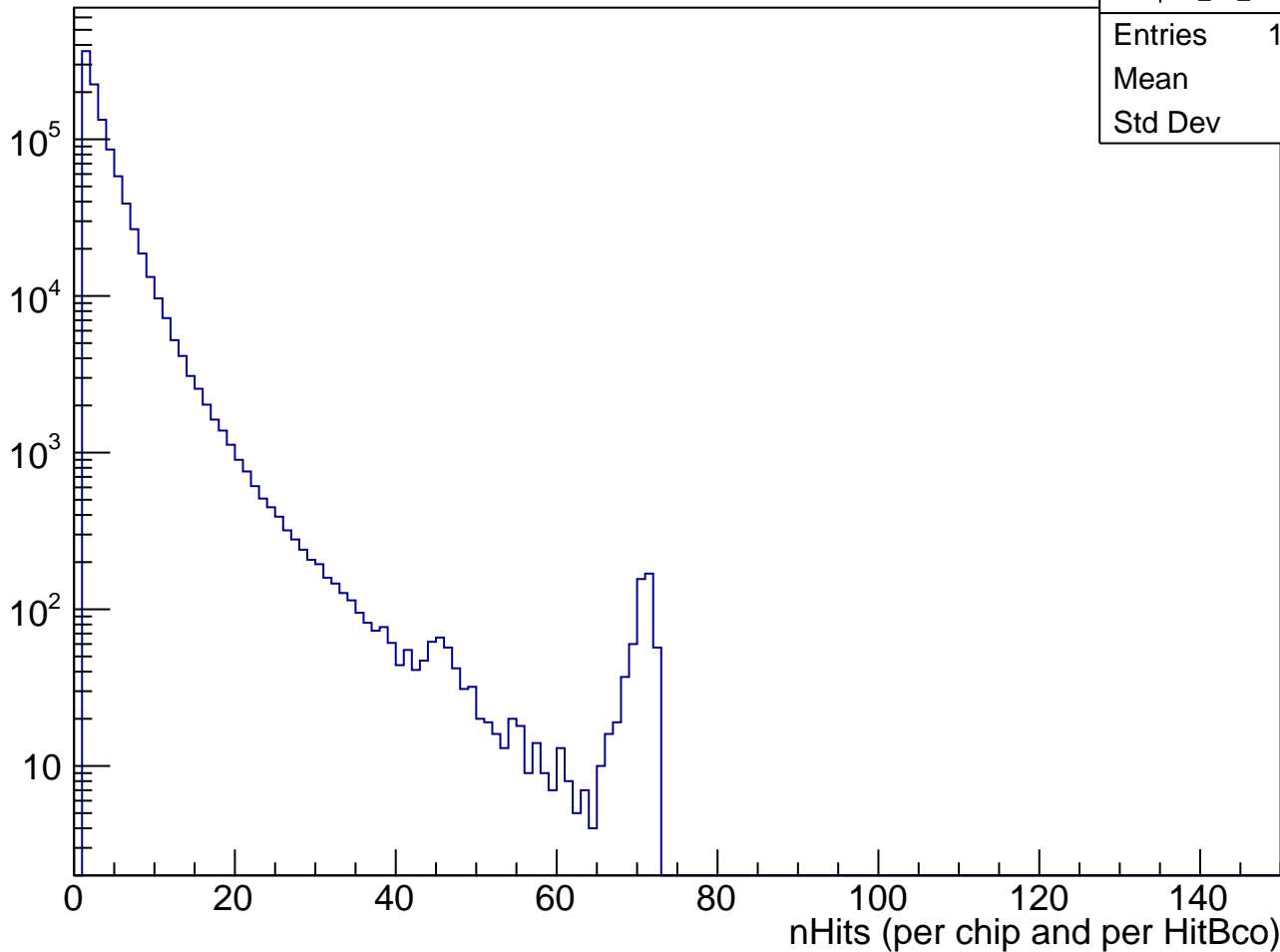
Entries 952870

Mean 3.024

Std Dev 3.741

nChipHit_F6_Fch13_Chip4

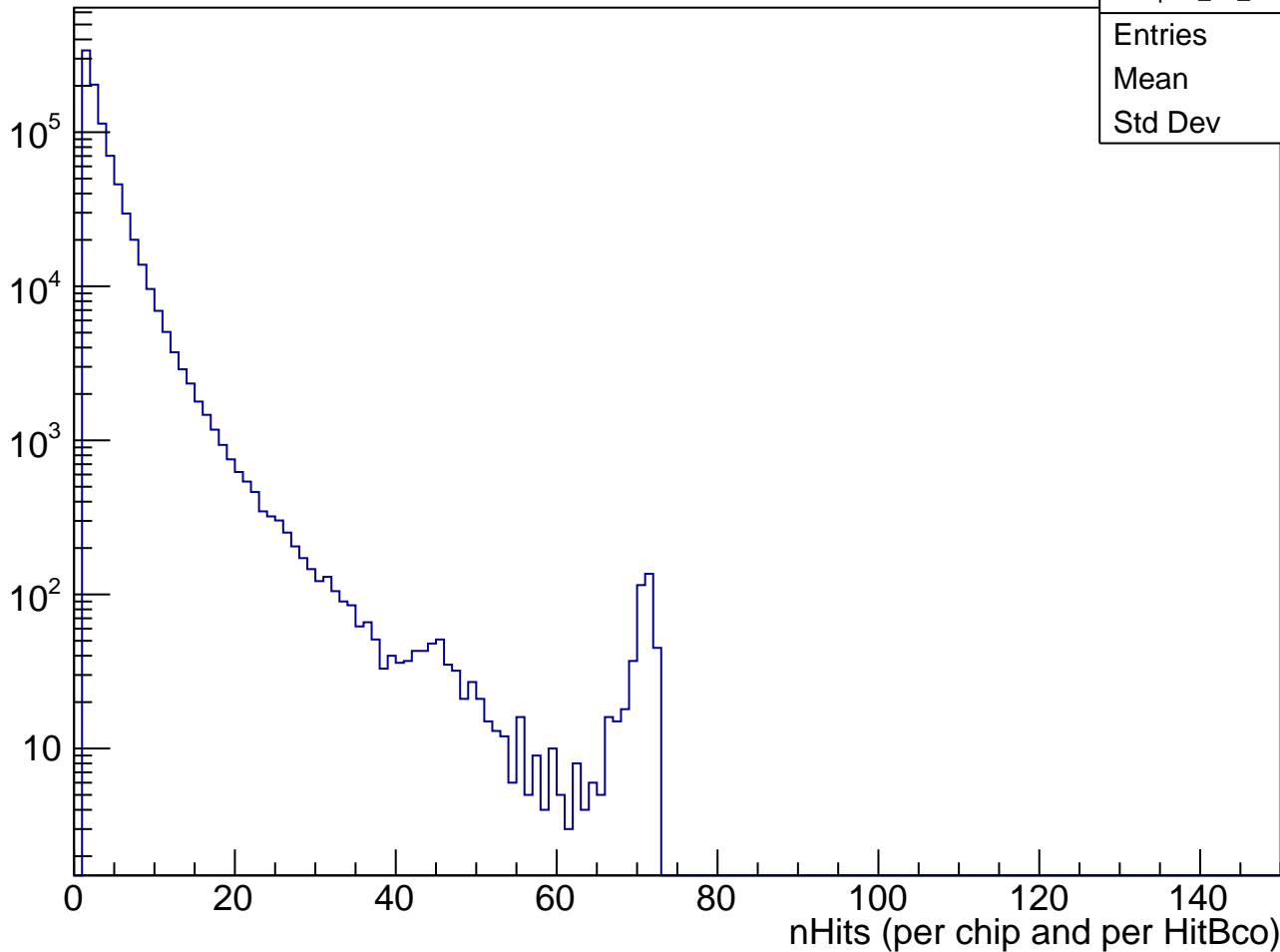
Entries



nChipHit_F6_Fch13_Chip4	
Entries	1009519
Mean	3.2
Std Dev	3.739

nChipHit_F6_Fch13_Chip5

Entries

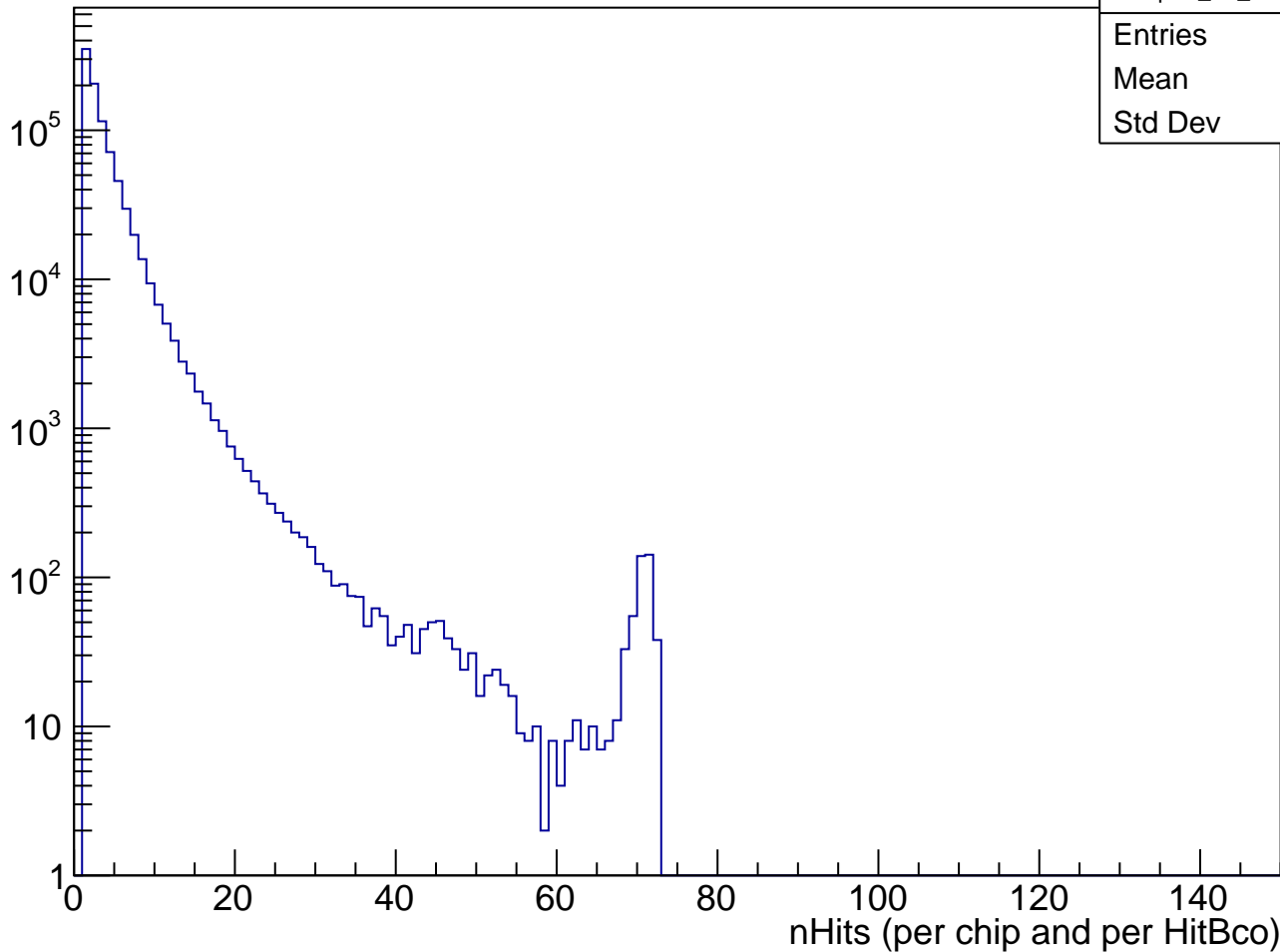


nChipHit_F6_Fch13_Chip5	
Entries	878088
Mean	2.973
Std Dev	3.483

nHits (per chip and per HitBco)

nChipHit_F6_Fch13_Chip6

Entries

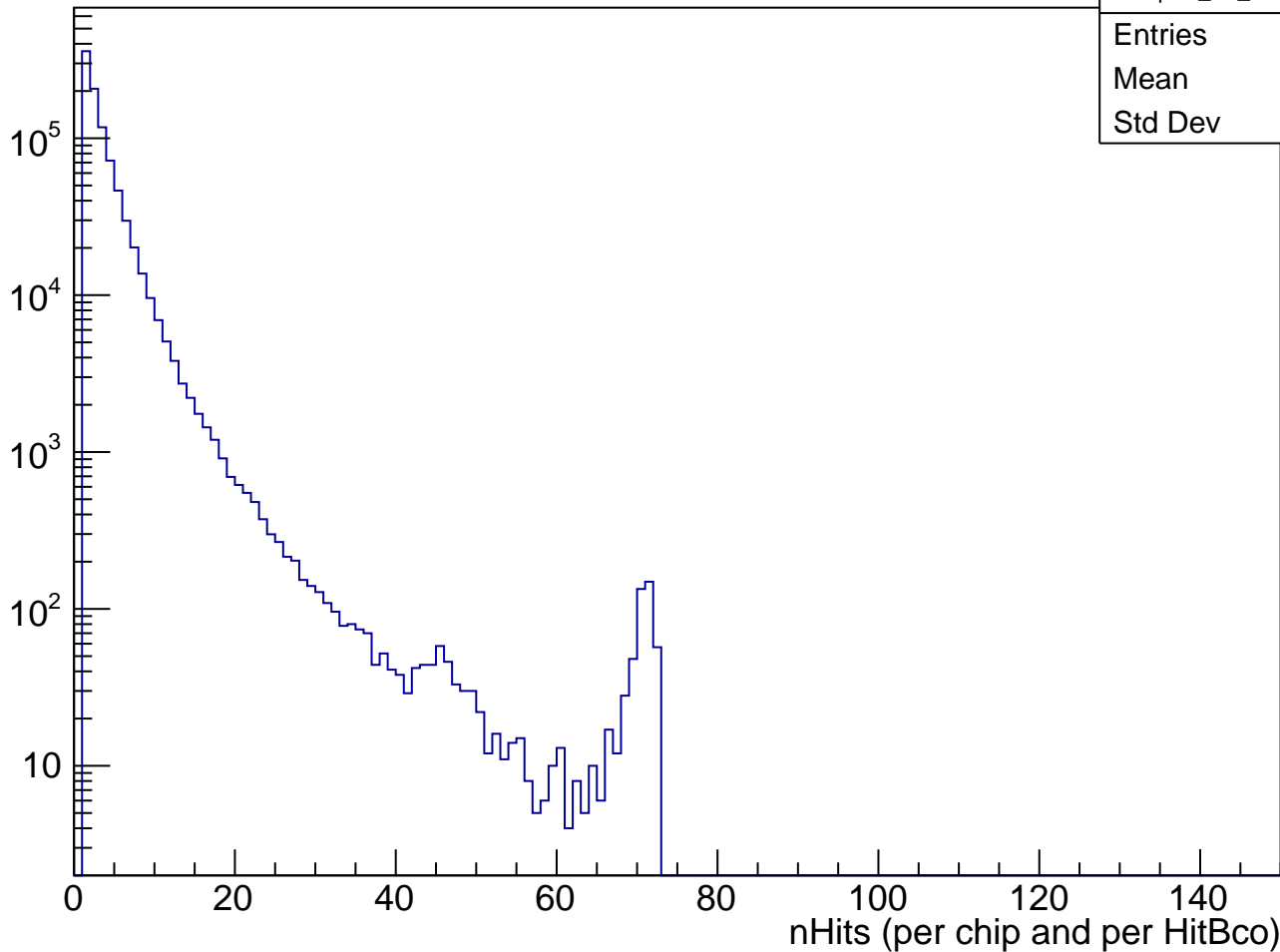


nChipHit_F6_Fch13_Chip6	
Entries	892975
Mean	2.946
Std Dev	3.507

nHits (per chip and per HitBco)

nChipHit_F6_Fch13_Chip7

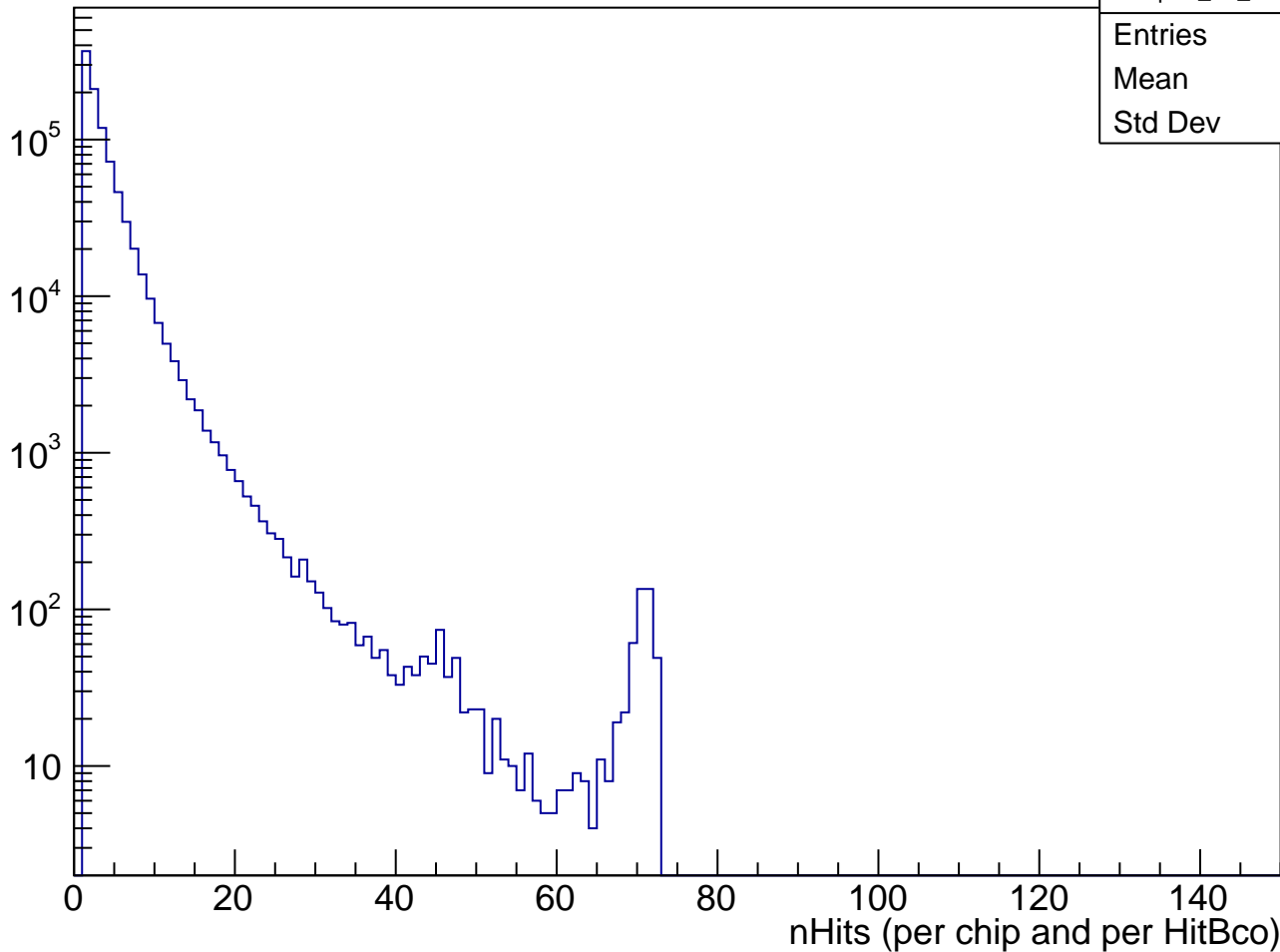
Entries



nChipHit_F6_Fch13_Chip7	
Entries	906693
Mean	2.931
Std Dev	3.489

nChipHit_F6_Fch13_Chip8

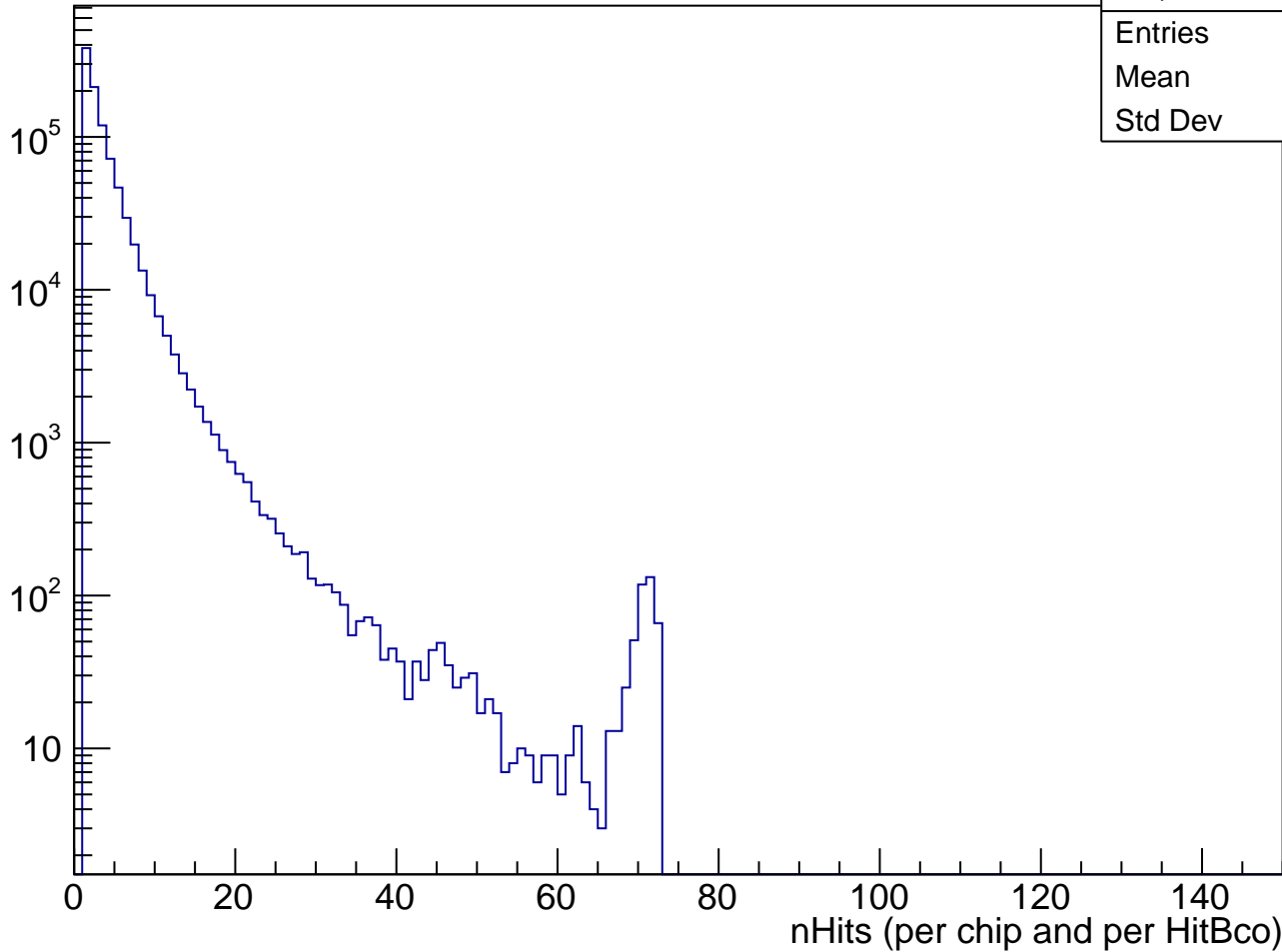
Entries



nChipHit_F6_Fch13_Chip8	
Entries	919809
Mean	2.912
Std Dev	3.464

nChipHit_F6_Fch13_Chip9

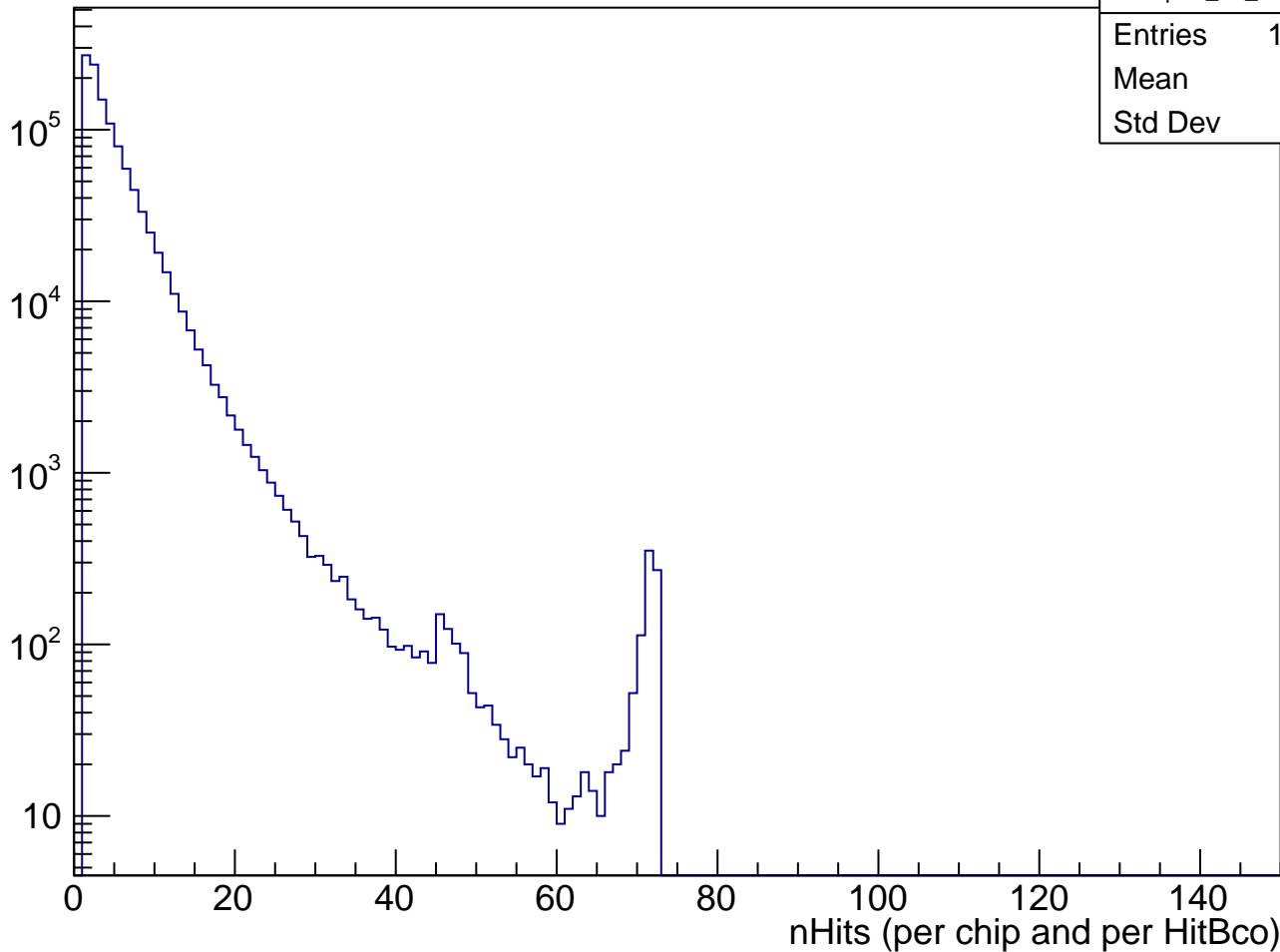
Entries



nChipHit_F6_Fch13_Chip9	
Entries	934357
Mean	2.864
Std Dev	3.398

nChipHit_F6_Fch1_Chip0

Entries



nChipHit_F6_Fch1_Chip0

Entries 1102858

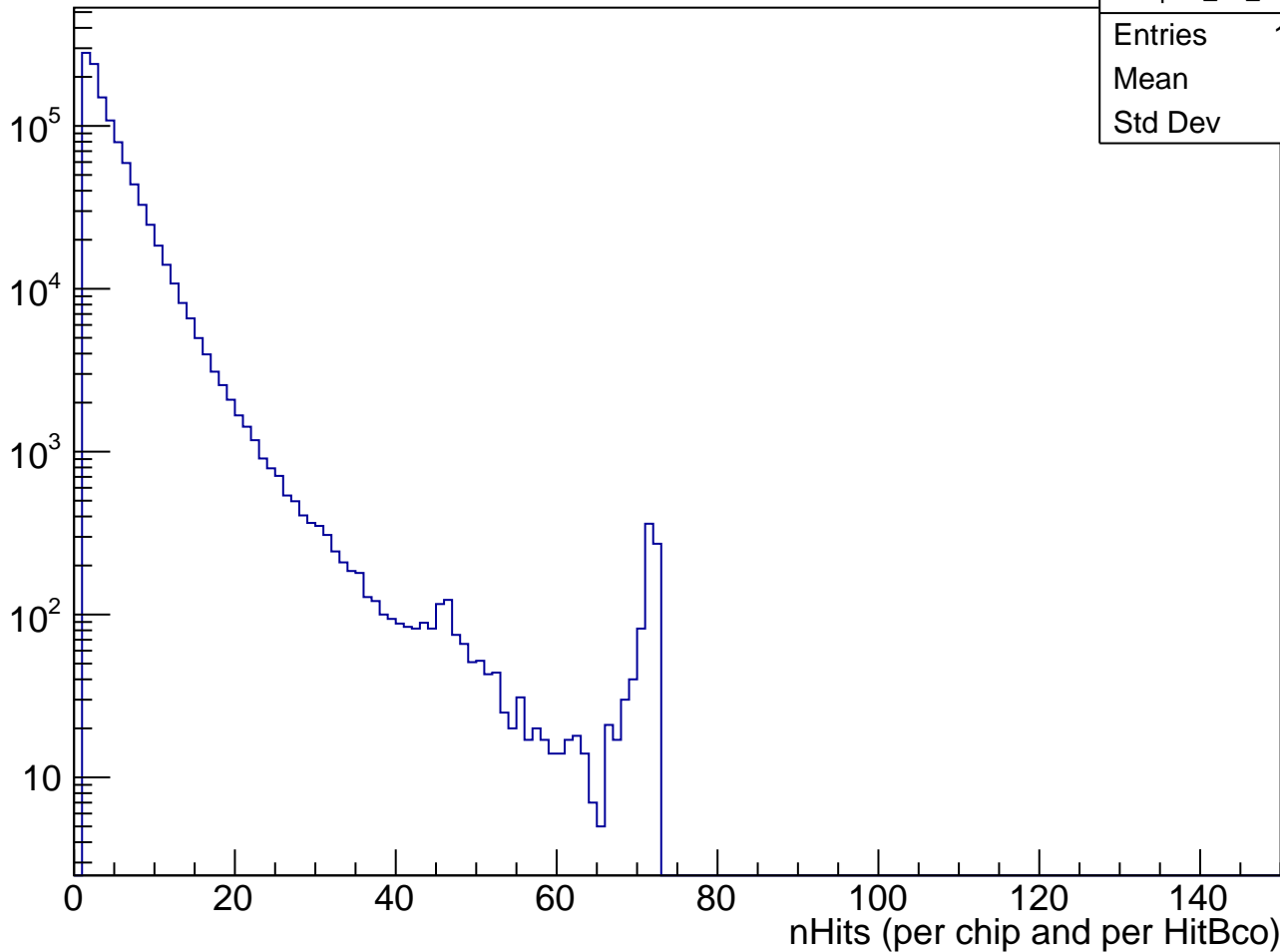
Mean 4.149

Std Dev 4.572

nHits (per chip and per HitBco)

nChipHit_F6_Fch1_Chip1

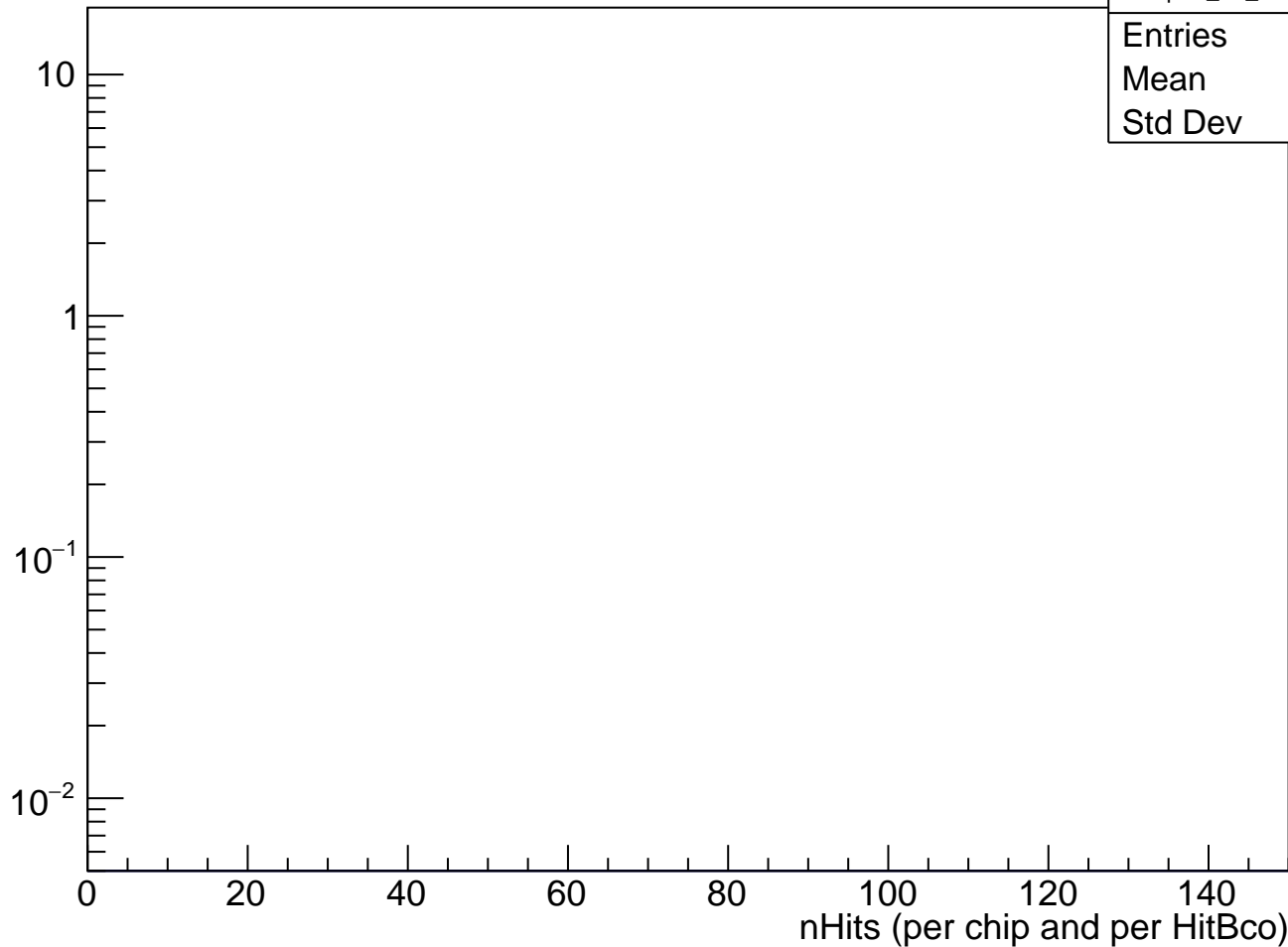
Entries



nChipHit_F6_Fch1_Chip1	
Entries	1105861
Mean	4.073
Std Dev	4.496

nChipHit_F6_Fch1_Chip10

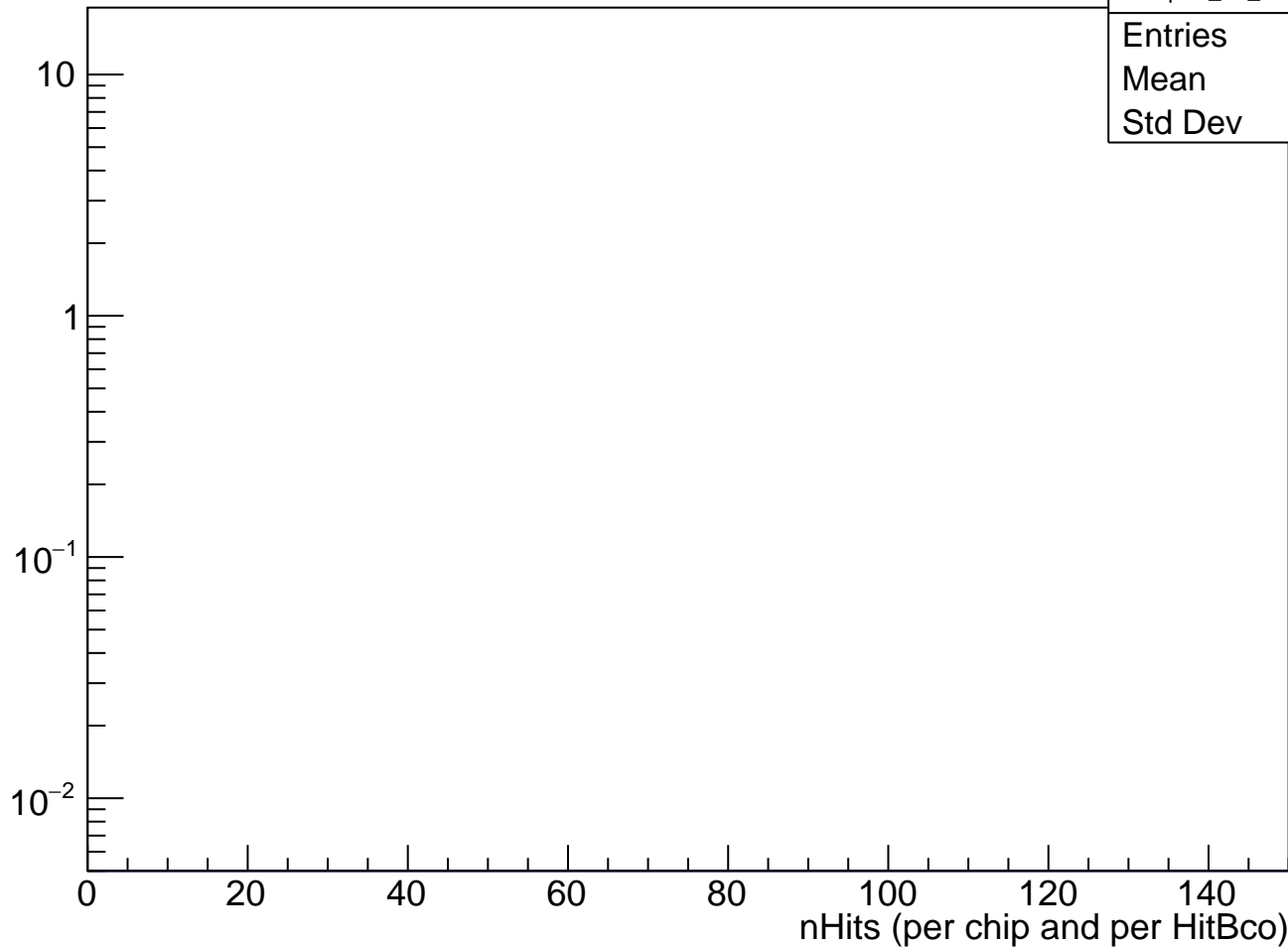
Entries



nChipHit_F6_Fch1_Chip10	
Entries	0
Mean	0
Std Dev	0

nChipHit_F6_Fch1_Chip11

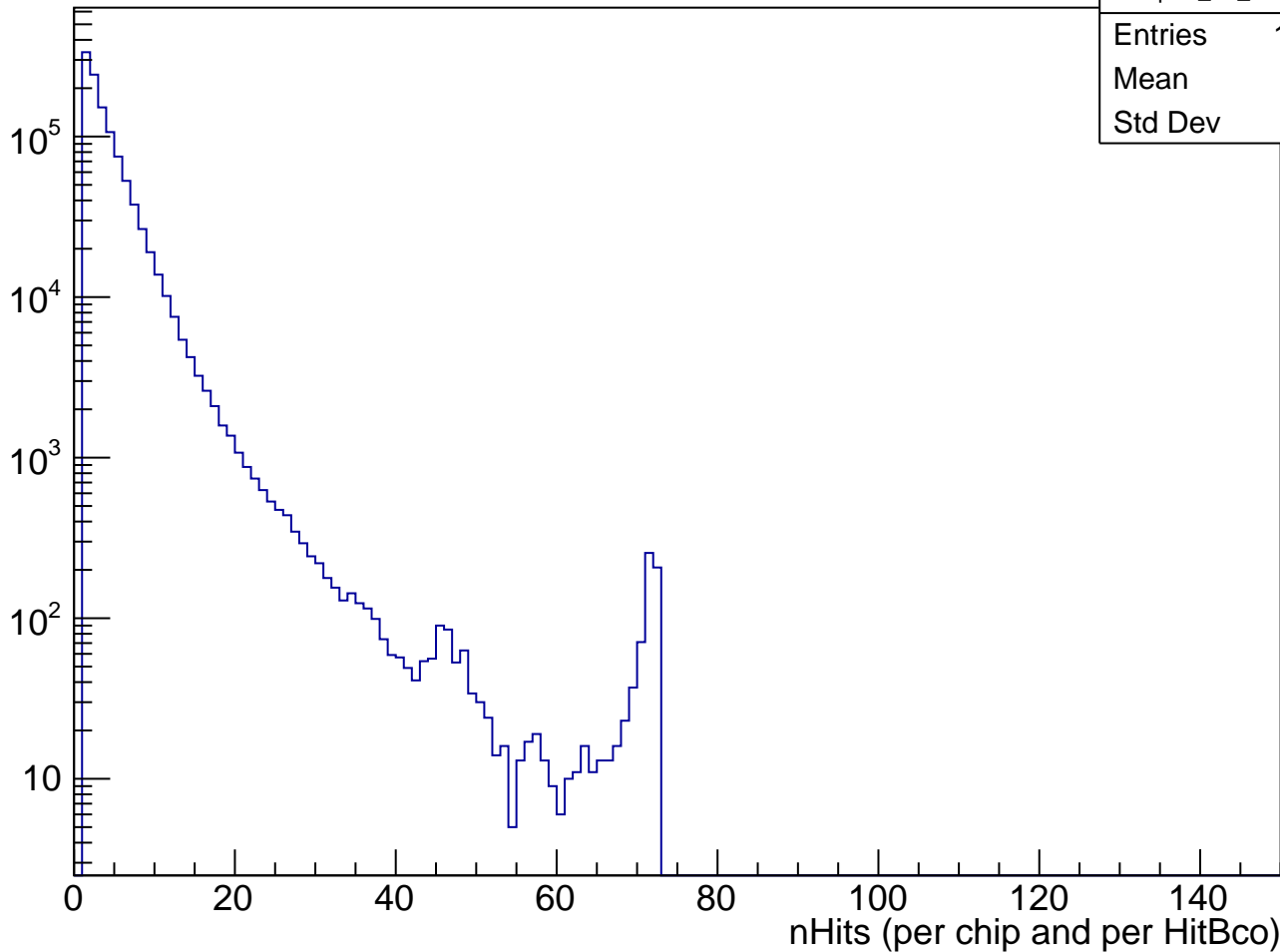
Entries



nChipHit_F6_Fch1_Chip11	
Entries	0
Mean	0
Std Dev	0

nChipHit_F6_Fch1_Chip12

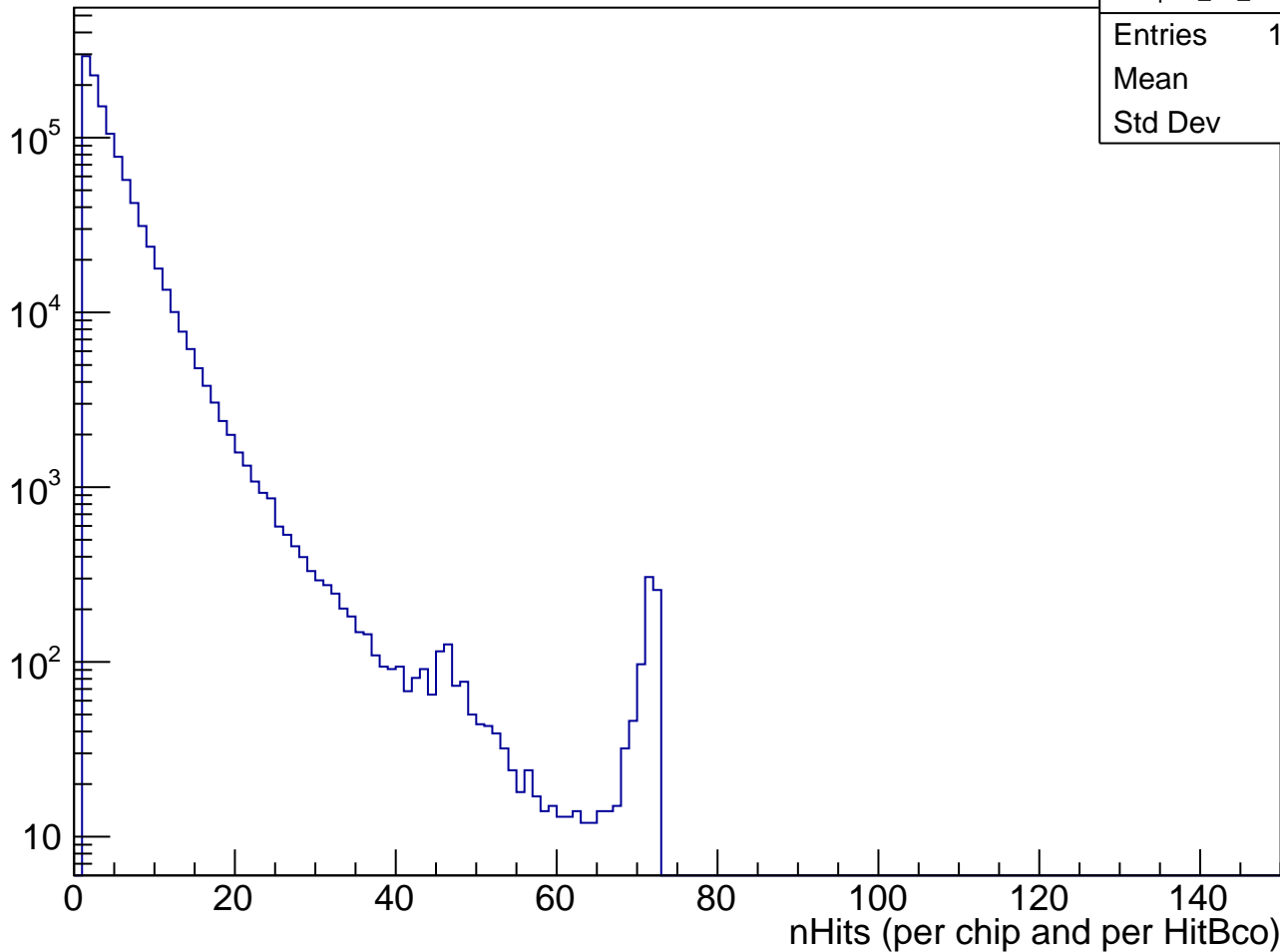
Entries



nChipHit_F6_Fch1_Chip12	
Entries	1108131
Mean	3.566
Std Dev	3.937

nChipHit_F6_Fch1_Chip13

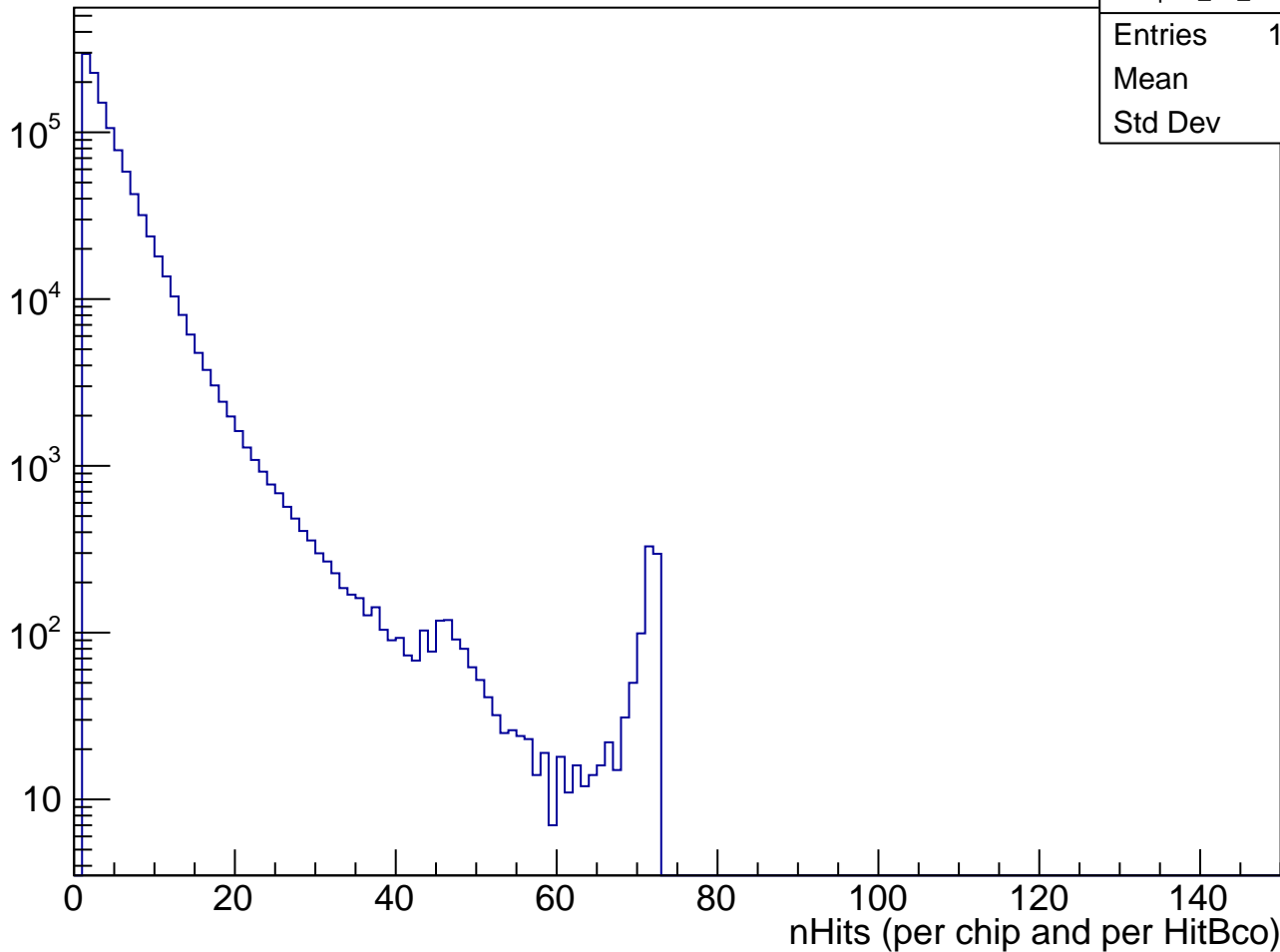
Entries



nChipHit_F6_Fch1_Chip13	
Entries	1091609
Mean	4.004
Std Dev	4.441

nChipHit_F6_Fch1_Chip14

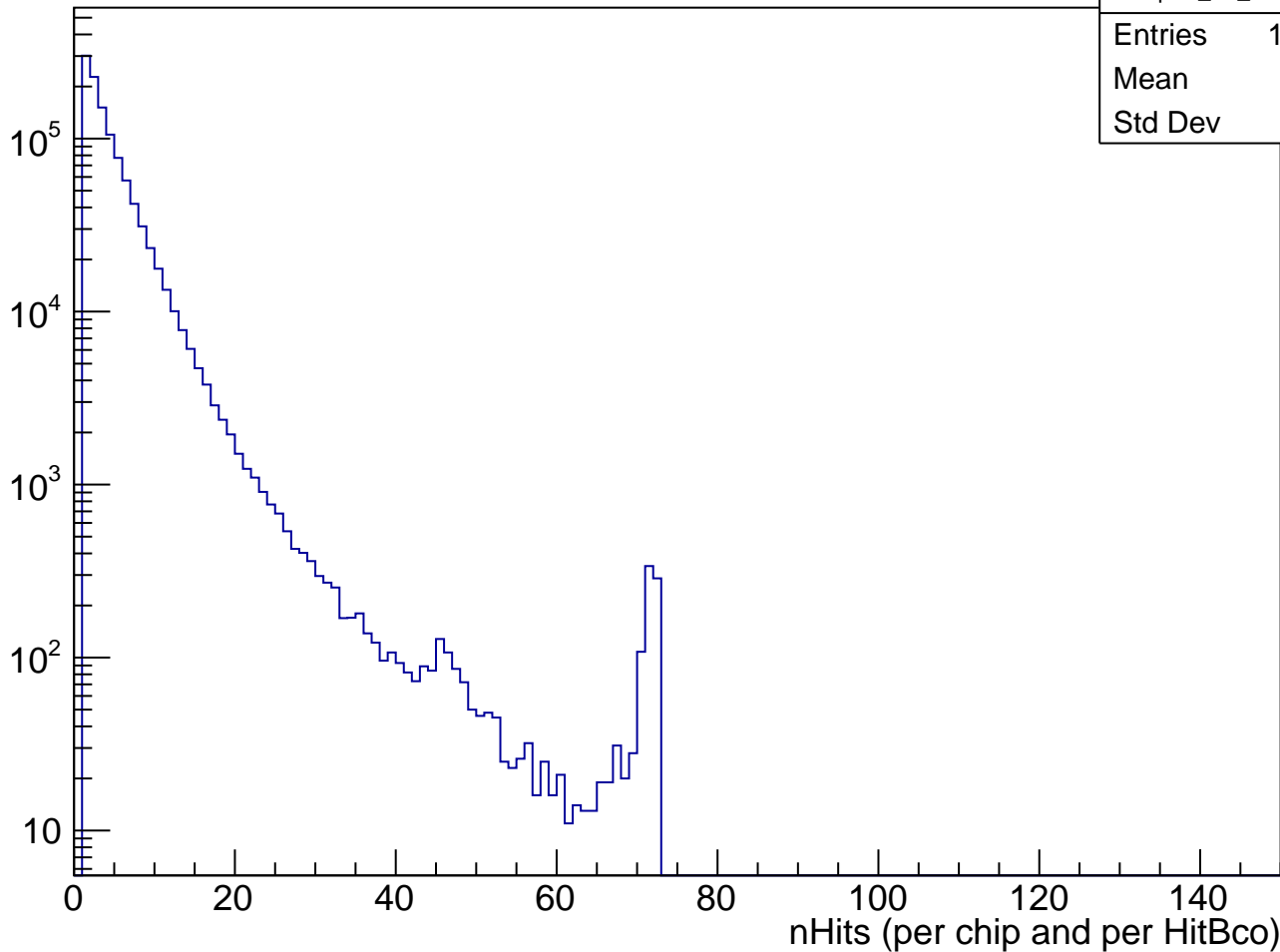
Entries



nChipHit_F6_Fch1_Chip14	
Entries	1097492
Mean	4.017
Std Dev	4.486

nChipHit_F6_Fch1_Chip15

Entries

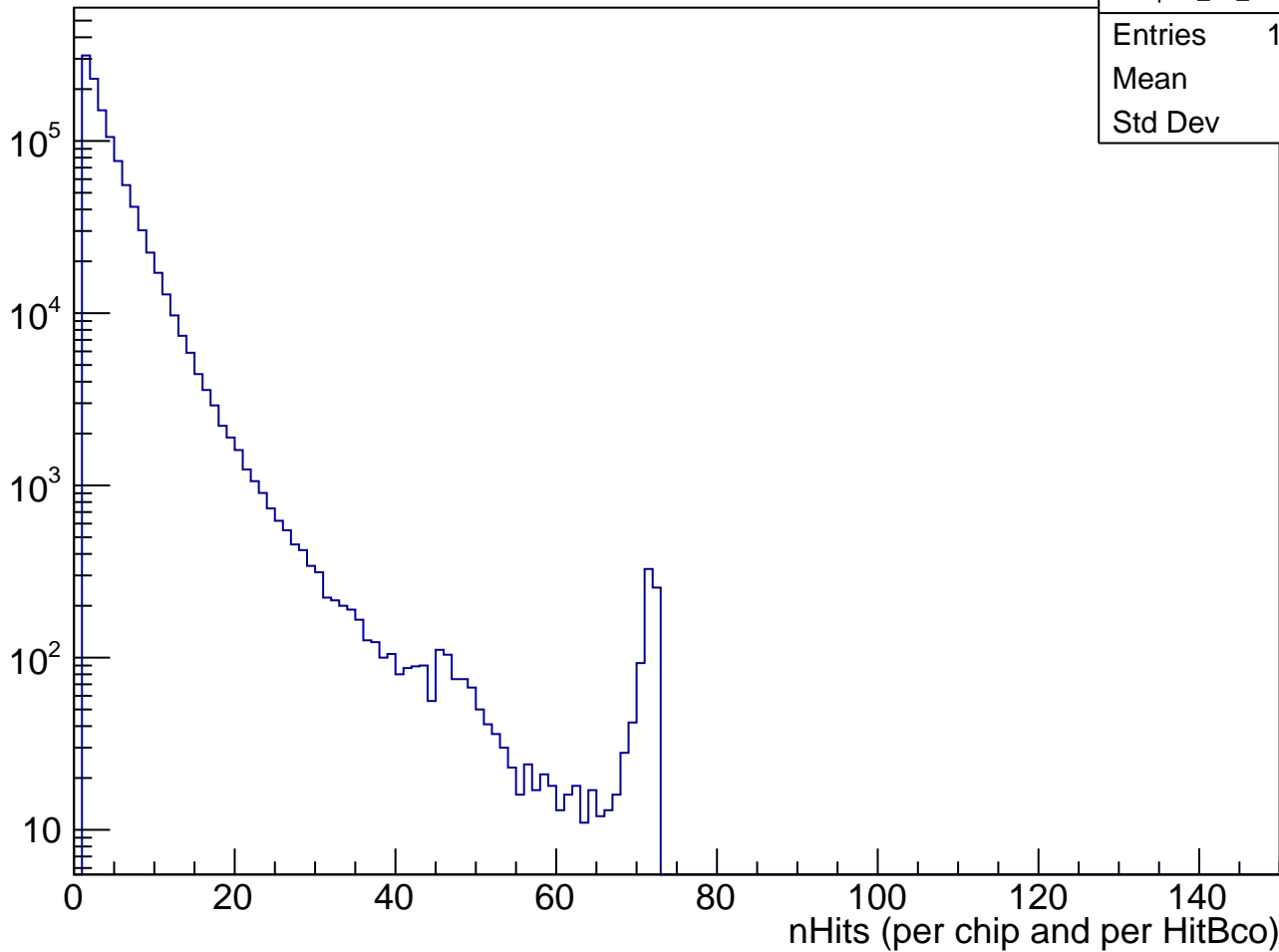


nChipHit_F6_Fch1_Chip15	
Entries	1098916
Mean	3.975
Std Dev	4.469

nHits (per chip and per HitBco)

nChipHit_F6_Fch1_Chip16

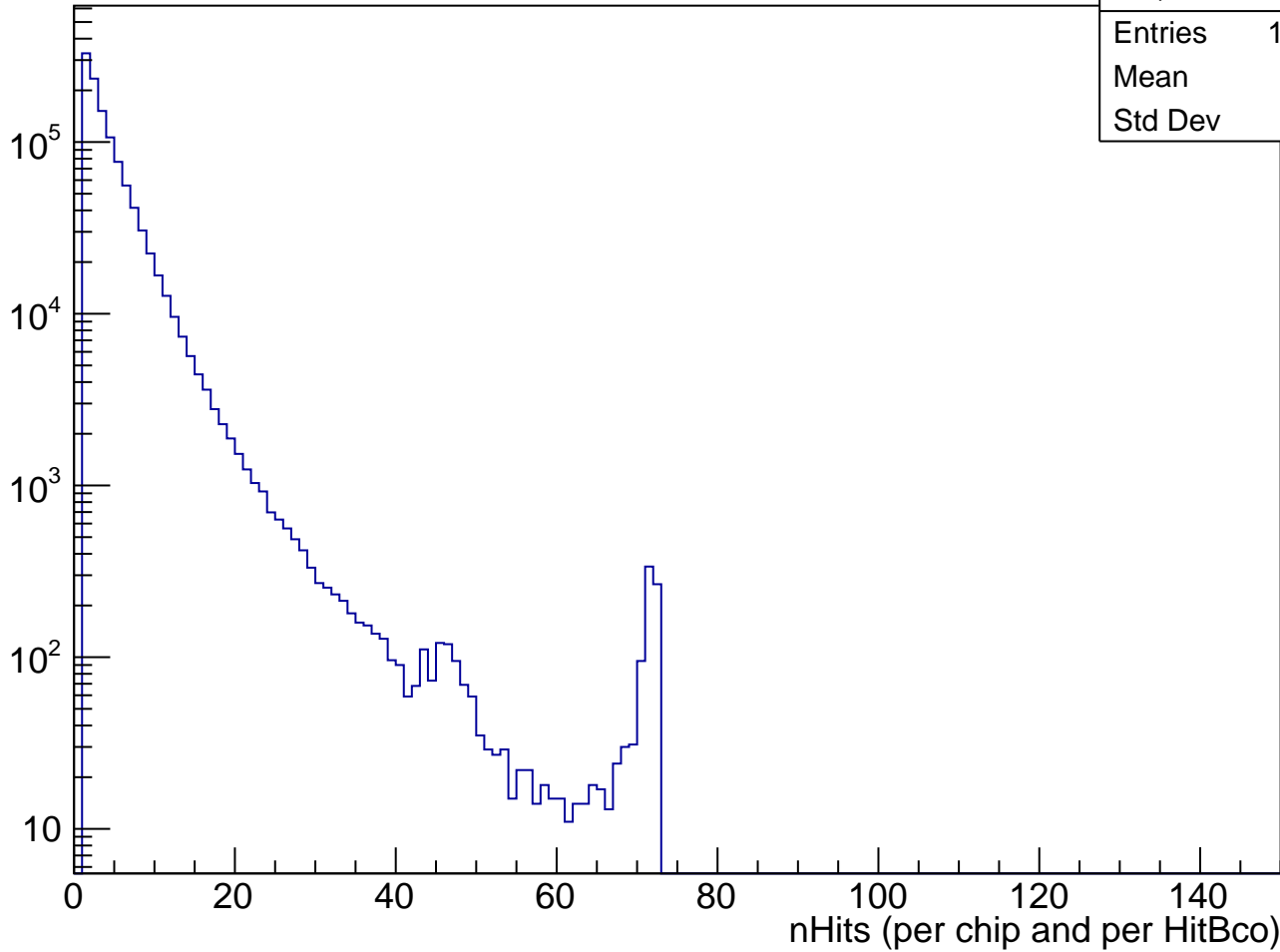
Entries



nChipHit_F6_Fch1_Chip16	
Entries	1105422
Mean	3.897
Std Dev	4.398

nChipHit_F6_Fch1_Chip17

Entries

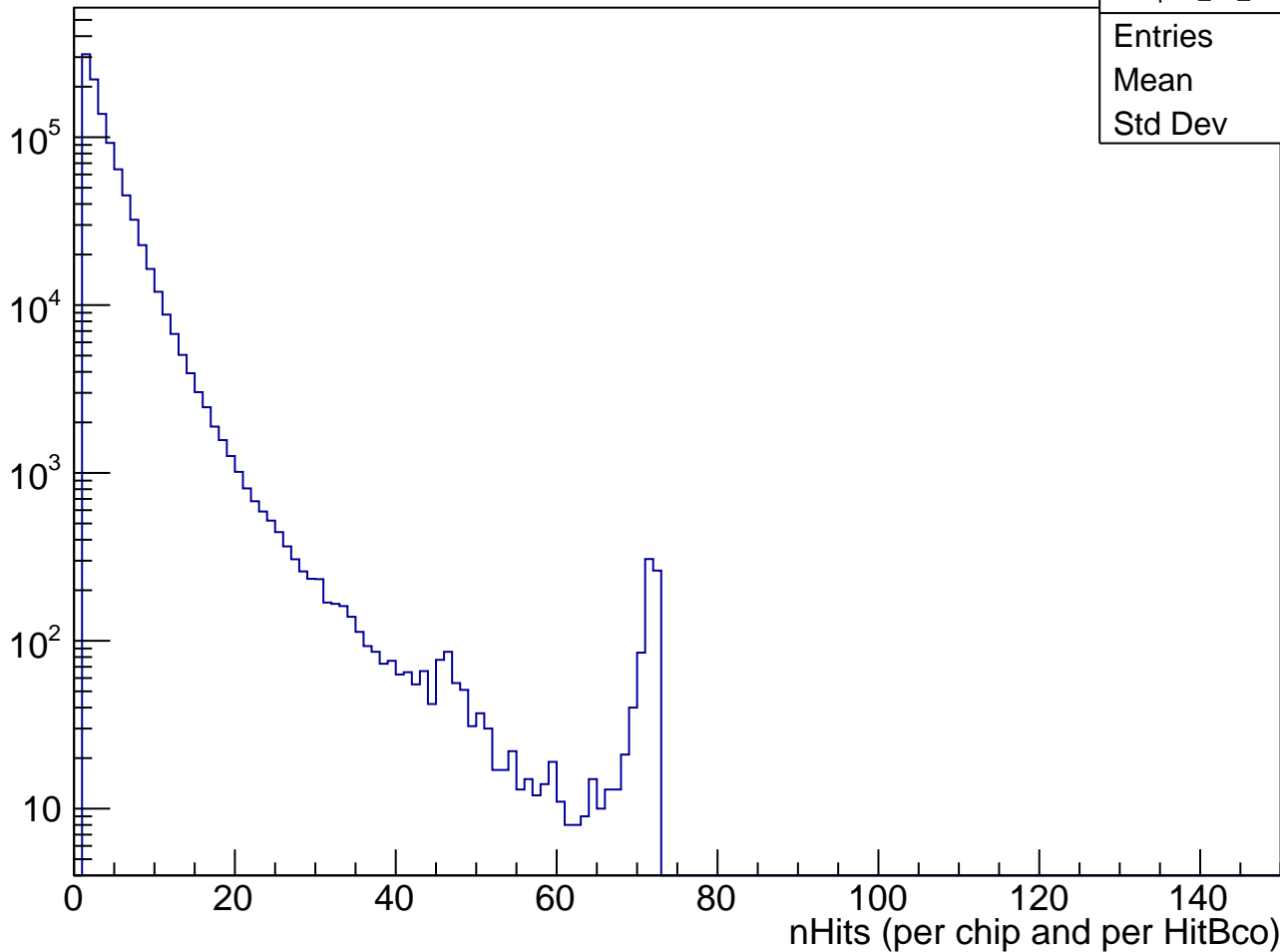


nChipHit_F6_Fch1_Chip17	
Entries	1126405
Mean	3.847
Std Dev	4.374

nHits (per chip and per HitBco)

nChipHit_F6_Fch1_Chip18

Entries

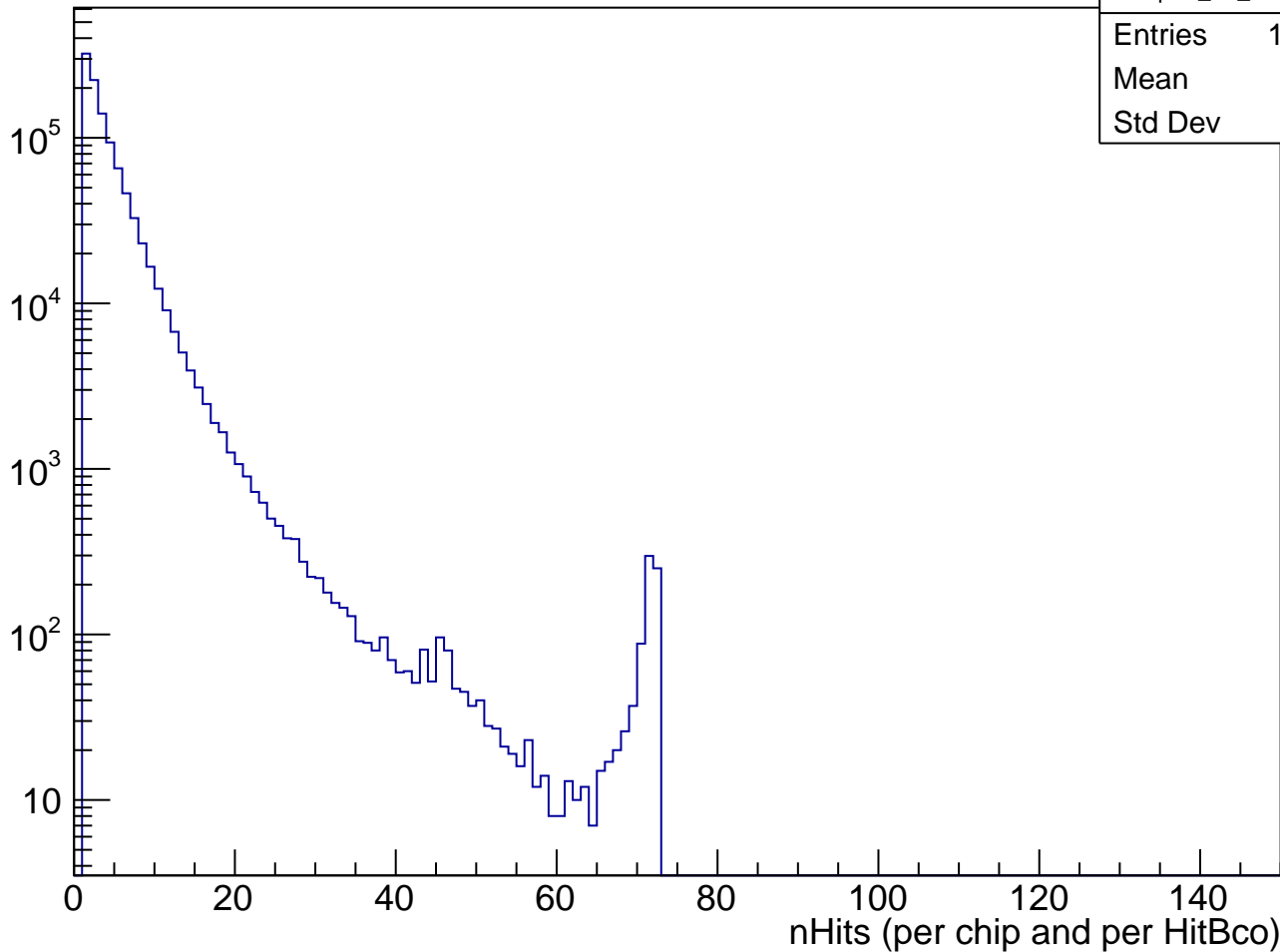


nChipHit_F6_Fch1_Chip18	
Entries	998961
Mean	3.533
Std Dev	4.093

nHits (per chip and per HitBco)

nChipHit_F6_Fch1_Chip19

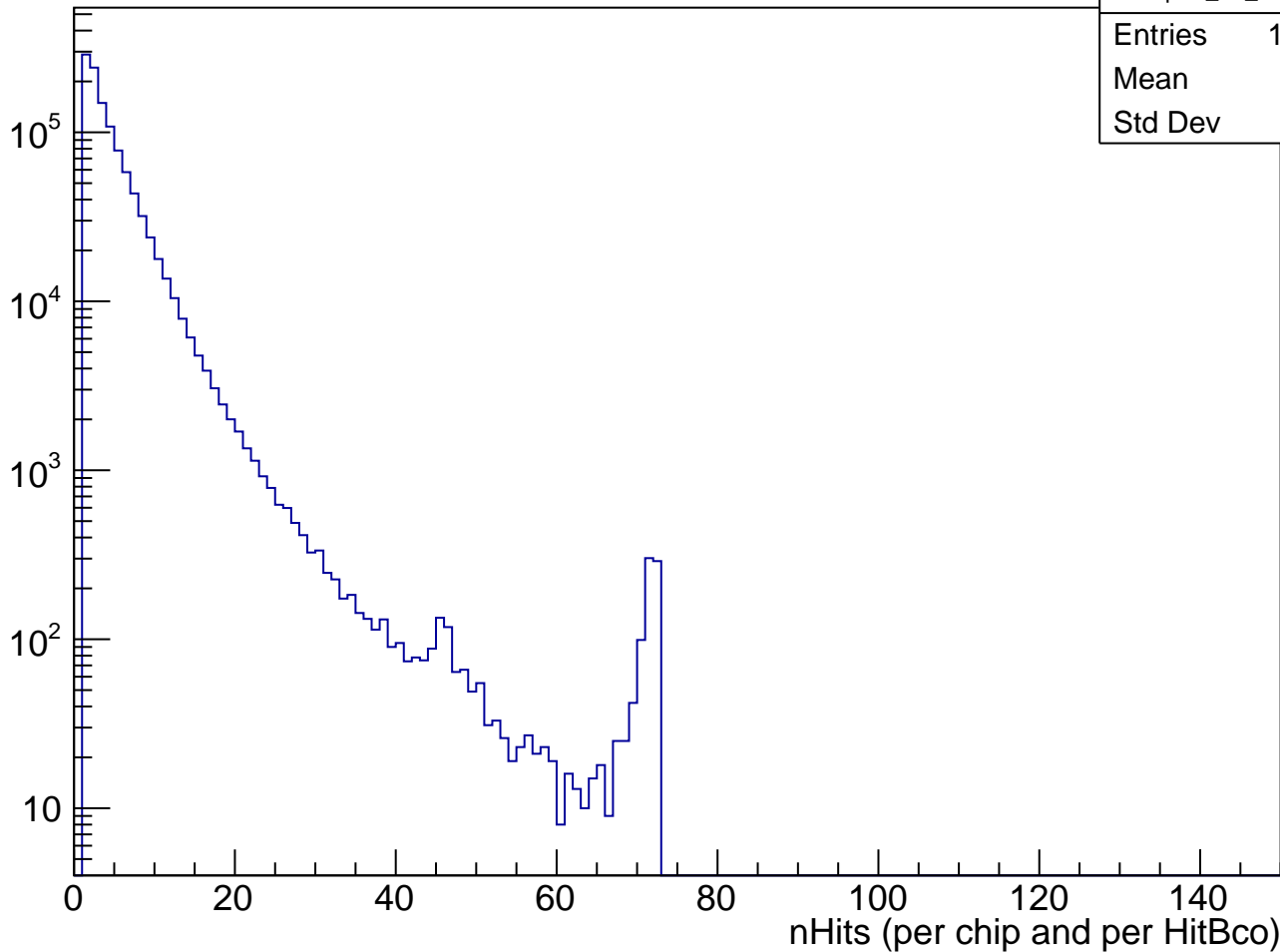
Entries



nChipHit_F6_Fch1_Chip19	
Entries	1020208
Mean	3.523
Std Dev	4.083

nChipHit_F6_Fch1_Chip2

Entries



nChipHit_F6_Fch1_Chip2

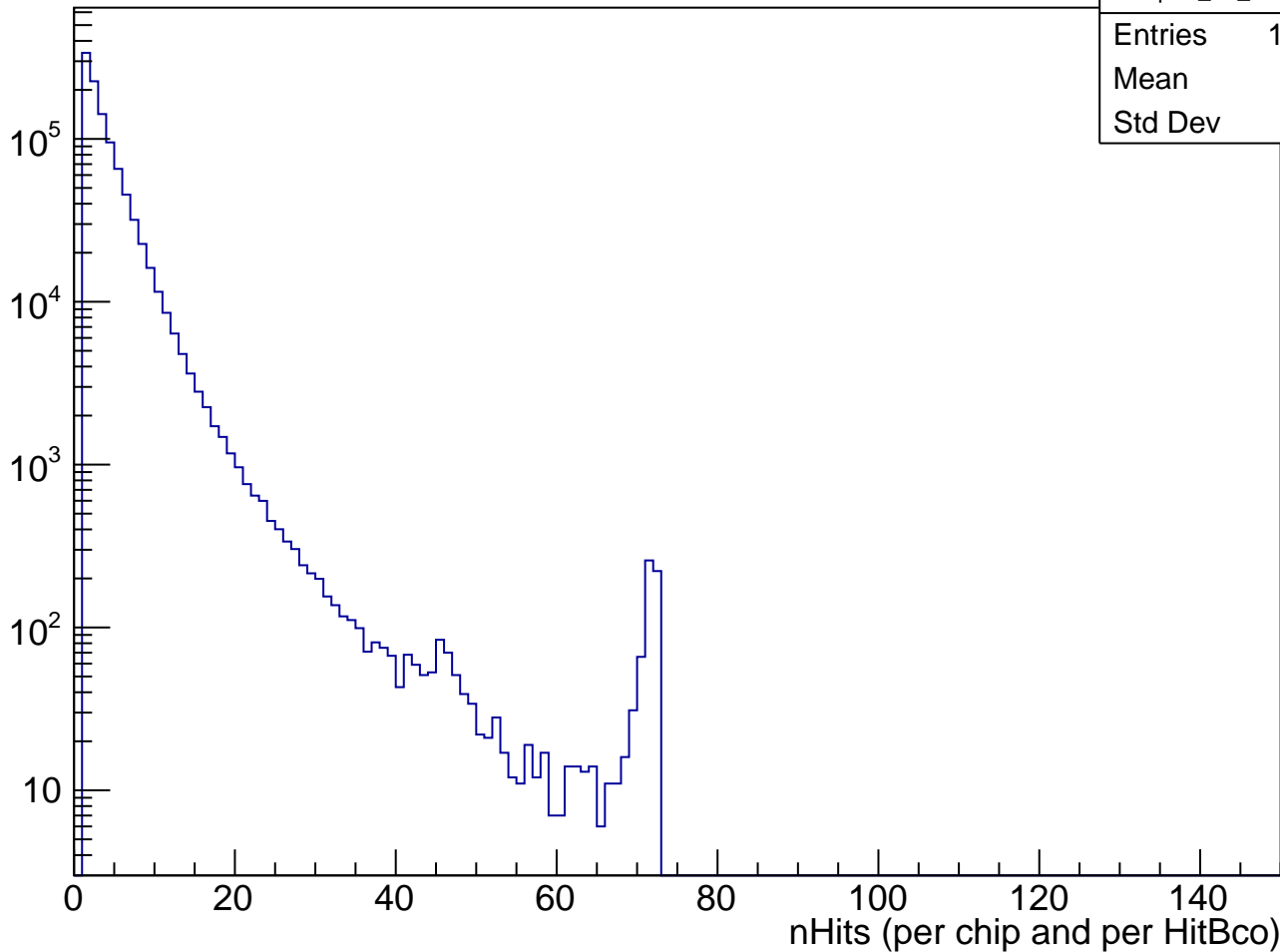
Entries 1106769

Mean 4.011

Std Dev 4.445

nChipHit_F6_Fch1_Chip20

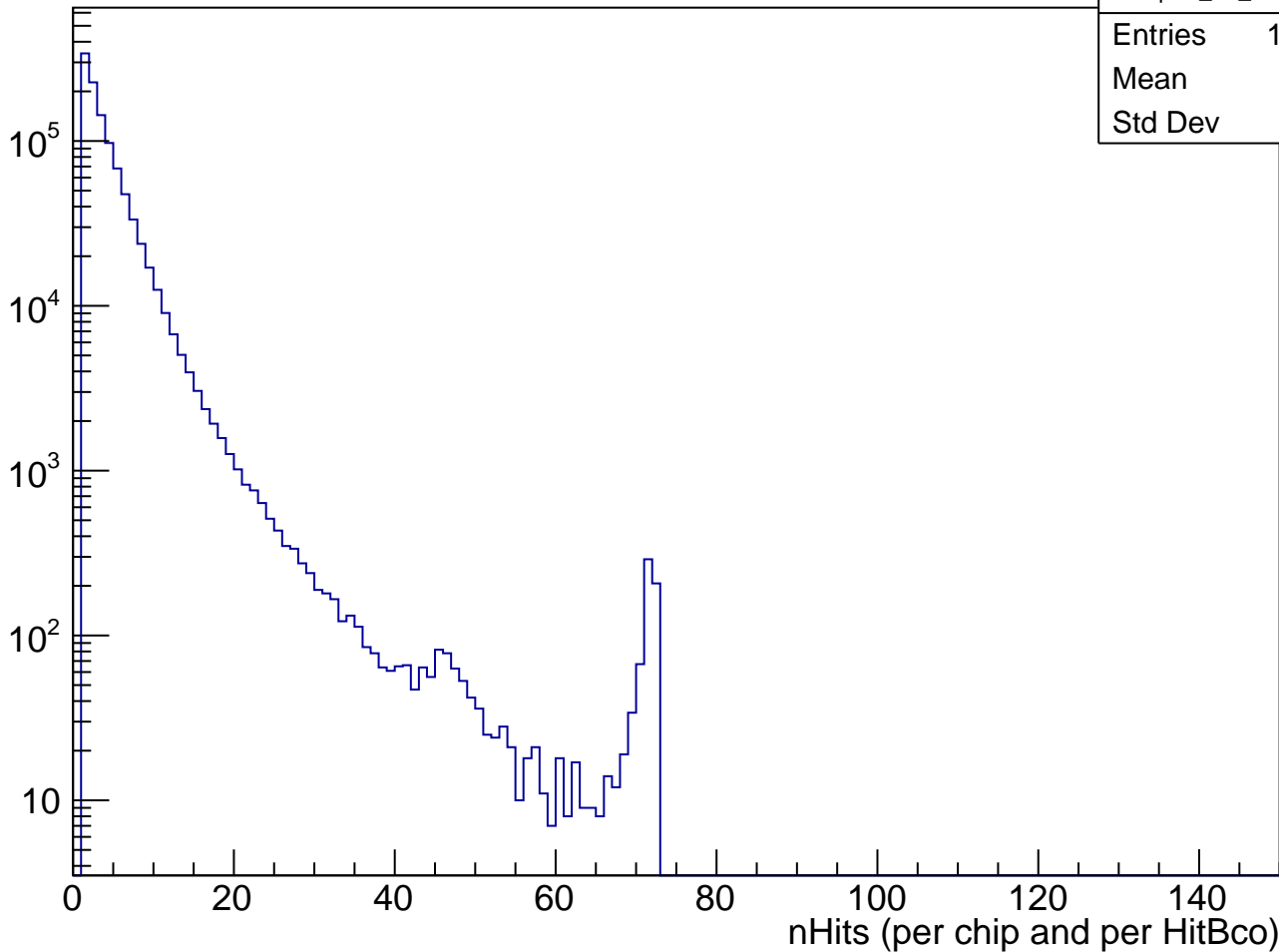
Entries



nChipHit_F6_Fch1_Chip20	
Entries	1034358
Mean	3.417
Std Dev	3.89

nChipHit_F6_Fch1_Chip21

Entries

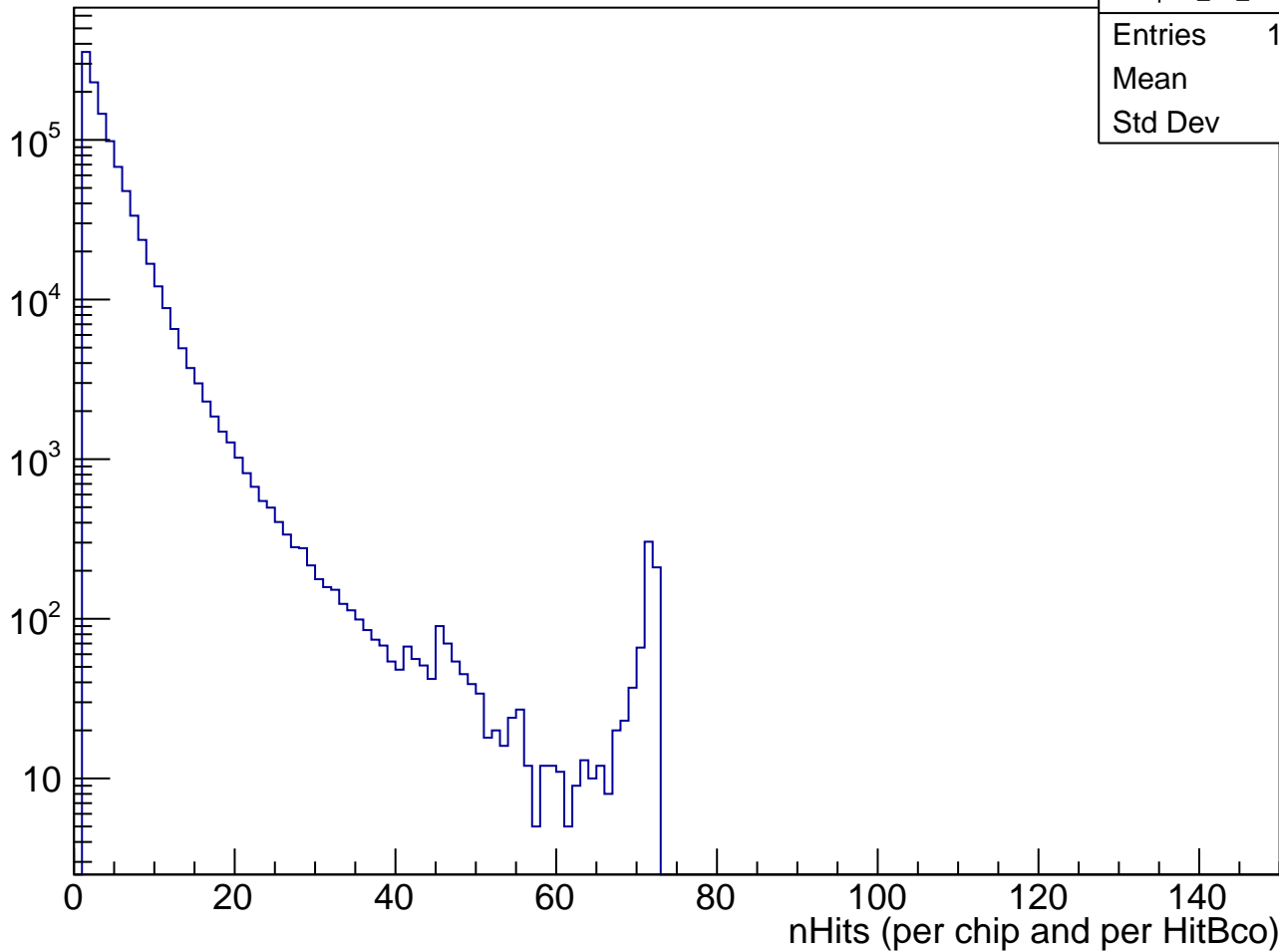


nChipHit_F6_Fch1_Chip21	
Entries	1053195
Mean	3.479
Std Dev	3.972

nHits (per chip and per HitBco)

nChipHit_F6_Fch1_Chip22

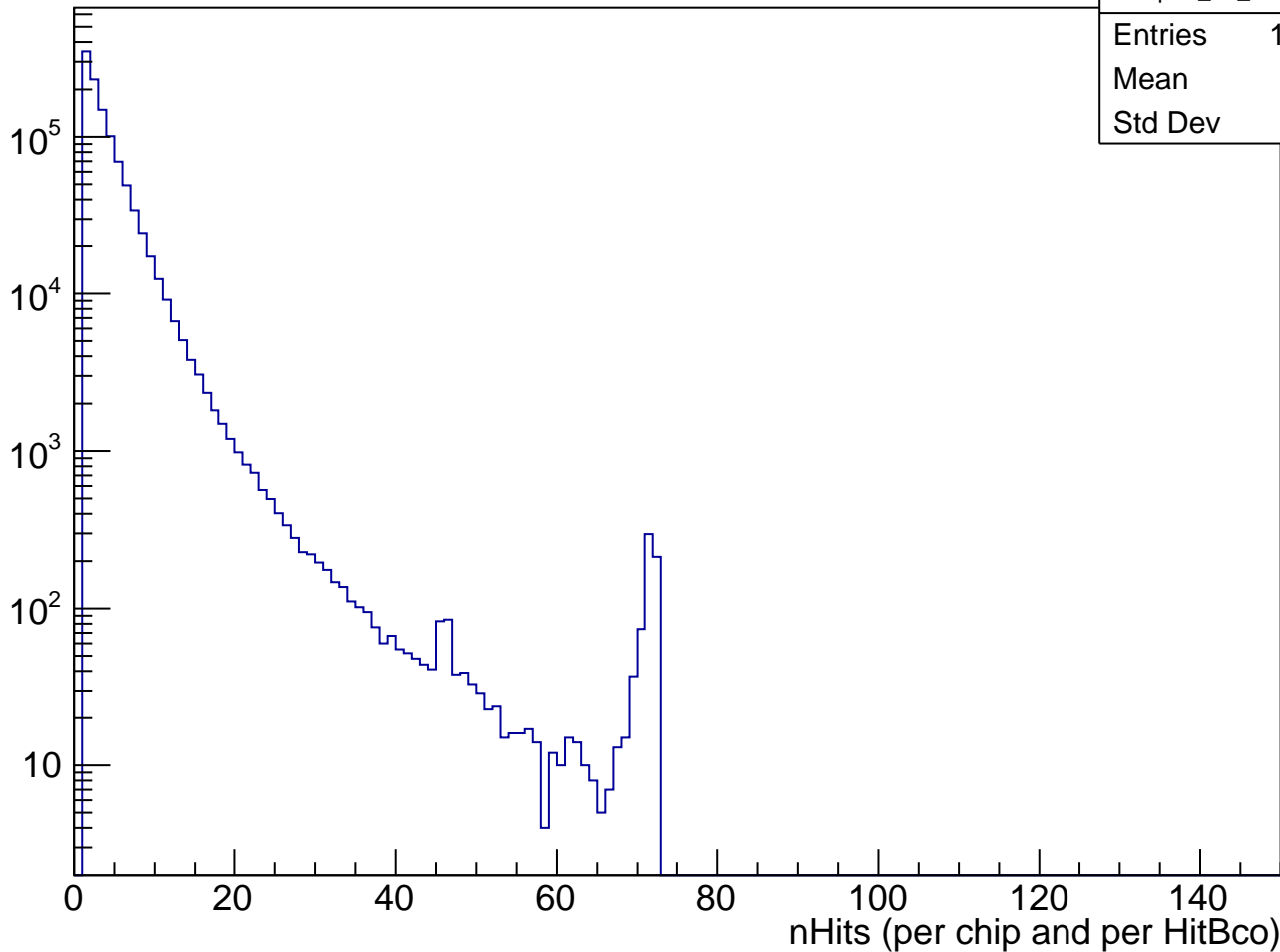
Entries



nChipHit_F6_Fch1_Chip22	
Entries	1072242
Mean	3.415
Std Dev	3.901

nChipHit_F6_Fch1_Chip23

Entries

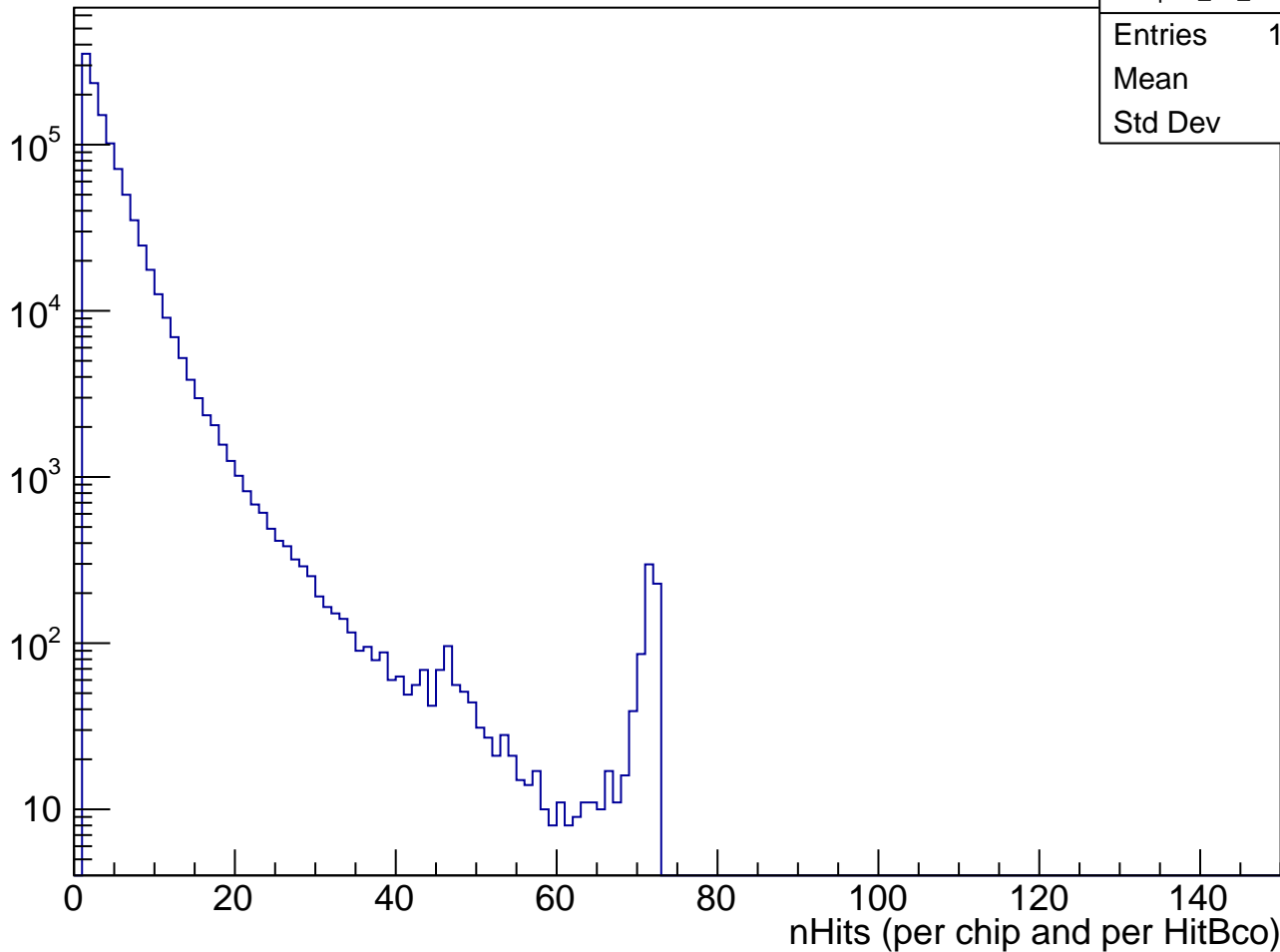


nChipHit_F6_Fch1_Chip23	
Entries	1079460
Mean	3.447
Std Dev	3.884

nHits (per chip and per HitBco)

nChipHit_F6_Fch1_Chip24

Entries

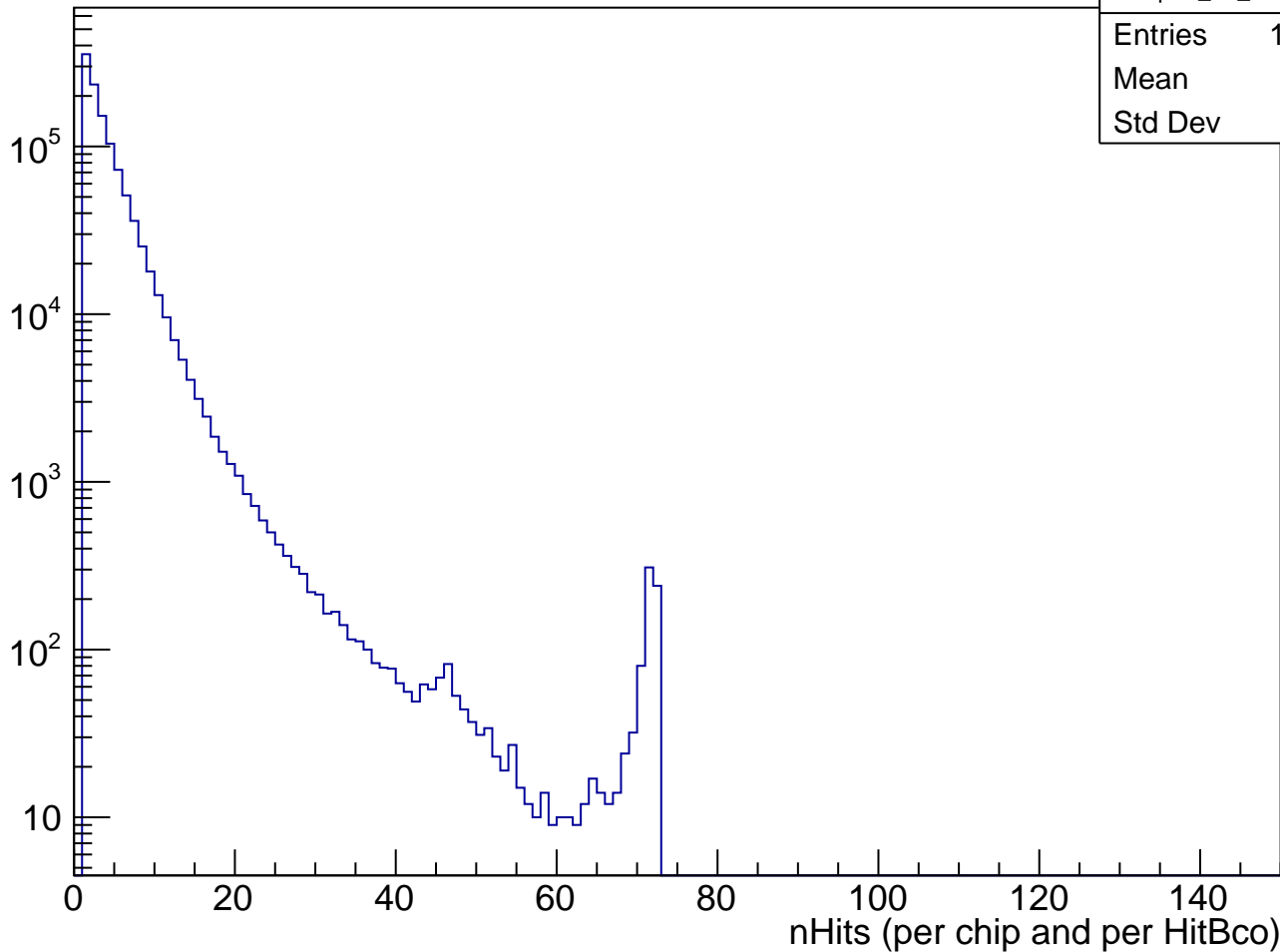


nChipHit_F6_Fch1_Chip24	
Entries	1094024
Mean	3.465
Std Dev	3.935

nHits (per chip and per HitBco)

nChipHit_F6_Fch1_Chip25

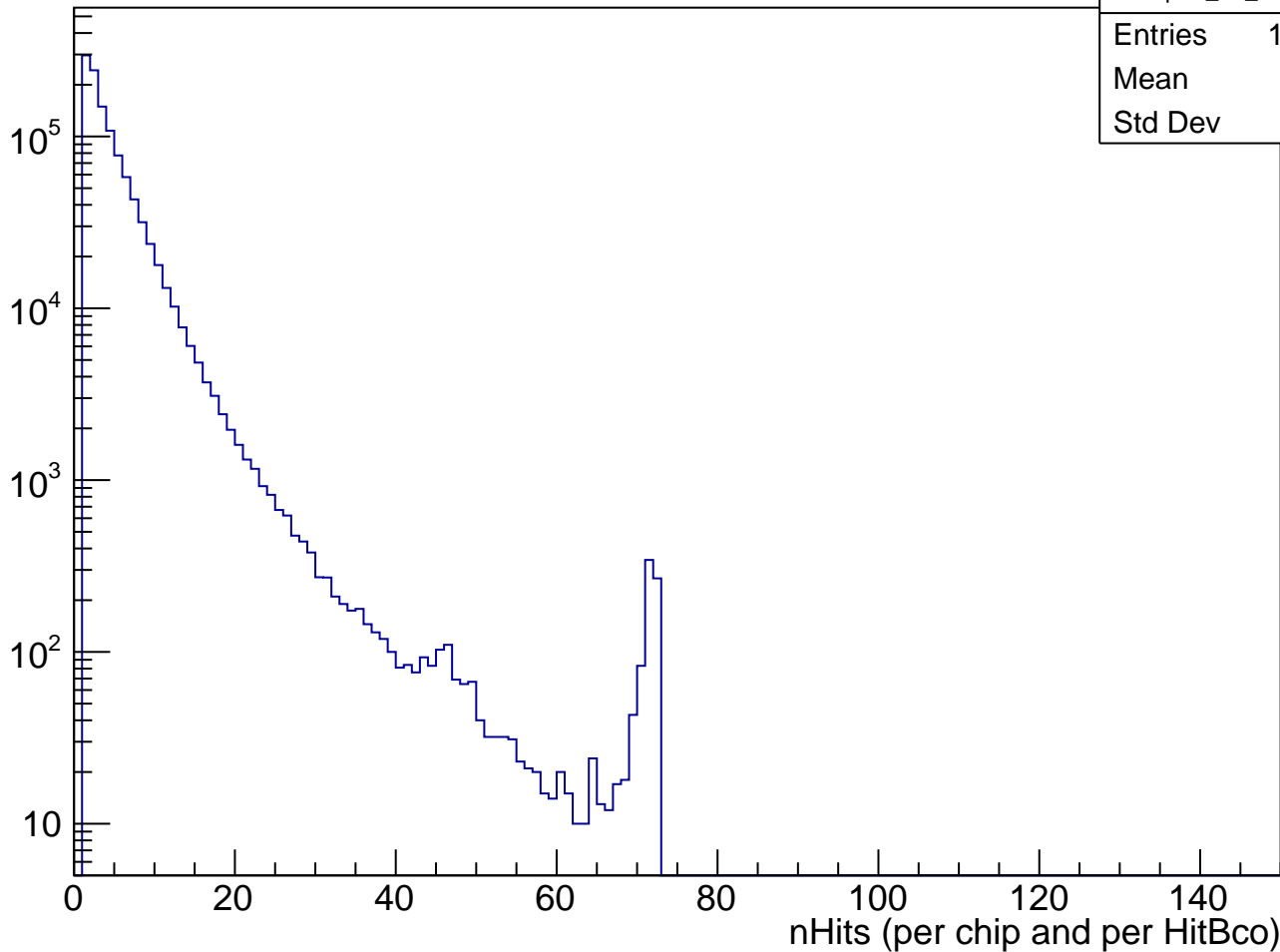
Entries



nChipHit_F6_Fch1_Chip25	
Entries	1105430
Mean	3.484
Std Dev	3.948

nChipHit_F6_Fch1_Chip3

Entries



nChipHit_F6_Fch1_Chip3

Entries 1113755

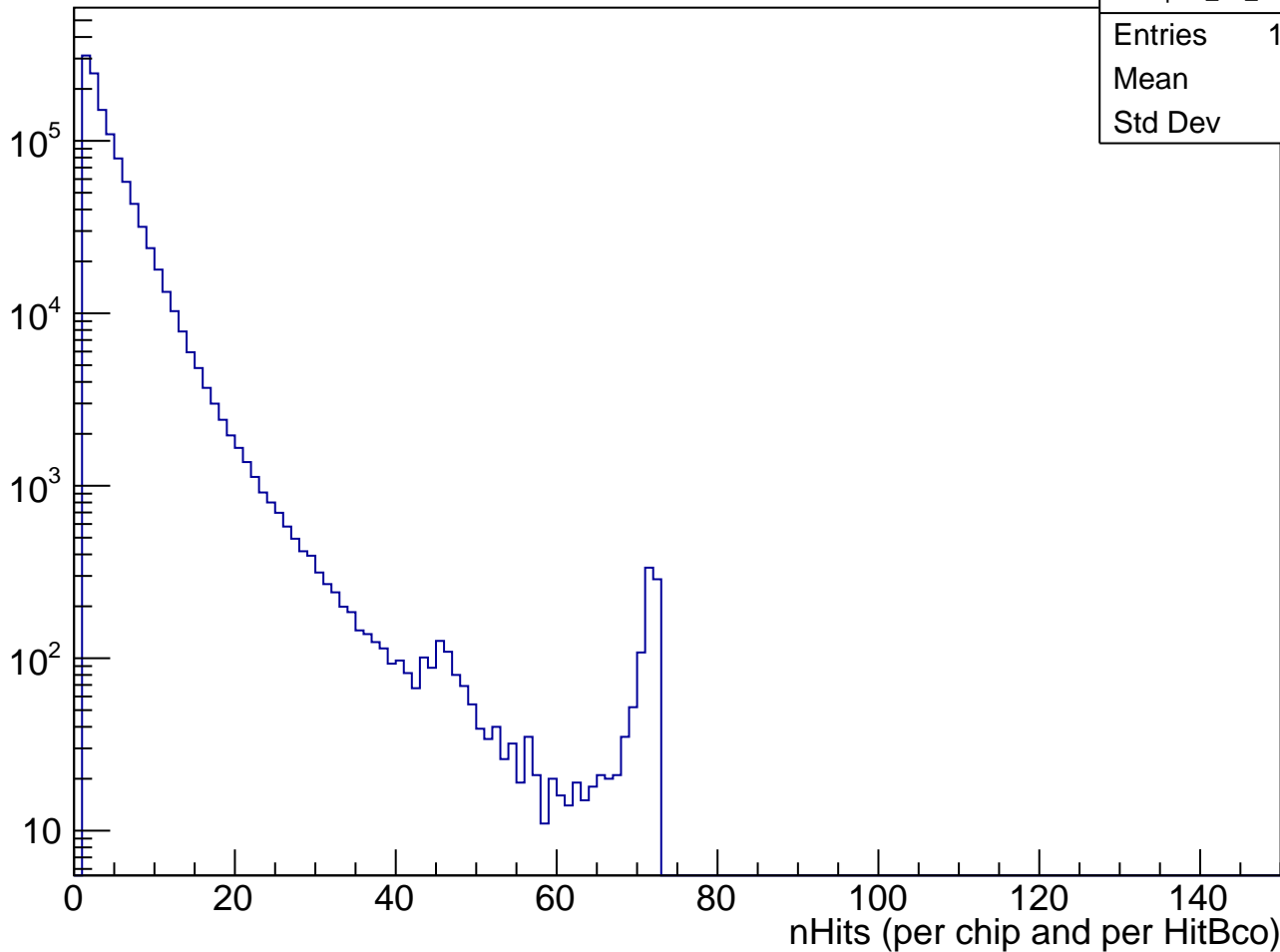
Mean 3.977

Std Dev 4.433

nHits (per chip and per HitBco)

nChipHit_F6_Fch1_Chip4

Entries



nChipHit_F6_Fch1_Chip4

Entries 1138112

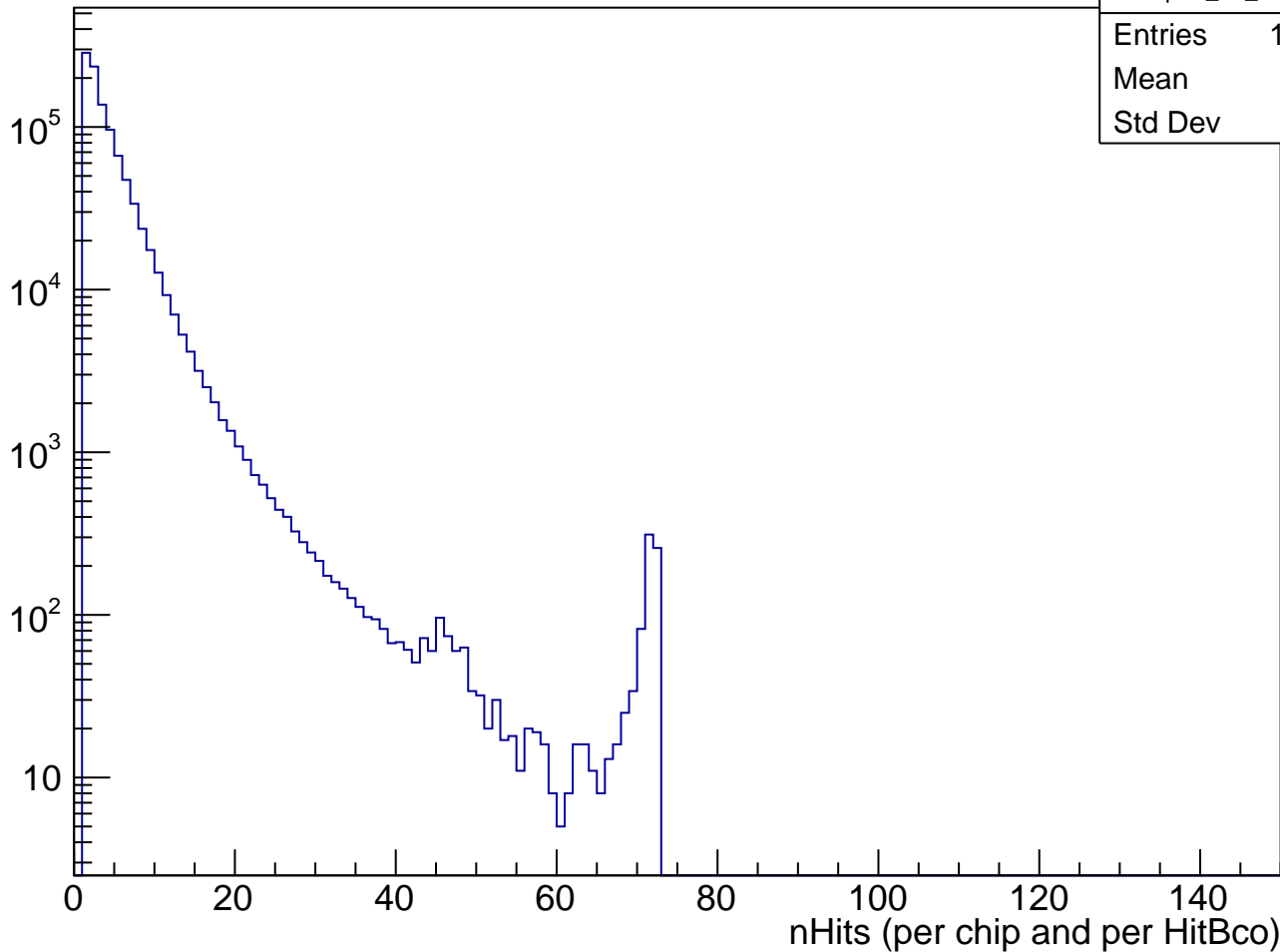
Mean 3.938

Std Dev 4.448

nHits (per chip and per HitBco)

nChipHit_F6_Fch1_Chip5

Entries



nChipHit_F6_Fch1_Chip5

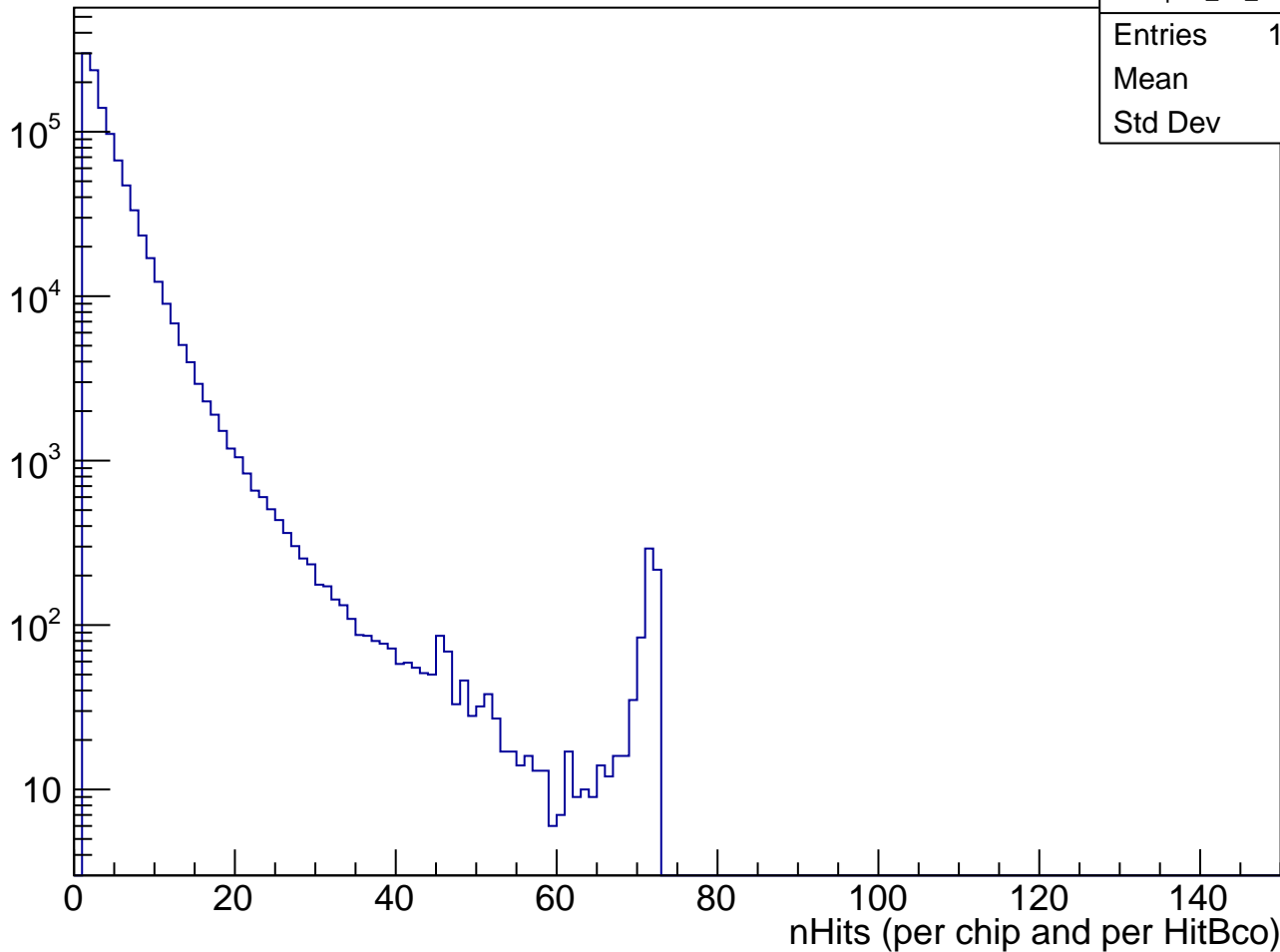
Entries 1000773

Mean 3.632

Std Dev 4.127

nChipHit_F6_Fch1_Chip6

Entries



nChipHit_F6_Fch1_Chip6

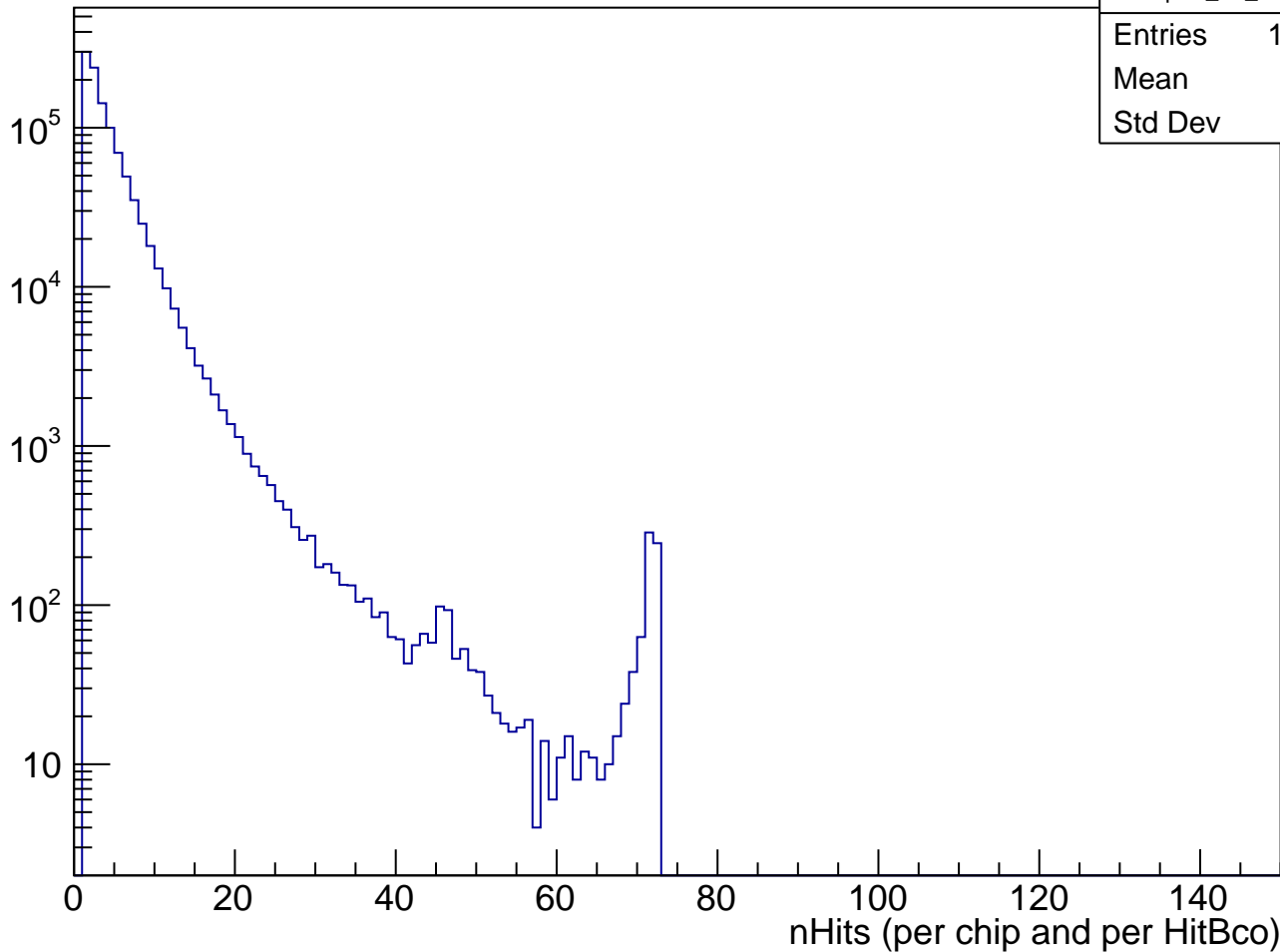
Entries 1016076

Mean 3.547

Std Dev 3.987

nChipHit_F6_Fch1_Chip7

Entries



nChipHit_F6_Fch1_Chip7

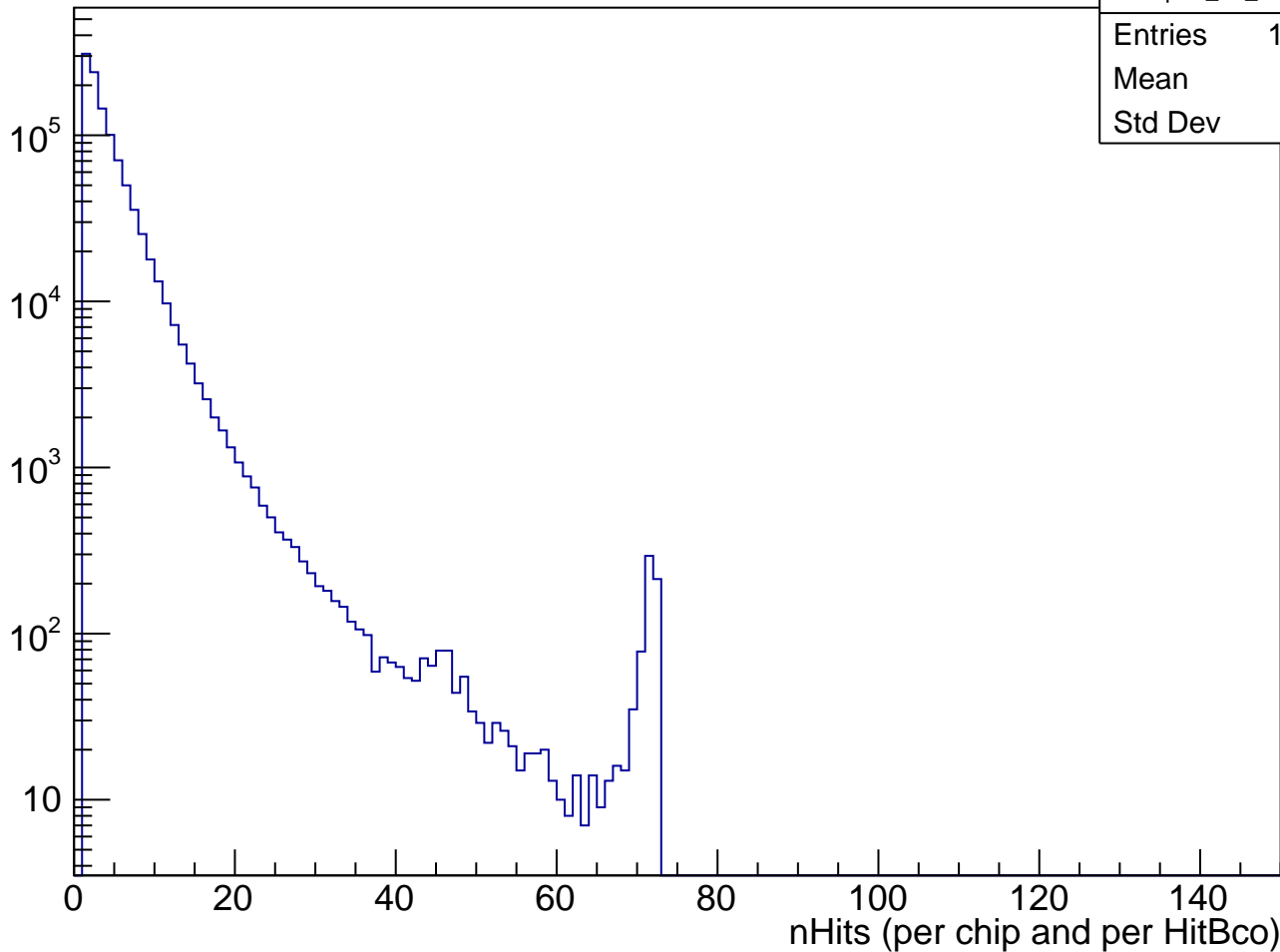
Entries 1037168

Mean 3.621

Std Dev 4.054

nChipHit_F6_Fch1_Chip8

Entries



nChipHit_F6_Fch1_Chip8

Entries 1053167

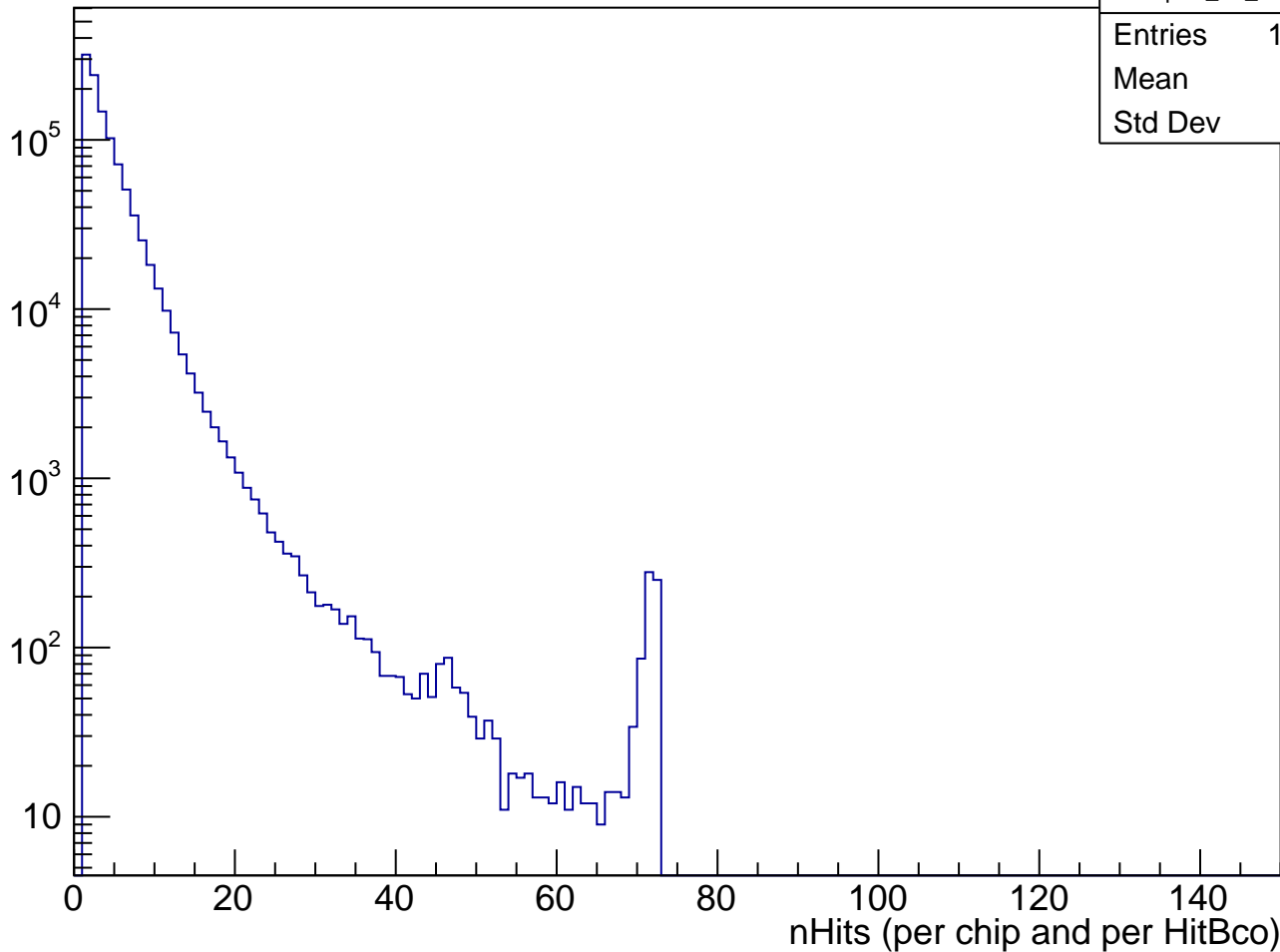
Mean 3.59

Std Dev 4.007

nHits (per chip and per HitBco)

nChipHit_F6_Fch1_Chip9

Entries



nChipHit_F6_Fch1_Chip9

Entries 1070545

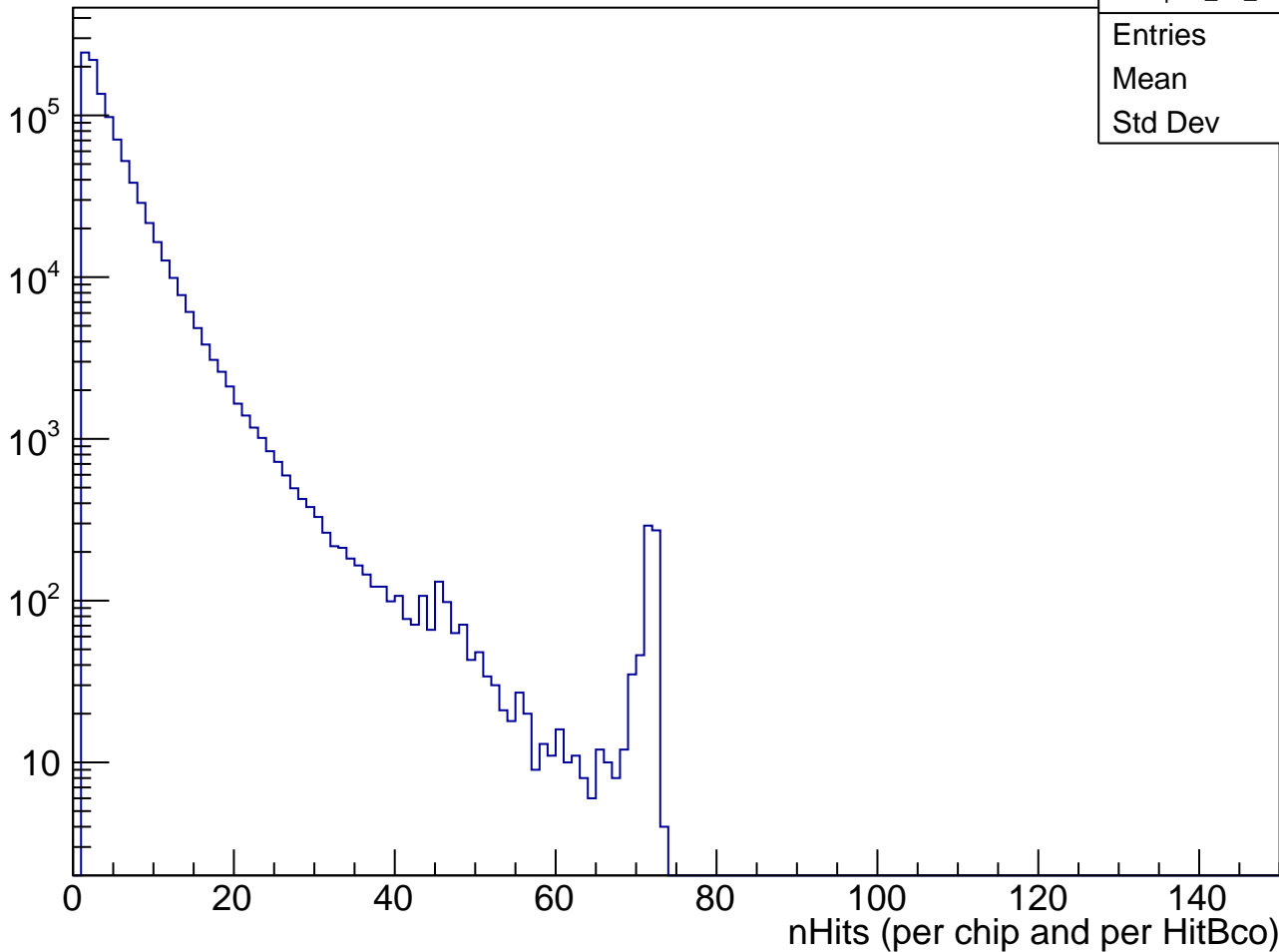
Mean 3.573

Std Dev 4.008

nHits (per chip and per HitBco)

nChipHit_F6_Fch2_Chip0

Entries



nChipHit_F6_Fch2_Chip0

Entries 991752

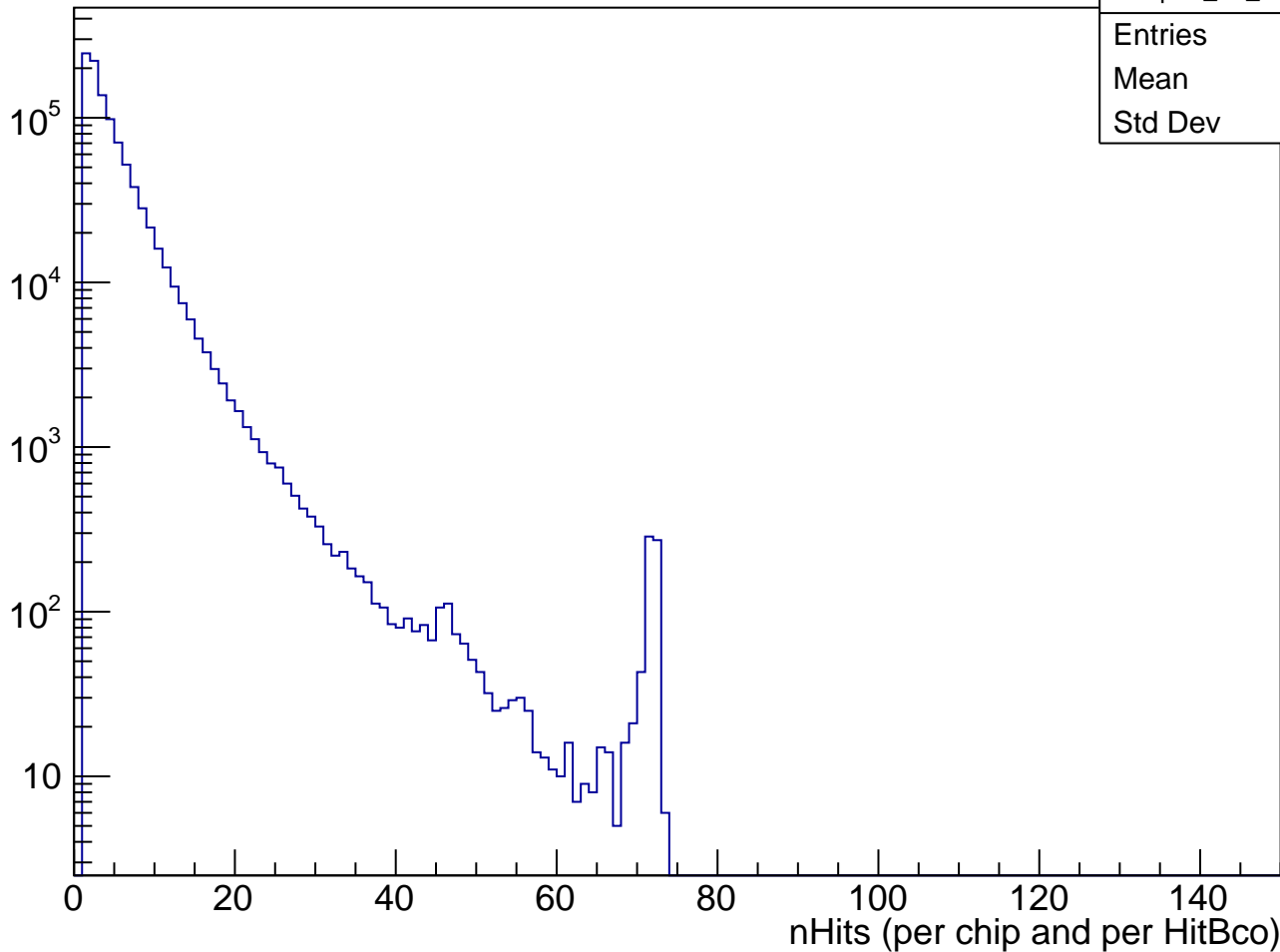
Mean 4.124

Std Dev 4.559

nHits (per chip and per HitBco)

nChipHit_F6_Fch2_Chip1

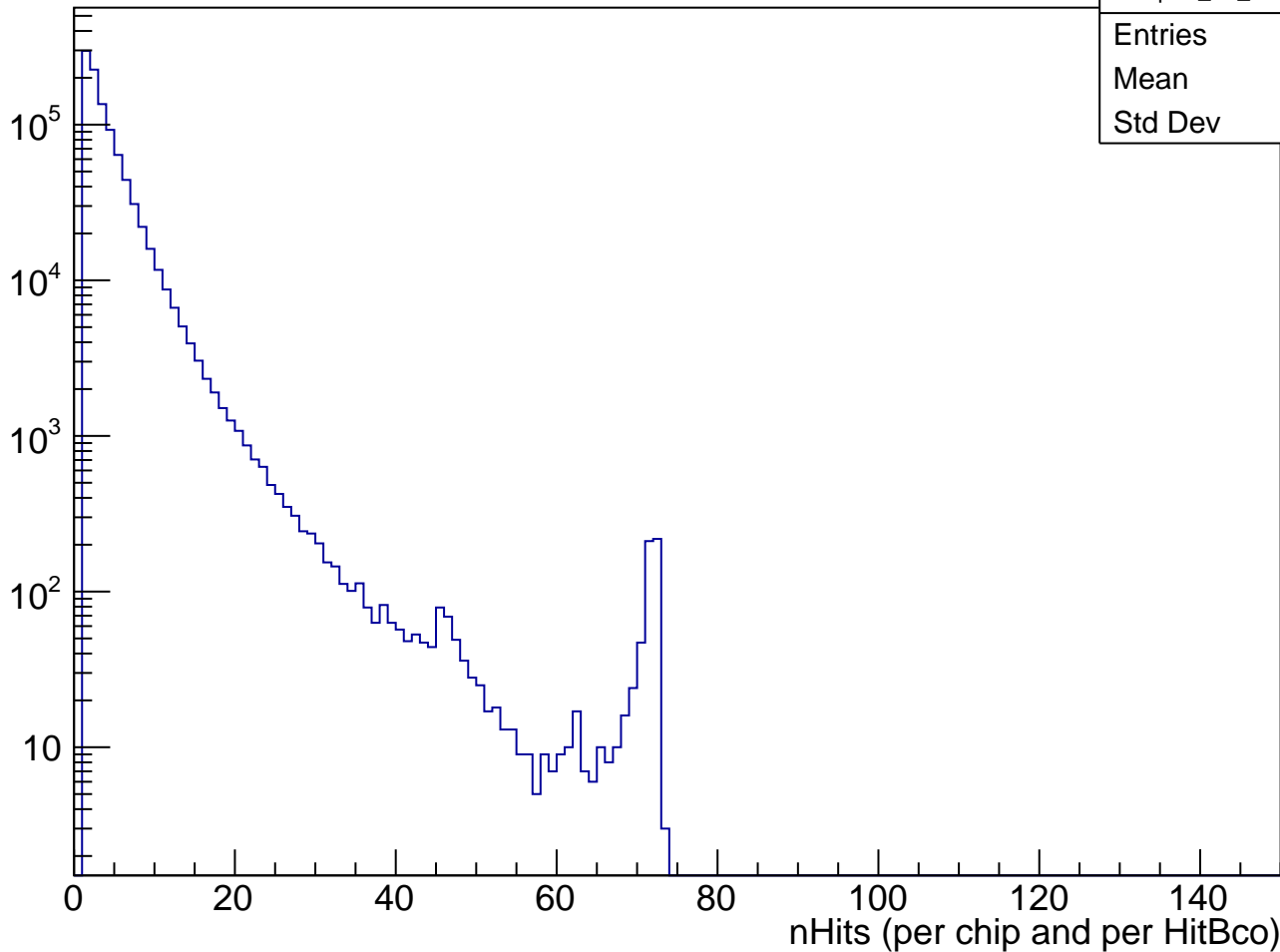
Entries



nChipHit_F6_Fch2_Chip1	
Entries	992505
Mean	4.081
Std Dev	4.517

nChipHit_F6_Fch2_Chip10

Entries

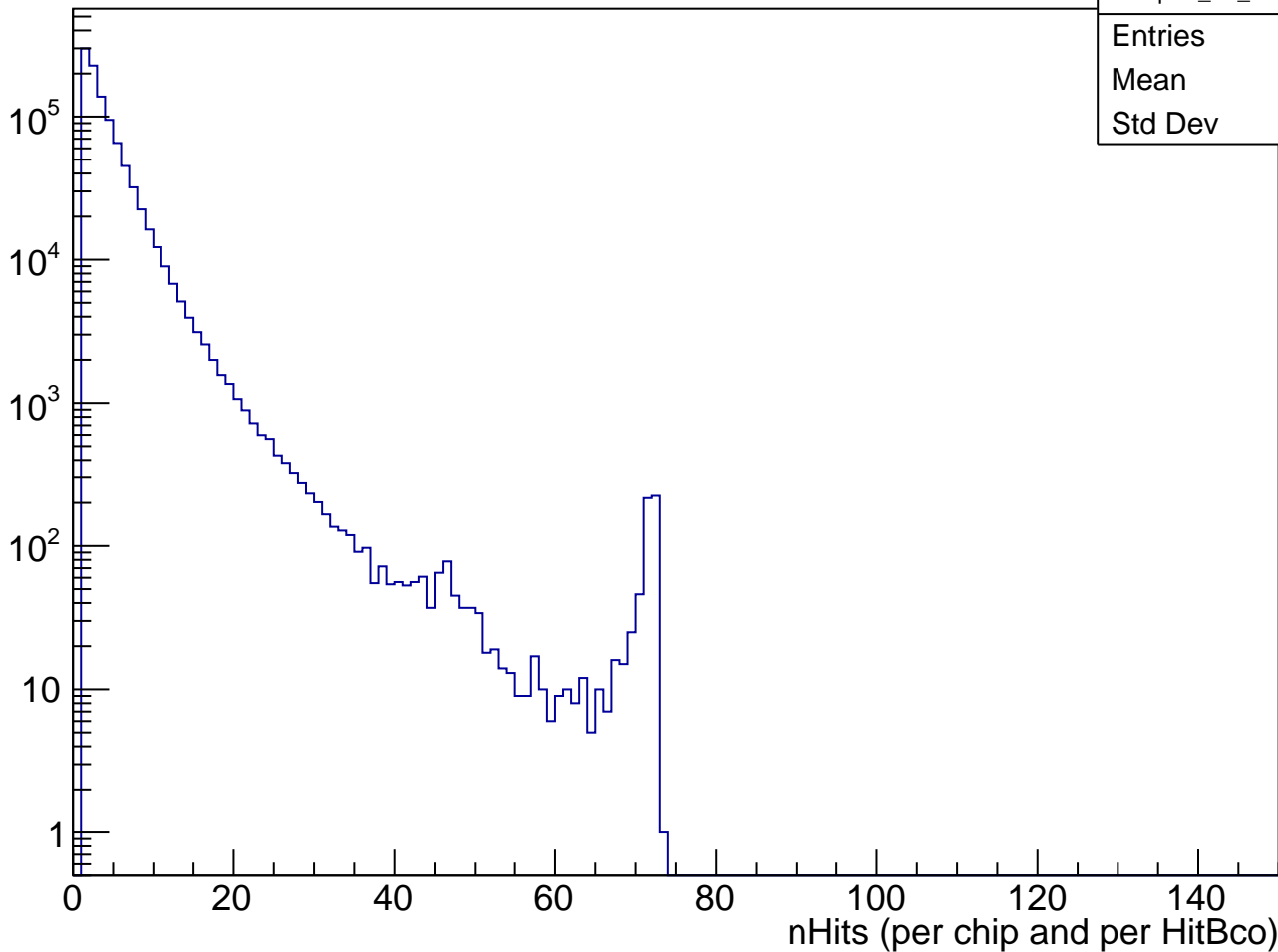


nChipHit_F6_Fch2_Chip10	
Entries	982437
Mean	3.52
Std Dev	3.927

nHits (per chip and per HitBco)

nChipHit_F6_Fch2_Chip11

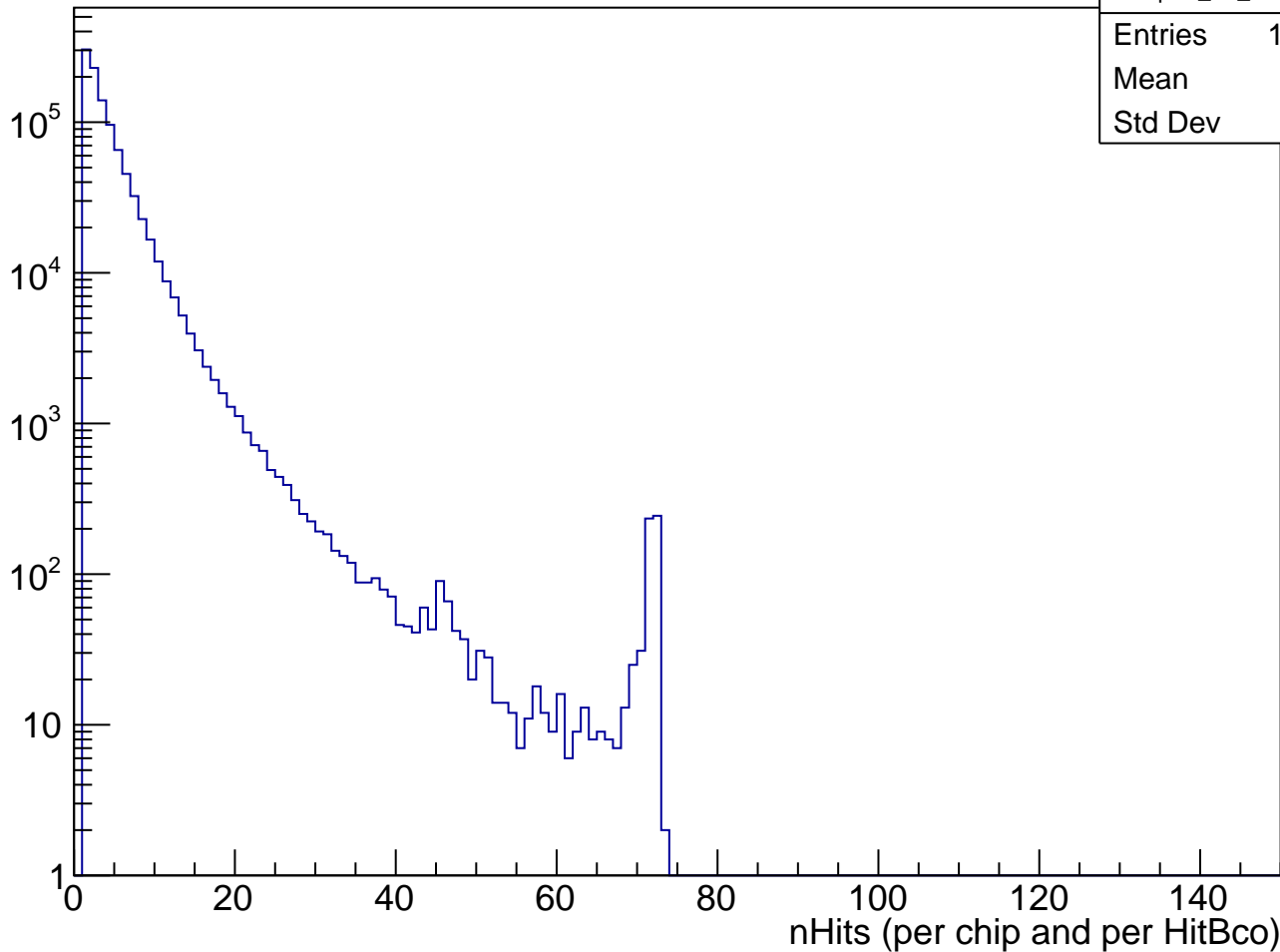
Entries



nChipHit_F6_Fch2_Chip11	
Entries	996083
Mean	3.546
Std Dev	3.952

nChipHit_F6_Fch2_Chip12

Entries

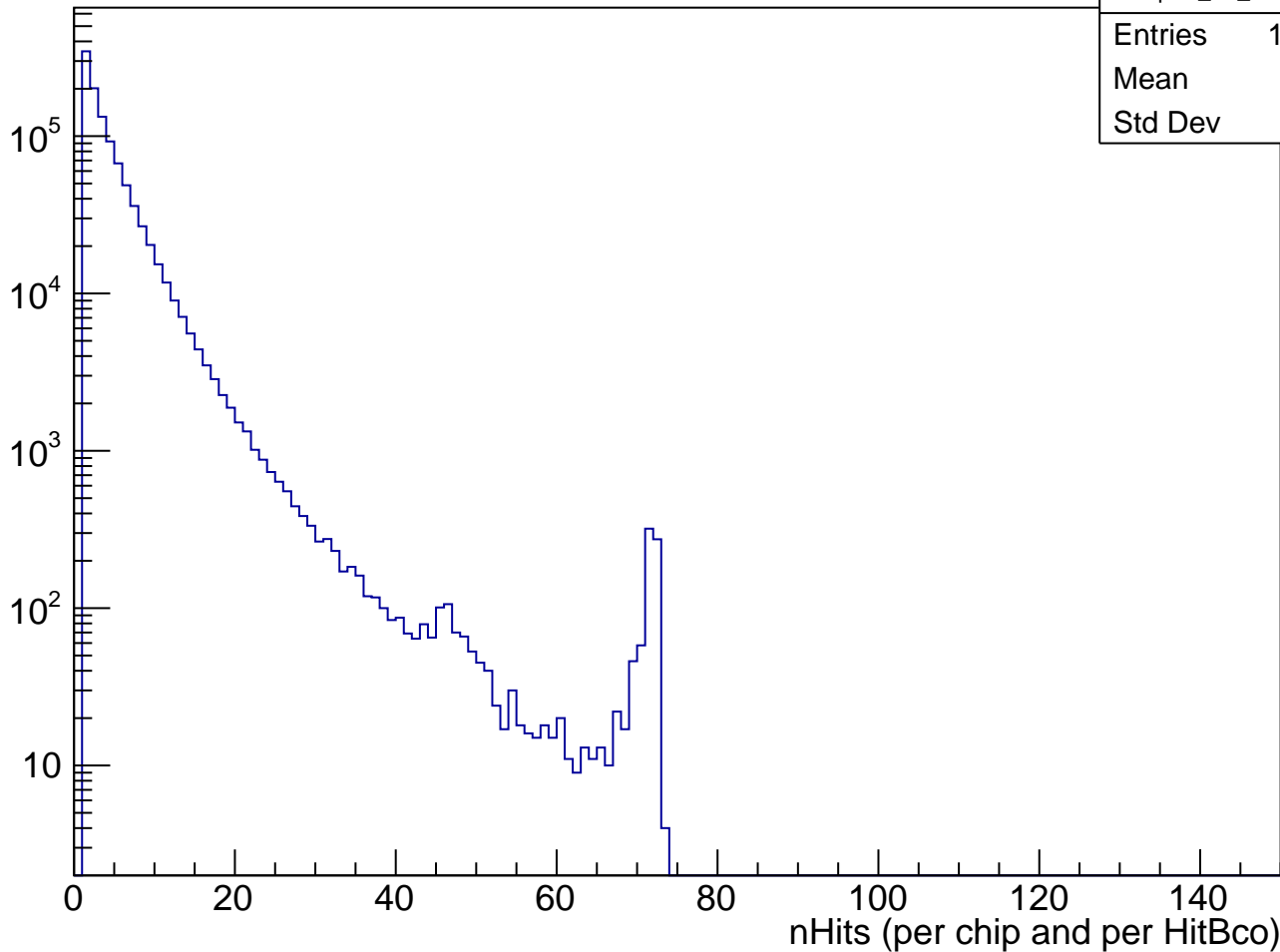


nChipHit_F6_Fch2_Chip12	
Entries	1006135
Mean	3.532
Std Dev	3.946

nHits (per chip and per HitBco)

nChipHit_F6_Fch2_Chip13

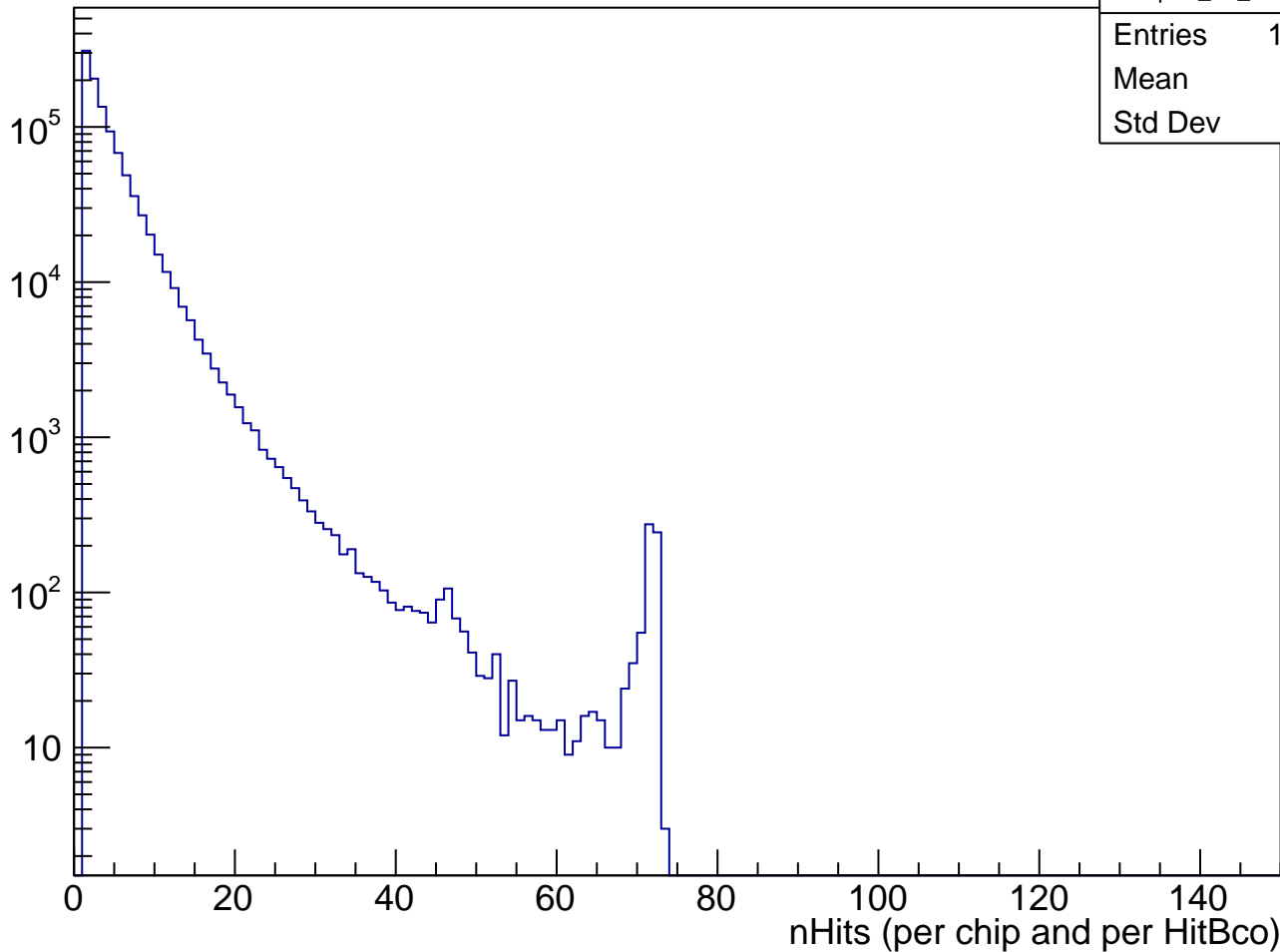
Entries



nChipHit_F6_Fch2_Chip13	
Entries	1046574
Mean	3.769
Std Dev	4.438

nChipHit_F6_Fch2_Chip14

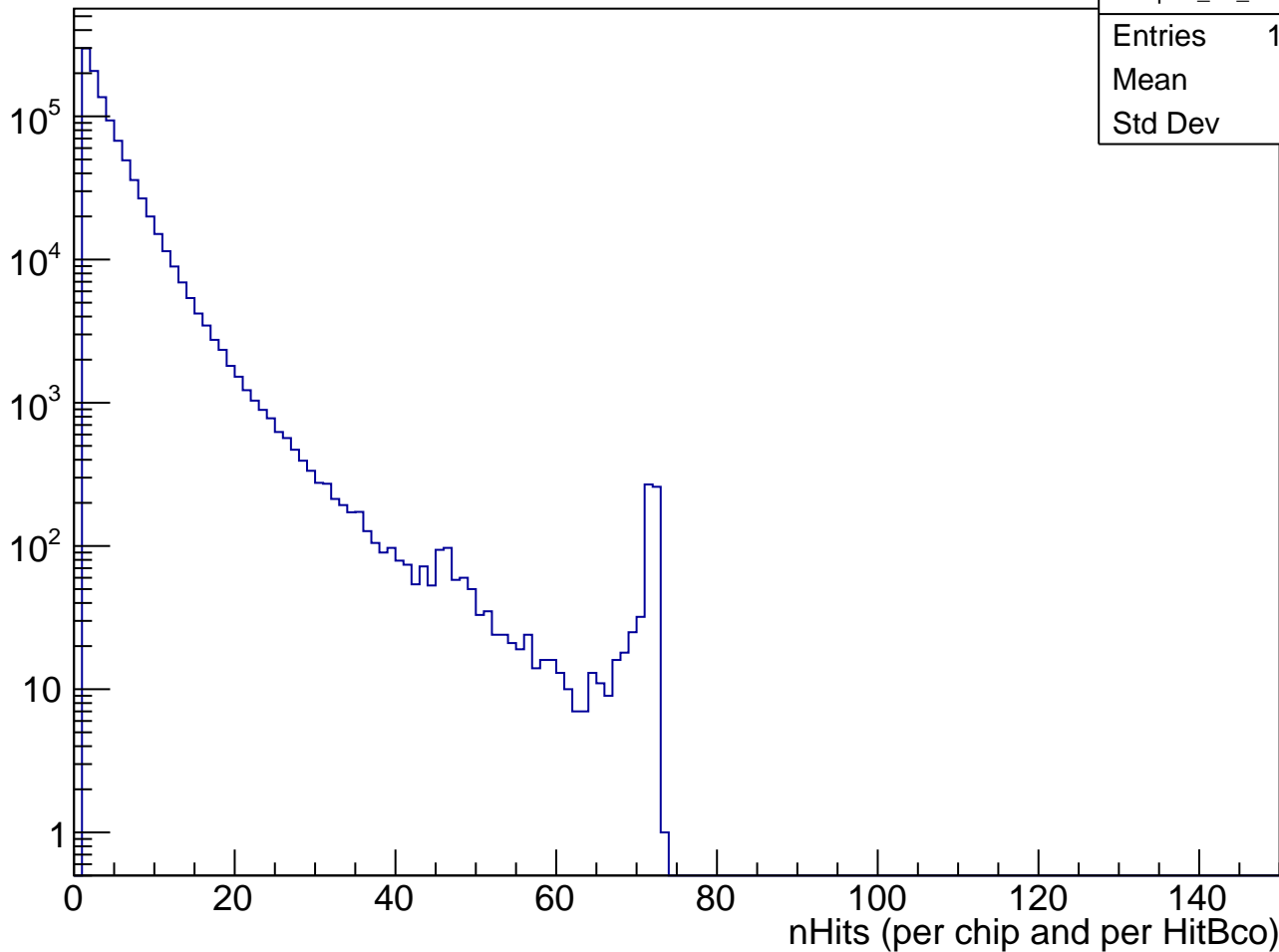
Entries



nChipHit_F6_Fch2_Chip14	
Entries	1017339
Mean	3.847
Std Dev	4.409

nChipHit_F6_Fch2_Chip15

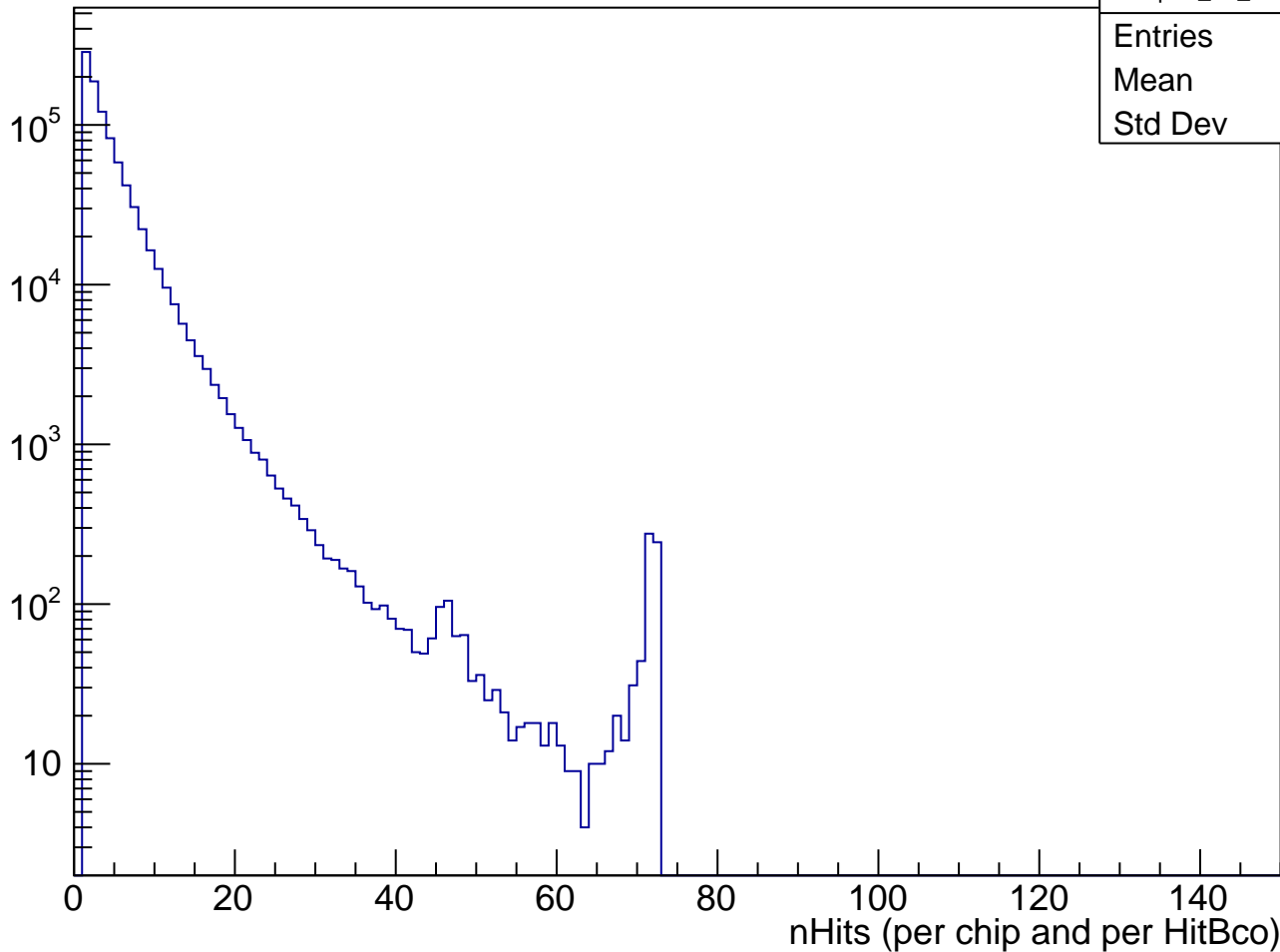
Entries



nChipHit_F6_Fch2_Chip15	
Entries	1008266
Mean	3.864
Std Dev	4.39

nChipHit_F6_Fch2_Chip16

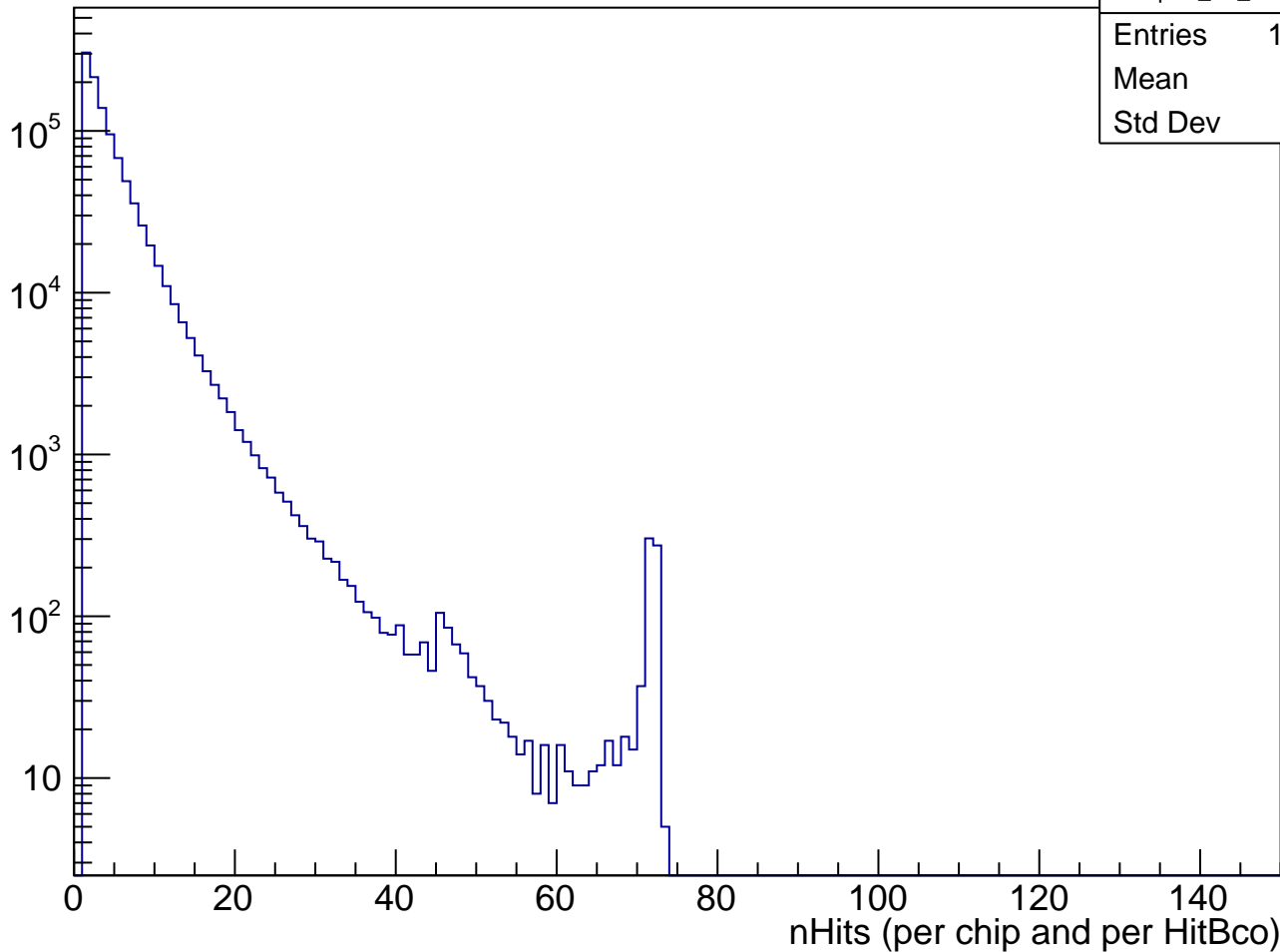
Entries



nChipHit_F6_Fch2_Chip16	
Entries	908511
Mean	3.735
Std Dev	4.377

nChipHit_F6_Fch2_Chip17

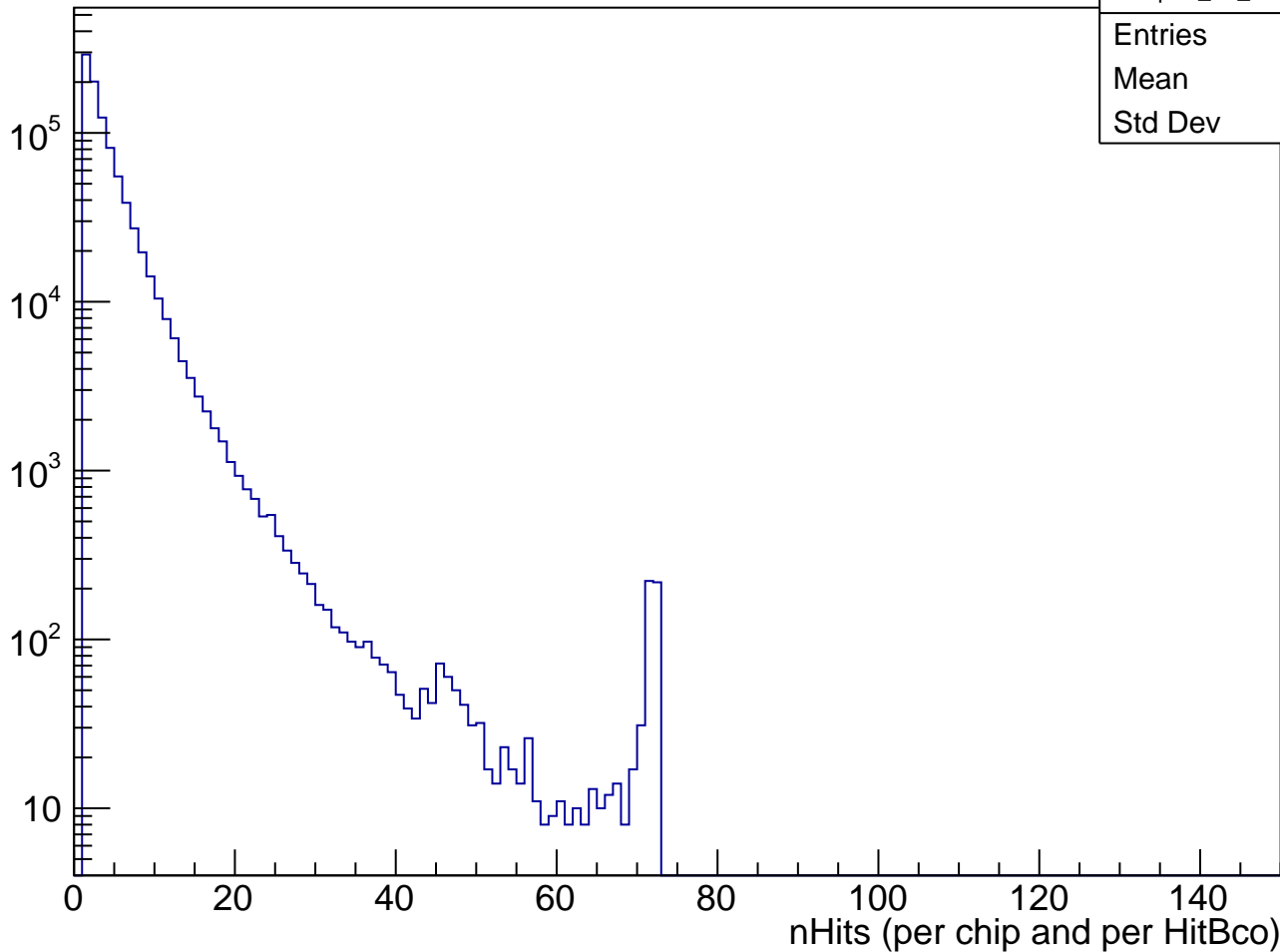
Entries



nChipHit_F6_Fch2_Chip17	
Entries	1022735
Mean	3.789
Std Dev	4.317

nChipHit_F6_Fch2_Chip18

Entries

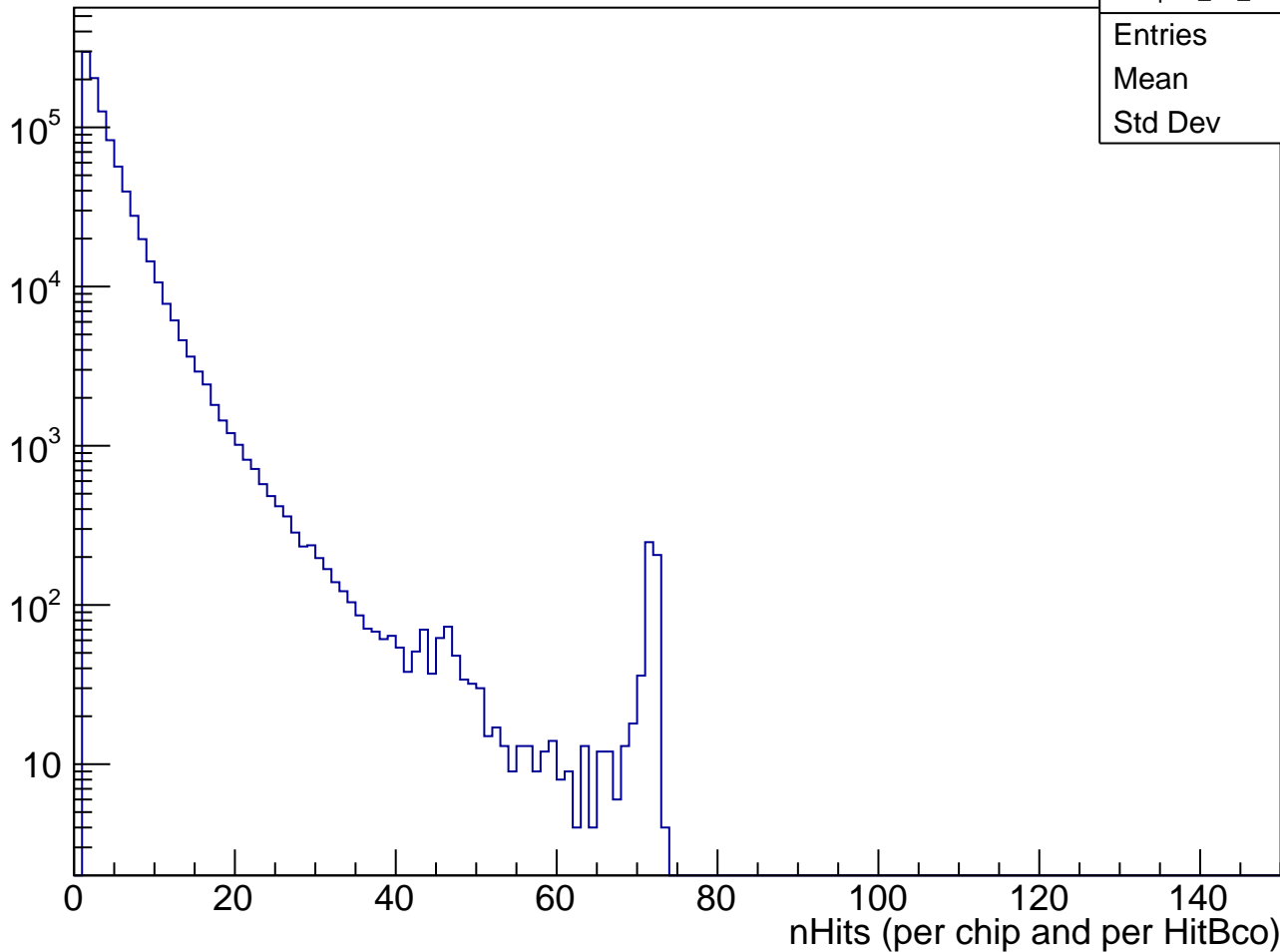


nChipHit_F6_Fch2_Chip18	
Entries	901473
Mean	3.462
Std Dev	3.984

nHits (per chip and per HitBco)

nChipHit_F6_Fch2_Chip19

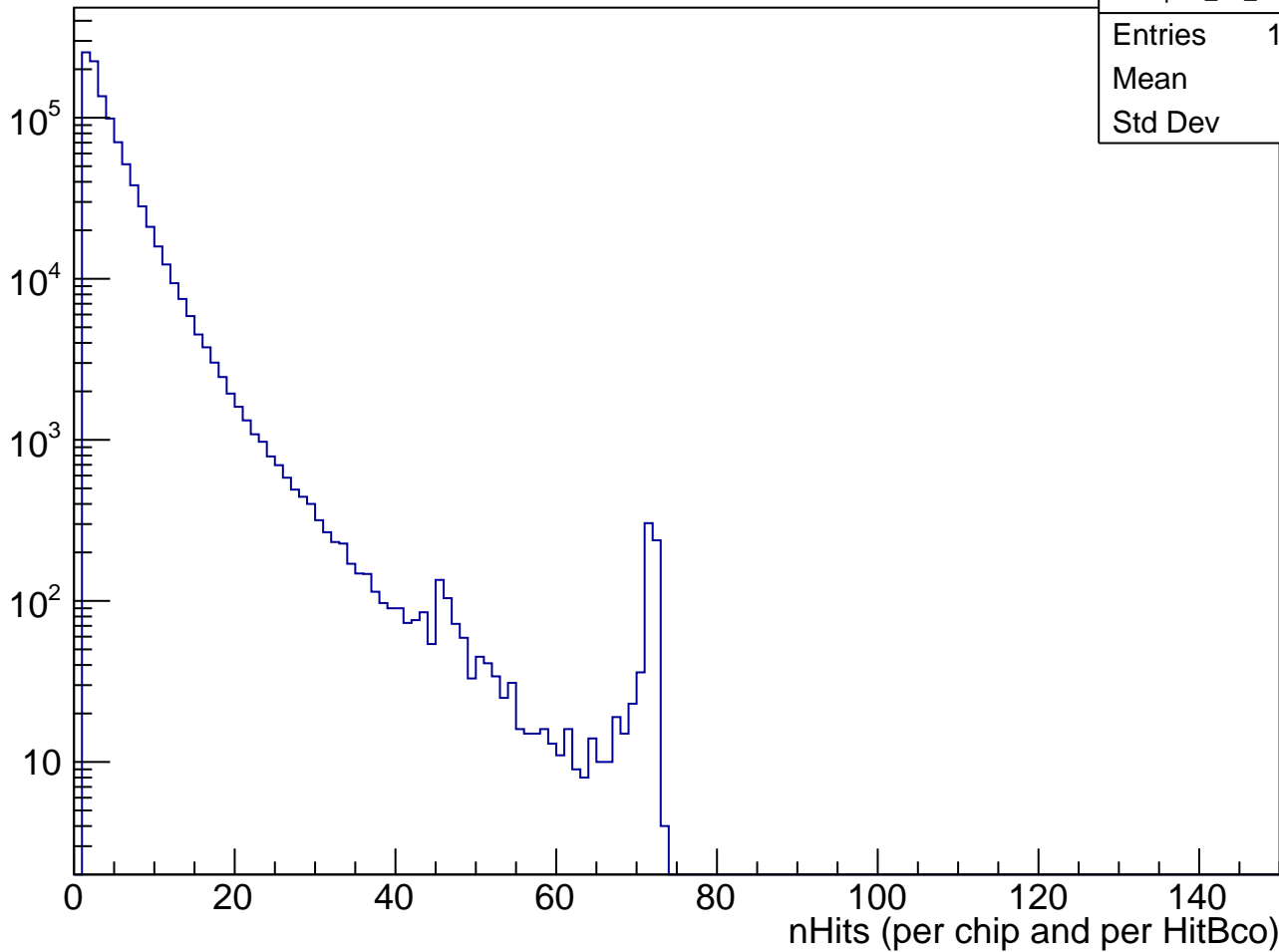
Entries



nChipHit_F6_Fch2_Chip19	
Entries	919245
Mean	3.464
Std Dev	3.985

nChipHit_F6_Fch2_Chip2

Entries



nChipHit_F6_Fch2_Chip2

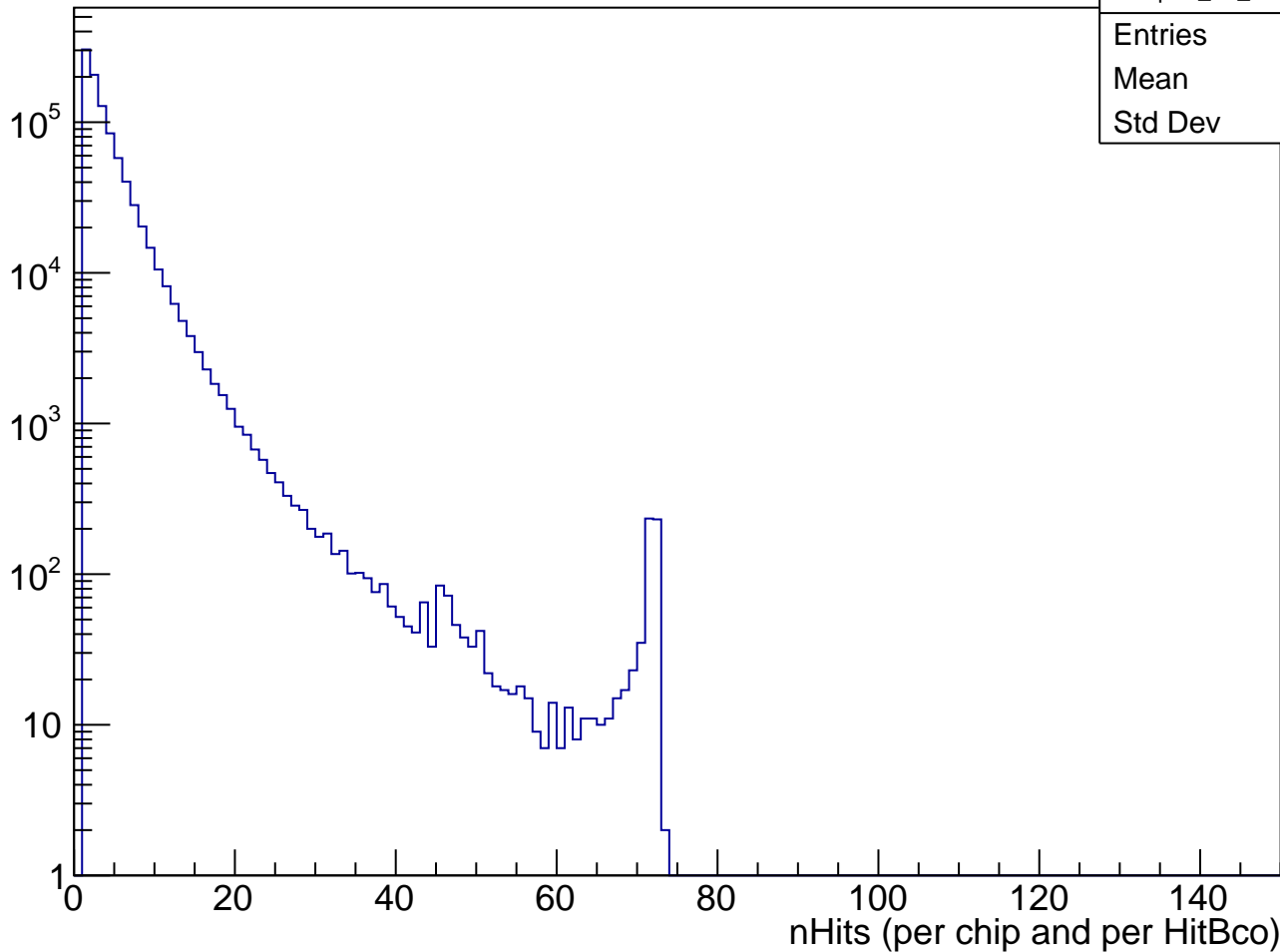
Entries 1001539

Mean 4.042

Std Dev 4.487

nChipHit_F6_Fch2_Chip20

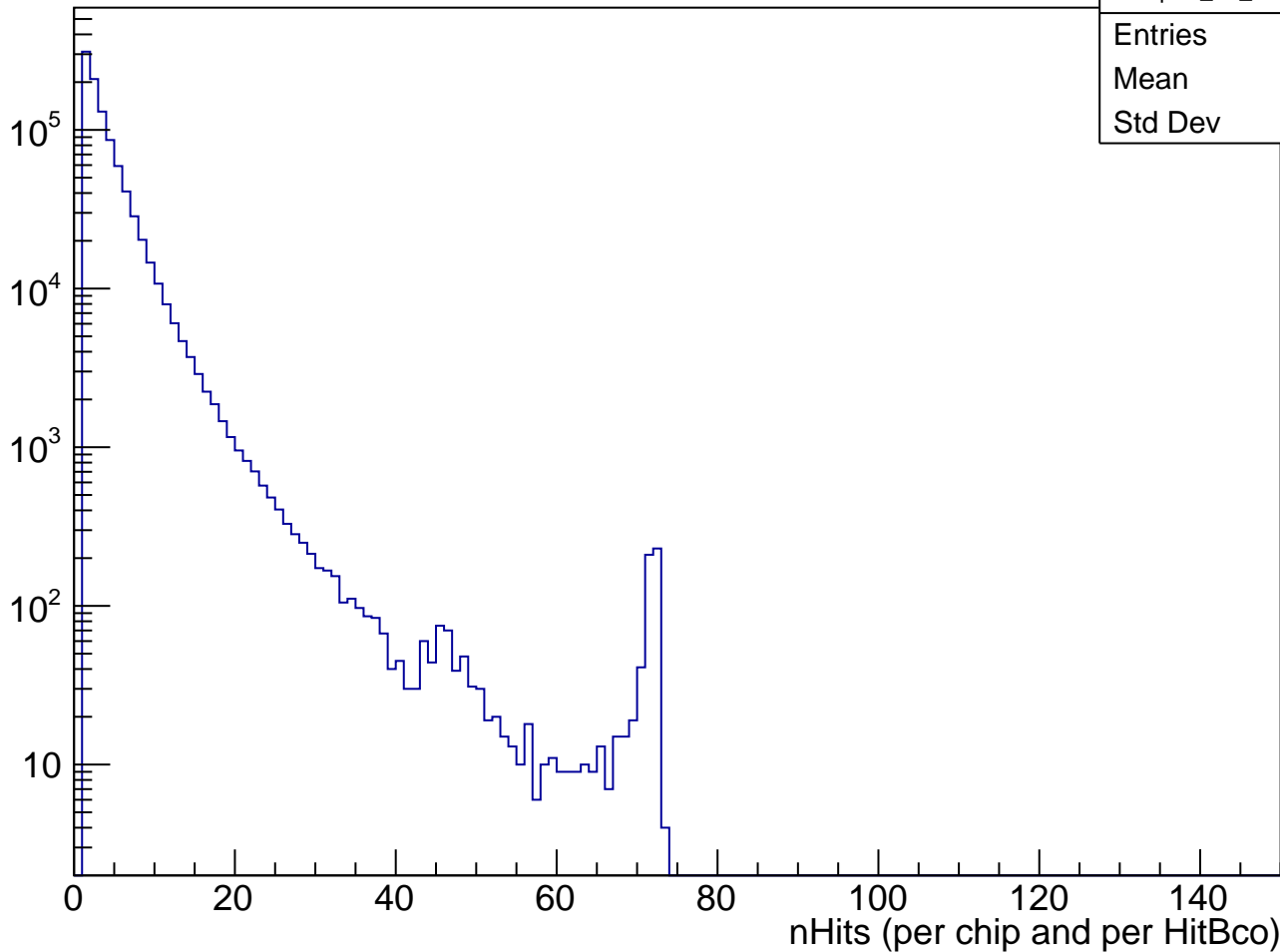
Entries



nChipHit_F6_Fch2_Chip20	
Entries	935039
Mean	3.466
Std Dev	4.01

nChipHit_F6_Fch2_Chip21

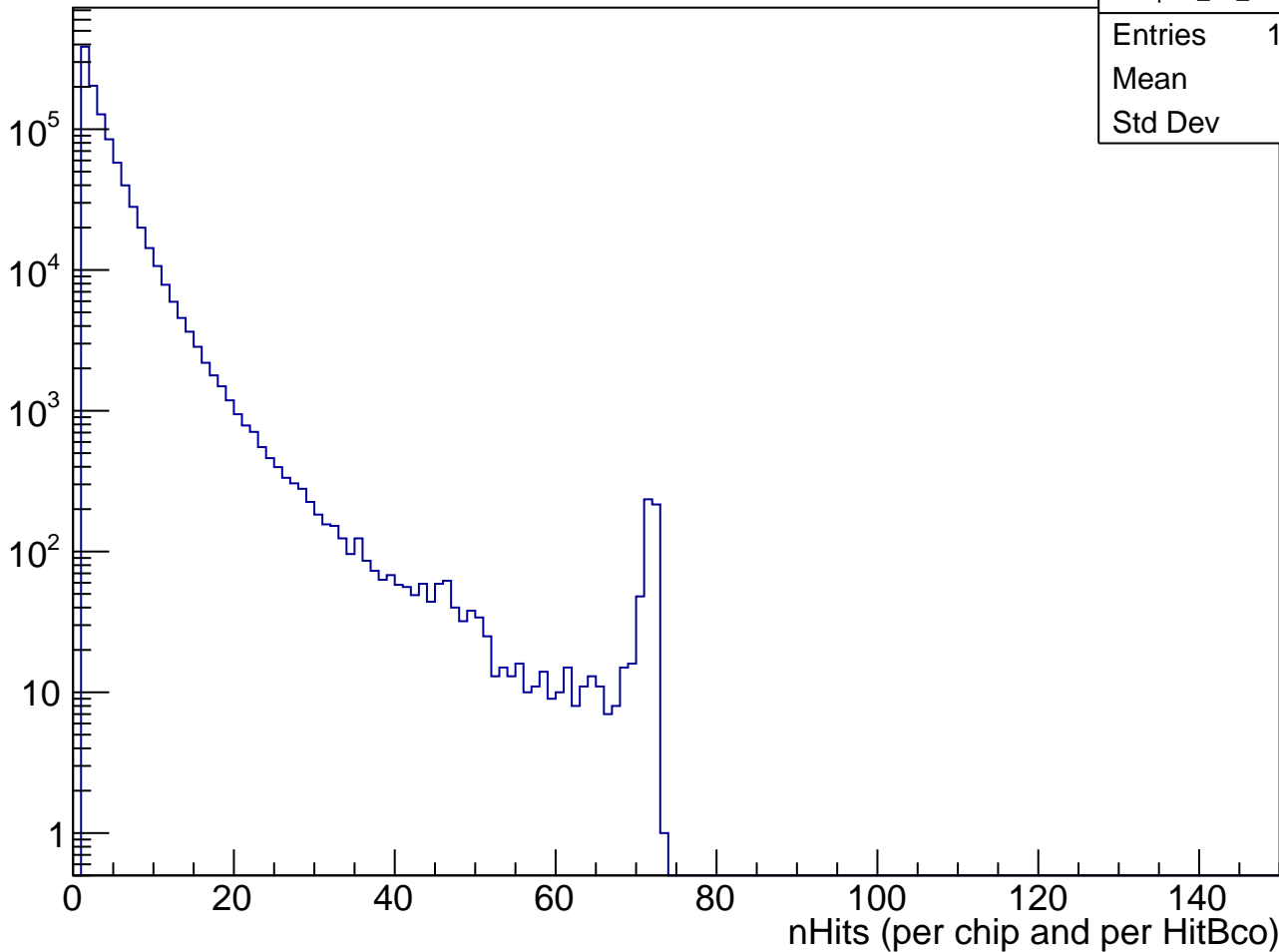
Entries



nChipHit_F6_Fch2_Chip21	
Entries	950772
Mean	3.433
Std Dev	3.931

nChipHit_F6_Fch2_Chip22

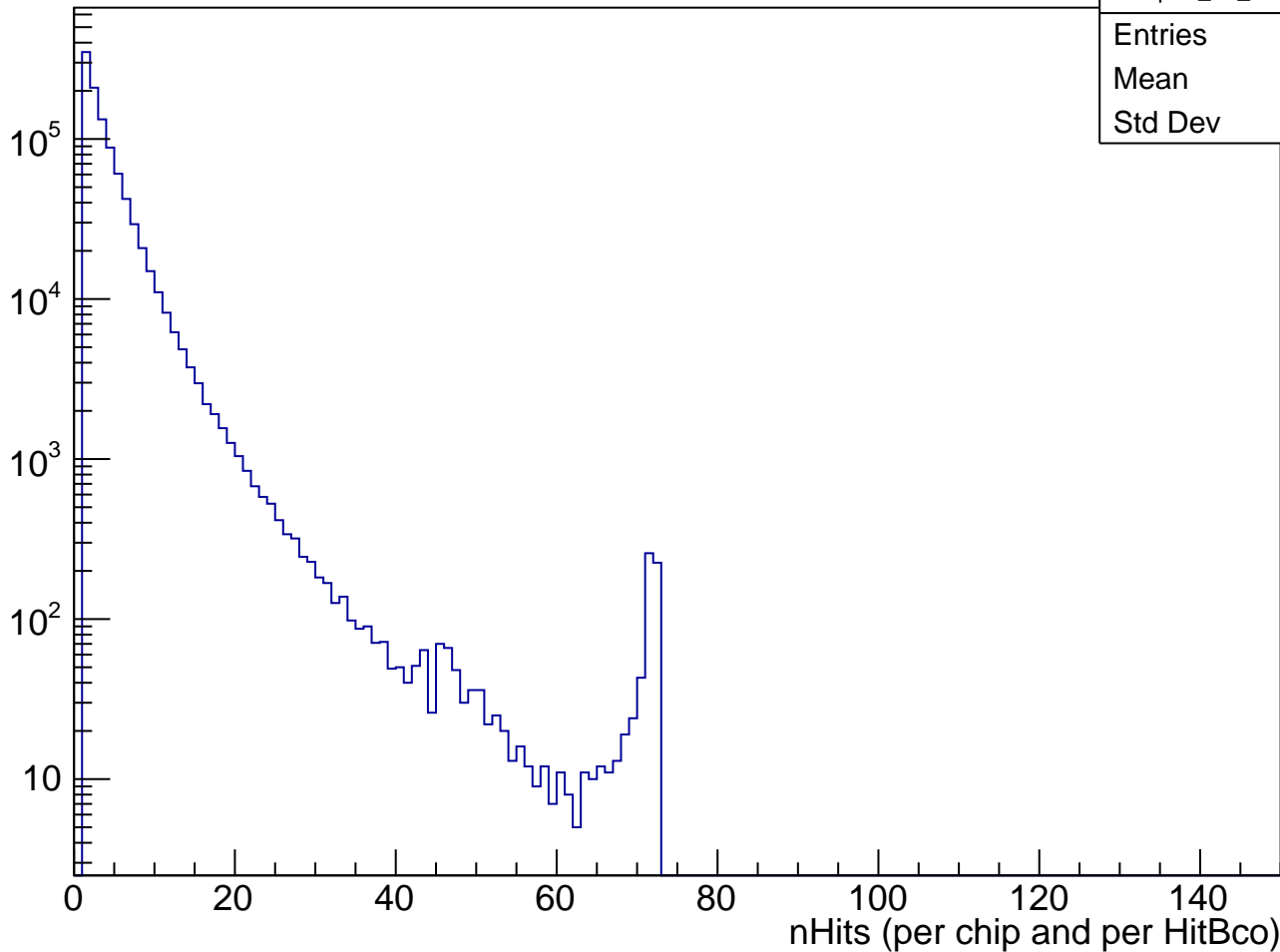
Entries



nChipHit_F6_Fch2_Chip22	
Entries	1011235
Mean	3.251
Std Dev	3.871

nChipHit_F6_Fch2_Chip23

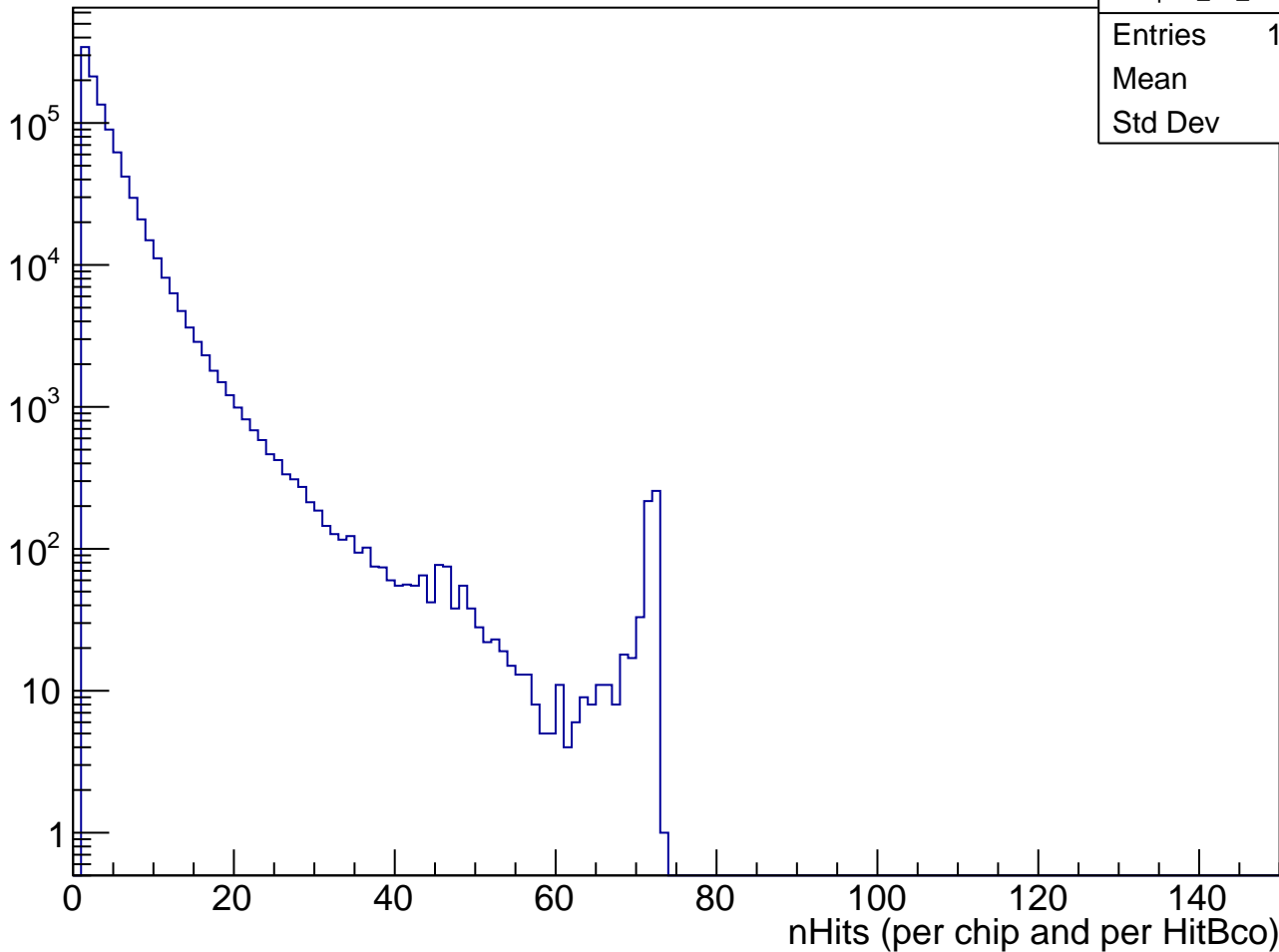
Entries



nChipHit_F6_Fch2_Chip23	
Entries	999128
Mean	3.373
Std Dev	3.929

nChipHit_F6_Fch2_Chip24

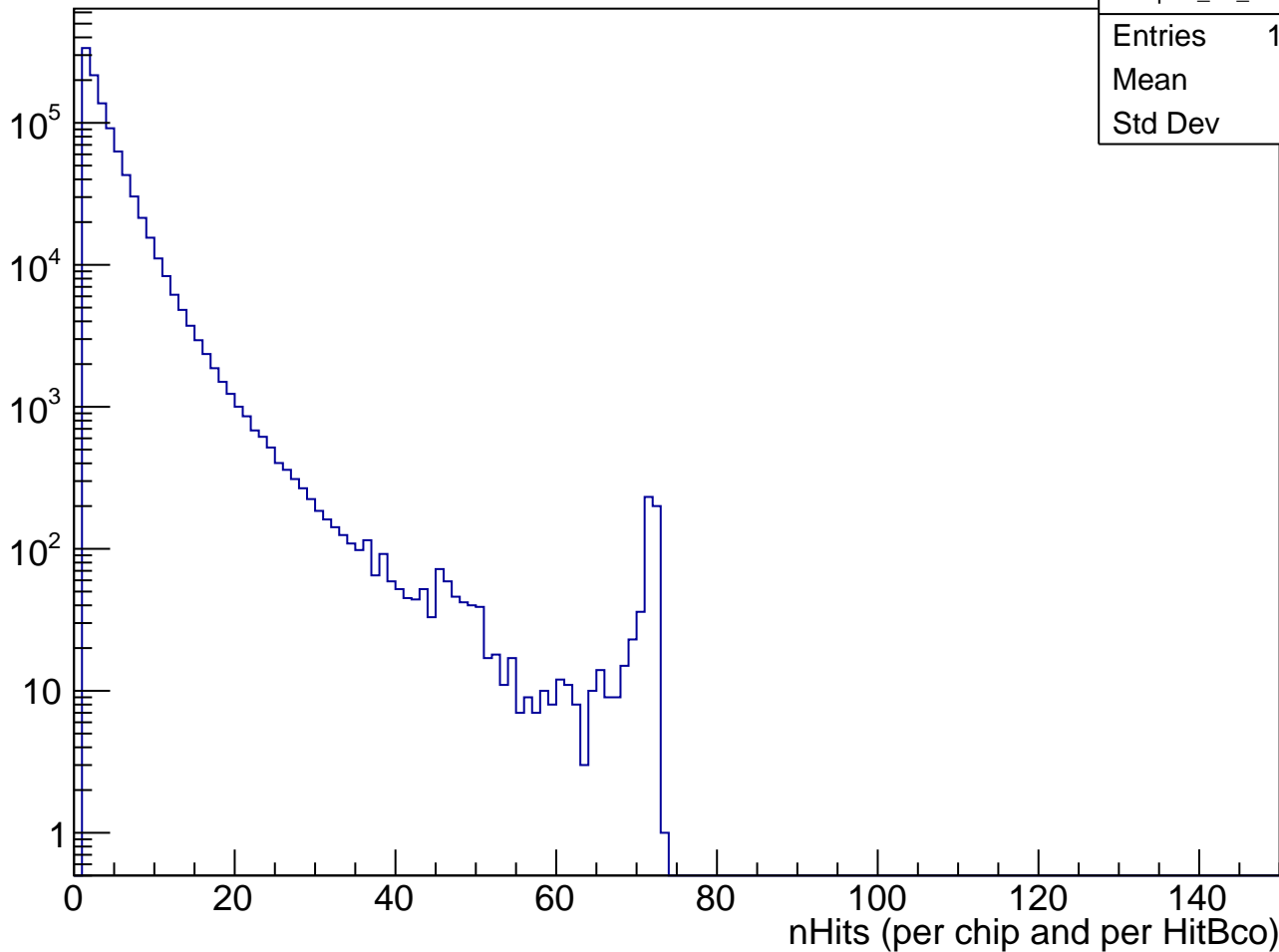
Entries



nChipHit_F6_Fch2_Chip24	
Entries	1001147
Mean	3.381
Std Dev	3.902

nChipHit_F6_Fch2_Chip25

Entries



nChipHit_F6_Fch2_Chip25

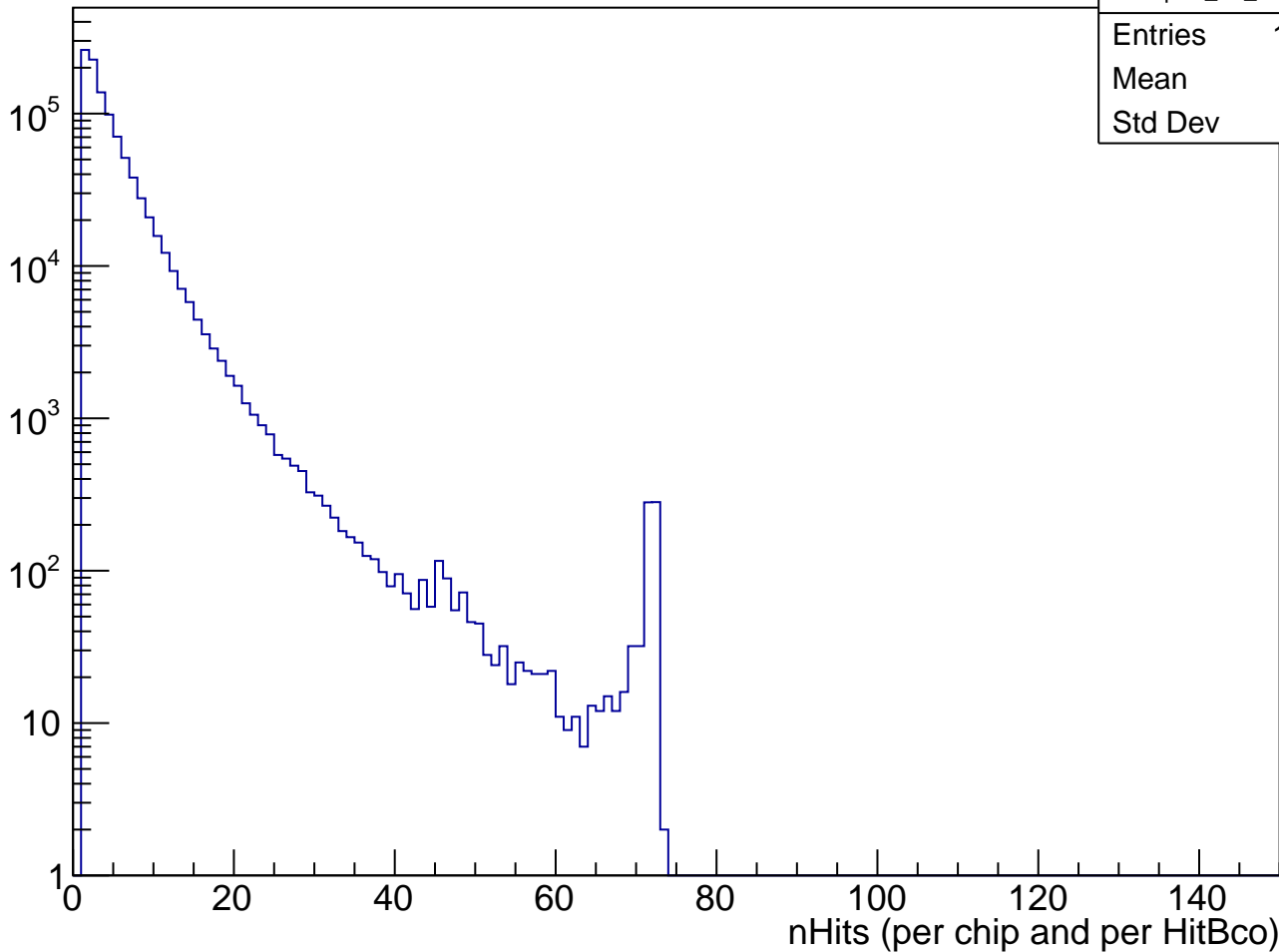
Entries 1006522

Mean 3.406

Std Dev 3.879

nChipHit_F6_Fch2_Chip3

Entries



nChipHit_F6_Fch2_Chip3

Entries 1009301

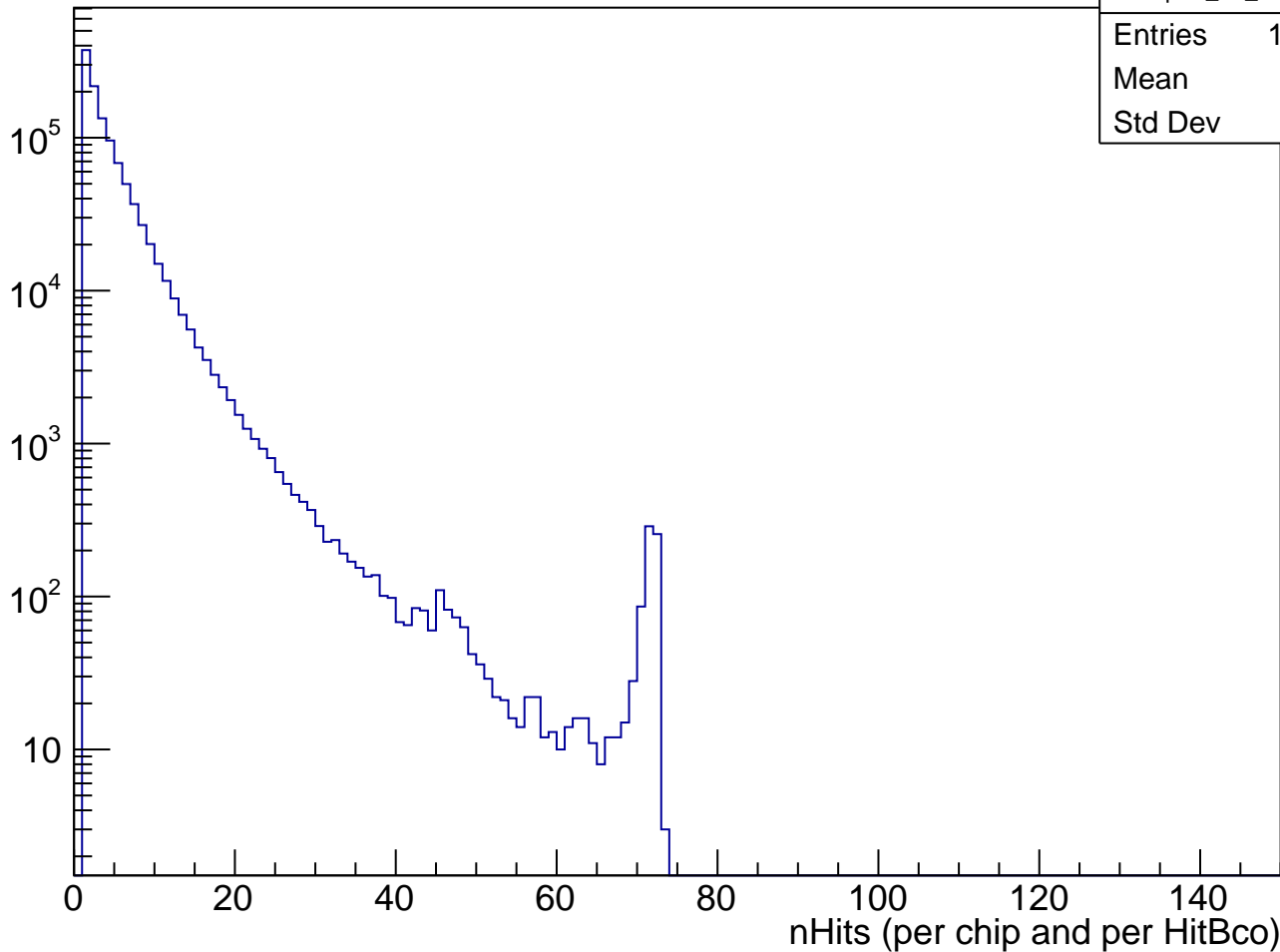
Mean 3.99

Std Dev 4.442

nHits (per chip and per HitBco)

nChipHit_F6_Fch2_Chip4

Entries



nChipHit_F6_Fch2_Chip4

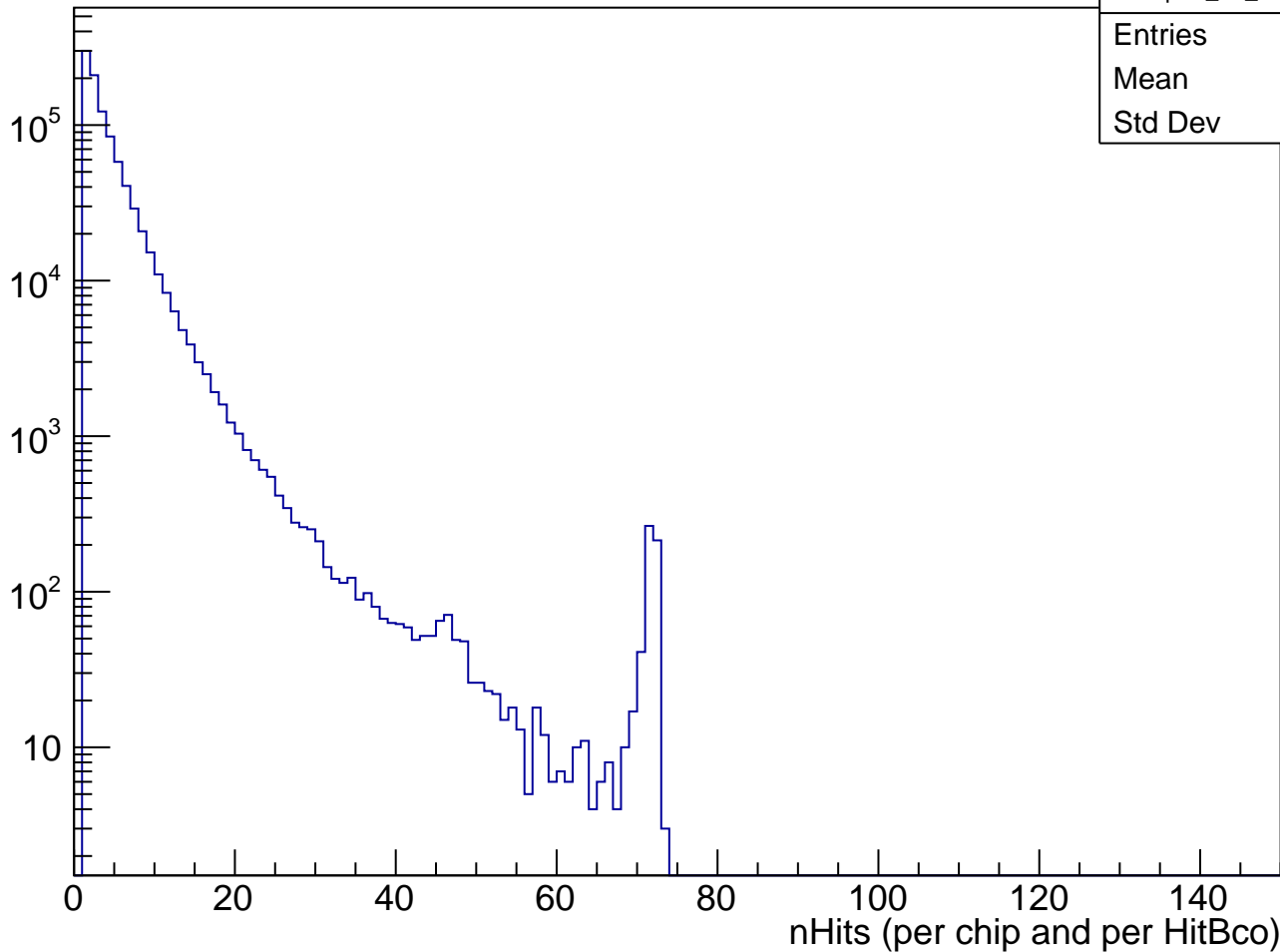
Entries 1097558

Mean 3.672

Std Dev 4.335

nChipHit_F6_Fch2_Chip5

Entries



nChipHit_F6_Fch2_Chip5

Entries 932037

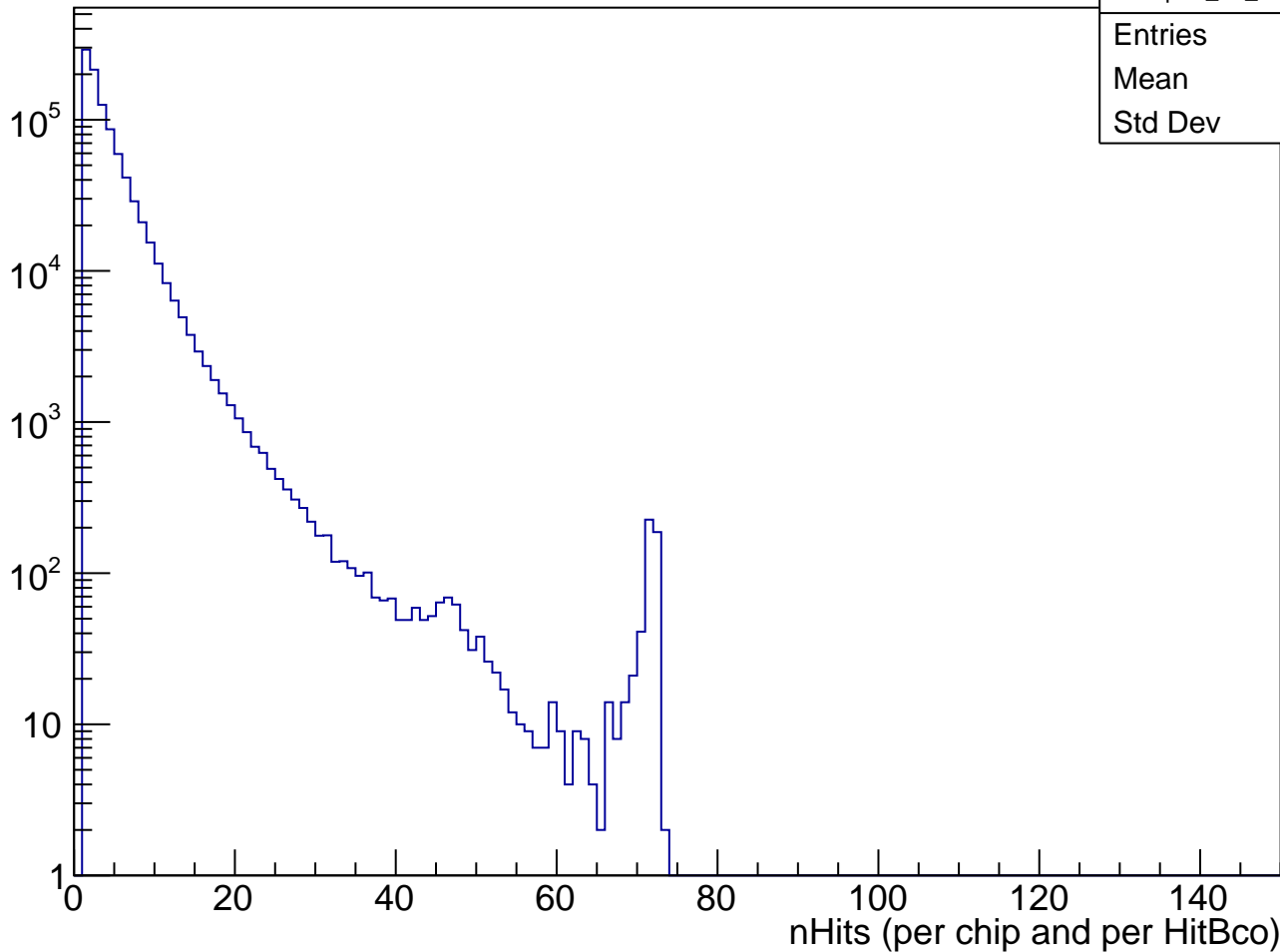
Mean 3.499

Std Dev 4.028

nHits (per chip and per HitBco)

nChipHit_F6_Fch2_Chip6

Entries



nChipHit_F6_Fch2_Chip6

Entries 935974

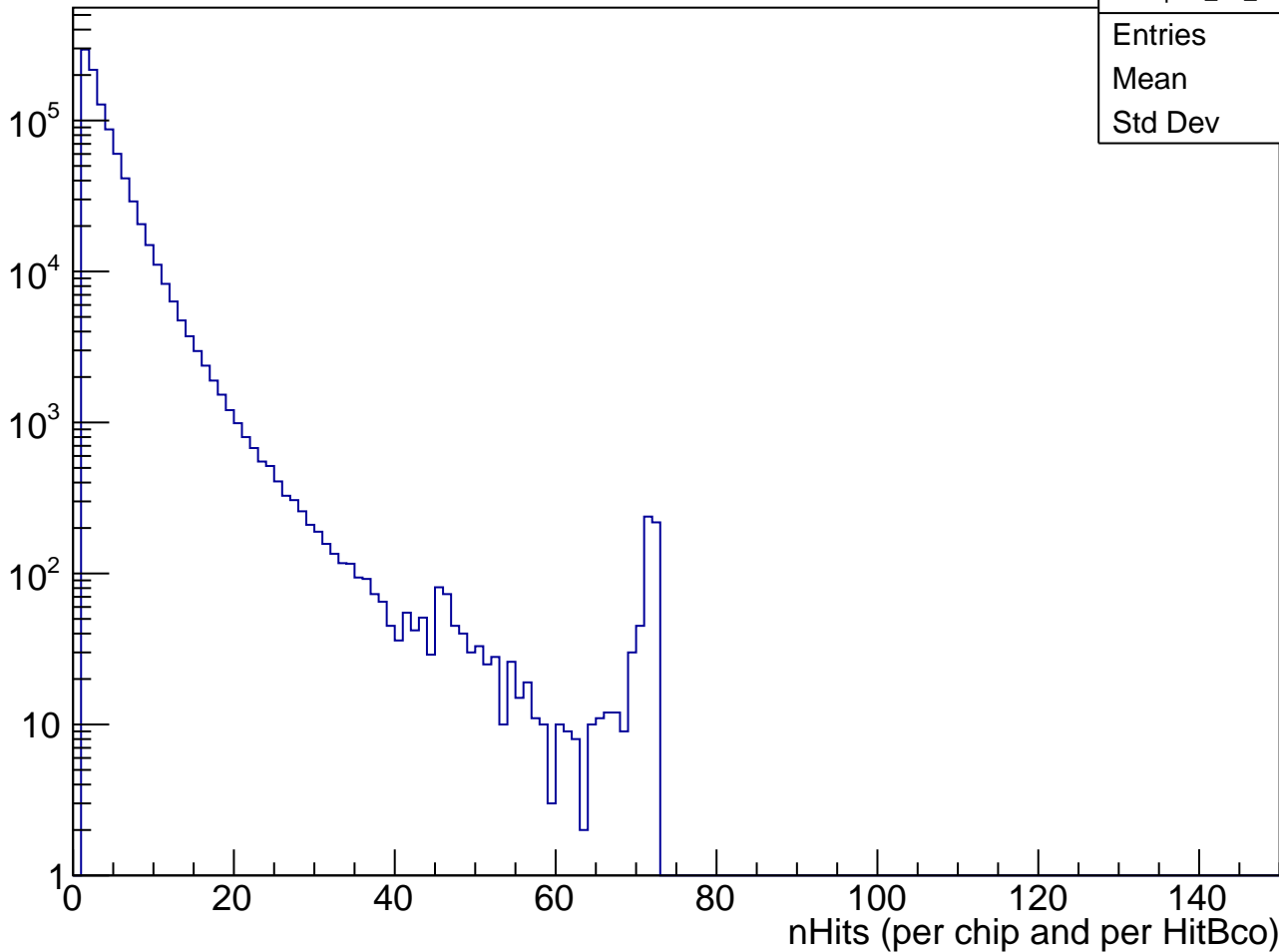
Mean 3.514

Std Dev 3.978

nHits (per chip and per HitBco)

nChipHit_F6_Fch2_Chip7

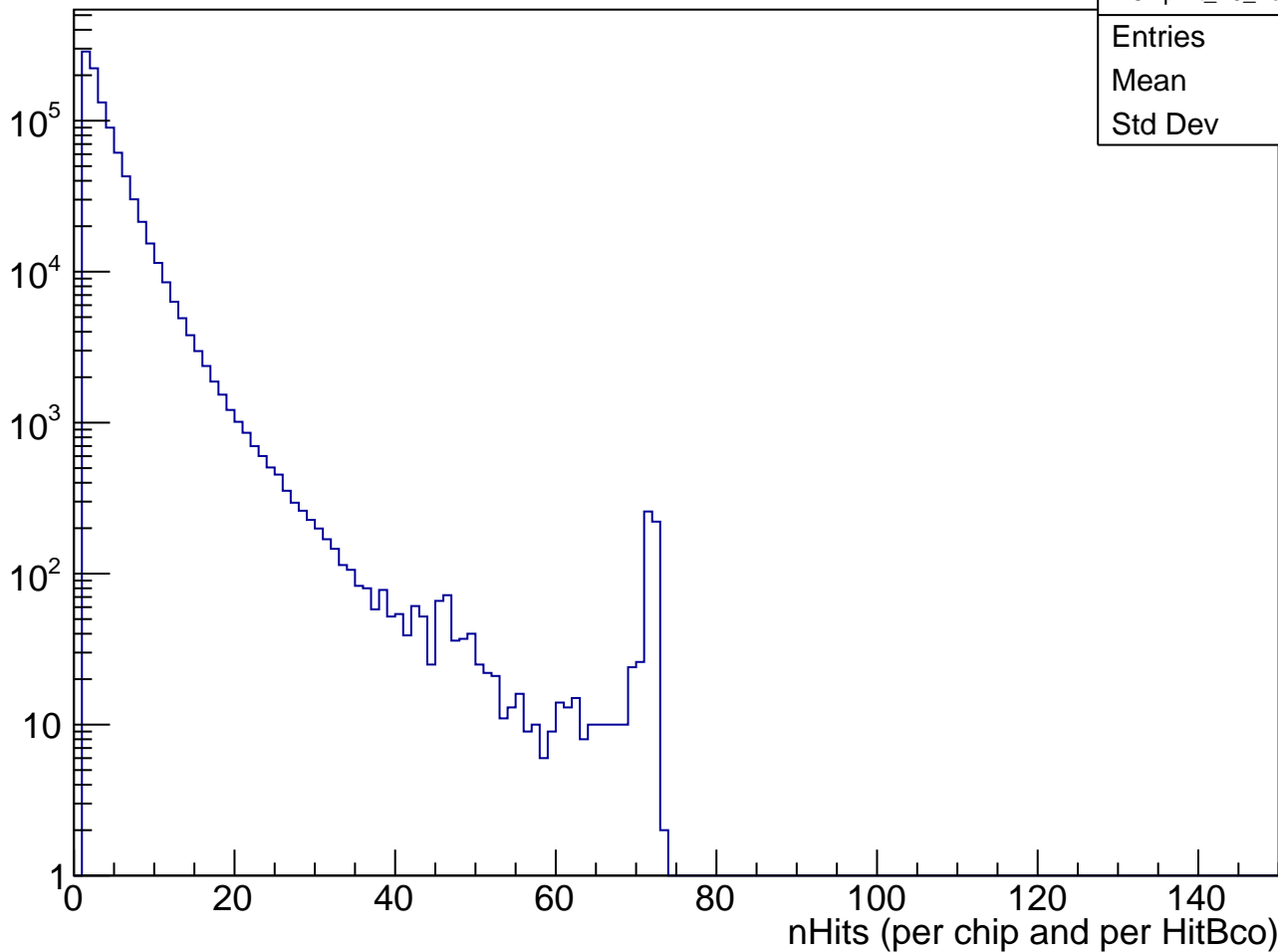
Entries



nChipHit_F6_Fch2_Chip7	
Entries	944206
Mean	3.491
Std Dev	3.975

nChipHit_F6_Fch2_Chip8

Entries



nChipHit_F6_Fch2_Chip8

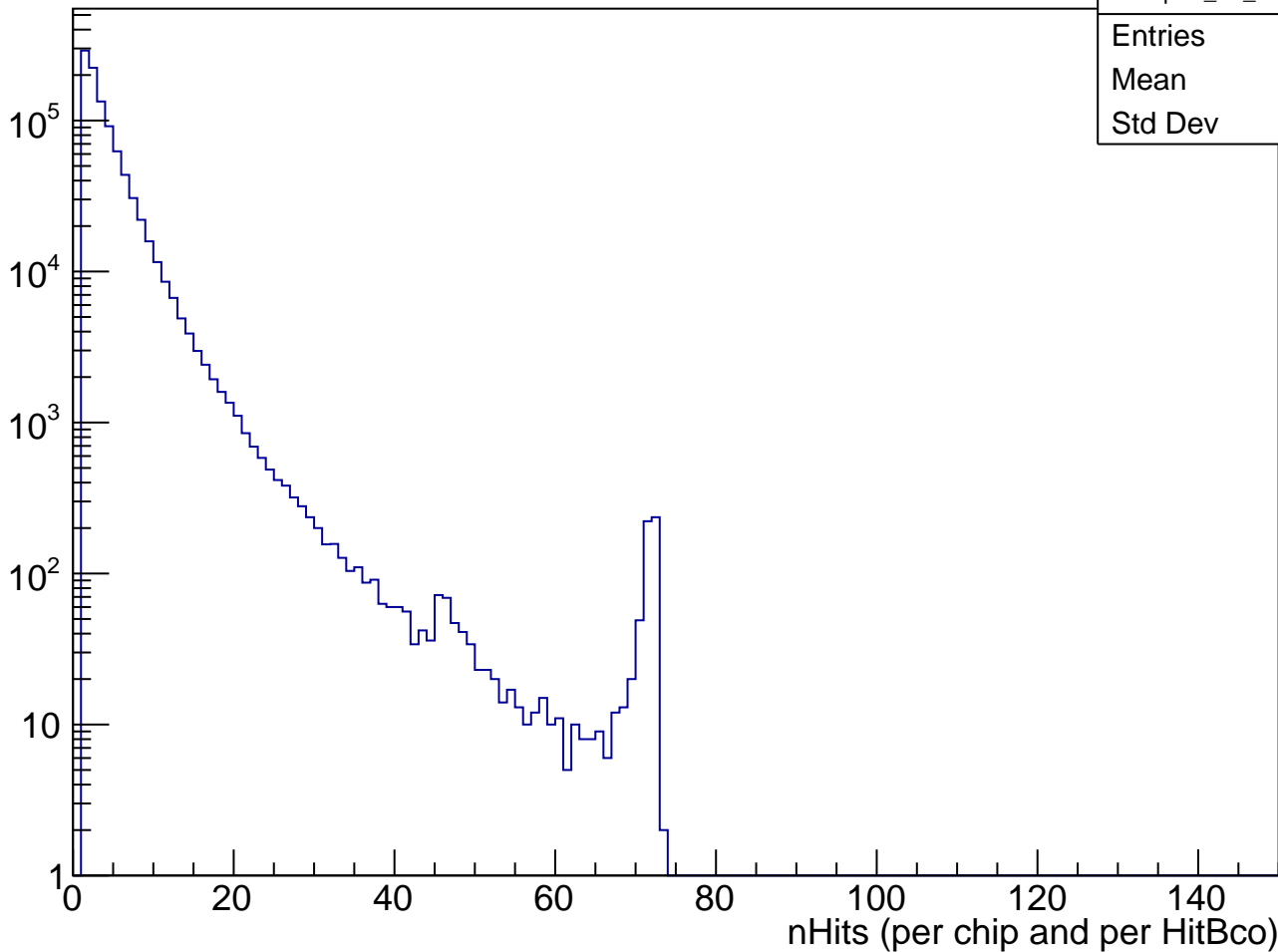
Entries 955285

Mean 3.527

Std Dev 3.967

nChipHit_F6_Fch2_Chip9

Entries



nChipHit_F6_Fch2_Chip9

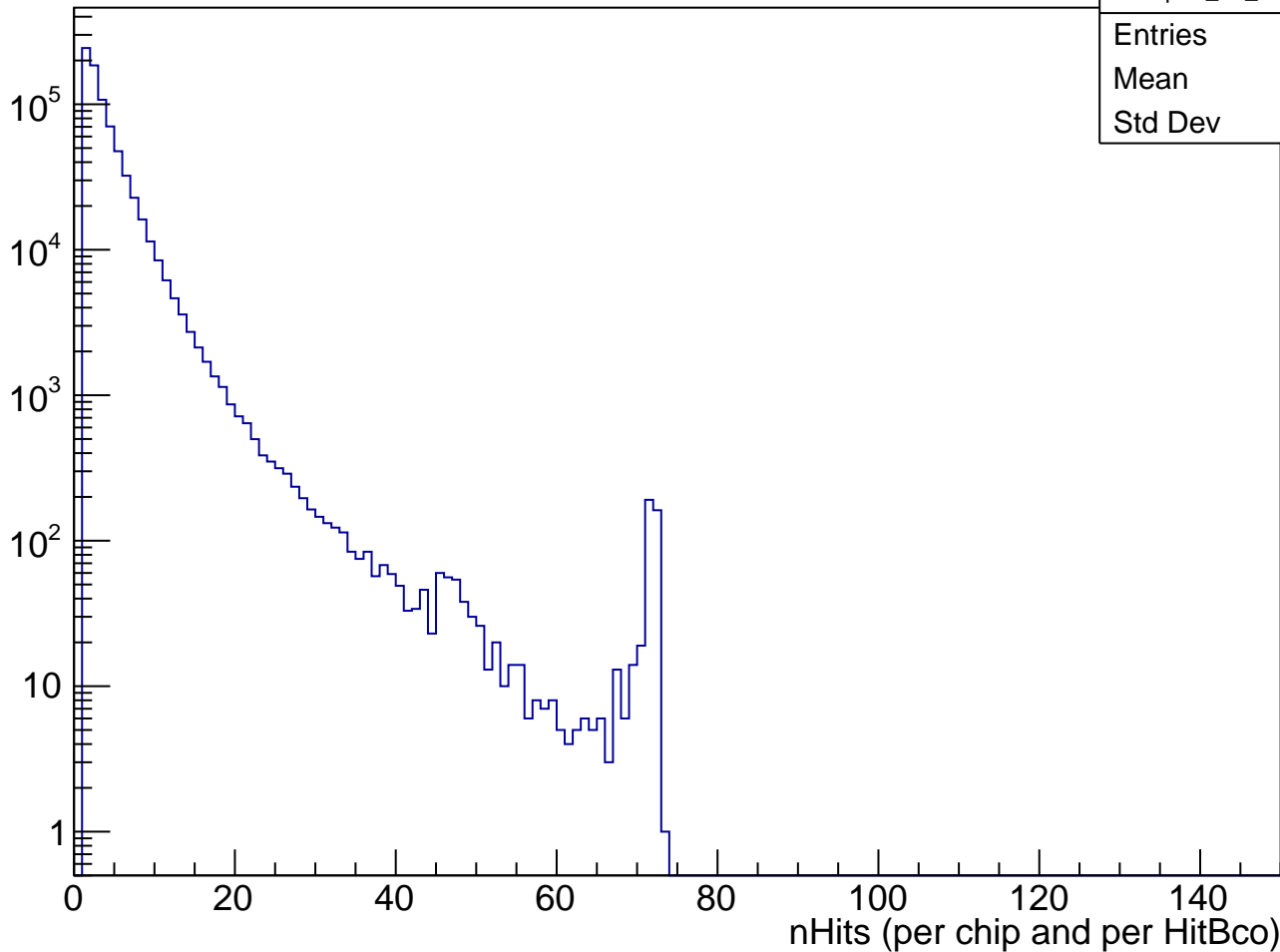
Entries 967762

Mean 3.541

Std Dev 3.977

nChipHit_F6_Fch3_Chip0

Entries



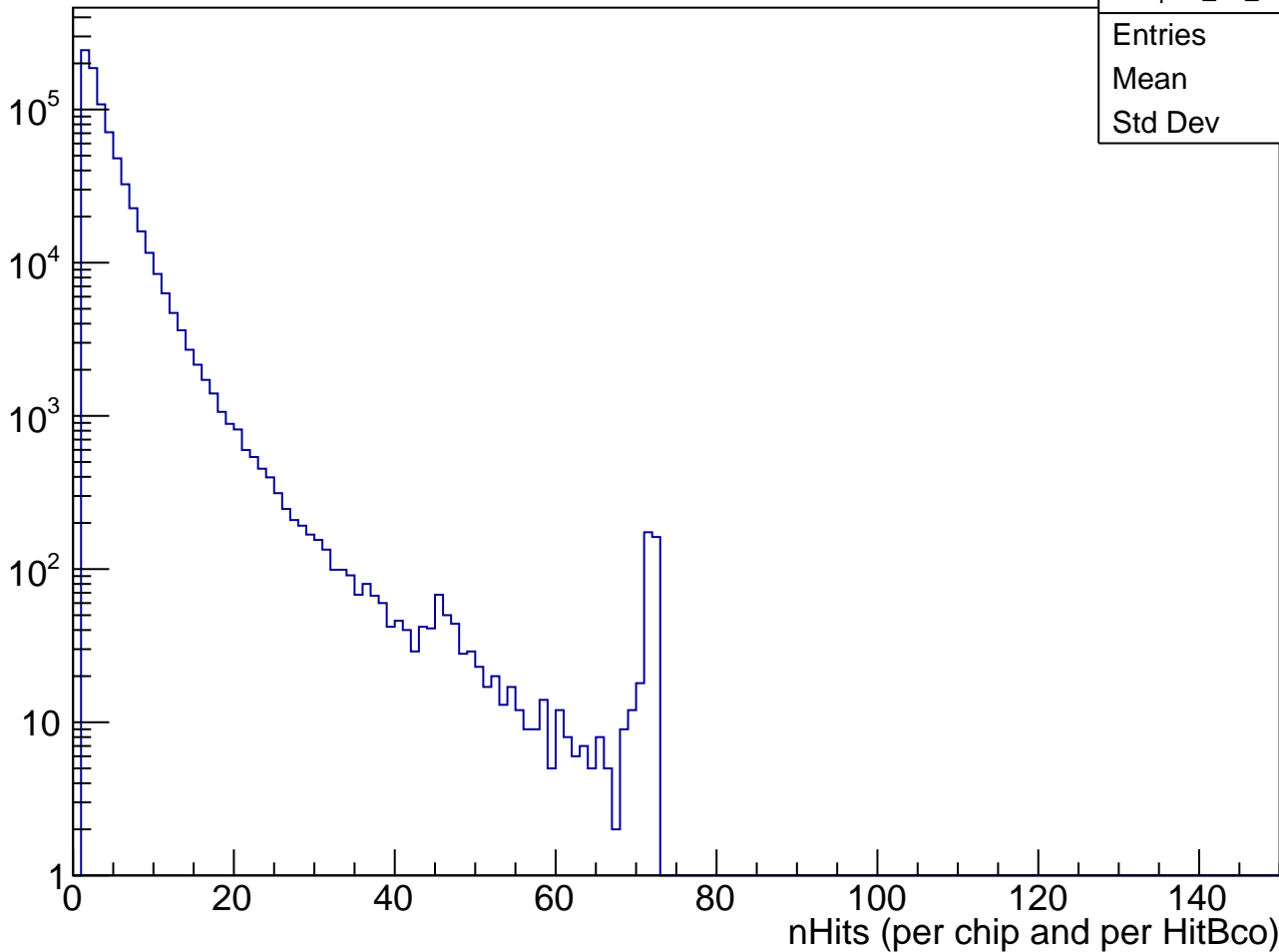
nChipHit_F6_Fch3_Chip0

Entries	775178
Mean	3.391
Std Dev	3.853

nHits (per chip and per HitBco)

nChipHit_F6_Fch3_Chip1

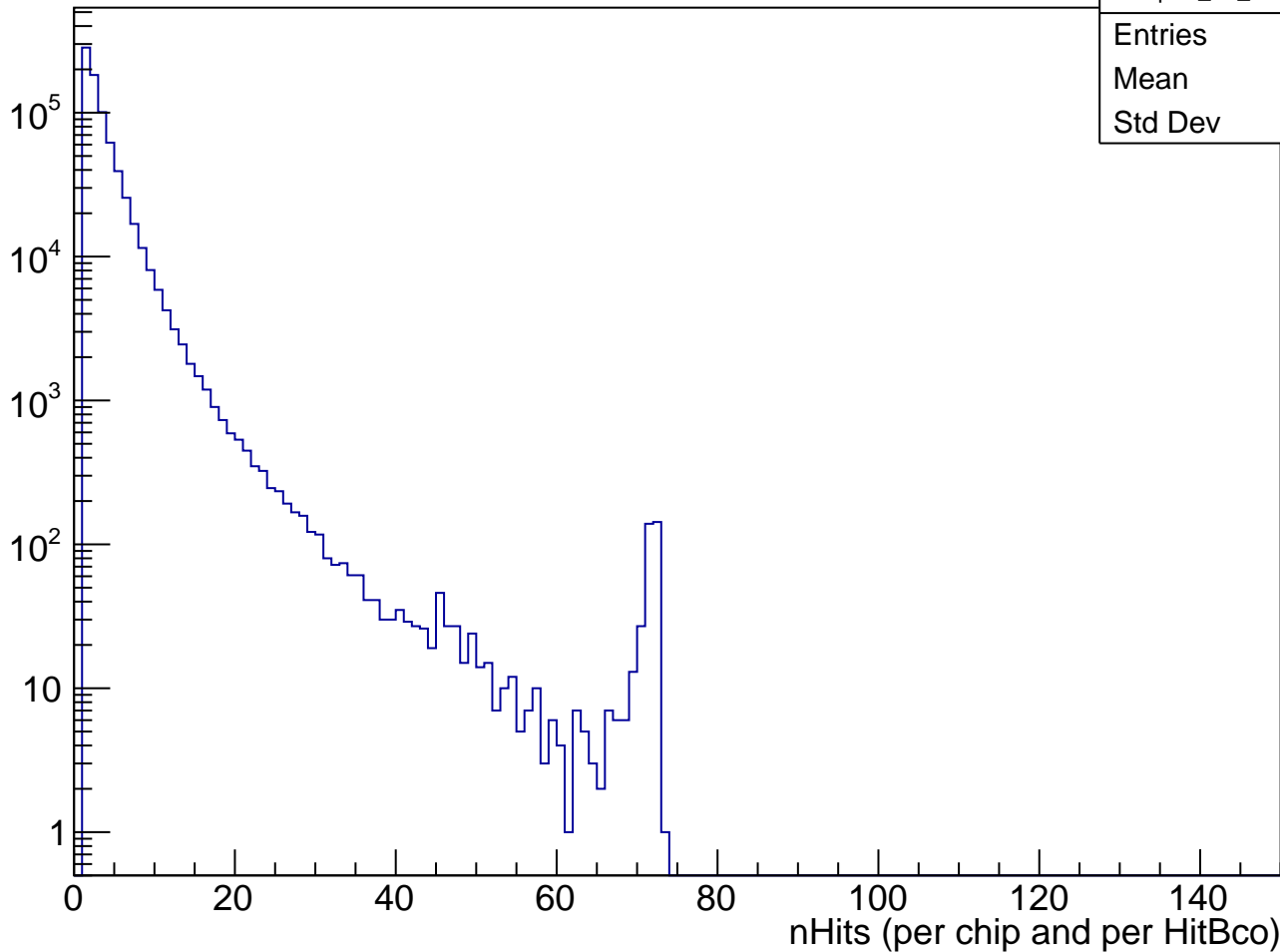
Entries



nChipHit_F6_Fch3_Chip1	
Entries	779276
Mean	3.392
Std Dev	3.837

nChipHit_F6_Fch3_Chip10

Entries

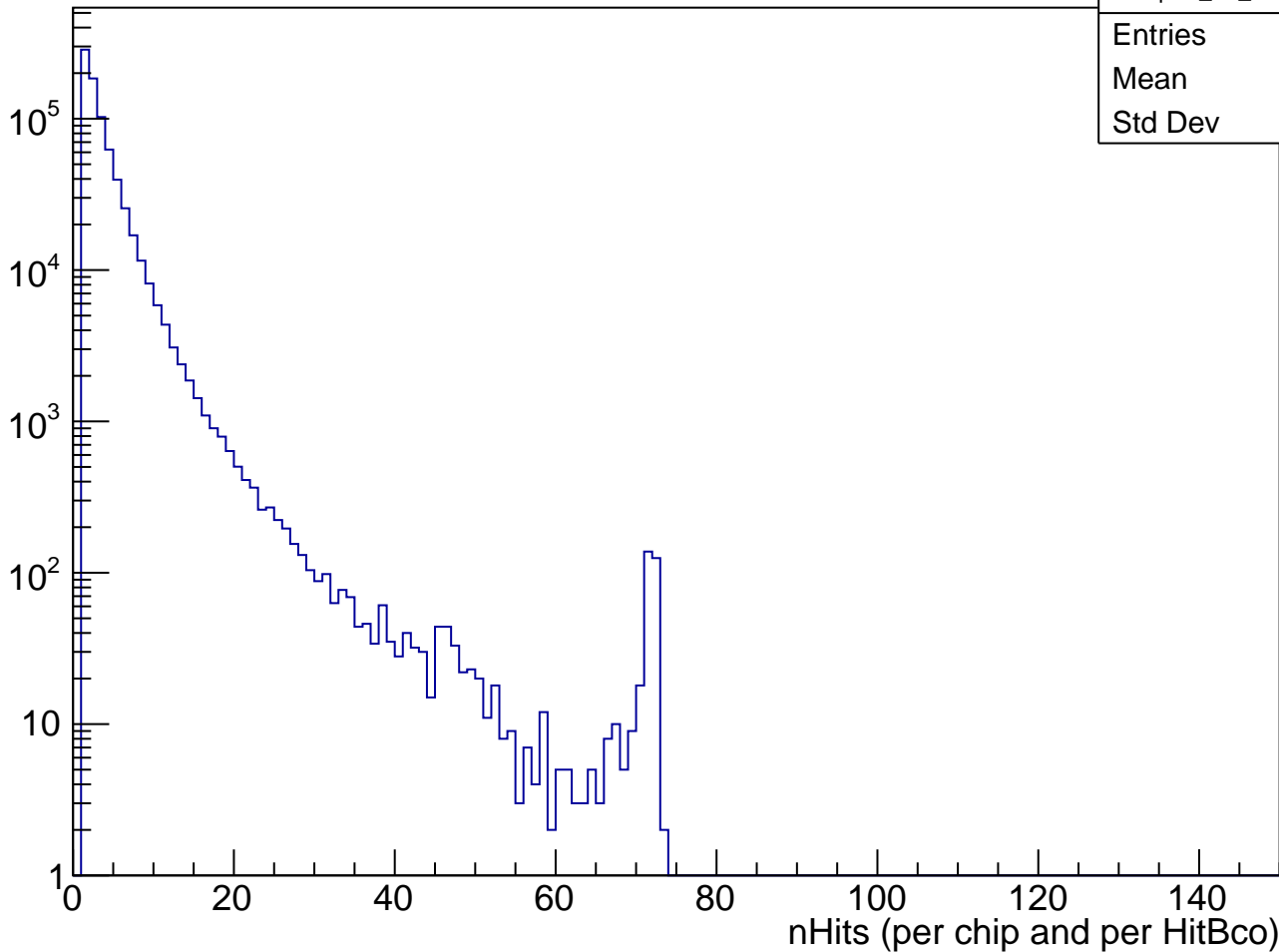


nChipHit_F6_Fch3_Chip10	
Entries	756799
Mean	2.959
Std Dev	3.424

nHits (per chip and per HitBco)

nChipHit_F6_Fch3_Chip11

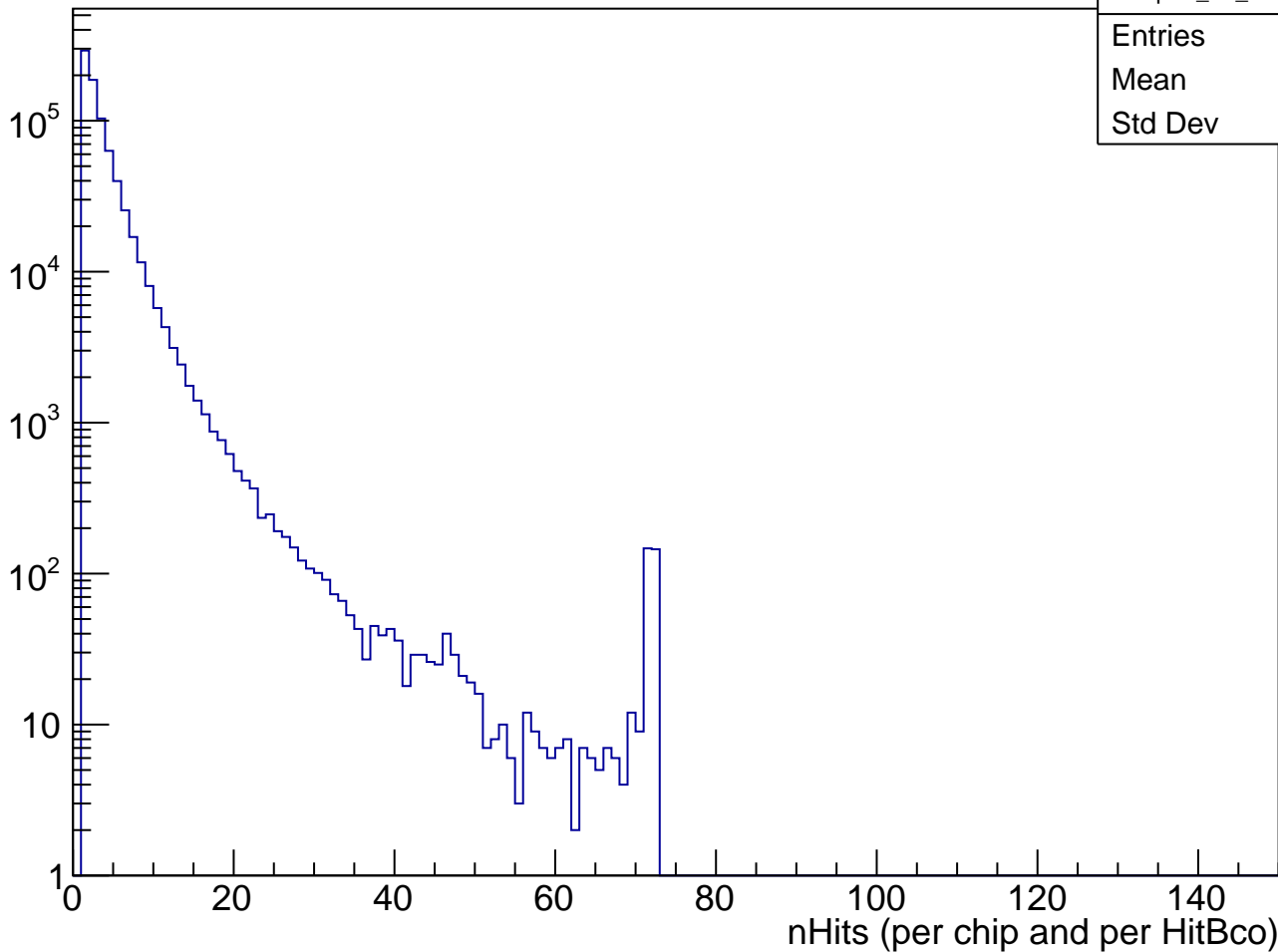
Entries



nChipHit_F6_Fch3_Chip11	
Entries	763569
Mean	2.951
Std Dev	3.395

nChipHit_F6_Fch3_Chip12

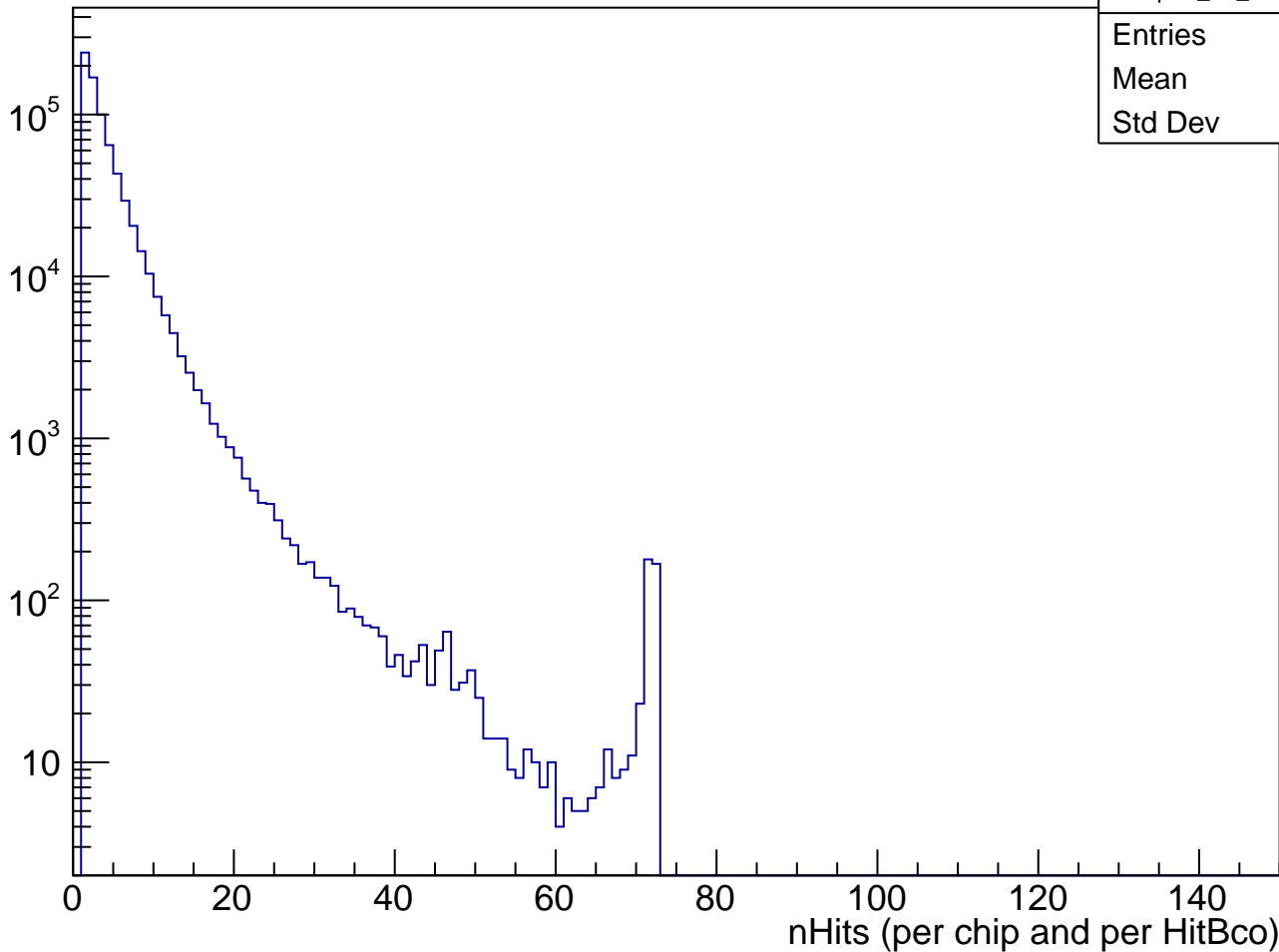
Entries



nChipHit_F6_Fch3_Chip12	
Entries	772784
Mean	2.926
Std Dev	3.36

nChipHit_F6_Fch3_Chip13

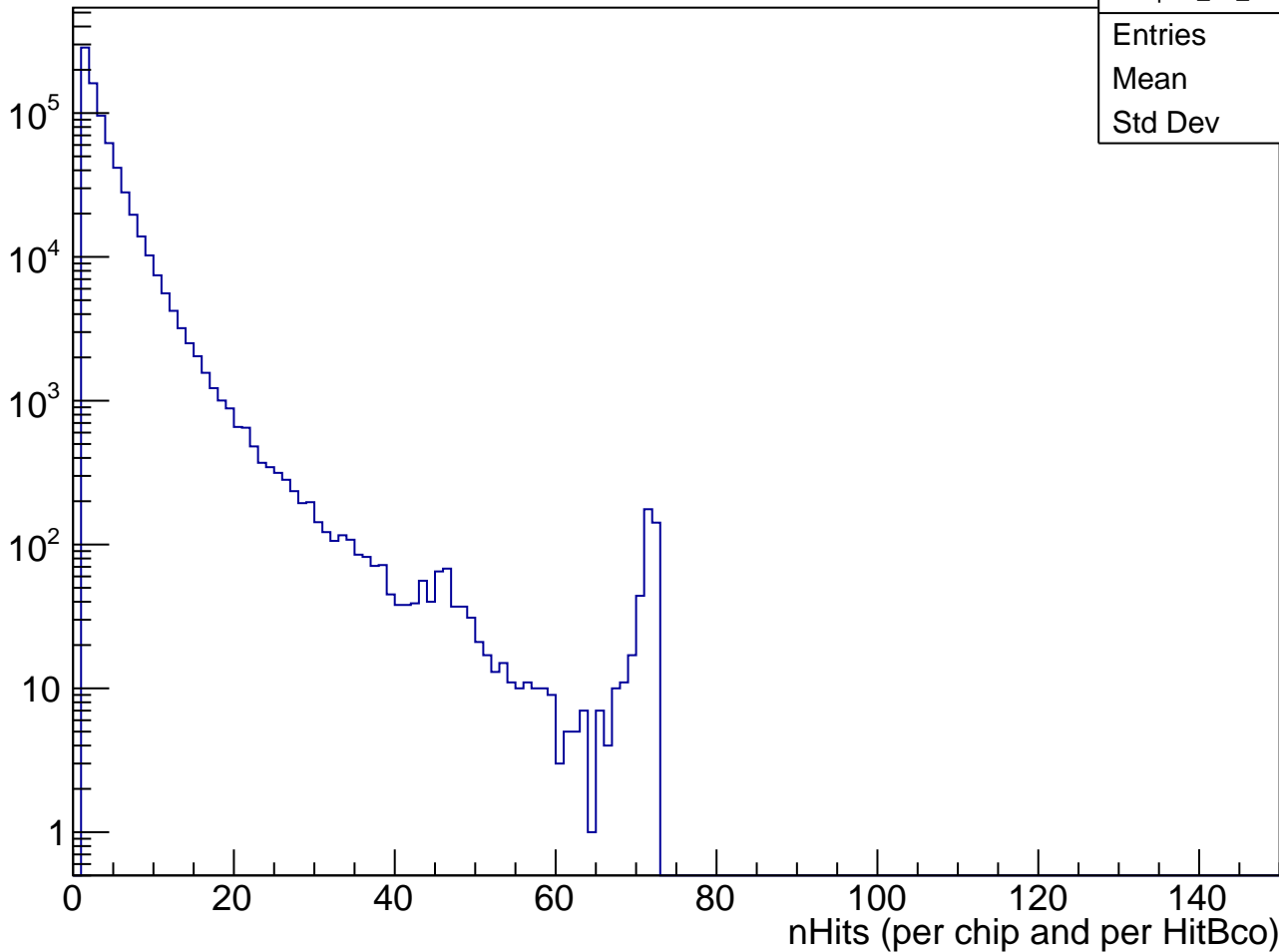
Entries



nChipHit_F6_Fch3_Chip13	
Entries	728860
Mean	3.342
Std Dev	3.887

nChipHit_F6_Fch3_Chip14

Entries

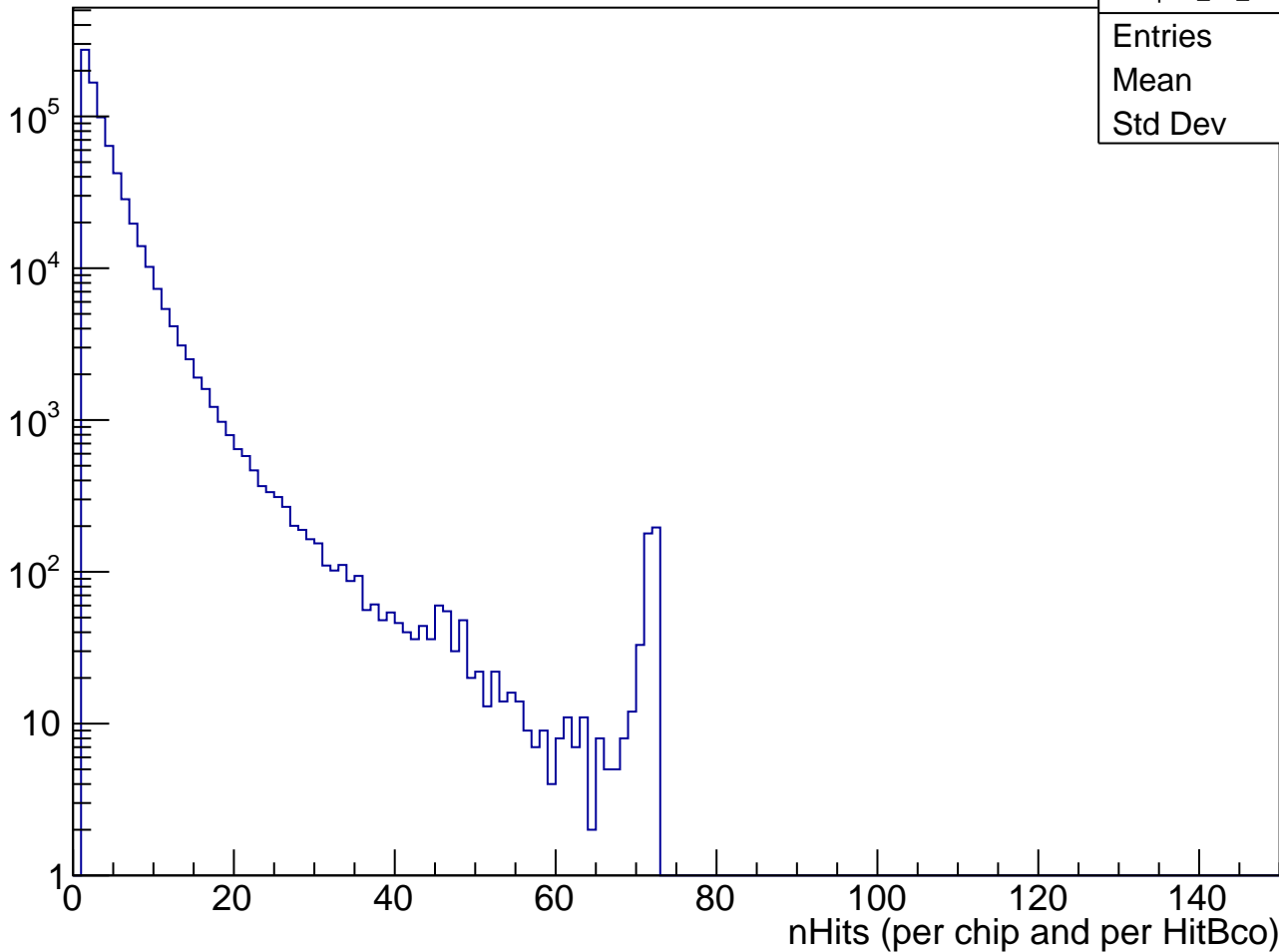


nChipHit_F6_Fch3_Chip14	
Entries	753316
Mean	3.2
Std Dev	3.866

nHits (per chip and per HitBco)

nChipHit_F6_Fch3_Chip15

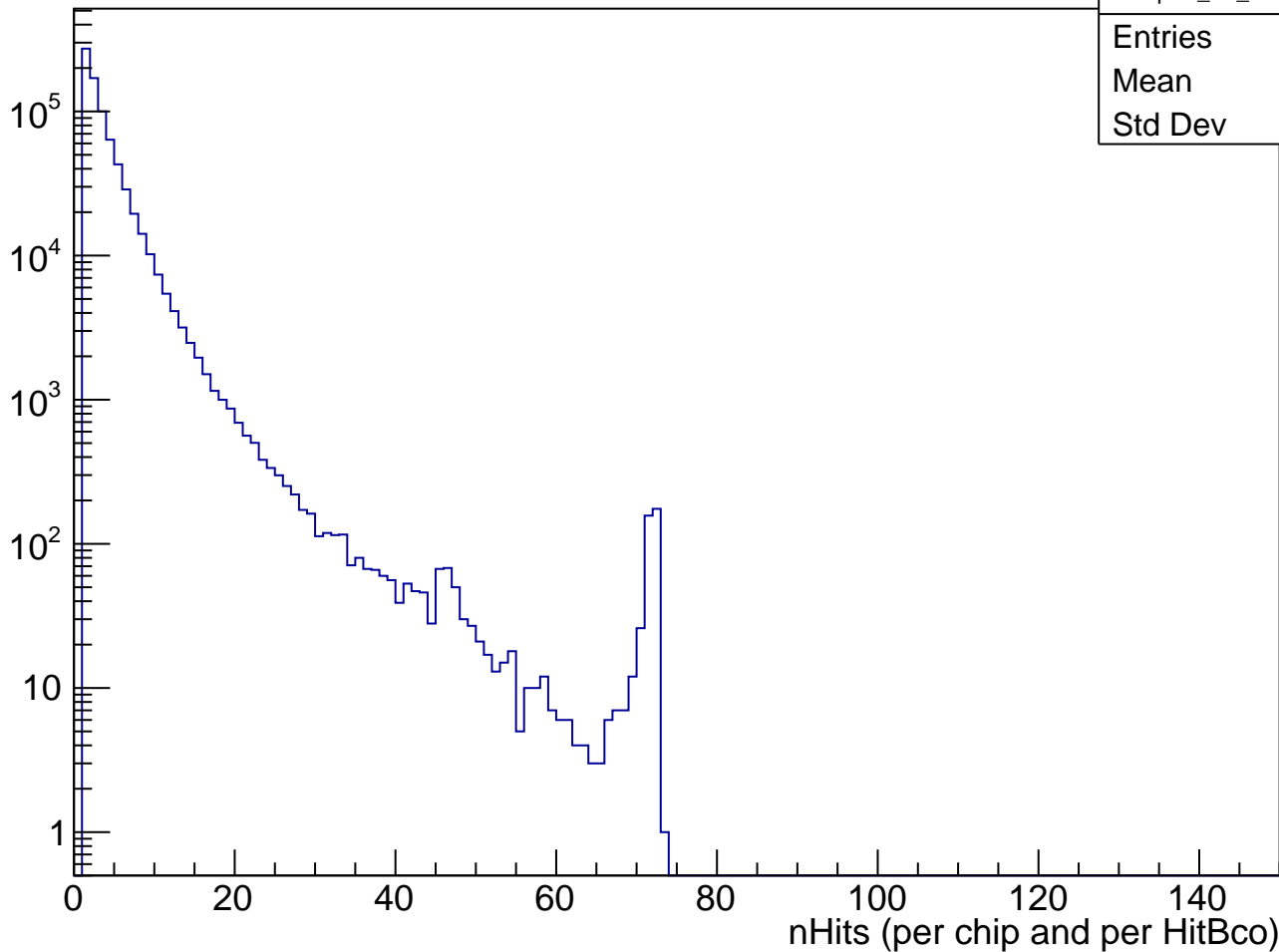
Entries



nChipHit_F6_Fch3_Chip15	
Entries	753271
Mean	3.212
Std Dev	3.848

nChipHit_F6_Fch3_Chip16

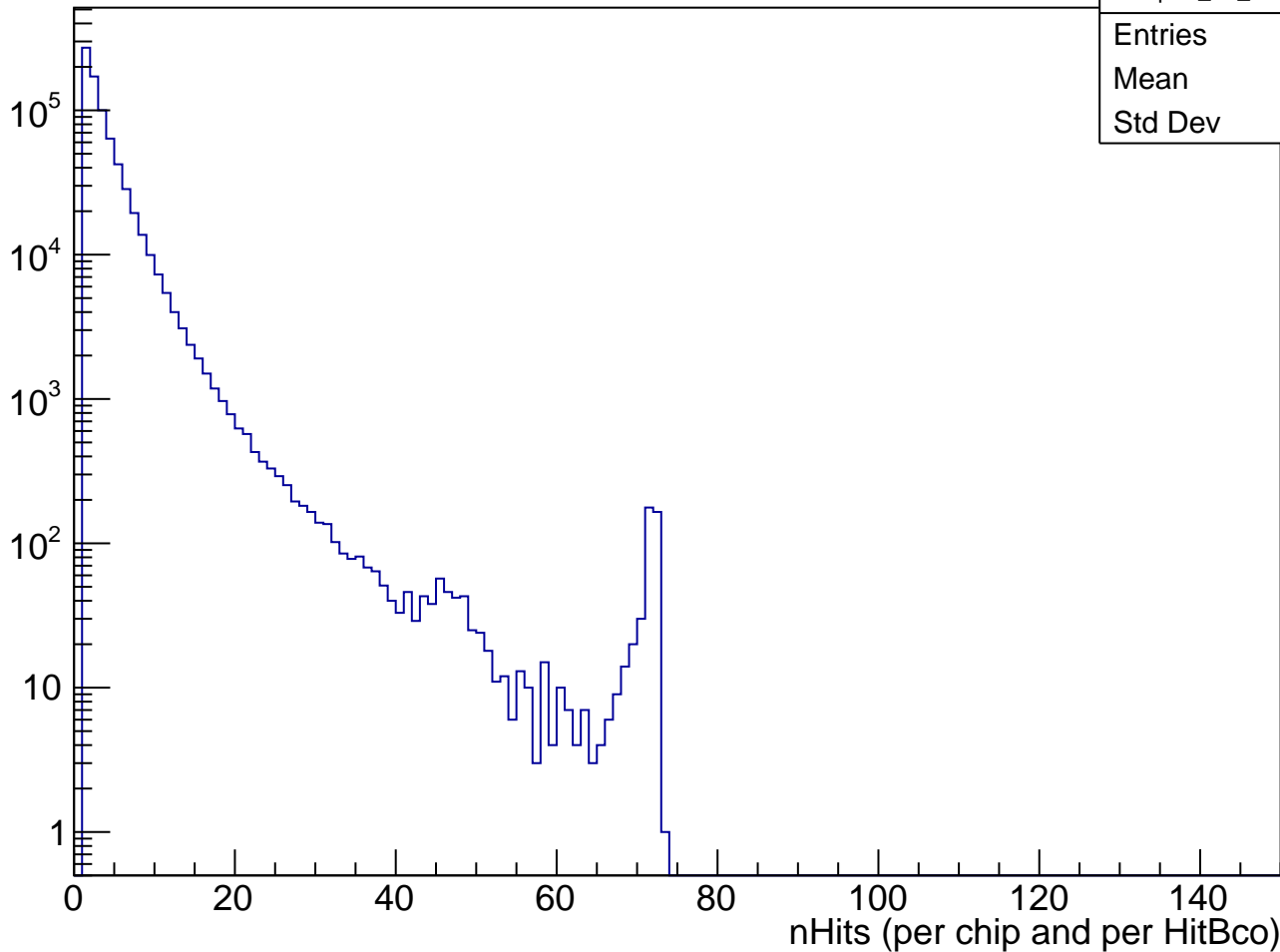
Entries



nChipHit_F6_Fch3_Chip16	
Entries	756821
Mean	3.214
Std Dev	3.803

nChipHit_F6_Fch3_Chip17

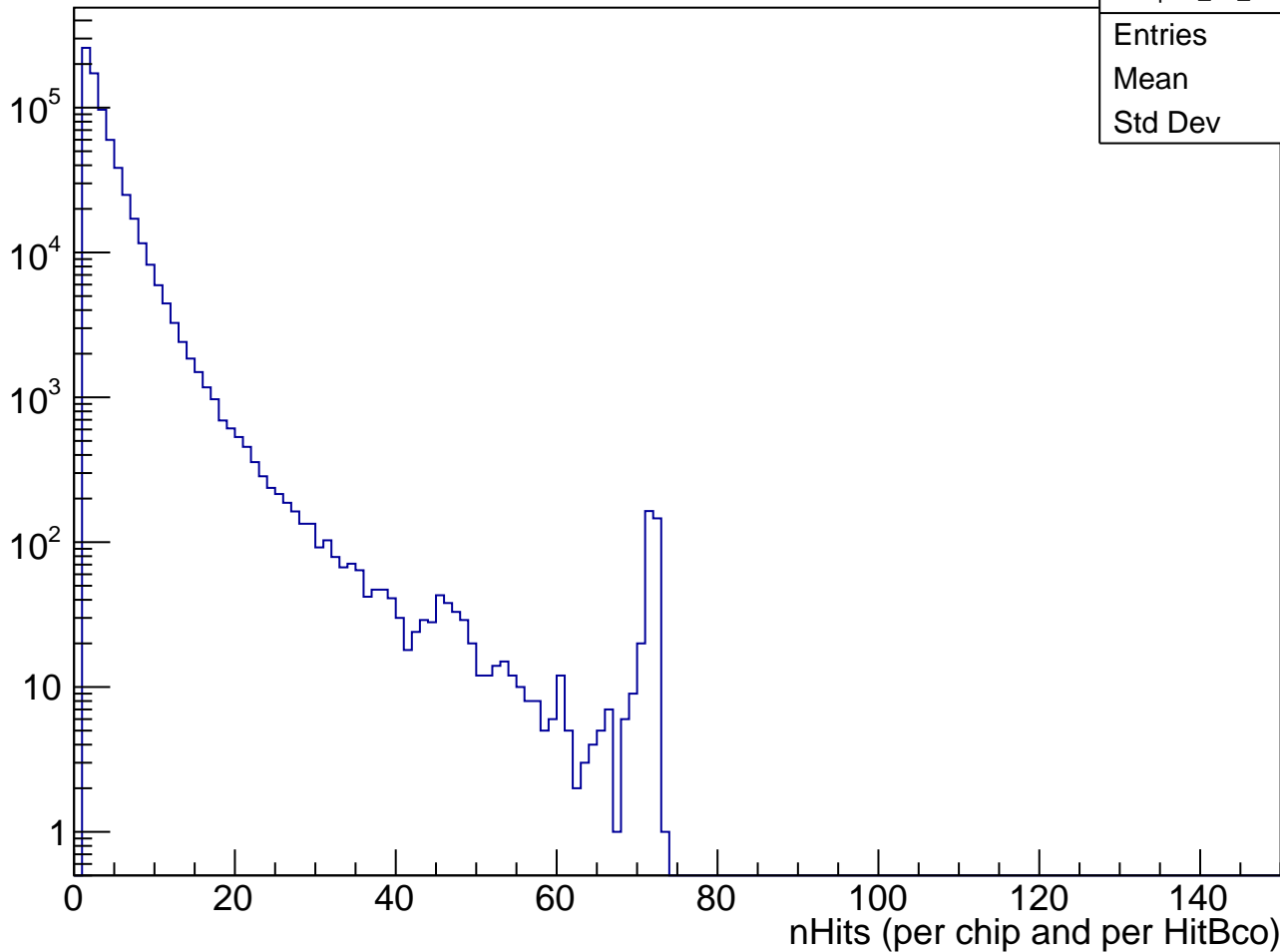
Entries



nChipHit_F6_Fch3_Chip17	
Entries	754397
Mean	3.194
Std Dev	3.786

nChipHit_F6_Fch3_Chip18

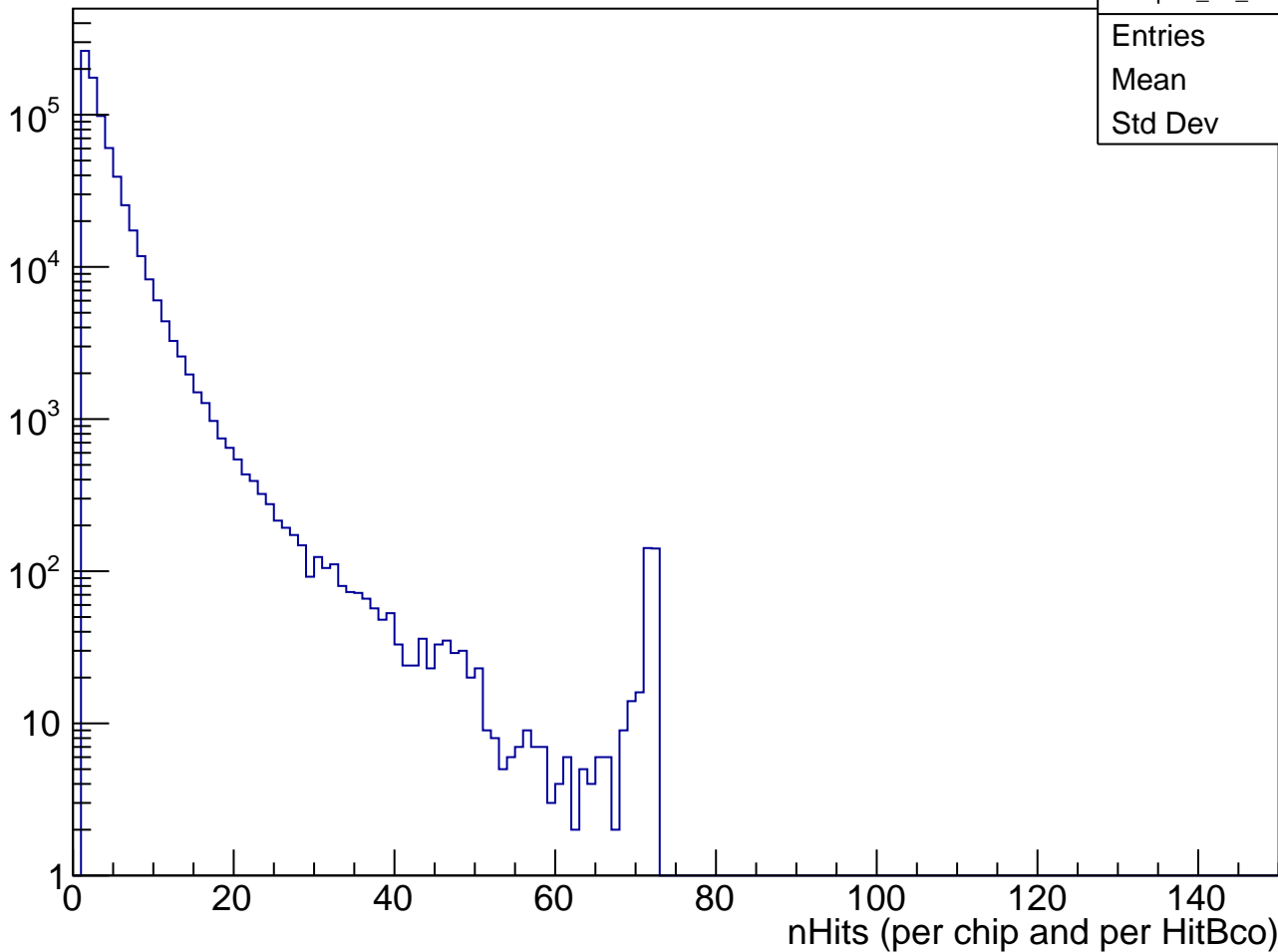
Entries



nChipHit_F6_Fch3_Chip18	
Entries	715138
Mean	3.046
Std Dev	3.545

nChipHit_F6_Fch3_Chip19

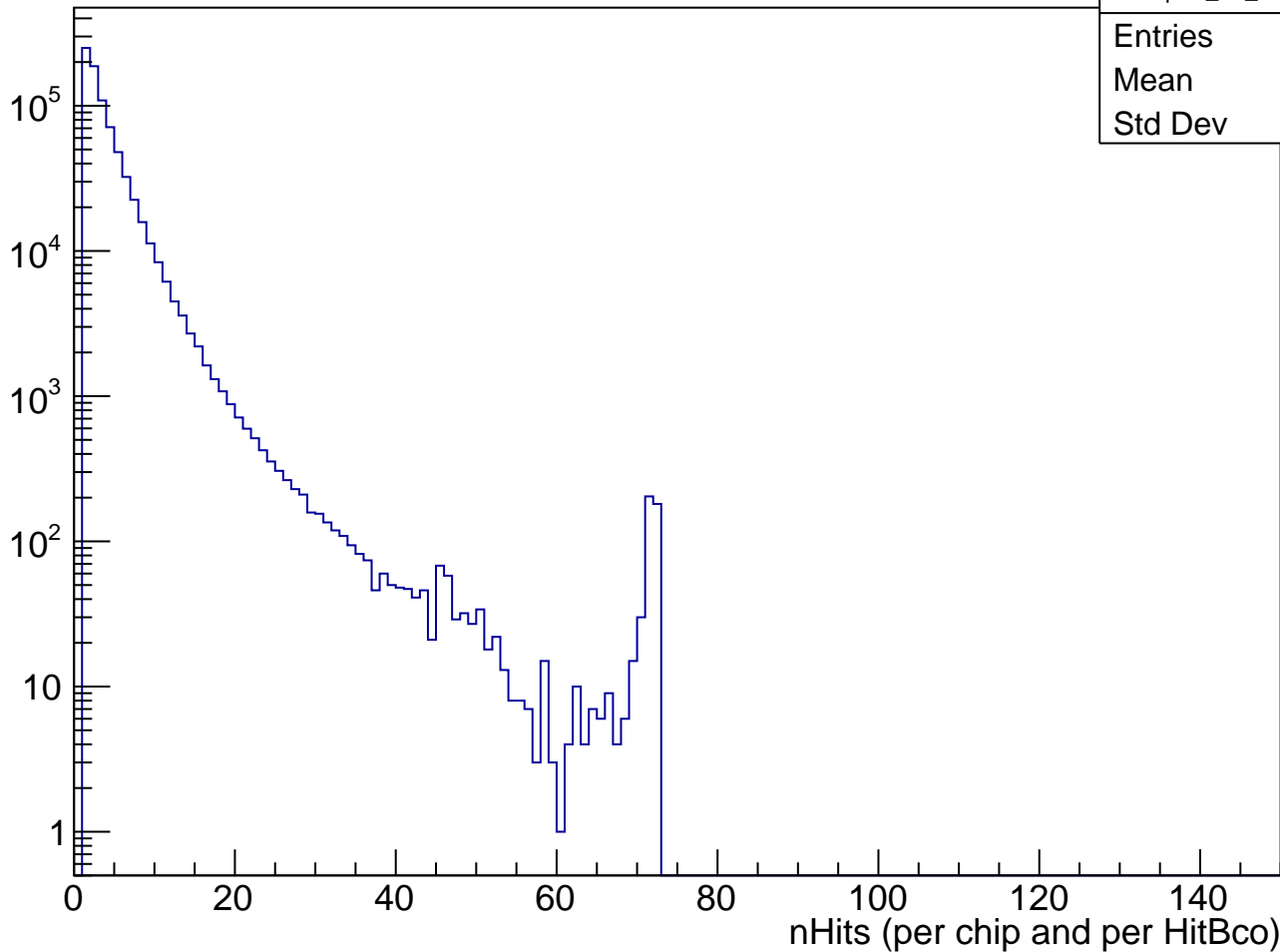
Entries



nChipHit_F6_Fch3_Chip19	
Entries	726622
Mean	3.05
Std Dev	3.532

nChipHit_F6_Fch3_Chip2

Entries



nChipHit_F6_Fch3_Chip2

Entries 785601

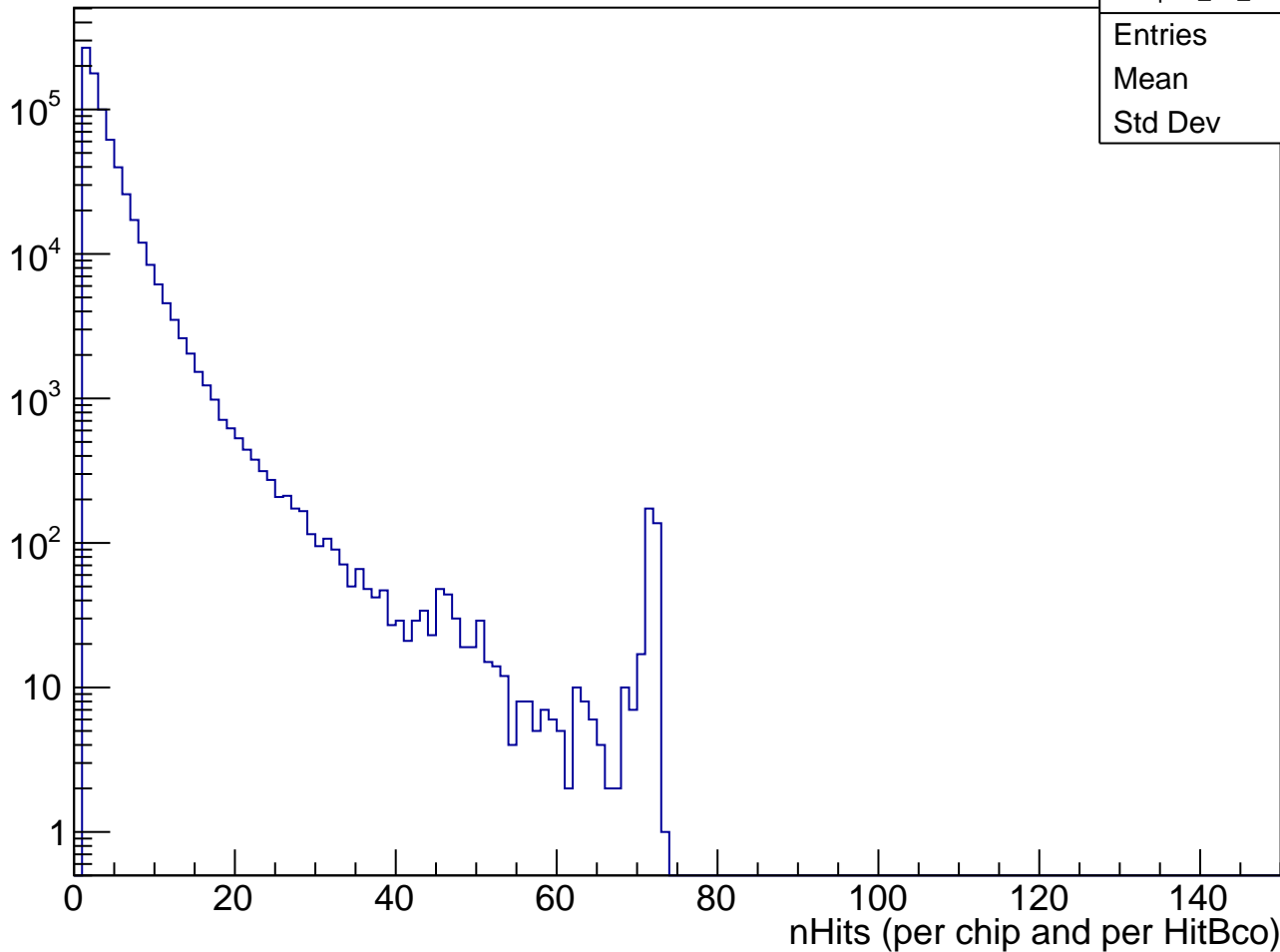
Mean 3.361

Std Dev 3.849

nHits (per chip and per HitBco)

nChipHit_F6_Fch3_Chip20

Entries

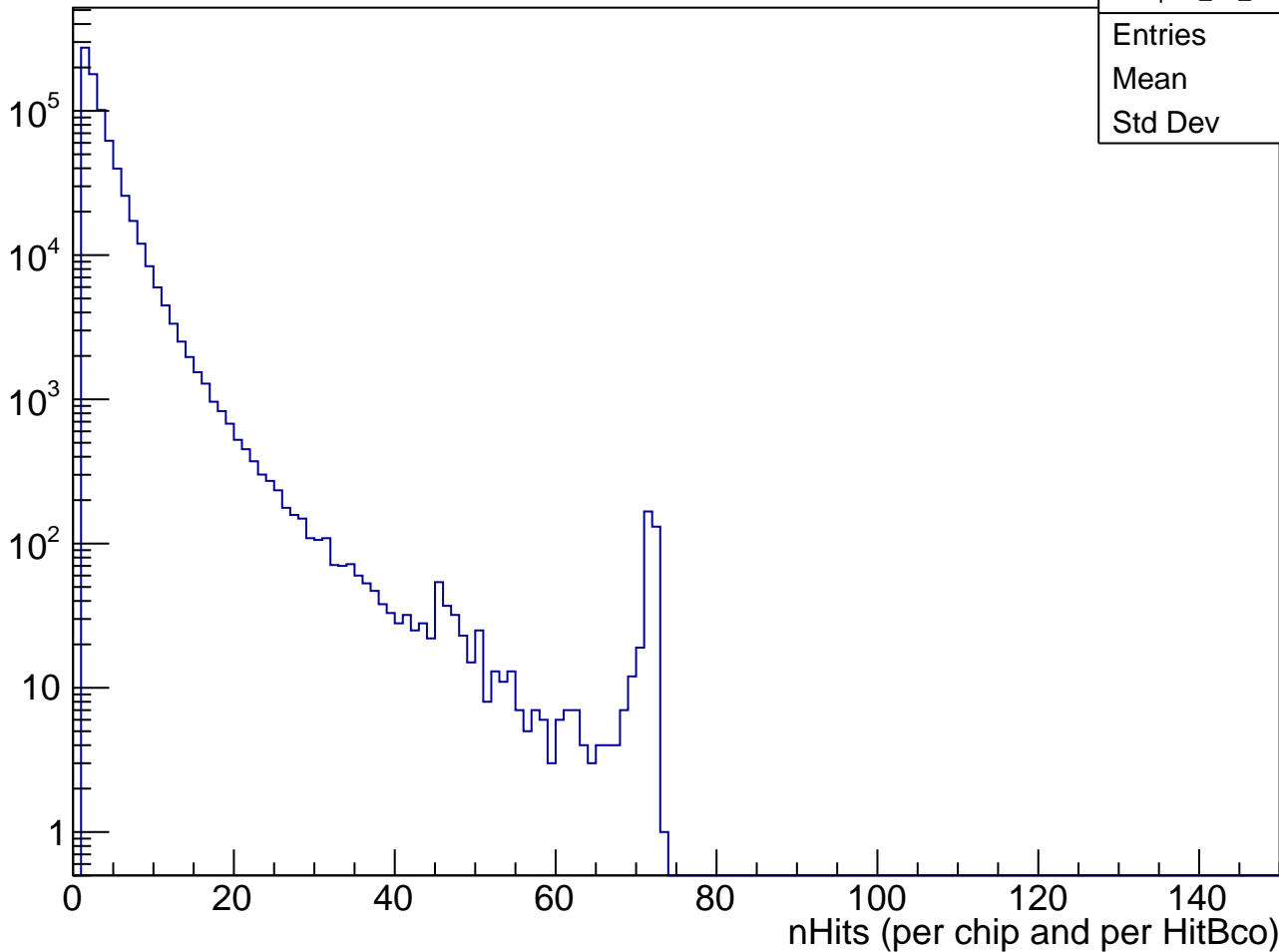


nChipHit_F6_Fch3_Chip20	
Entries	737910
Mean	3.046
Std Dev	3.531

nHits (per chip and per HitBco)

nChipHit_F6_Fch3_Chip21

Entries

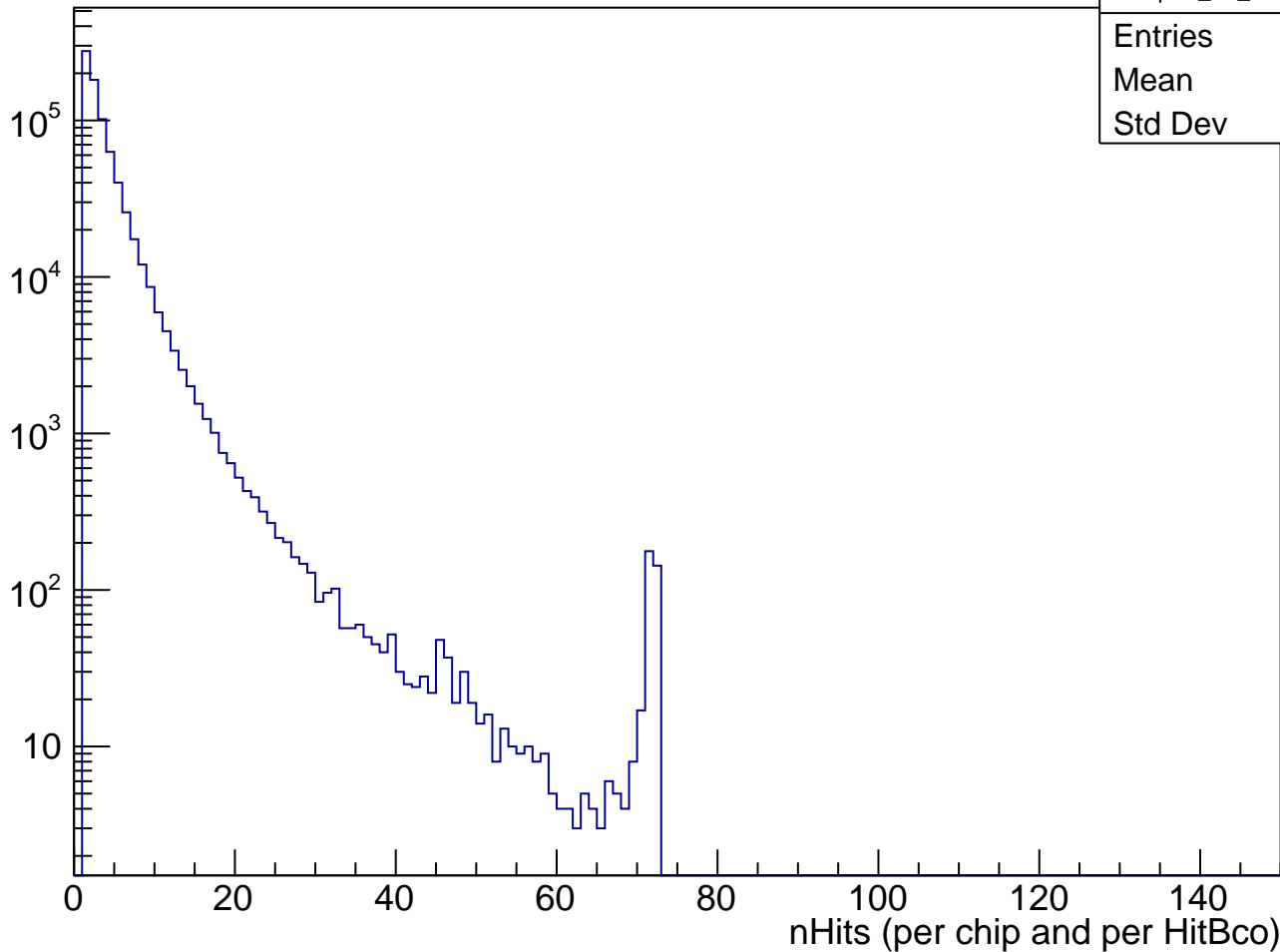


nChipHit_F6_Fch3_Chip21	
Entries	748382
Mean	3.019
Std Dev	3.497

nHits (per chip and per HitBco)

nChipHit_F6_Fch3_Chip22

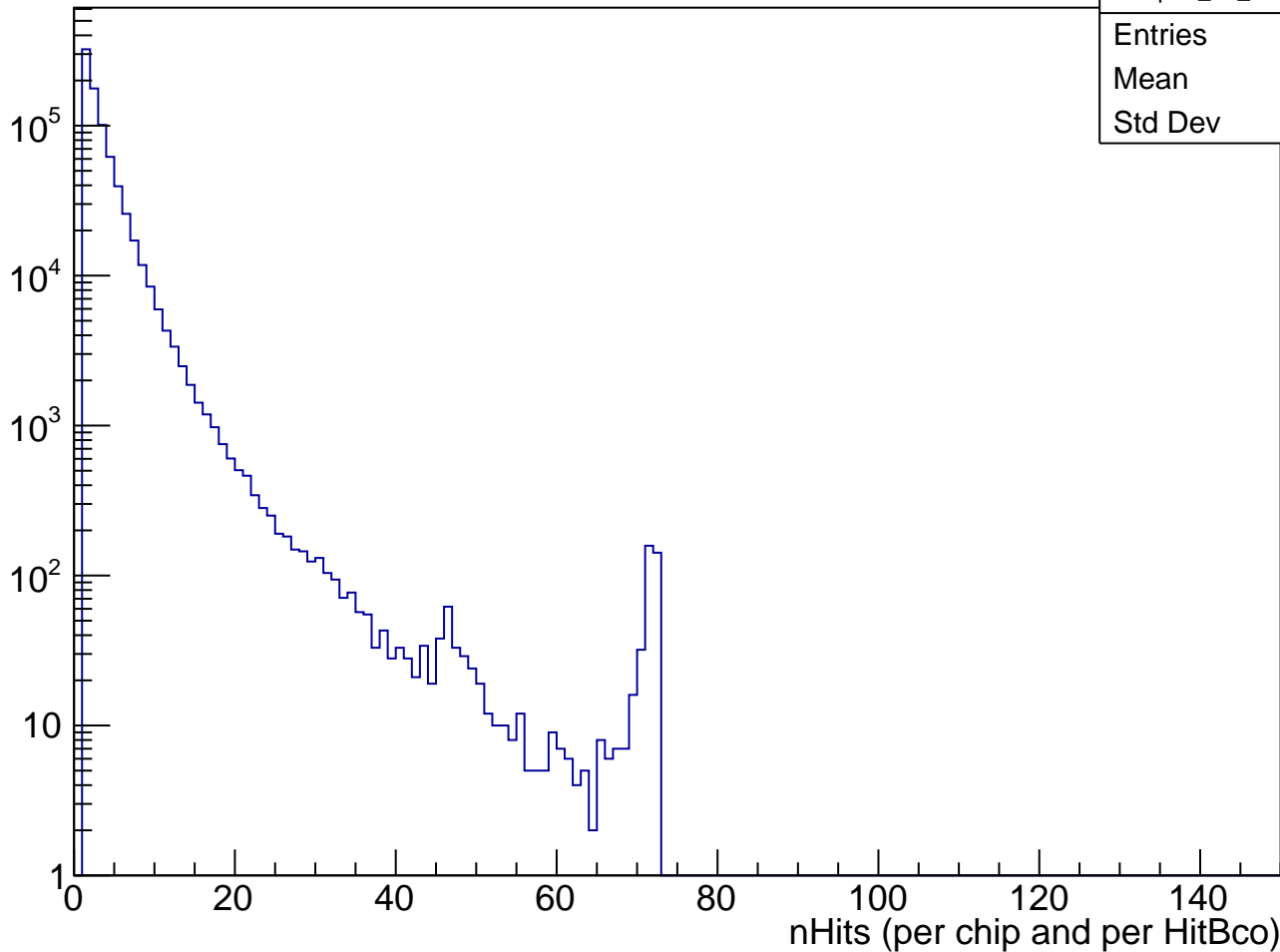
Entries



nChipHit_F6_Fch3_Chip22	
Entries	755521
Mean	3.014
Std Dev	3.494

nChipHit_F6_Fch3_Chip23

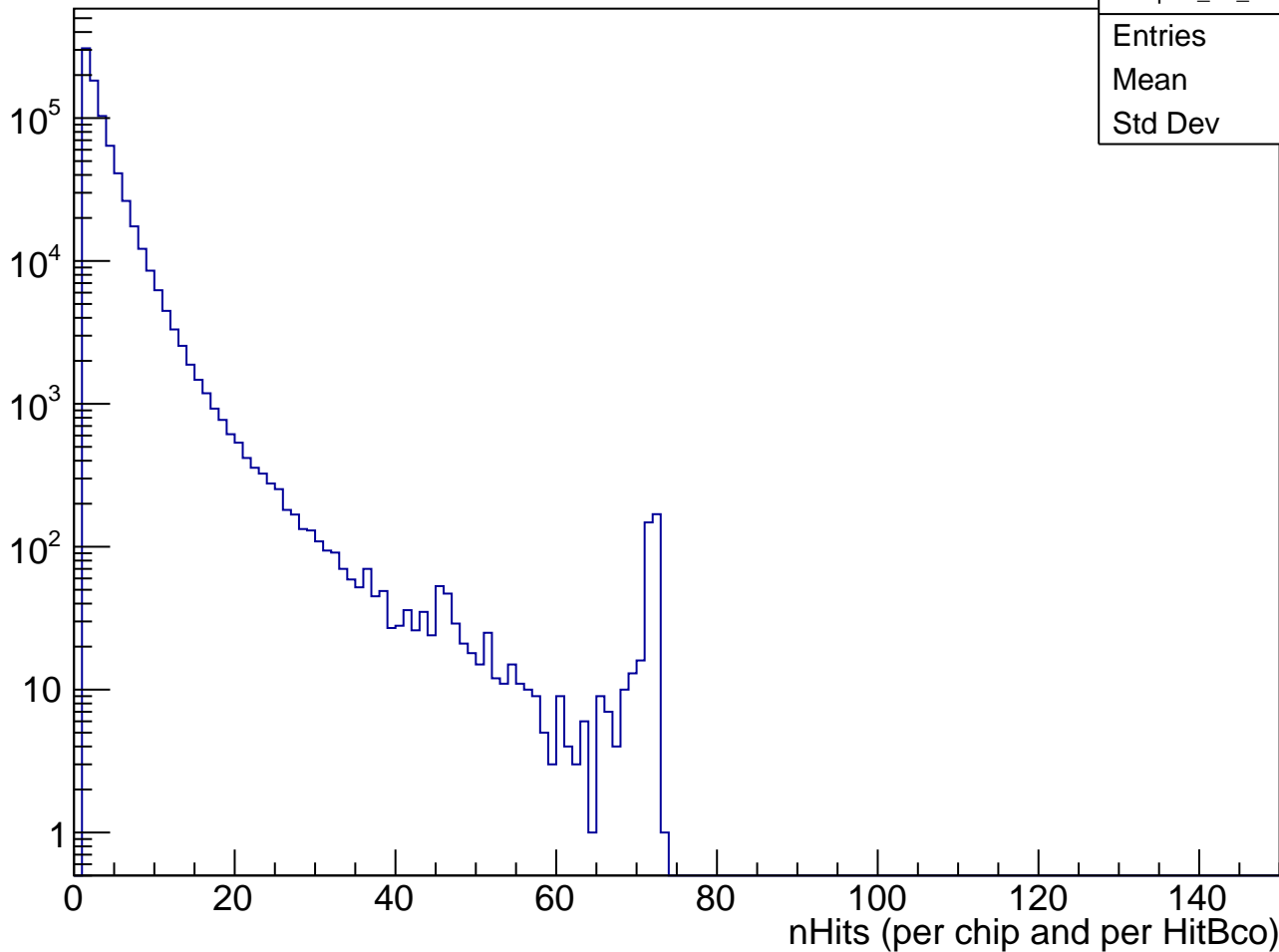
Entries



nChipHit_F6_Fch3_Chip23	
Entries	792802
Mean	2.888
Std Dev	3.437

nChipHit_F6_Fch3_Chip24

Entries

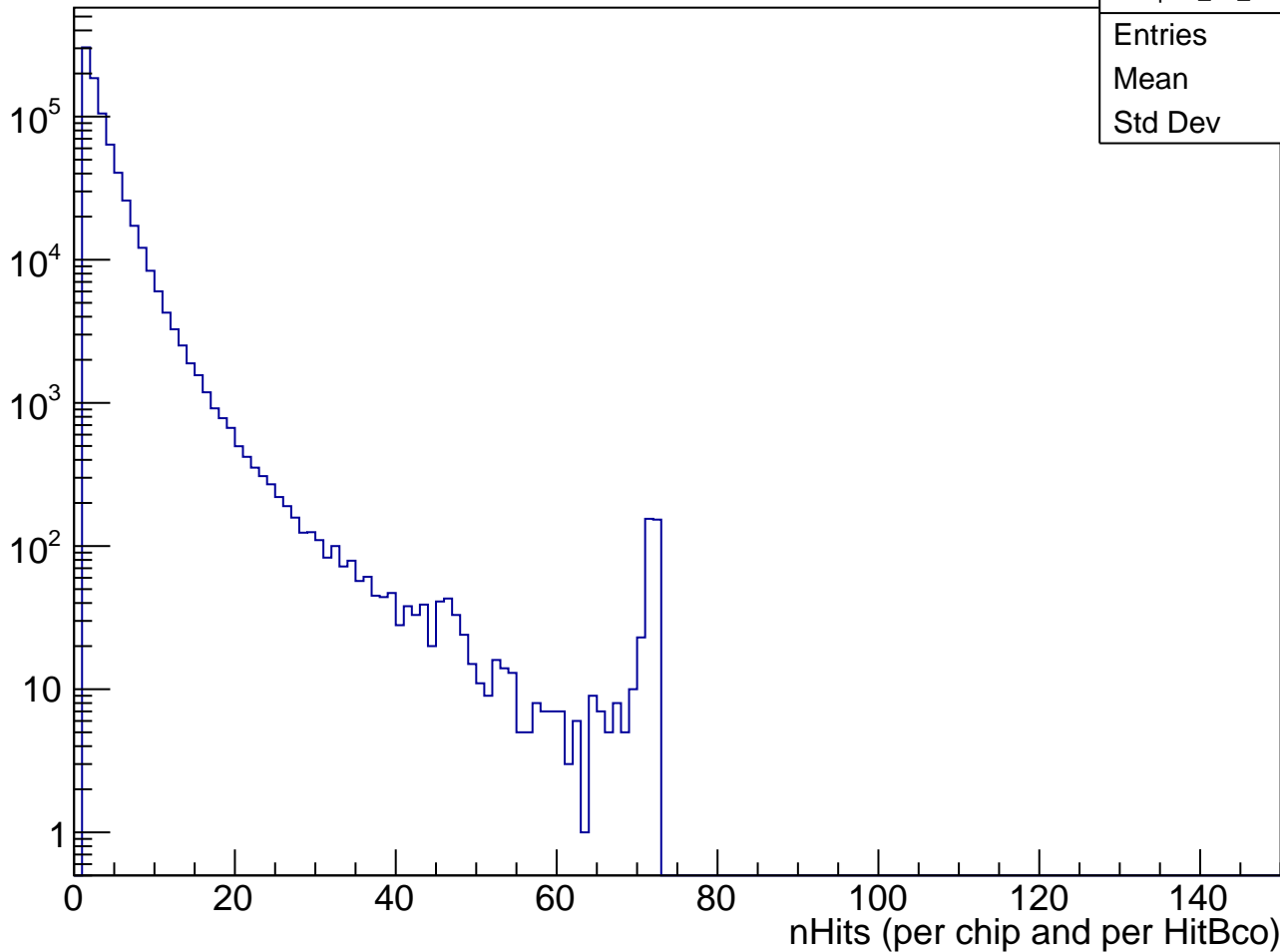


nChipHit_F6_Fch3_Chip24	
Entries	791418
Mean	2.942
Std Dev	3.461

nHits (per chip and per HitBco)

nChipHit_F6_Fch3_Chip25

Entries

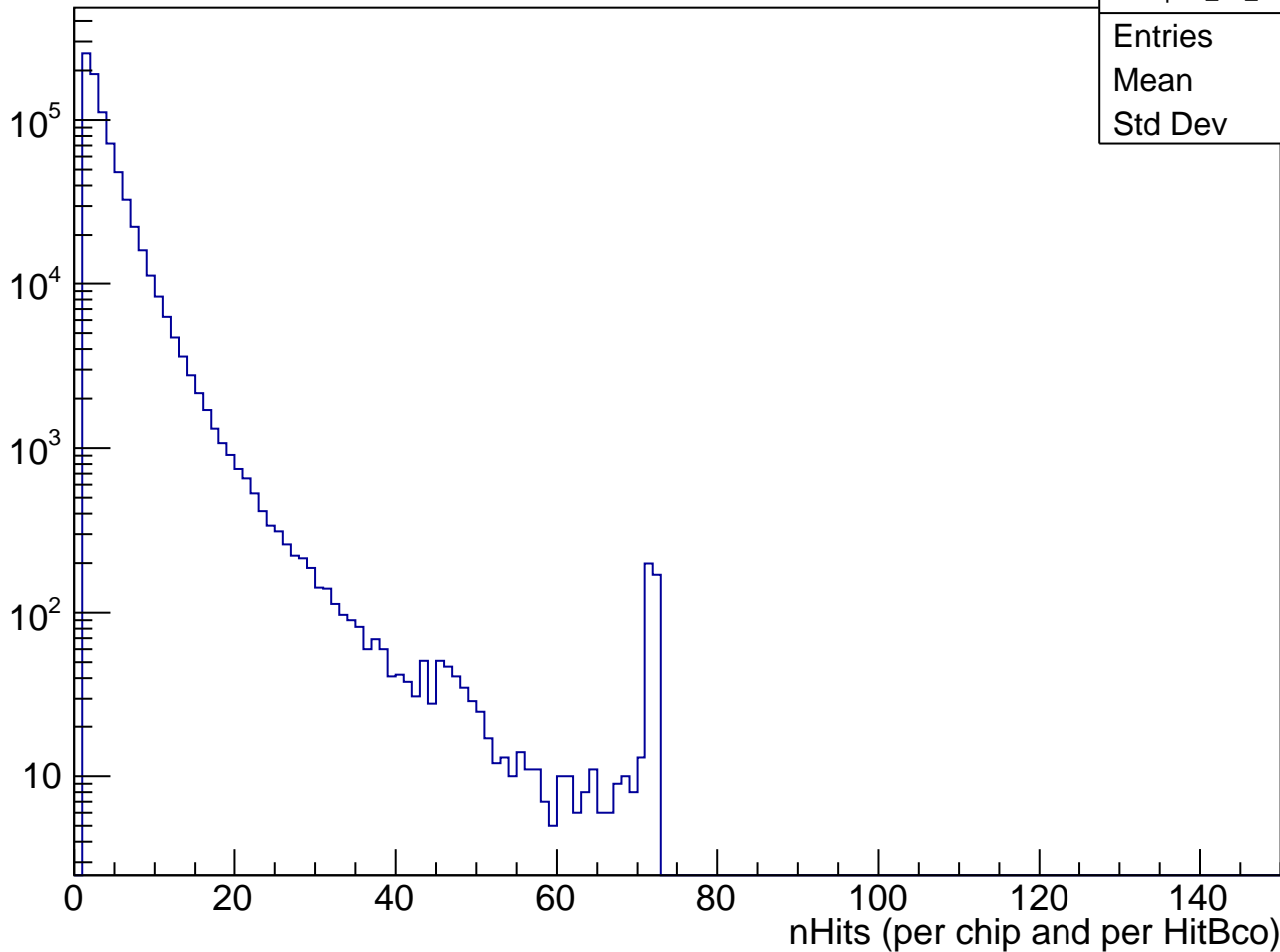


nChipHit_F6_Fch3_Chip25	
Entries	790288
Mean	2.935
Std Dev	3.438

nHits (per chip and per HitBco)

nChipHit_F6_Fch3_Chip3

Entries



nChipHit_F6_Fch3_Chip3

Entries 797091

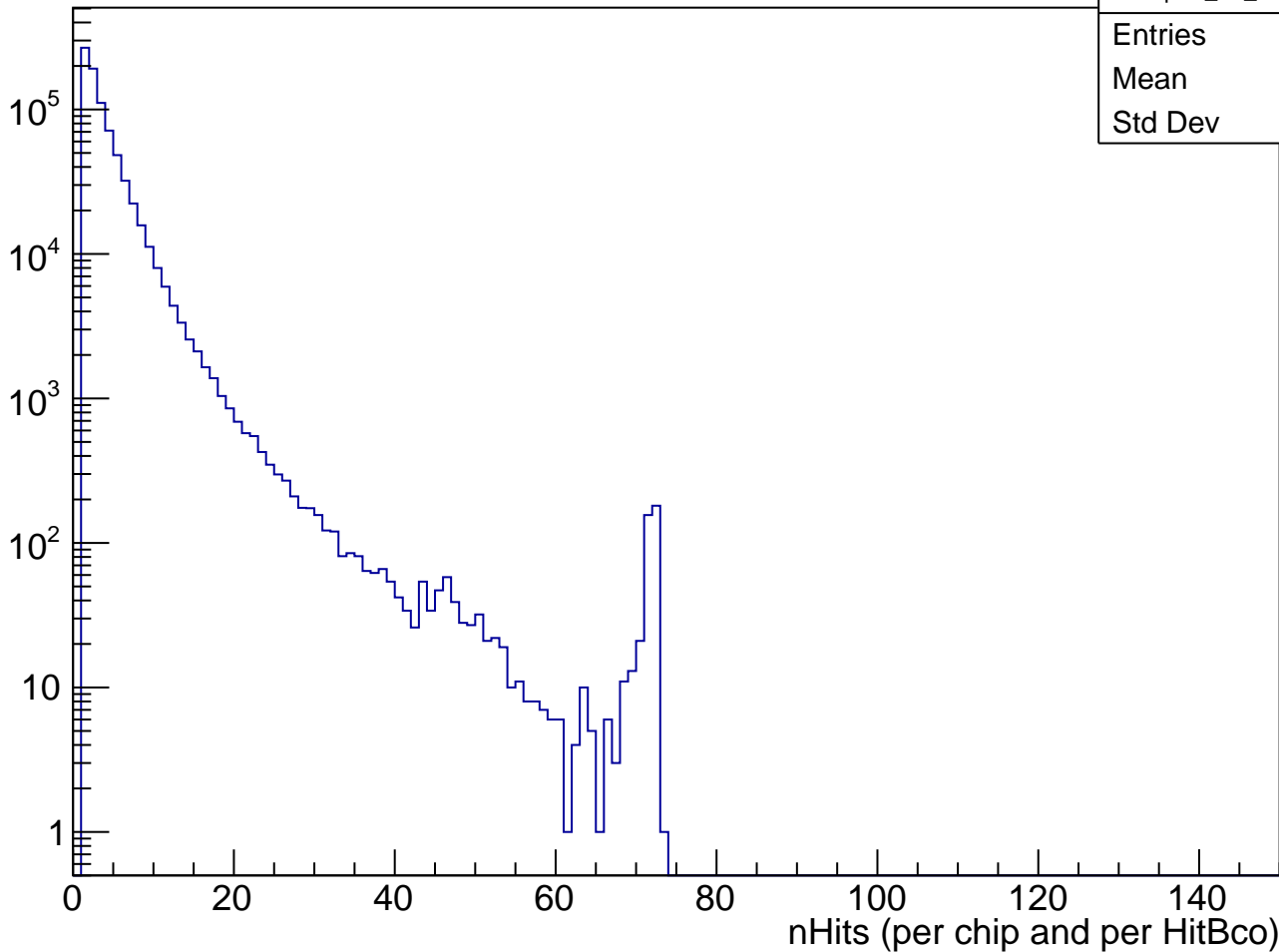
Mean 3.349

Std Dev 3.814

nHits (per chip and per HitBco)

nChipHit_F6_Fch3_Chip4

Entries



nChipHit_F6_Fch3_Chip4

Entries 808049

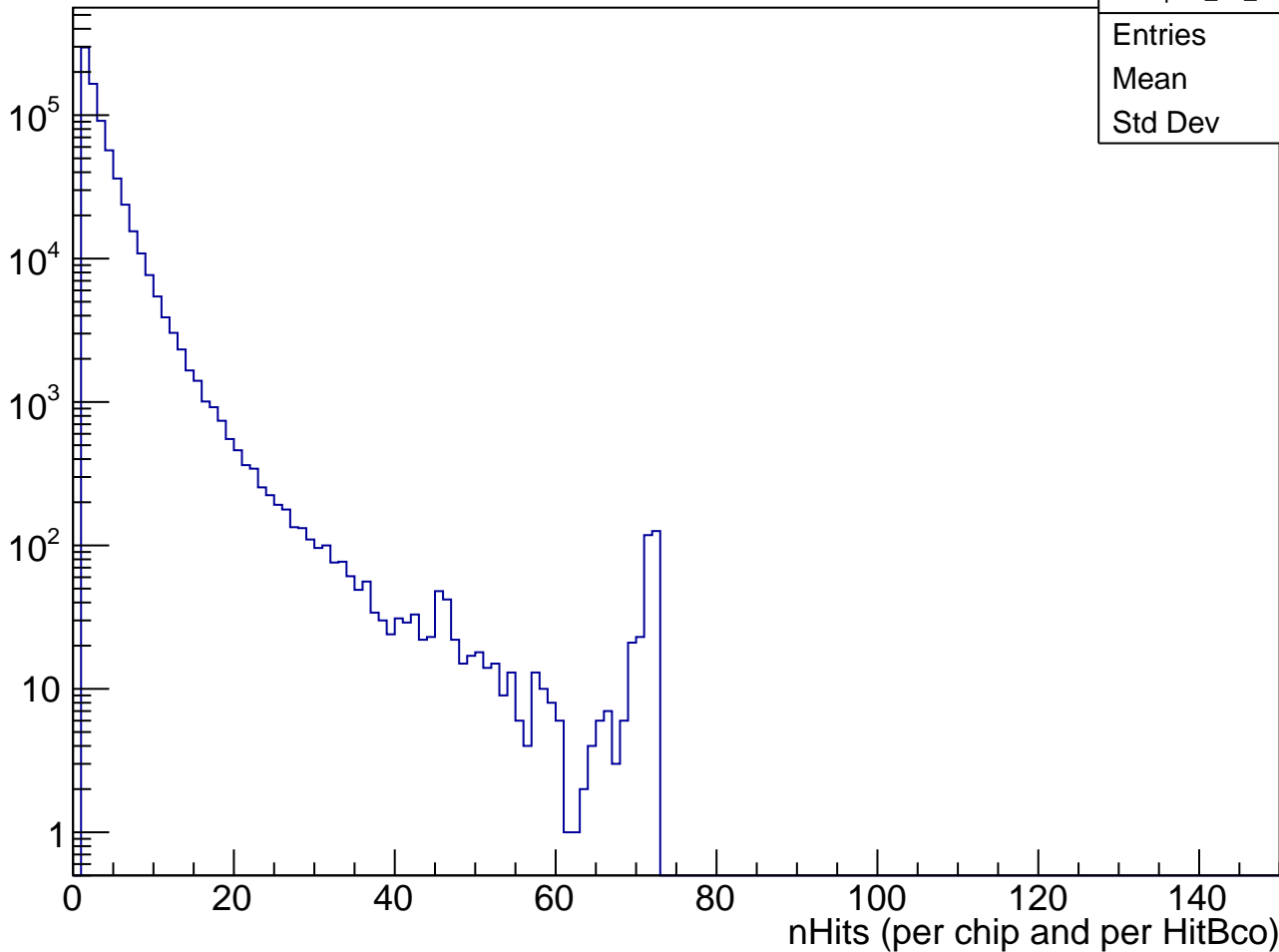
Mean 3.281

Std Dev 3.742

nHits (per chip and per HitBco)

nChipHit_F6_Fch3_Chip5

Entries



nChipHit_F6_Fch3_Chip5

Entries 728798

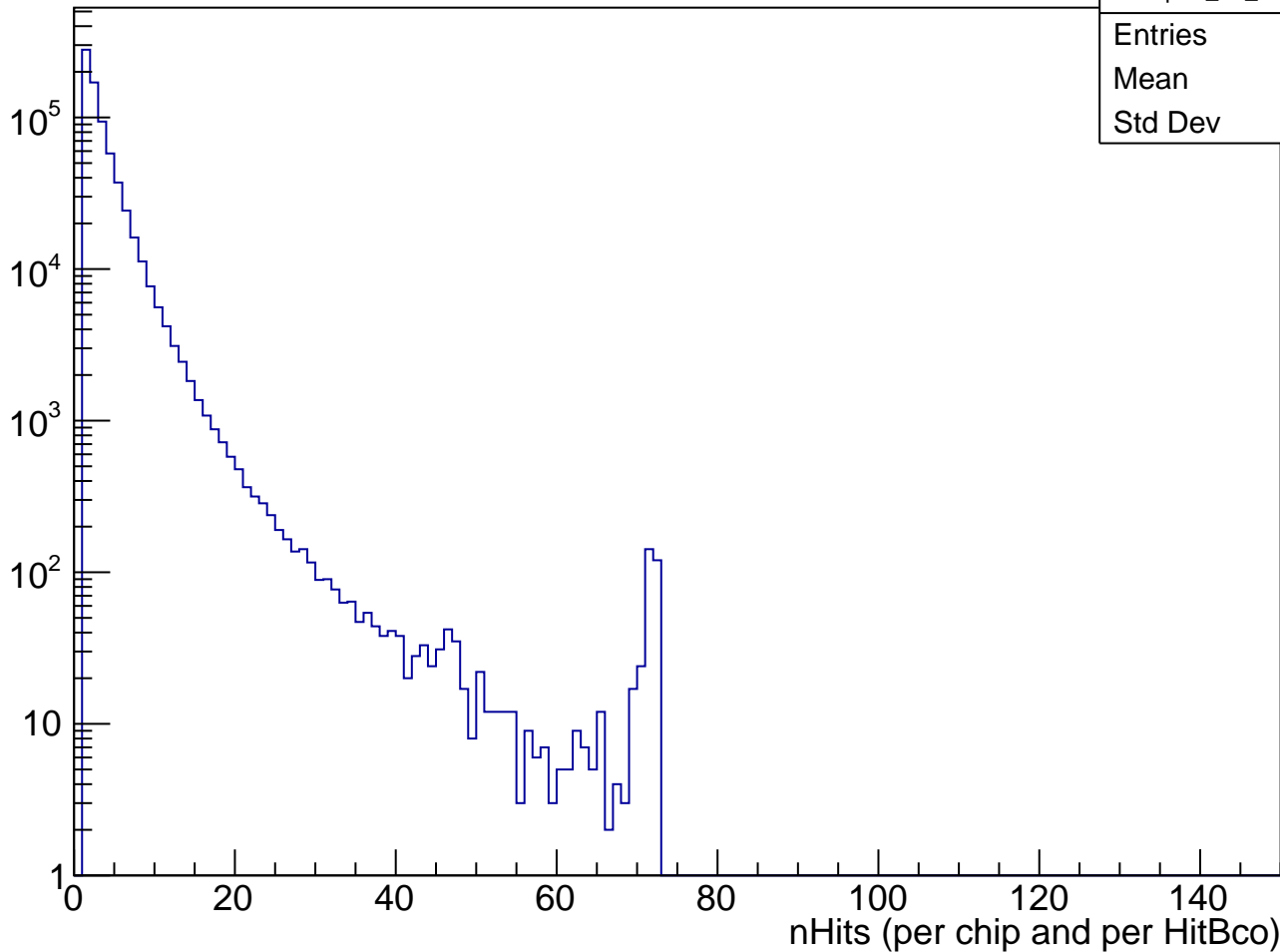
Mean 2.878

Std Dev 3.396

nHits (per chip and per HitBco)

nChipHit_F6_Fch3_Chip6

Entries



nChipHit_F6_Fch3_Chip6

Entries 723813

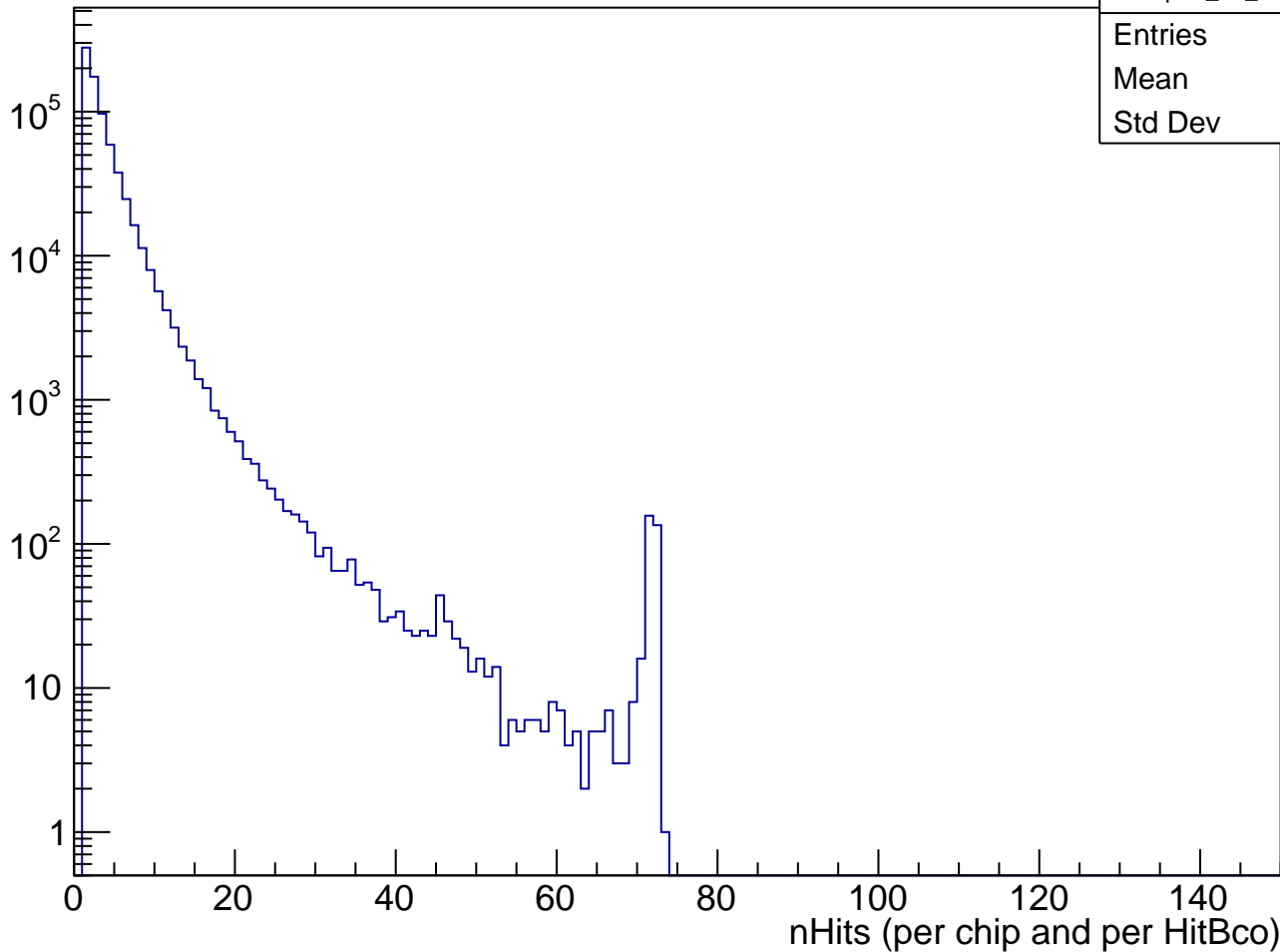
Mean 2.942

Std Dev 3.438

nHits (per chip and per HitBco)

nChipHit_F6_Fch3_Chip7

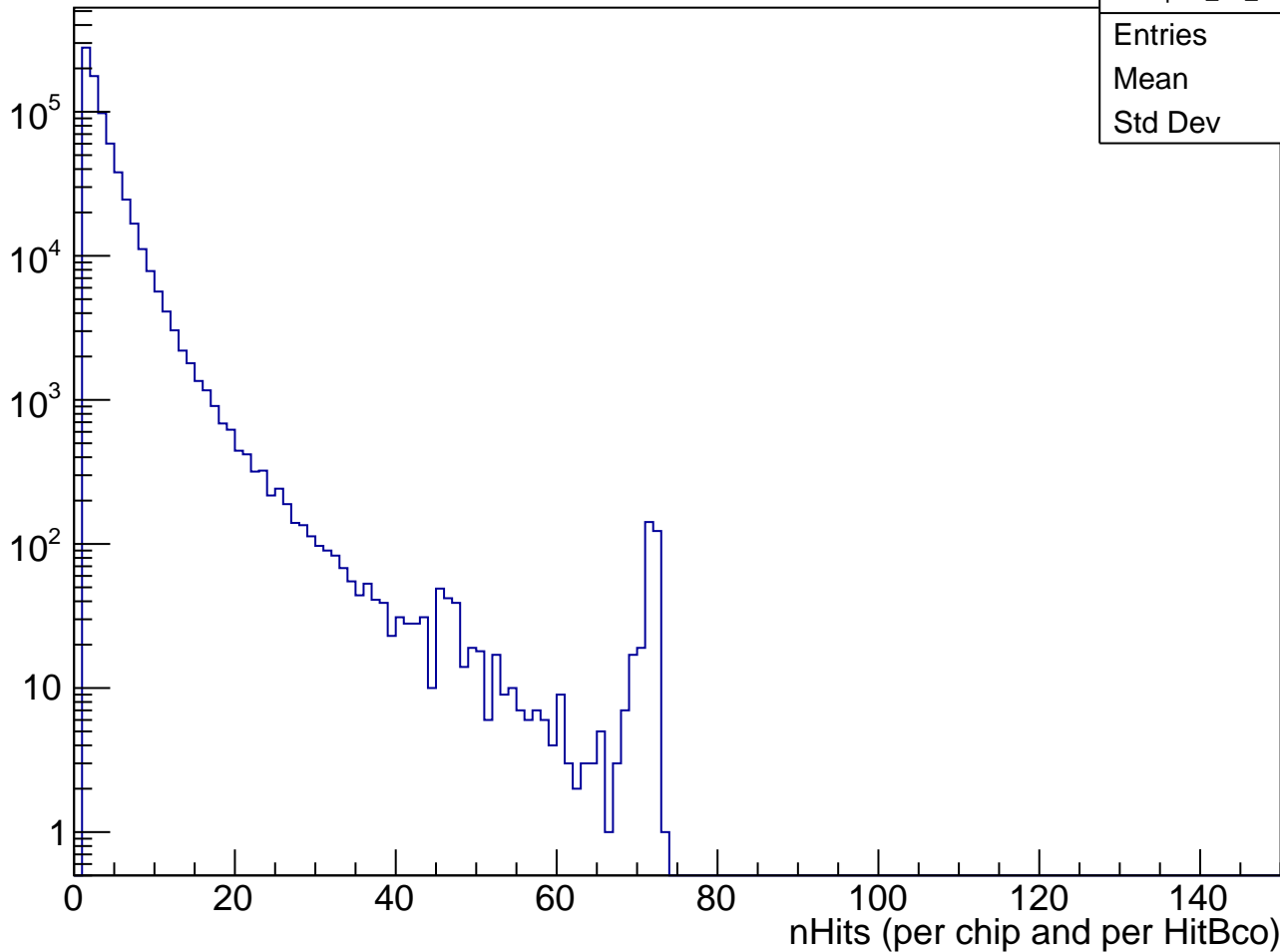
Entries



nChipHit_F6_Fch3_Chip7	
Entries	732707
Mean	2.955
Std Dev	3.429

nChipHit_F6_Fch3_Chip8

Entries



nChipHit_F6_Fch3_Chip8

Entries 737260

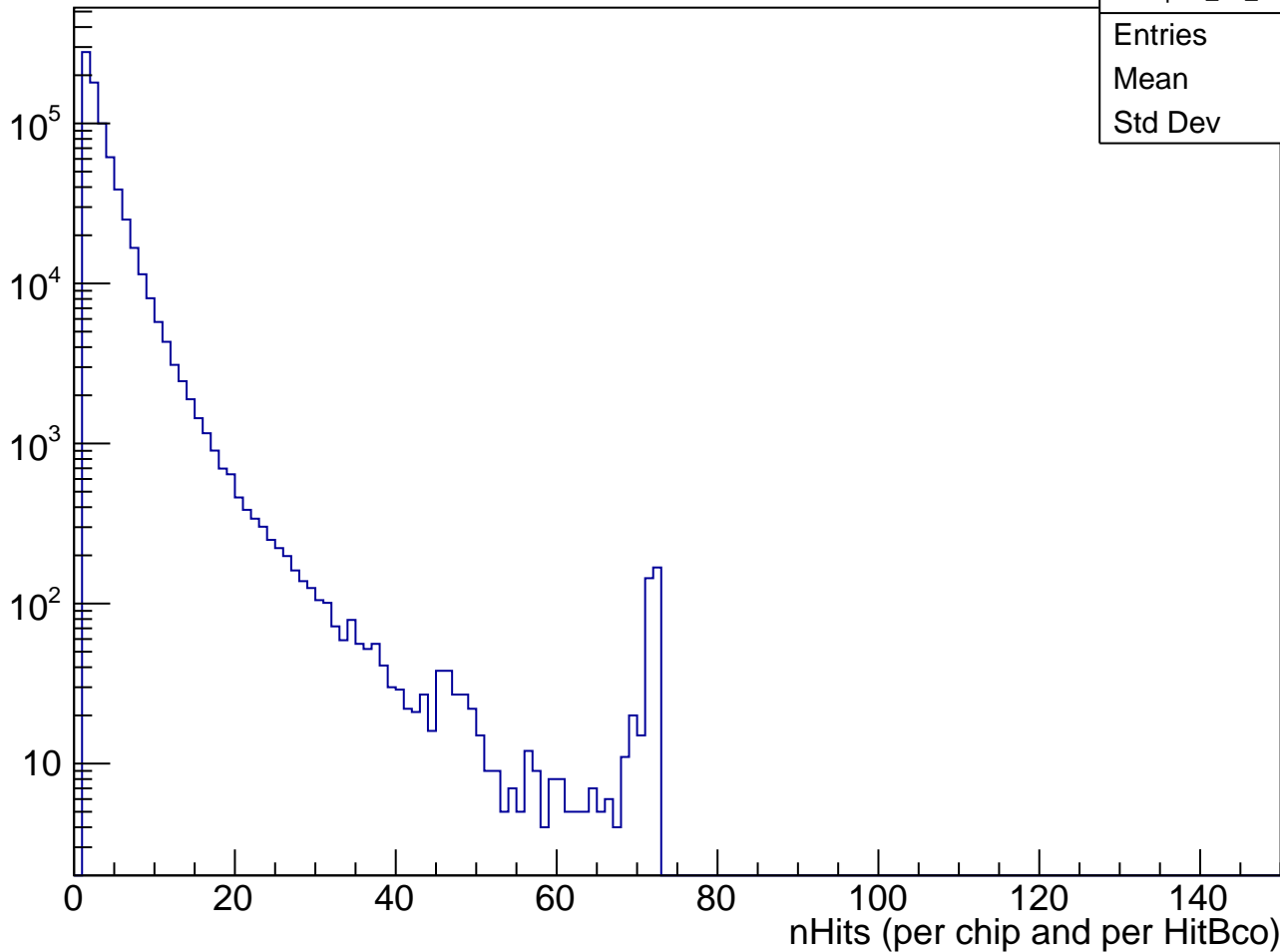
Mean 2.945

Std Dev 3.404

nHits (per chip and per HitBco)

nChipHit_F6_Fch3_Chip9

Entries



nChipHit_F6_Fch3_Chip9

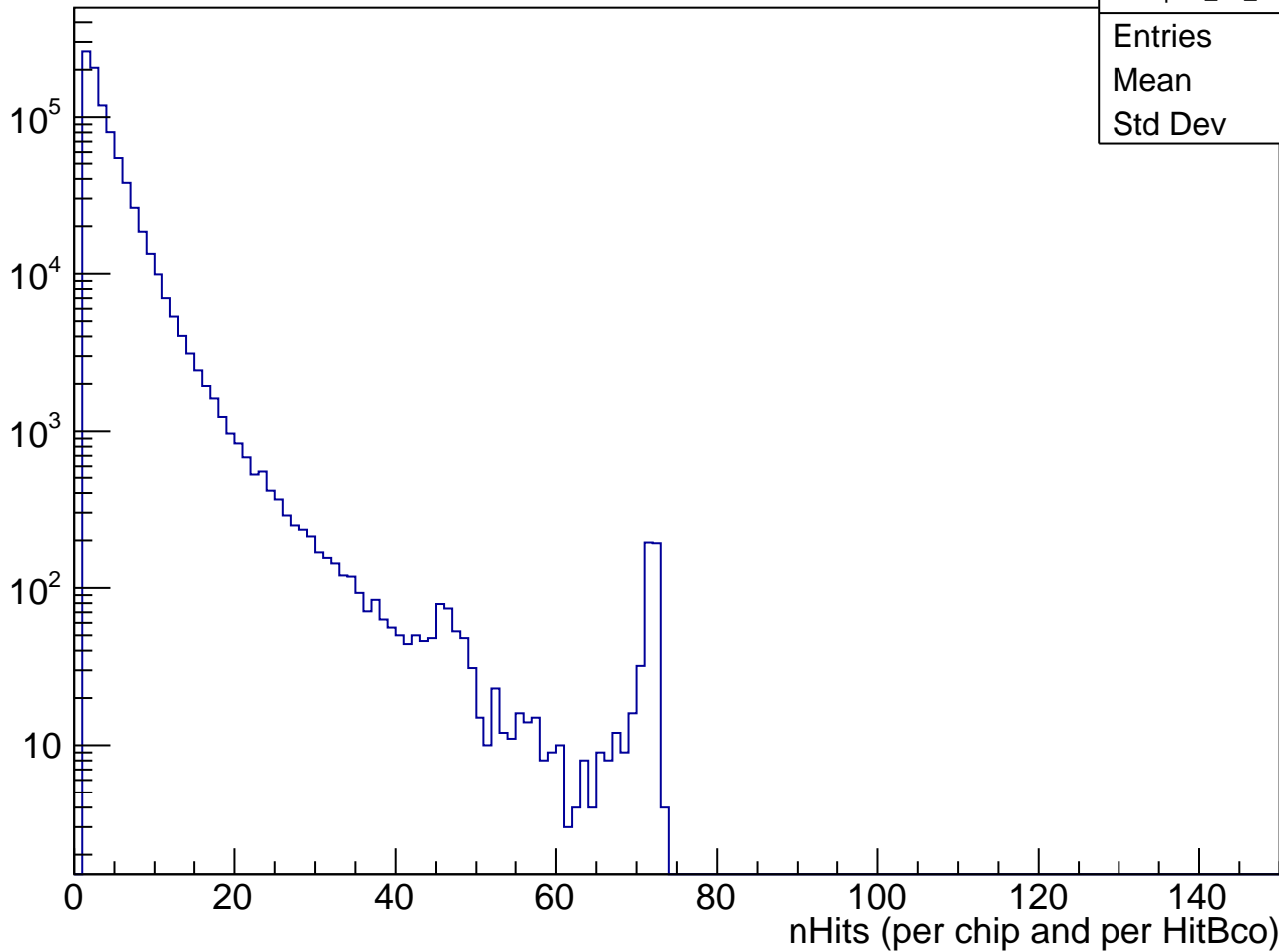
Entries 746839

Mean 2.969

Std Dev 3.467

nChipHit_F6_Fch4_Chip0

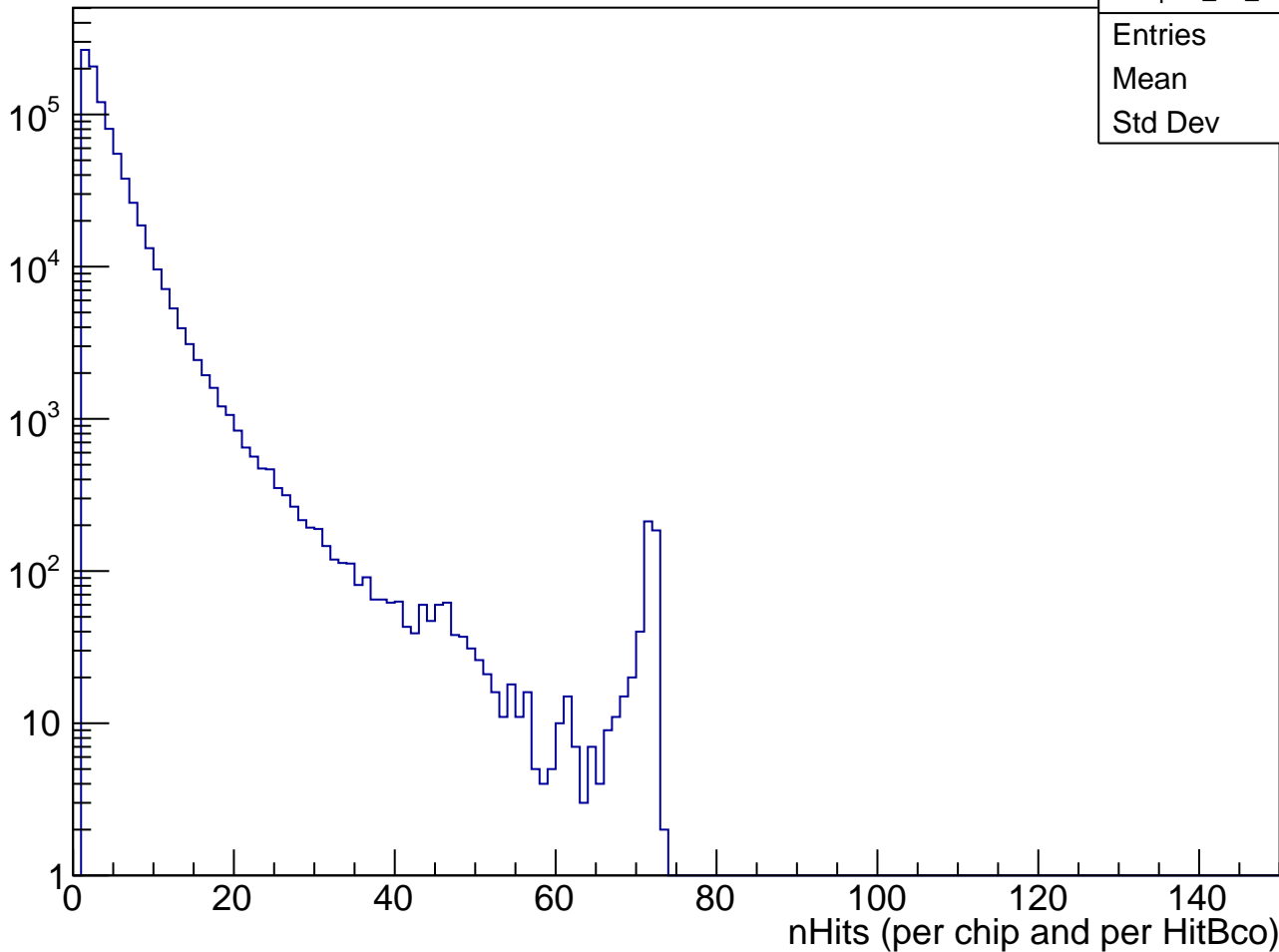
Entries



nChipHit_F6_Fch4_Chip0	
Entries	861311
Mean	3.459
Std Dev	3.903

nChipHit_F6_Fch4_Chip1

Entries

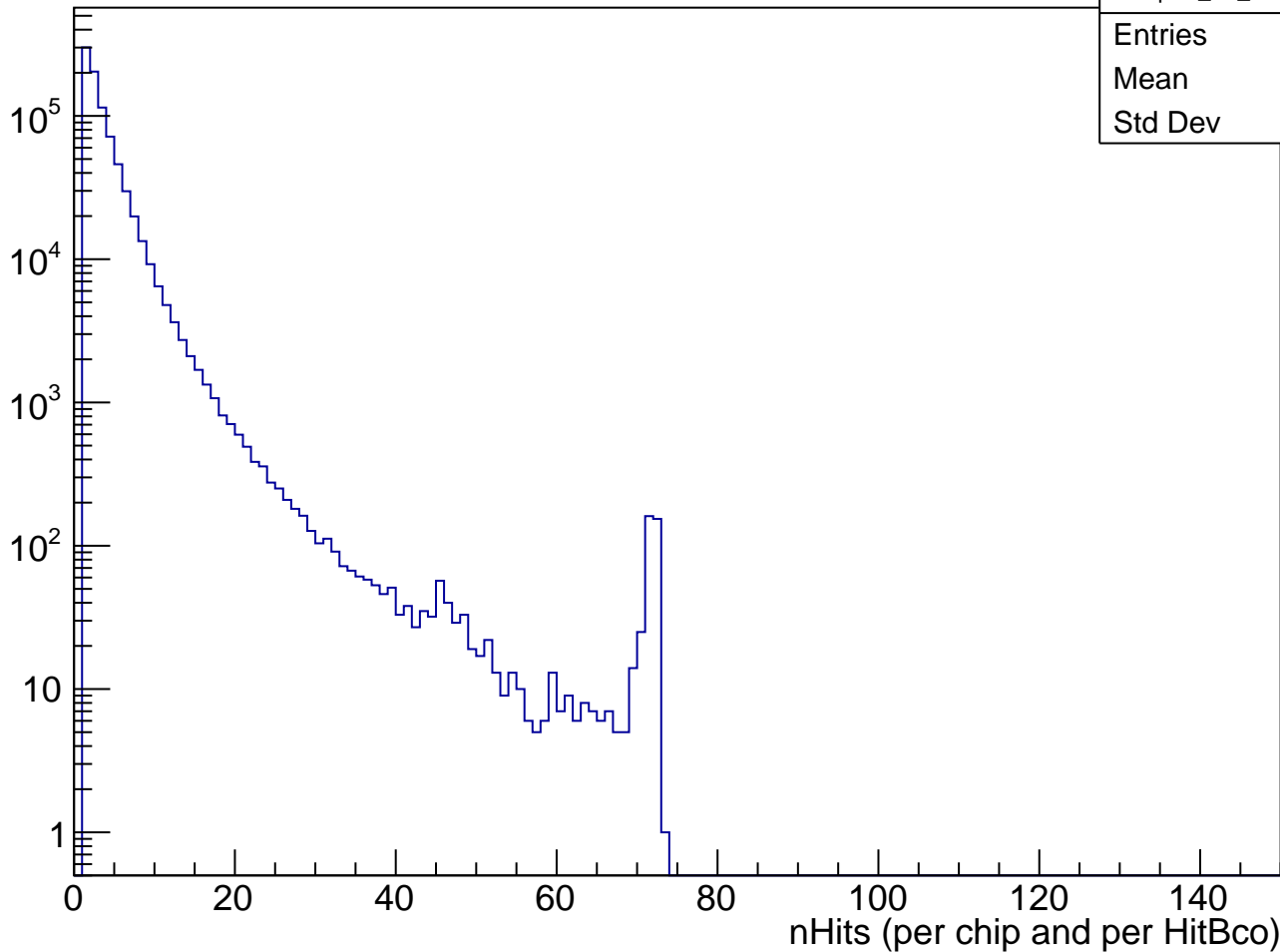


nChipHit_F6_Fch4_Chip1	
Entries	868178
Mean	3.442
Std Dev	3.891

nHits (per chip and per HitBco)

nChipHit_F6_Fch4_Chip10

Entries

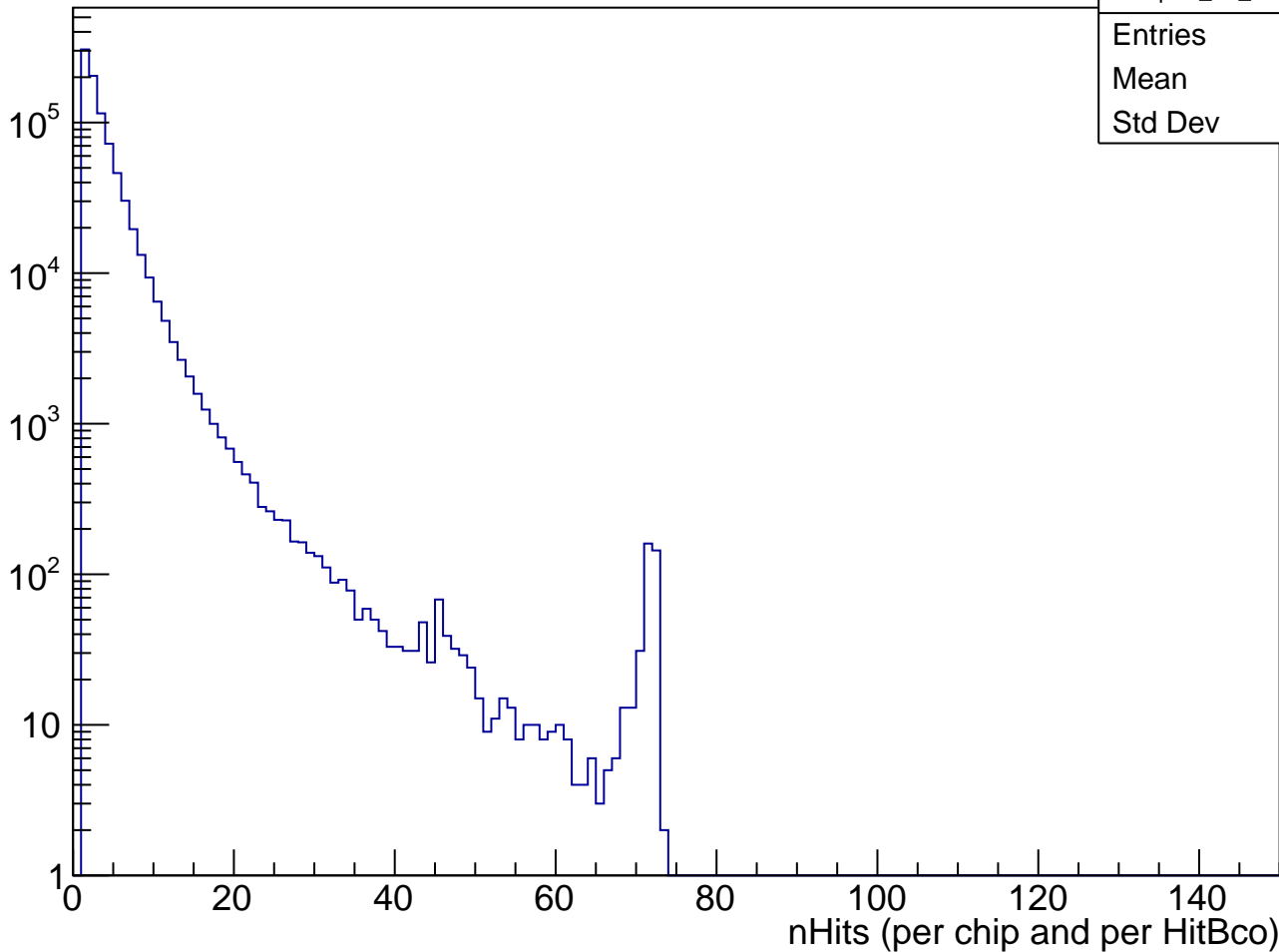


nChipHit_F6_Fch4_Chip10	
Entries	838847
Mean	3.028
Std Dev	3.47

nHits (per chip and per HitBco)

nChipHit_F6_Fch4_Chip11

Entries

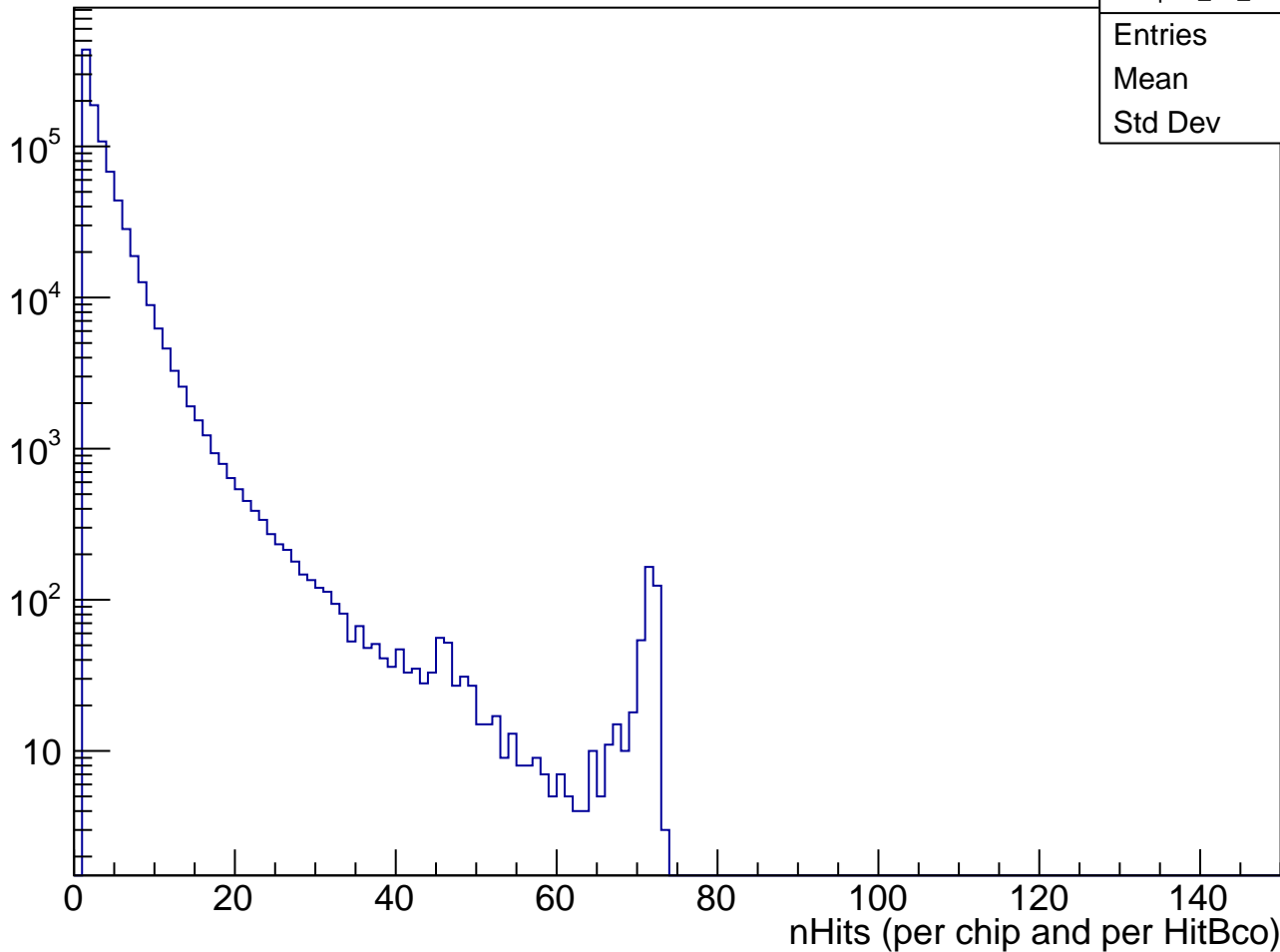


nChipHit_F6_Fch4_Chip11	
Entries	845360
Mean	3.006
Std Dev	3.439

nHits (per chip and per HitBco)

nChipHit_F6_Fch4_Chip12

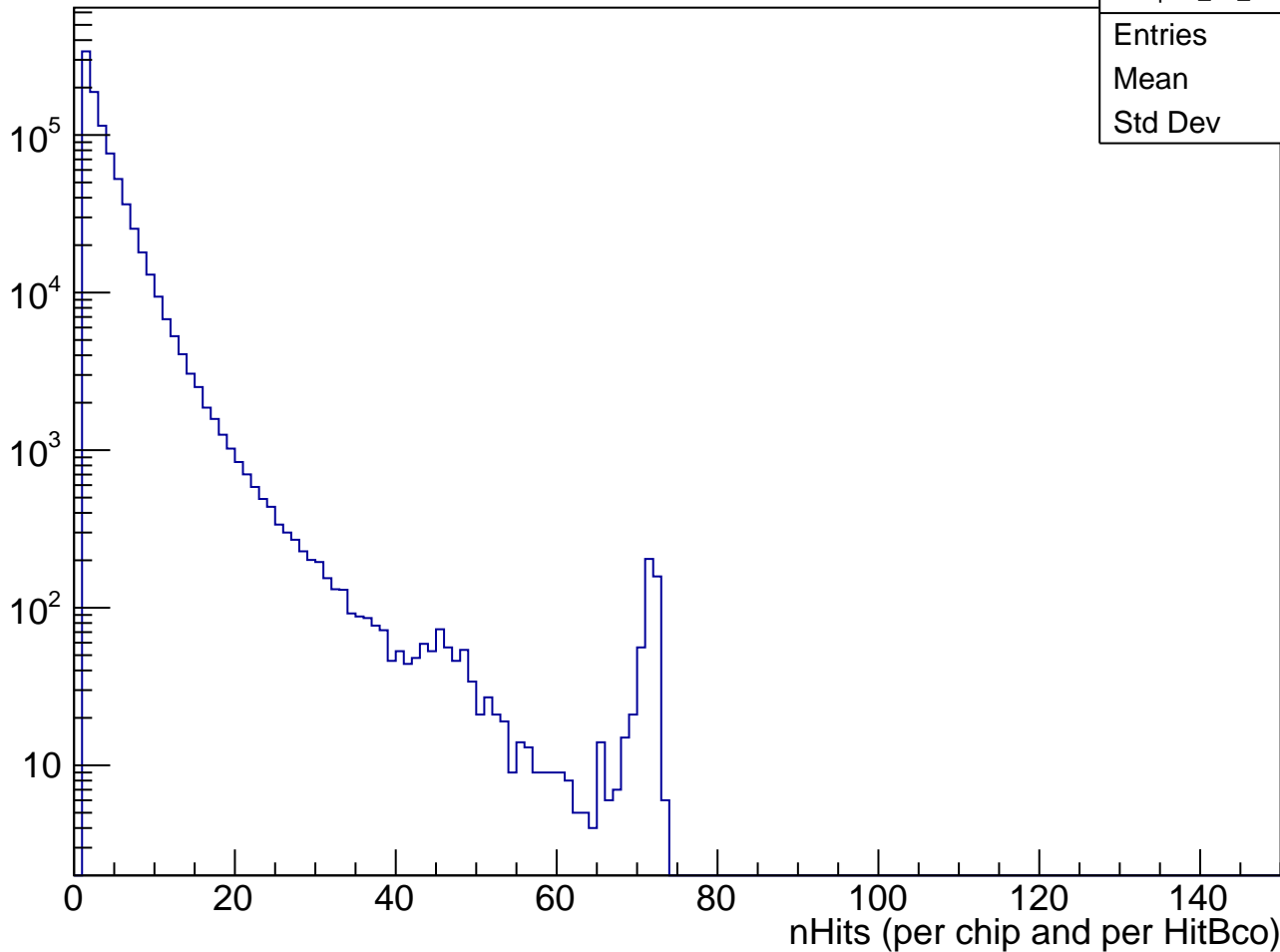
Entries



nChipHit_F6_Fch4_Chip12	
Entries	940969
Mean	2.71
Std Dev	3.313

nChipHit_F6_Fch4_Chip13

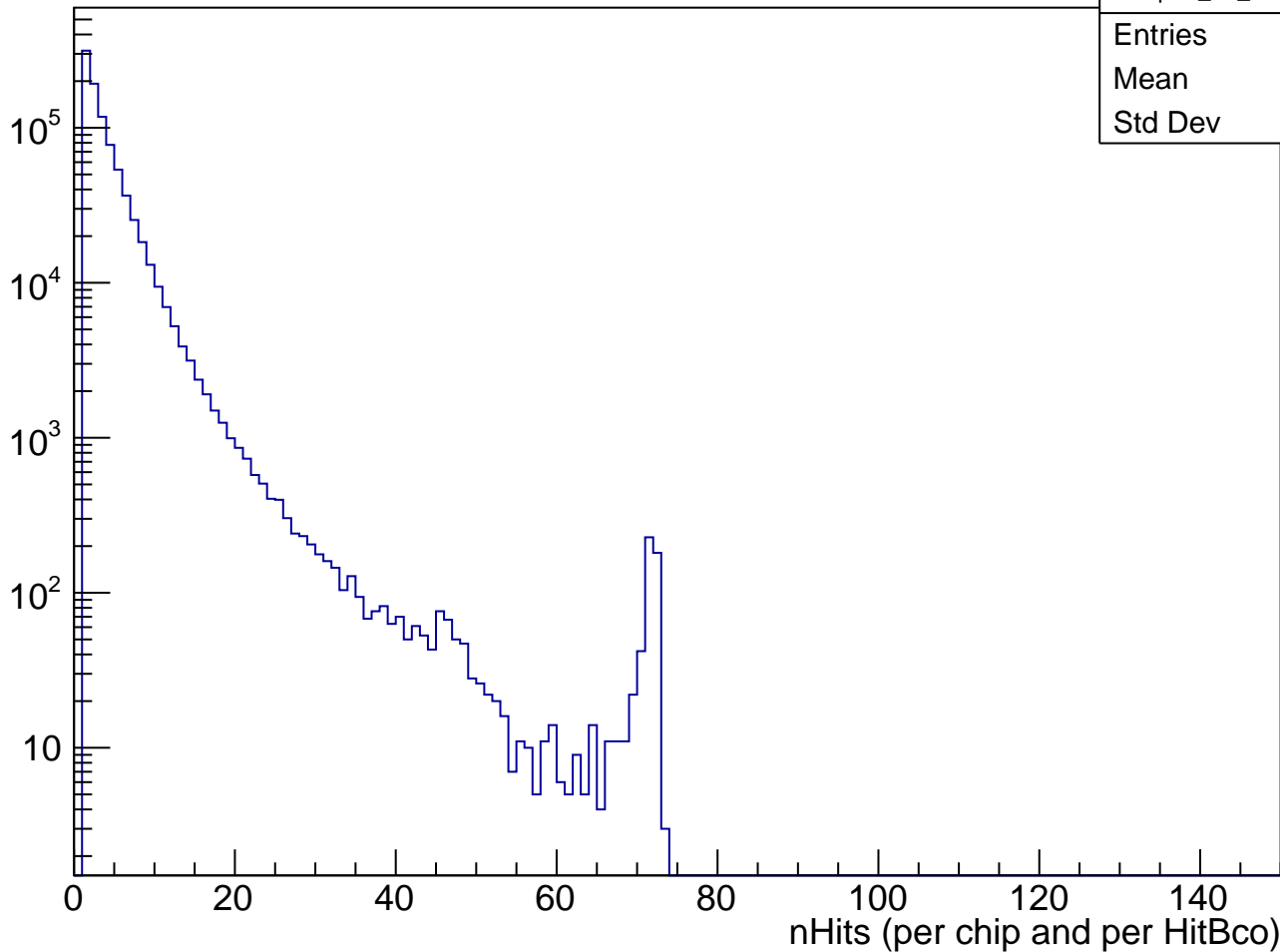
Entries



nChipHit_F6_Fch4_Chip13	
Entries	906535
Mean	3.257
Std Dev	3.859

nChipHit_F6_Fch4_Chip14

Entries

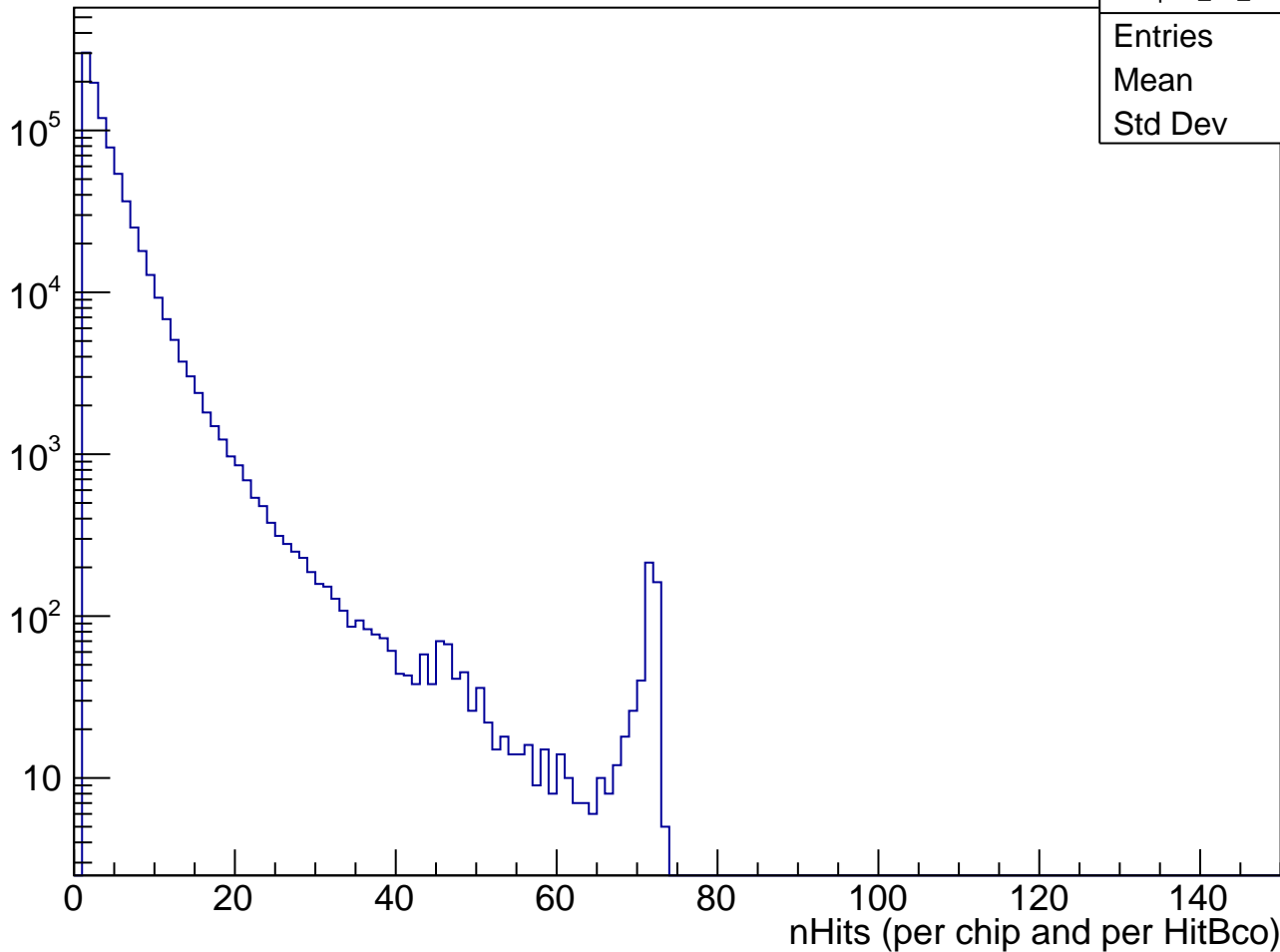


nChipHit_F6_Fch4_Chip14	
Entries	892667
Mean	3.32
Std Dev	3.901

nHits (per chip and per HitBco)

nChipHit_F6_Fch4_Chip15

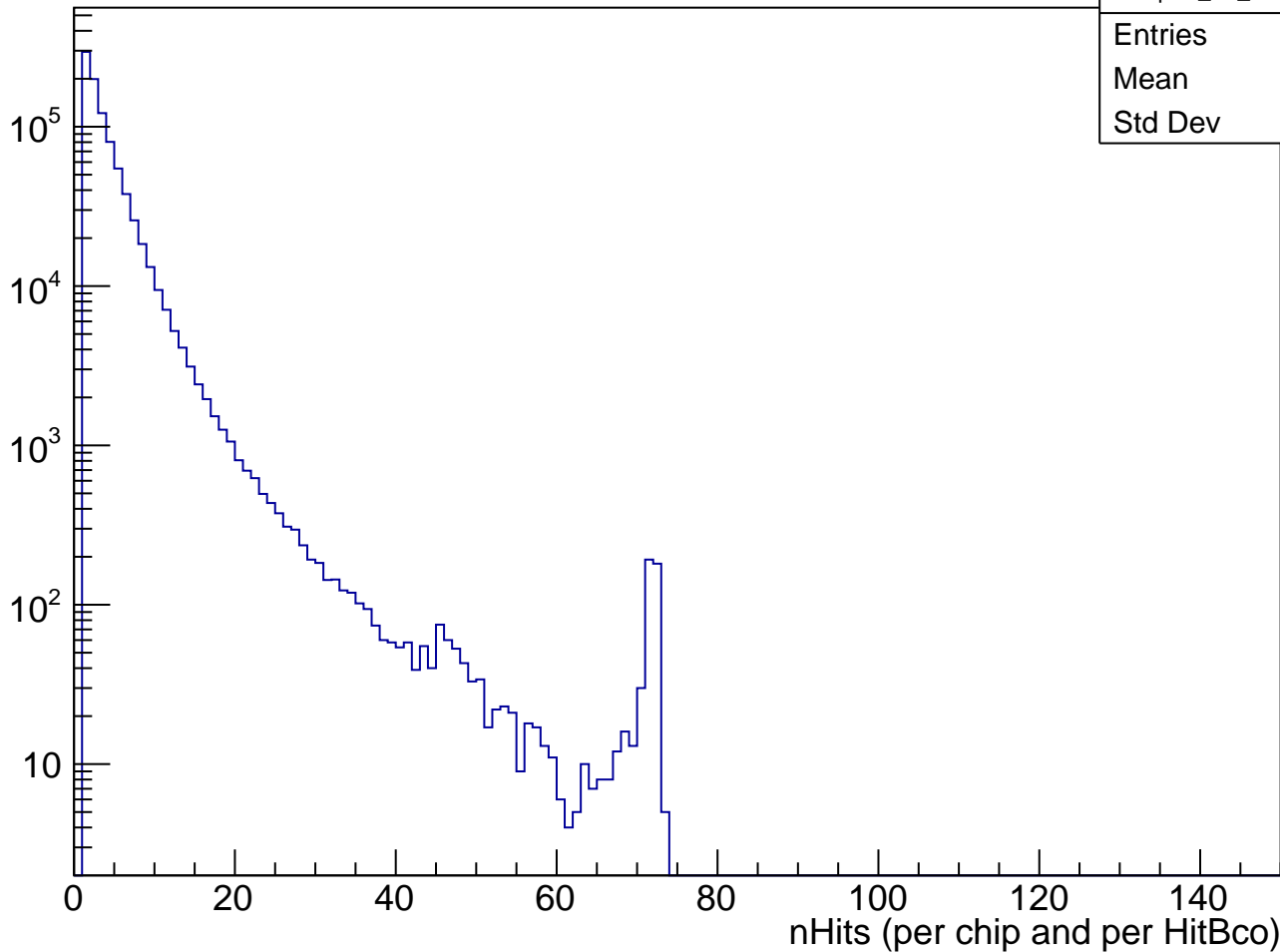
Entries



nChipHit_F6_Fch4_Chip15	
Entries	885871
Mean	3.32
Std Dev	3.849

nChipHit_F6_Fch4_Chip16

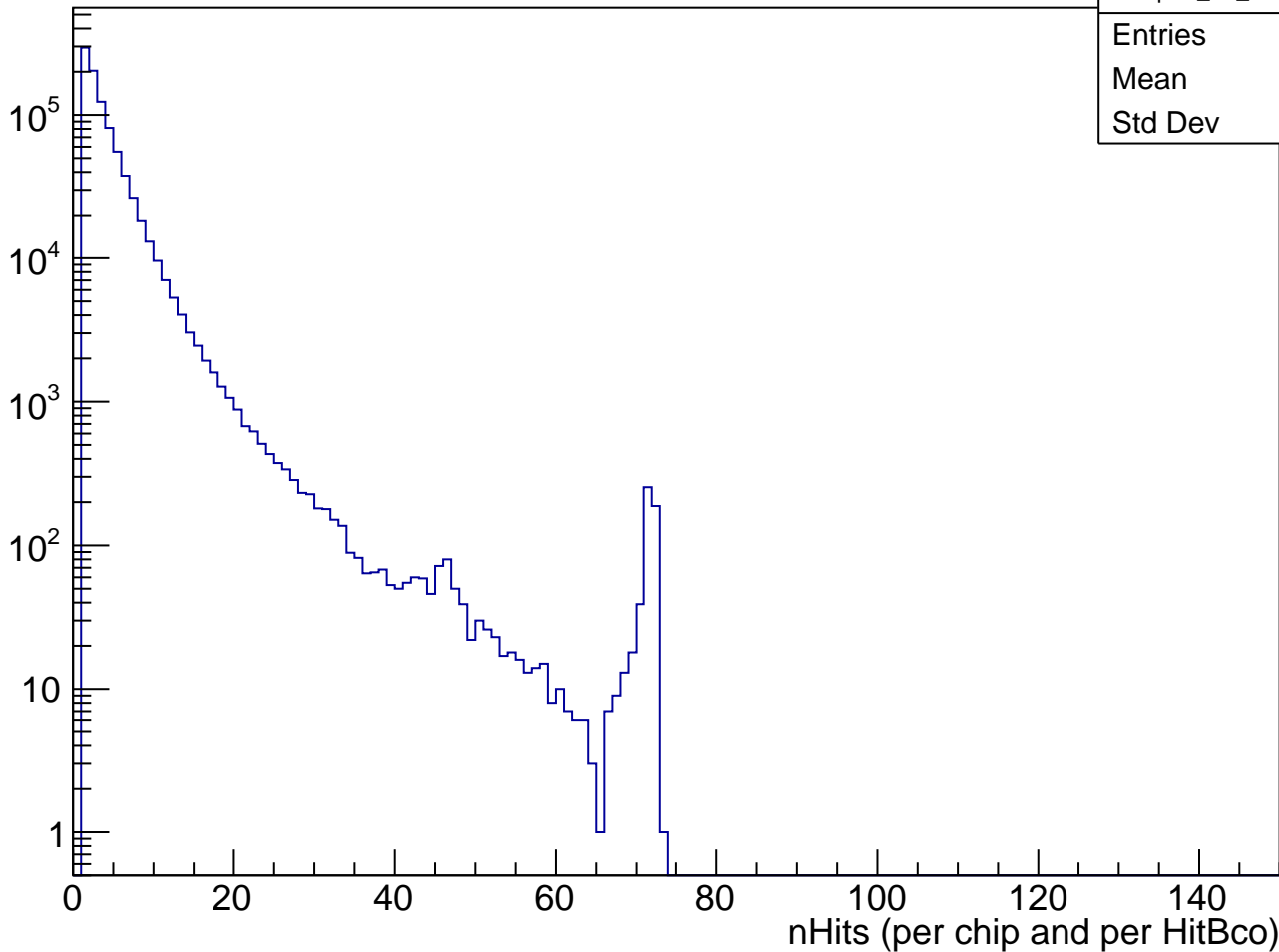
Entries



nChipHit_F6_Fch4_Chip16	
Entries	890003
Mean	3.374
Std Dev	3.881

nChipHit_F6_Fch4_Chip17

Entries

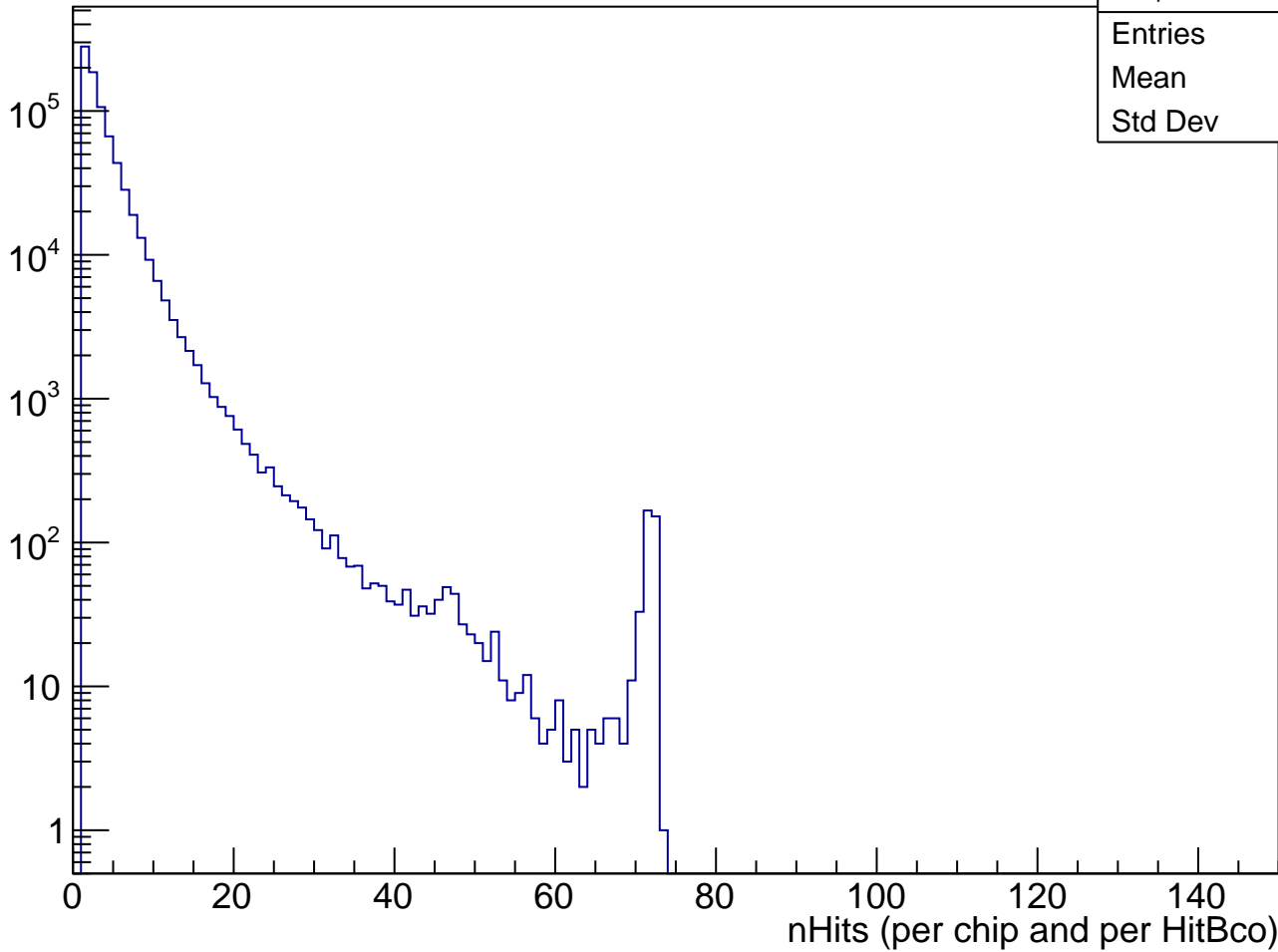


nChipHit_F6_Fch4_Chip17	
Entries	898156
Mean	3.379
Std Dev	3.913

nHits (per chip and per HitBco)

nChipHit_F6_Fch4_Chip18

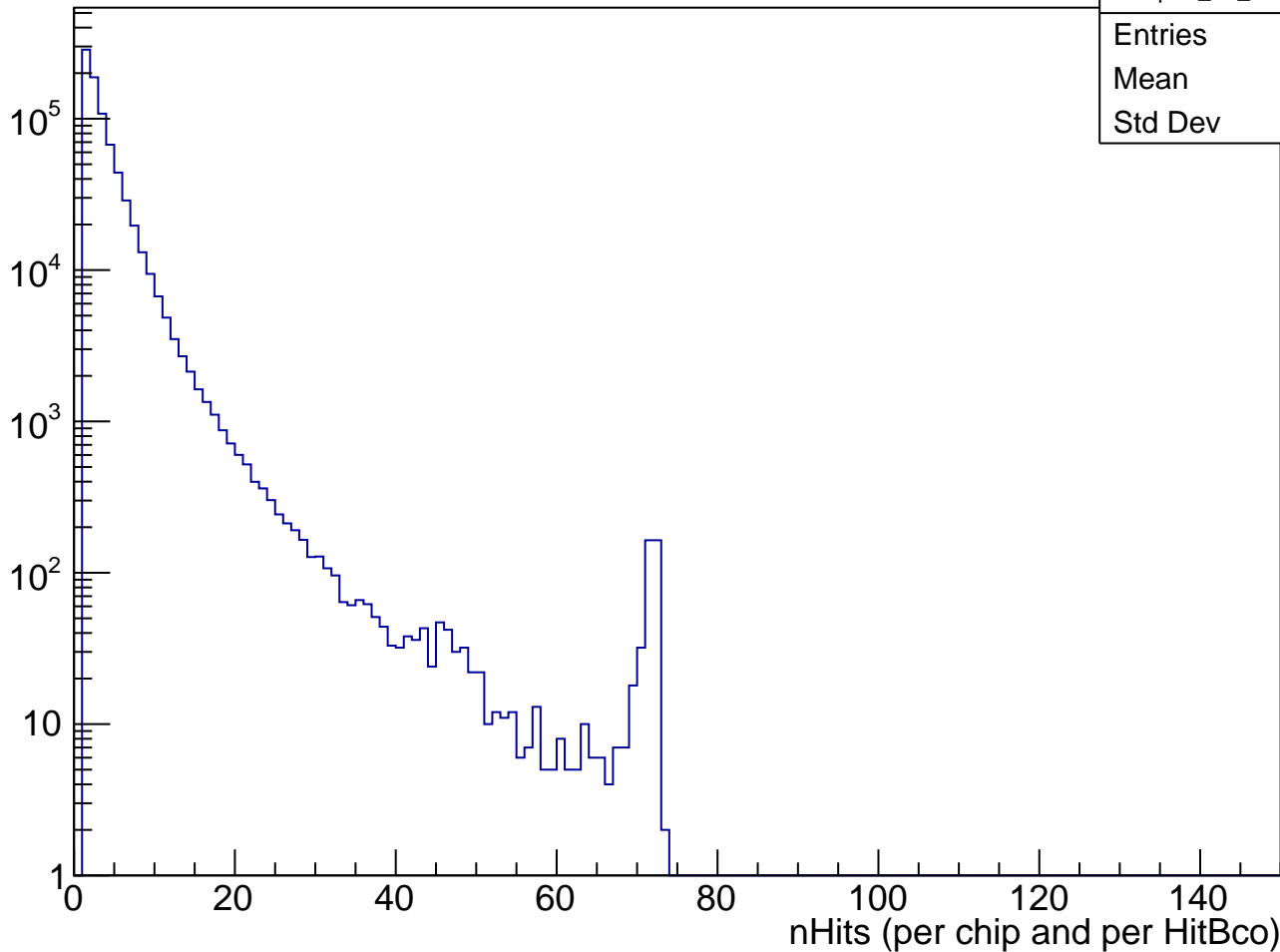
Entries



nChipHit_F6_Fch4_Chip18	
Entries	782912
Mean	3.084
Std Dev	3.577

nChipHit_F6_Fch4_Chip19

Entries

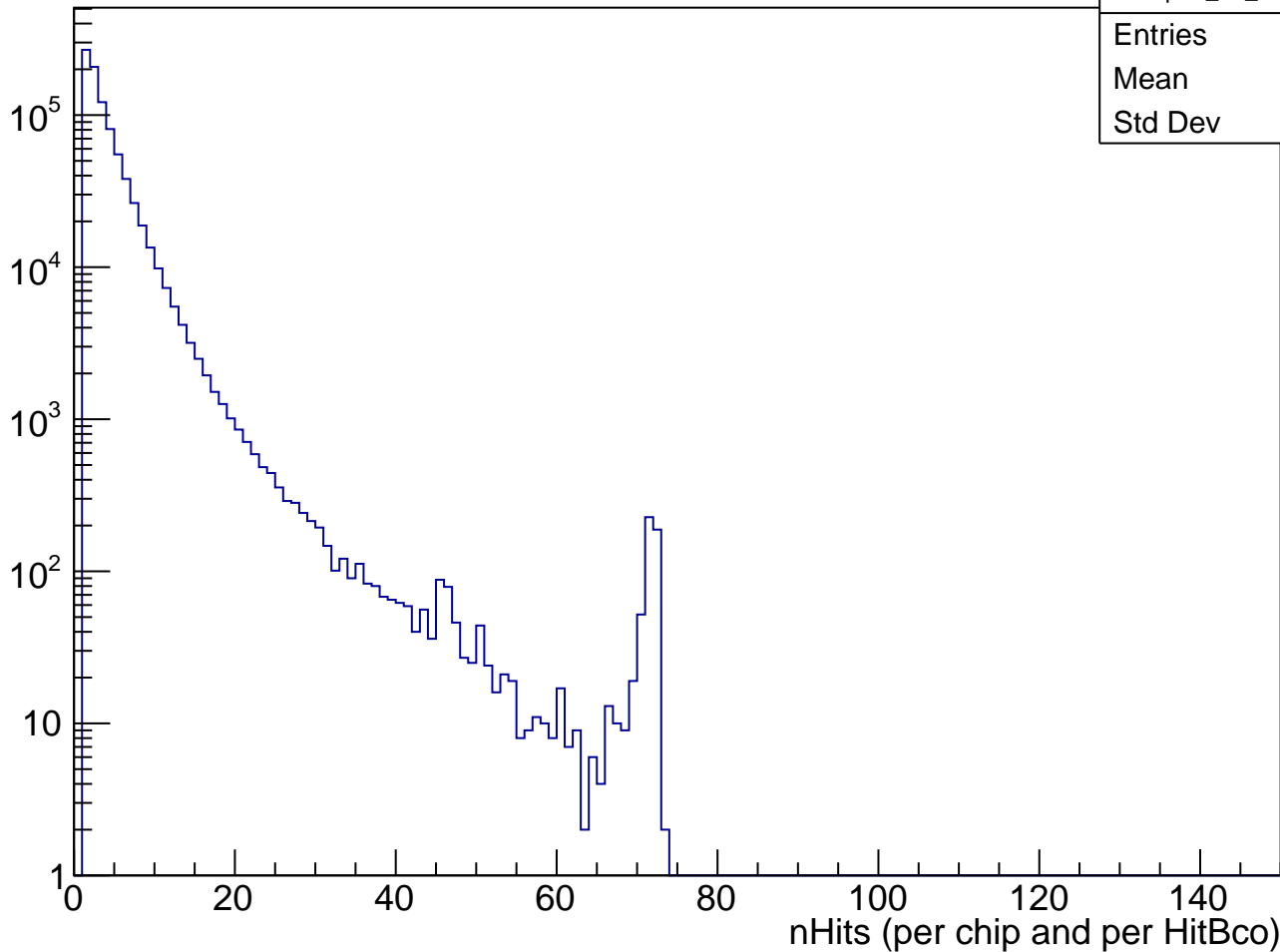


nChipHit_F6_Fch4_Chip19	
Entries	794490
Mean	3.077
Std Dev	3.569

nHits (per chip and per HitBco)

nChipHit_F6_Fch4_Chip2

Entries



nChipHit_F6_Fch4_Chip2

Entries 875175

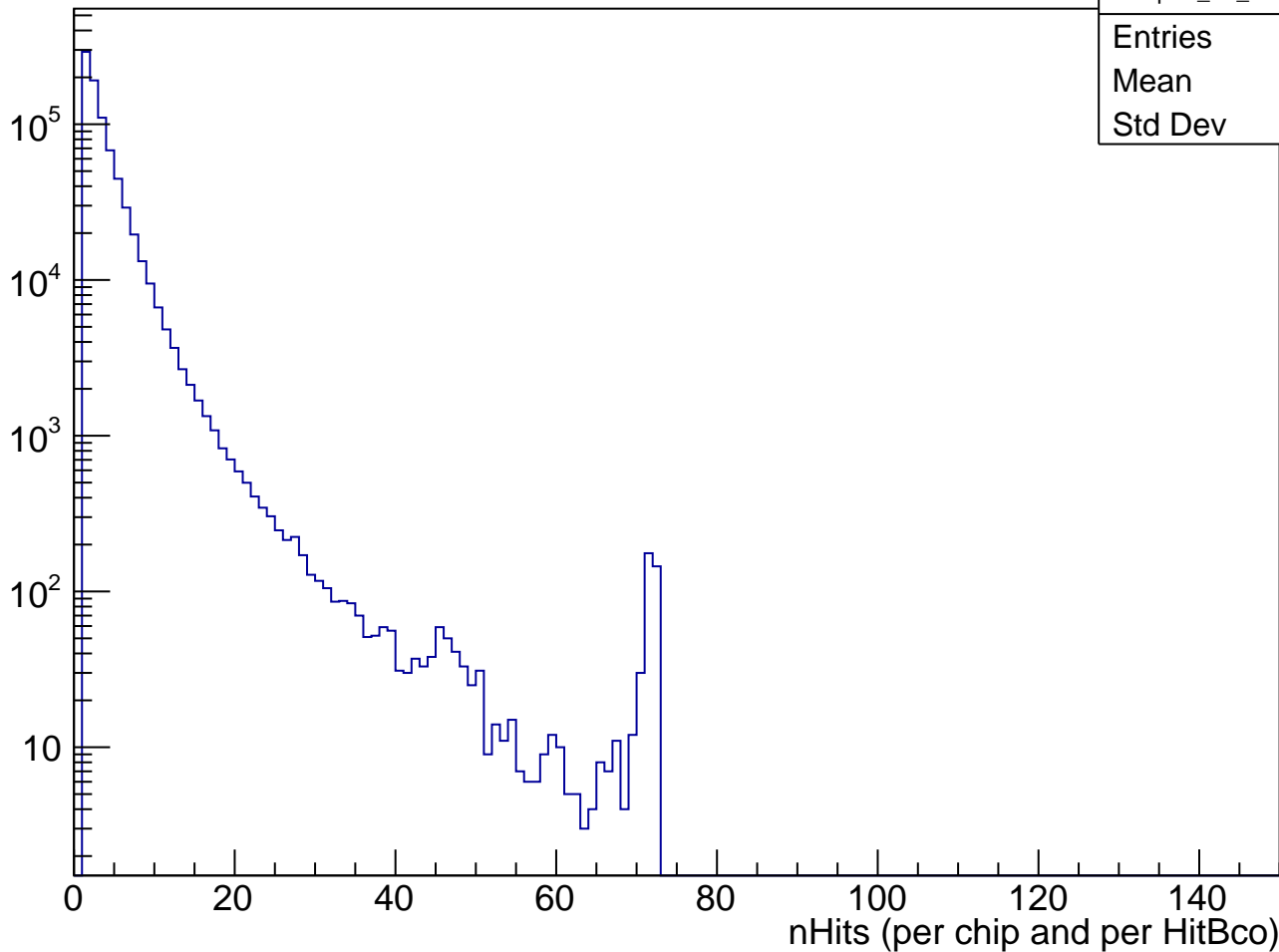
Mean 3.453

Std Dev 3.938

nHits (per chip and per HitBco)

nChipHit_F6_Fch4_Chip20

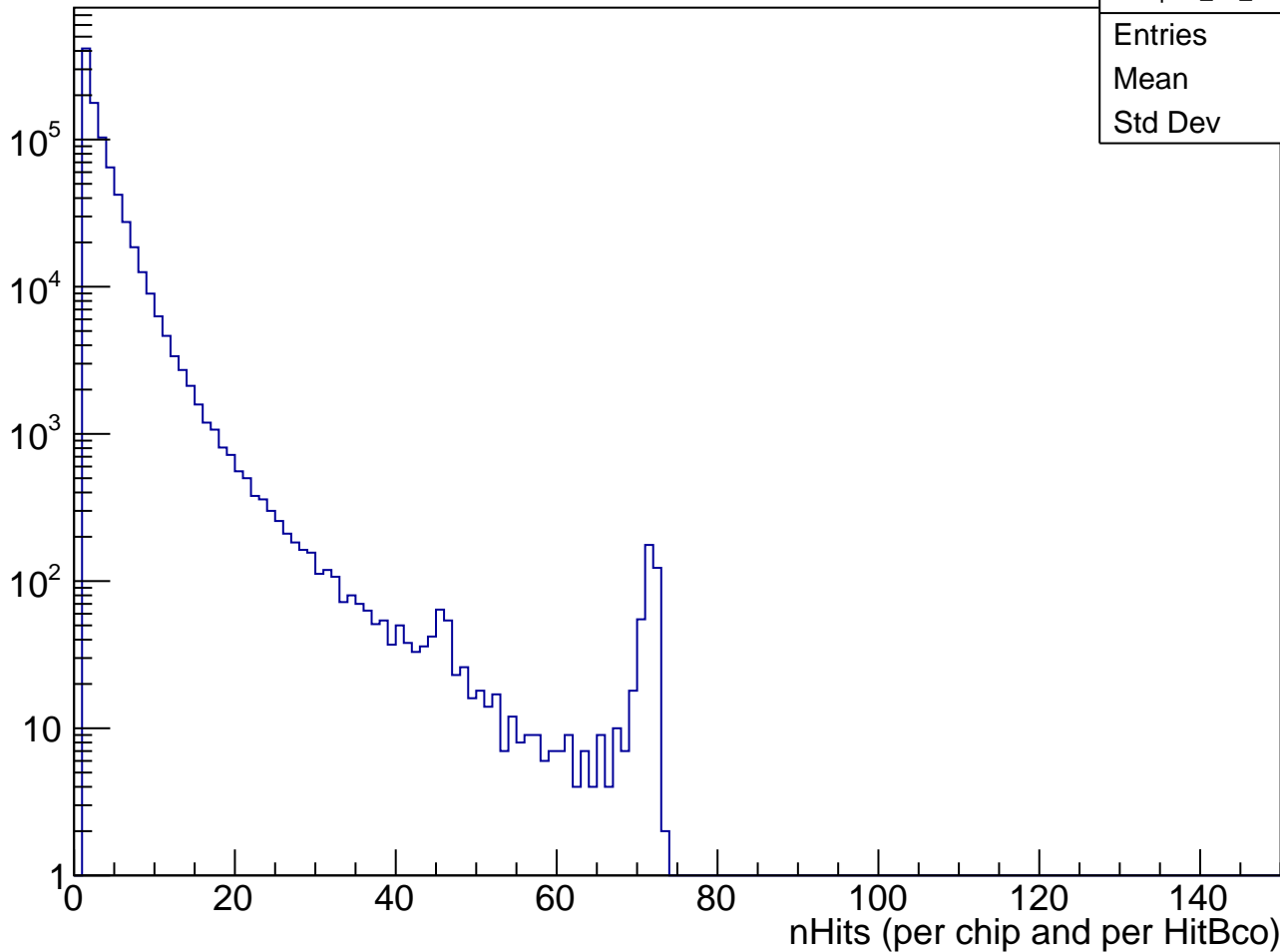
Entries



nChipHit_F6_Fch4_Chip20	
Entries	807271
Mean	3.066
Std Dev	3.564

nChipHit_F6_Fch4_Chip21

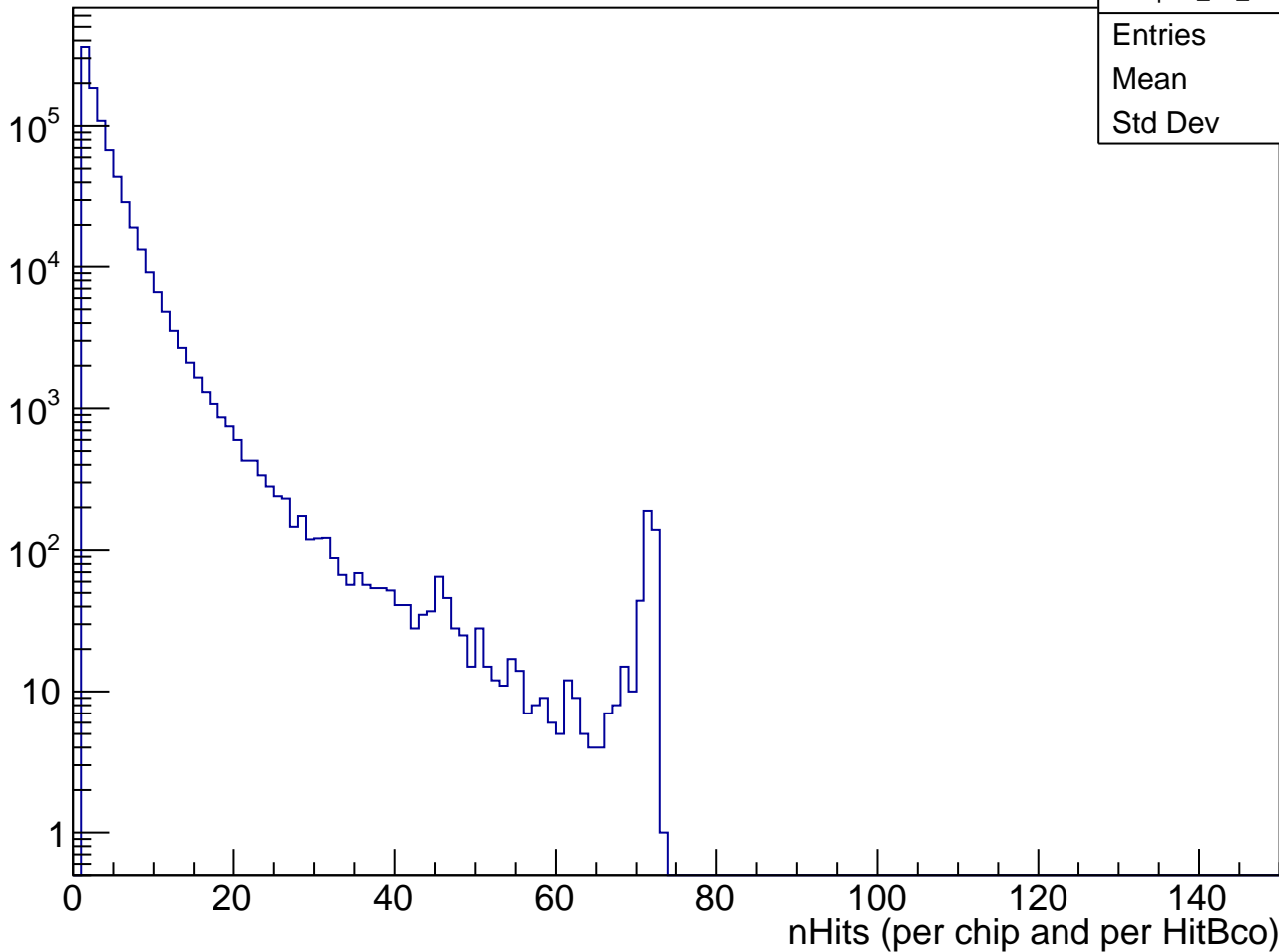
Entries



nChipHit_F6_Fch4_Chip21	
Entries	899684
Mean	2.761
Std Dev	3.416

nChipHit_F6_Fch4_Chip22

Entries

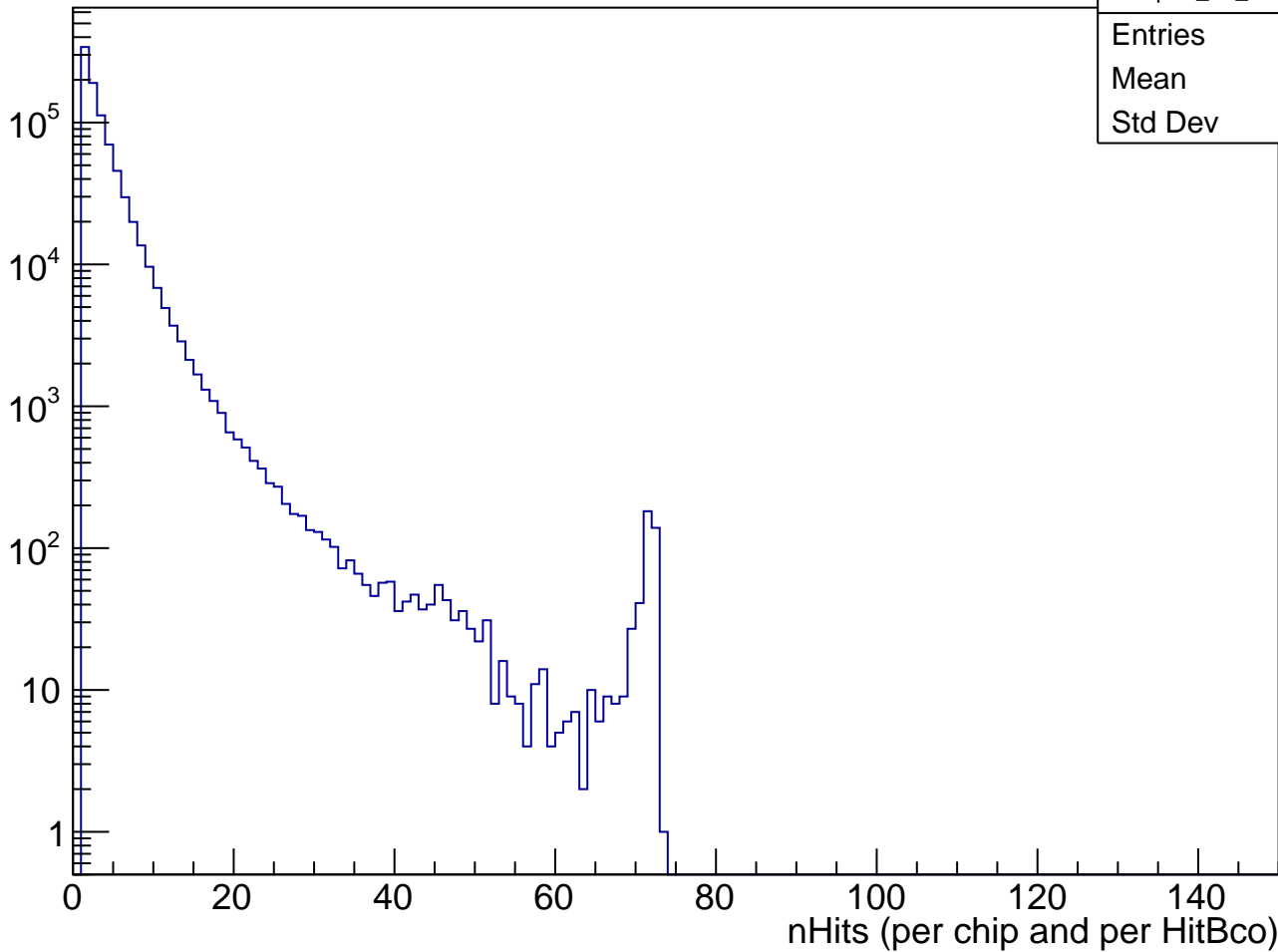


nChipHit_F6_Fch4_Chip22	
Entries	865604
Mean	2.898
Std Dev	3.482

nHits (per chip and per HitBco)

nChipHit_F6_Fch4_Chip23

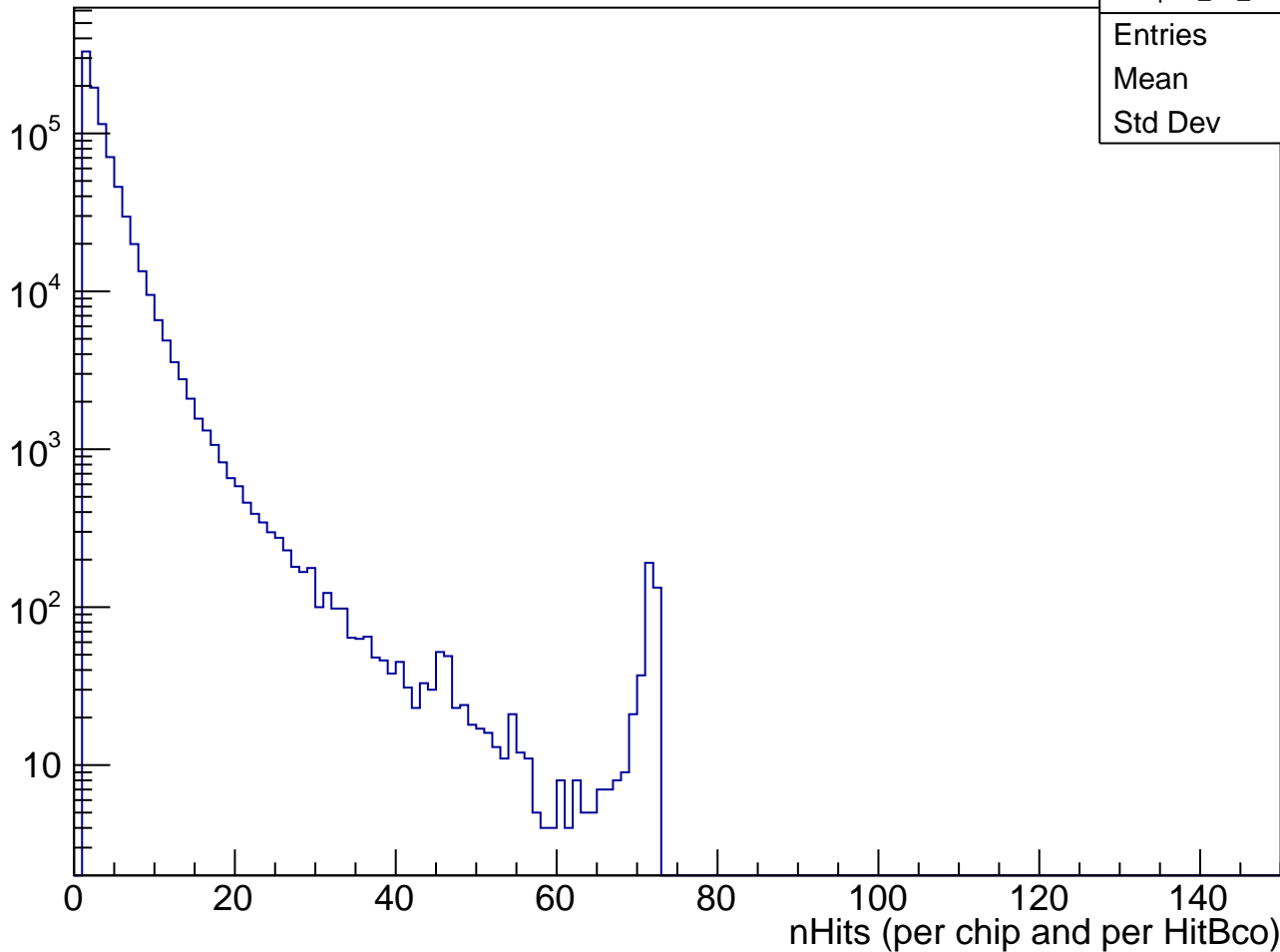
Entries



nChipHit_F6_Fch4_Chip23	
Entries	862832
Mean	2.969
Std Dev	3.52

nChipHit_F6_Fch4_Chip24

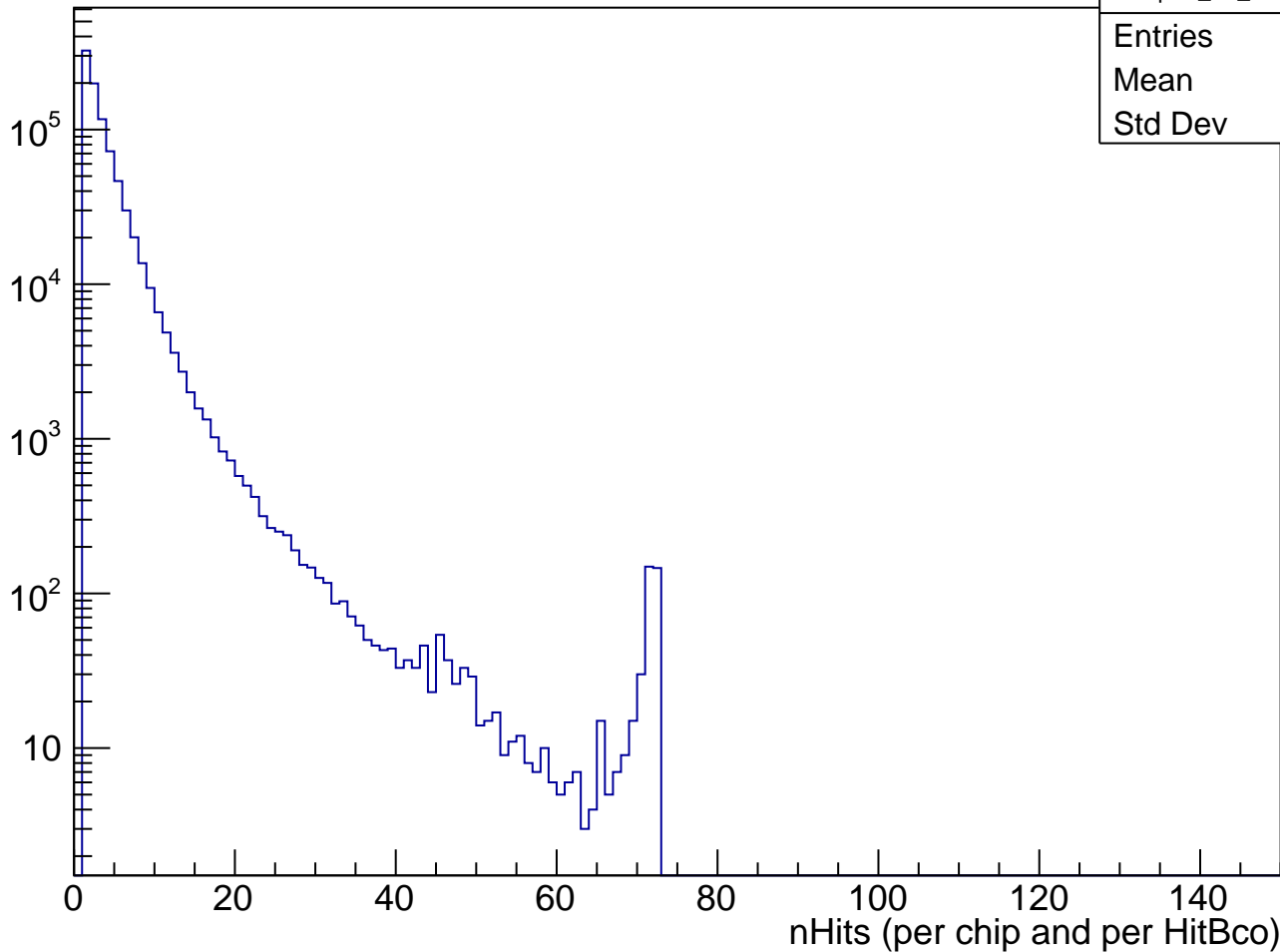
Entries



nChipHit_F6_Fch4_Chip24	
Entries	859039
Mean	2.972
Std Dev	3.472

nChipHit_F6_Fch4_Chip25

Entries

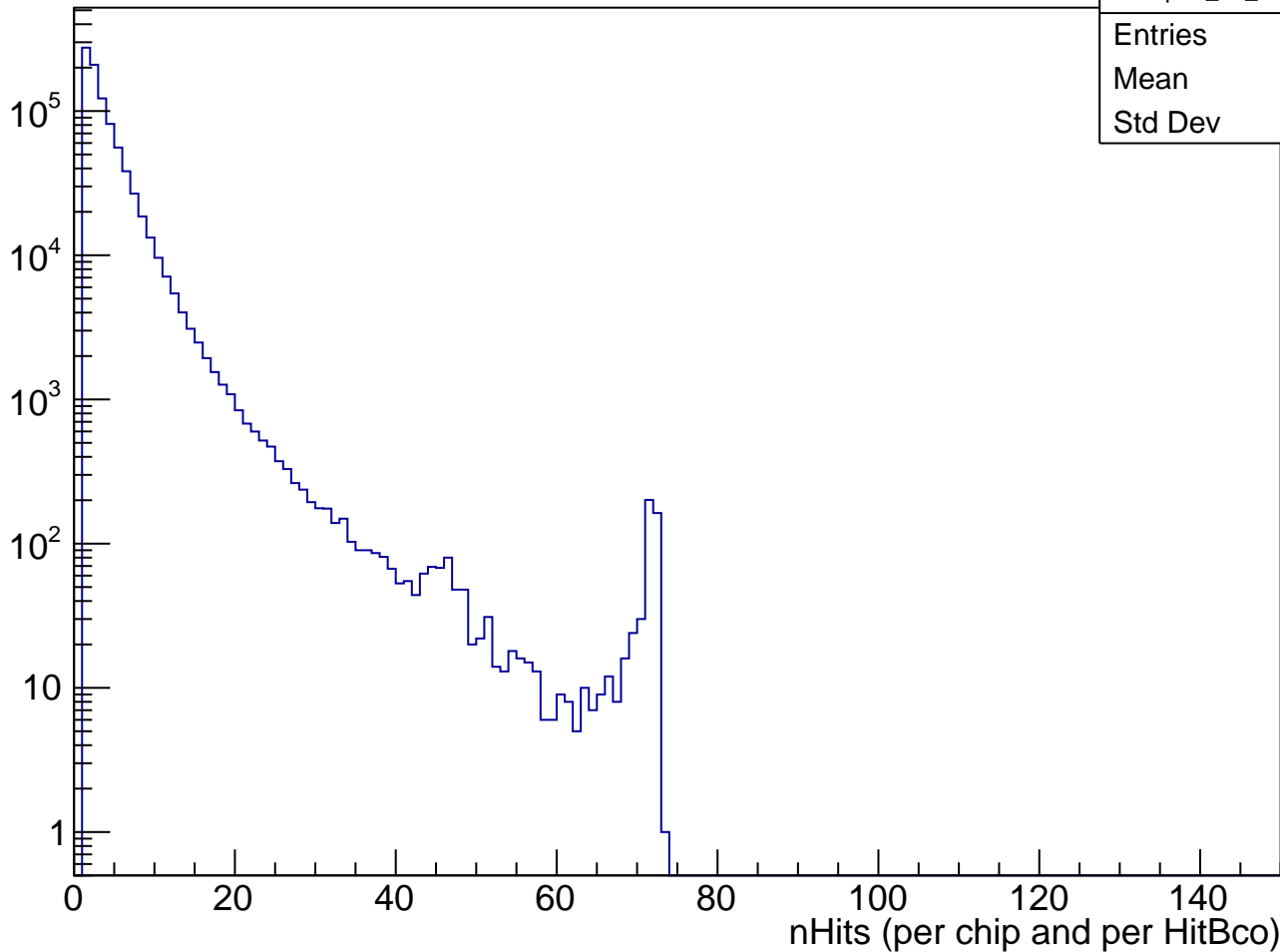


nChipHit_F6_Fch4_Chip25	
Entries	861721
Mean	2.983
Std Dev	3.432

nHits (per chip and per HitBco)

nChipHit_F6_Fch4_Chip3

Entries



nChipHit_F6_Fch4_Chip3

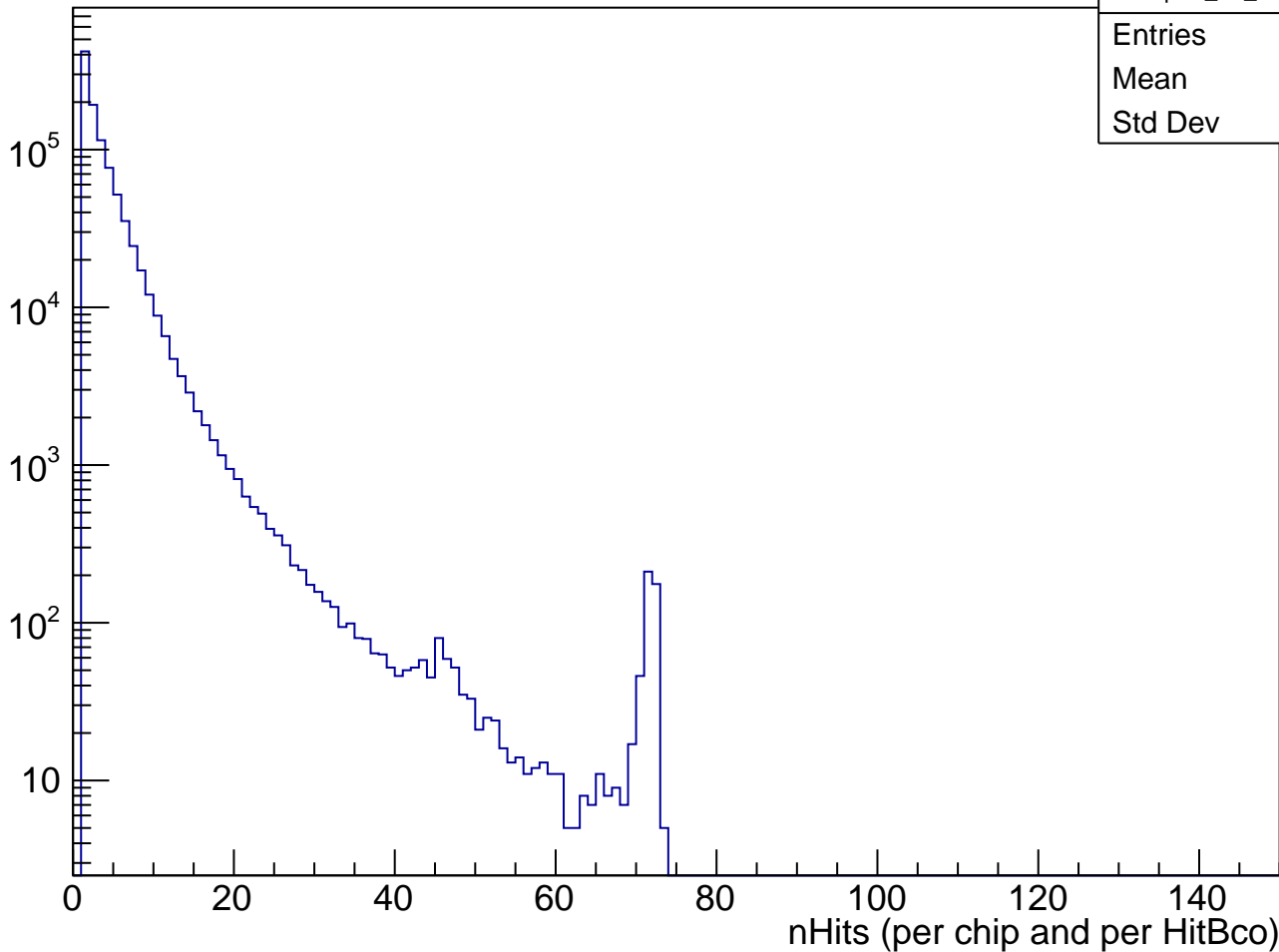
Entries 885449

Mean 3.428

Std Dev 3.894

nChipHit_F6_Fch4_Chip4

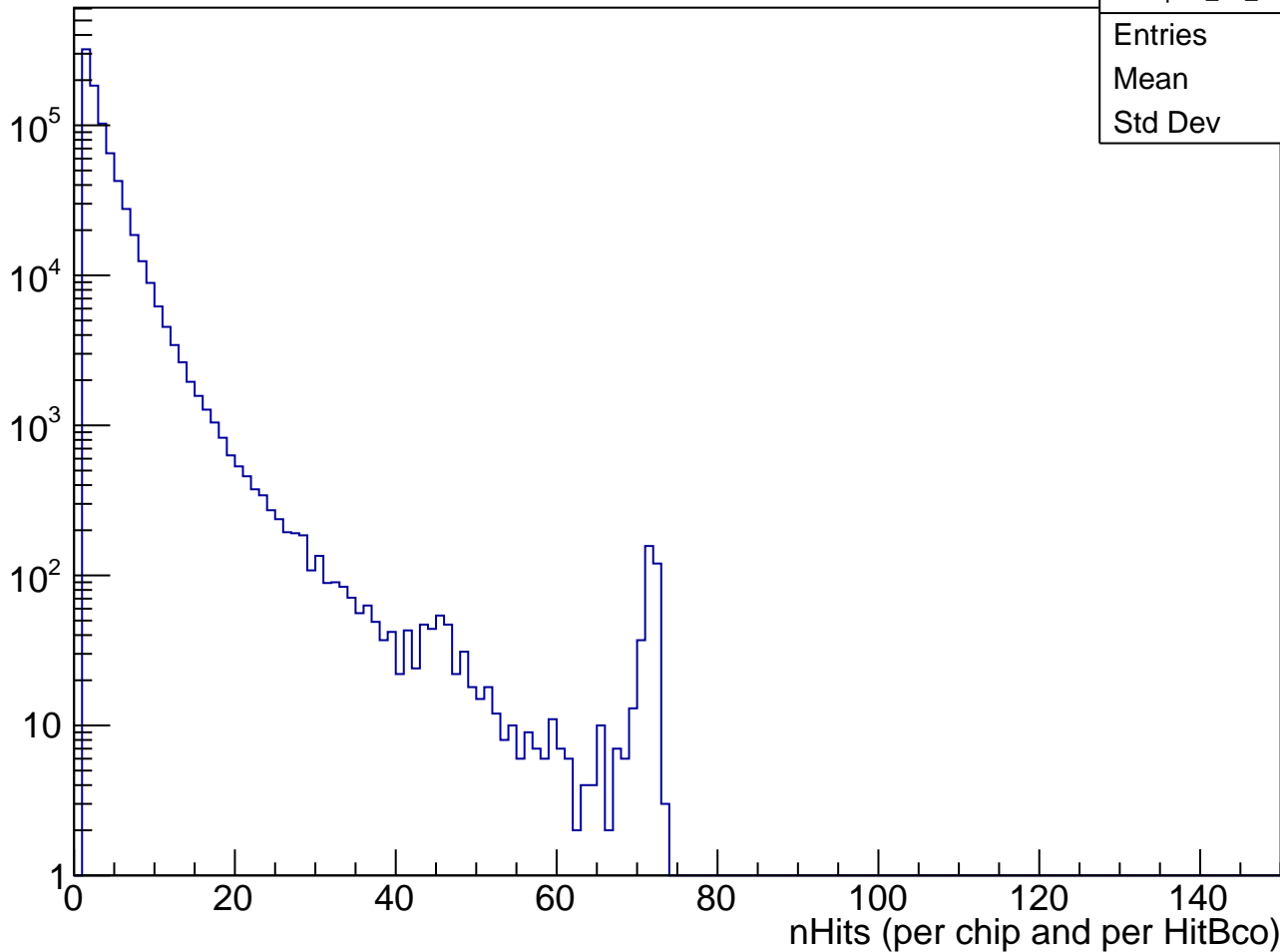
Entries



nChipHit_F6_Fch4_Chip4	
Entries	984097
Mean	3.016
Std Dev	3.686

nChipHit_F6_Fch4_Chip5

Entries



nChipHit_F6_Fch4_Chip5

Entries 811465

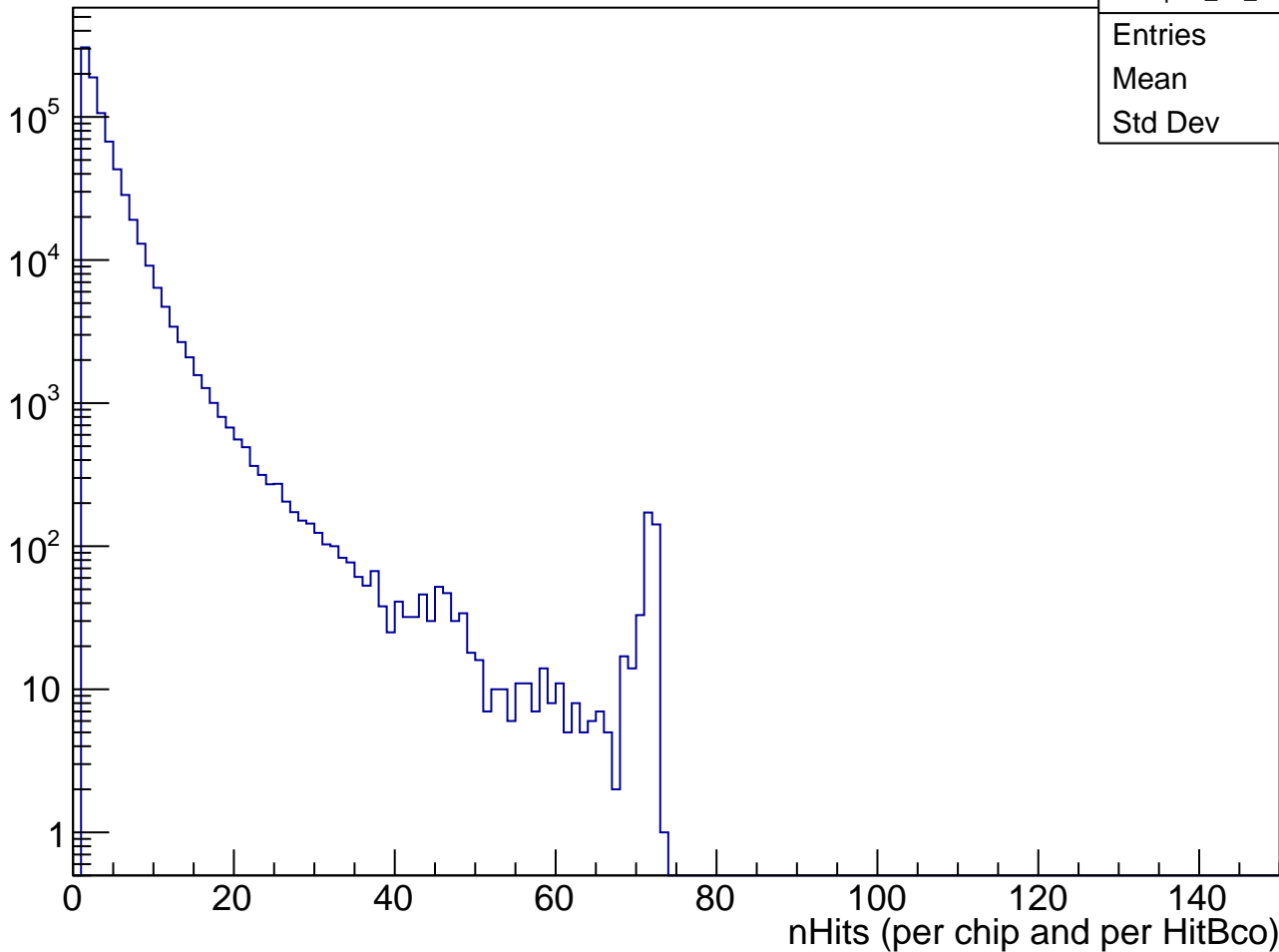
Mean 2.943

Std Dev 3.458

nHits (per chip and per HitBco)

nChipHit_F6_Fch4_Chip6

Entries

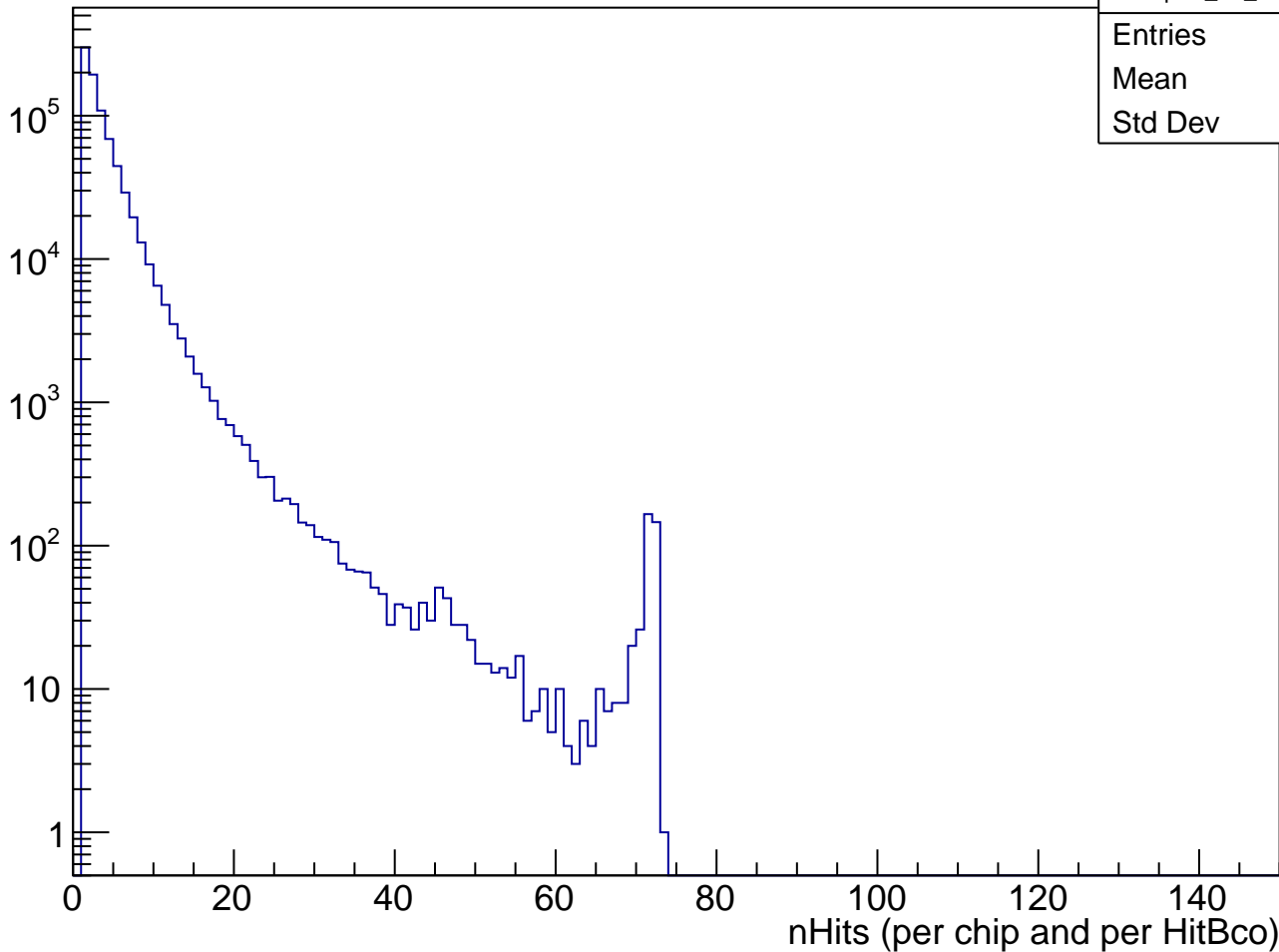


nChipHit_F6_Fch4_Chip6	
Entries	810962
Mean	2.999
Std Dev	3.511

nHits (per chip and per HitBco)

nChipHit_F6_Fch4_Chip7

Entries



nChipHit_F6_Fch4_Chip7

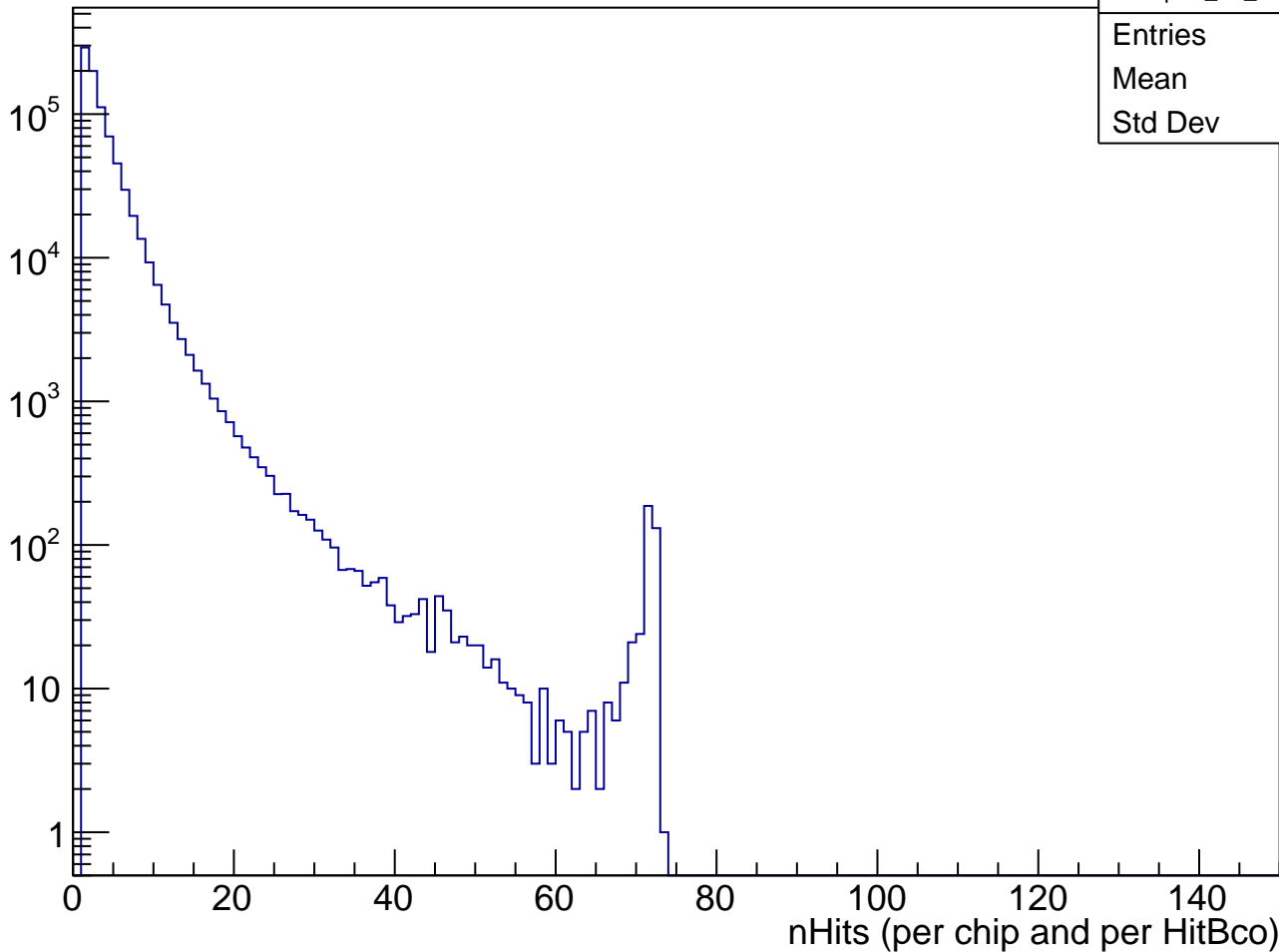
Entries 815565

Mean 3.025

Std Dev 3.502

nChipHit_F6_Fch4_Chip8

Entries



nChipHit_F6_Fch4_Chip8

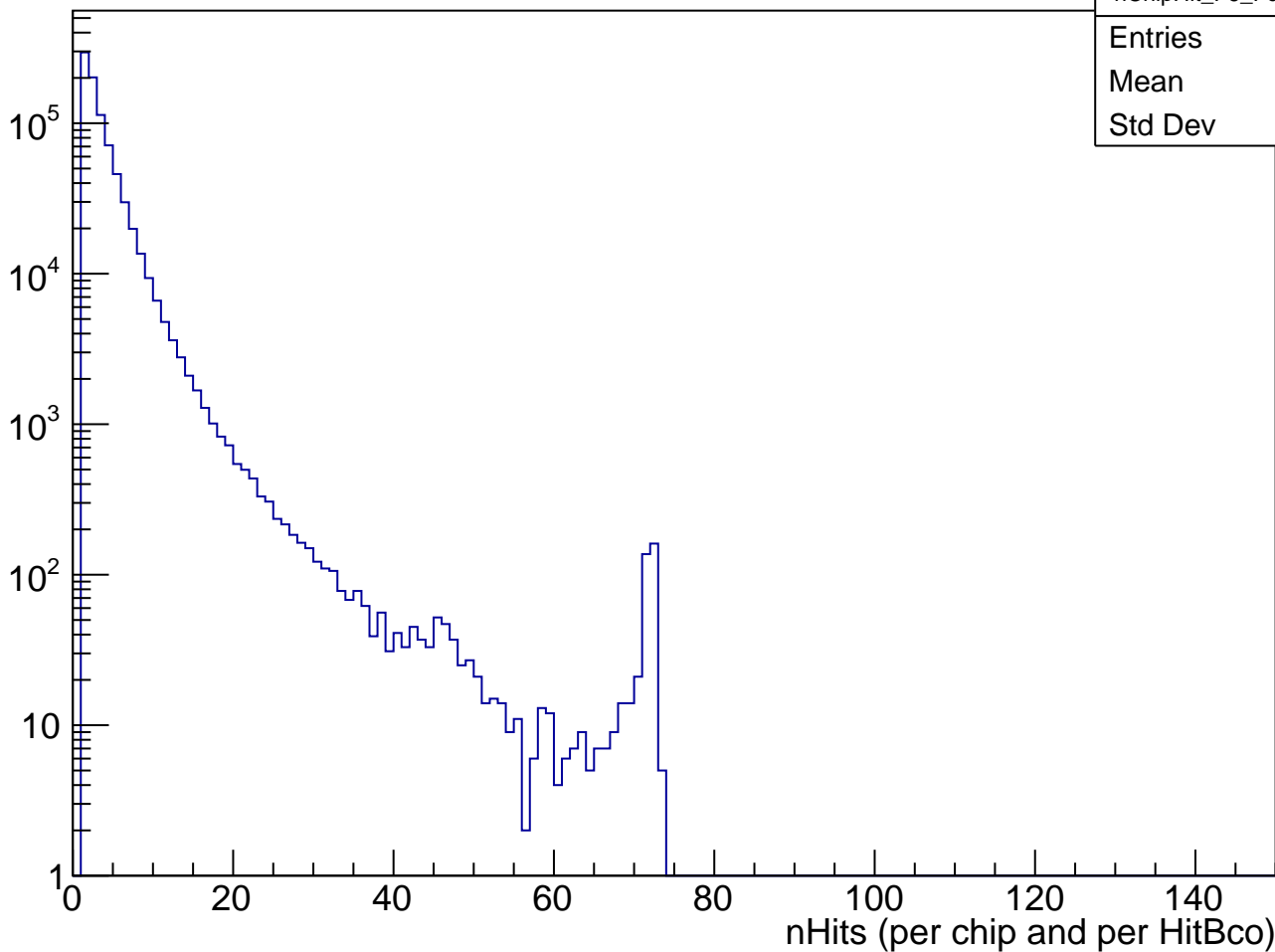
Entries 819395

Mean 3.049

Std Dev 3.487

nChipHit_F6_Fch4_Chip9

Entries



nChipHit_F6_Fch4_Chip9

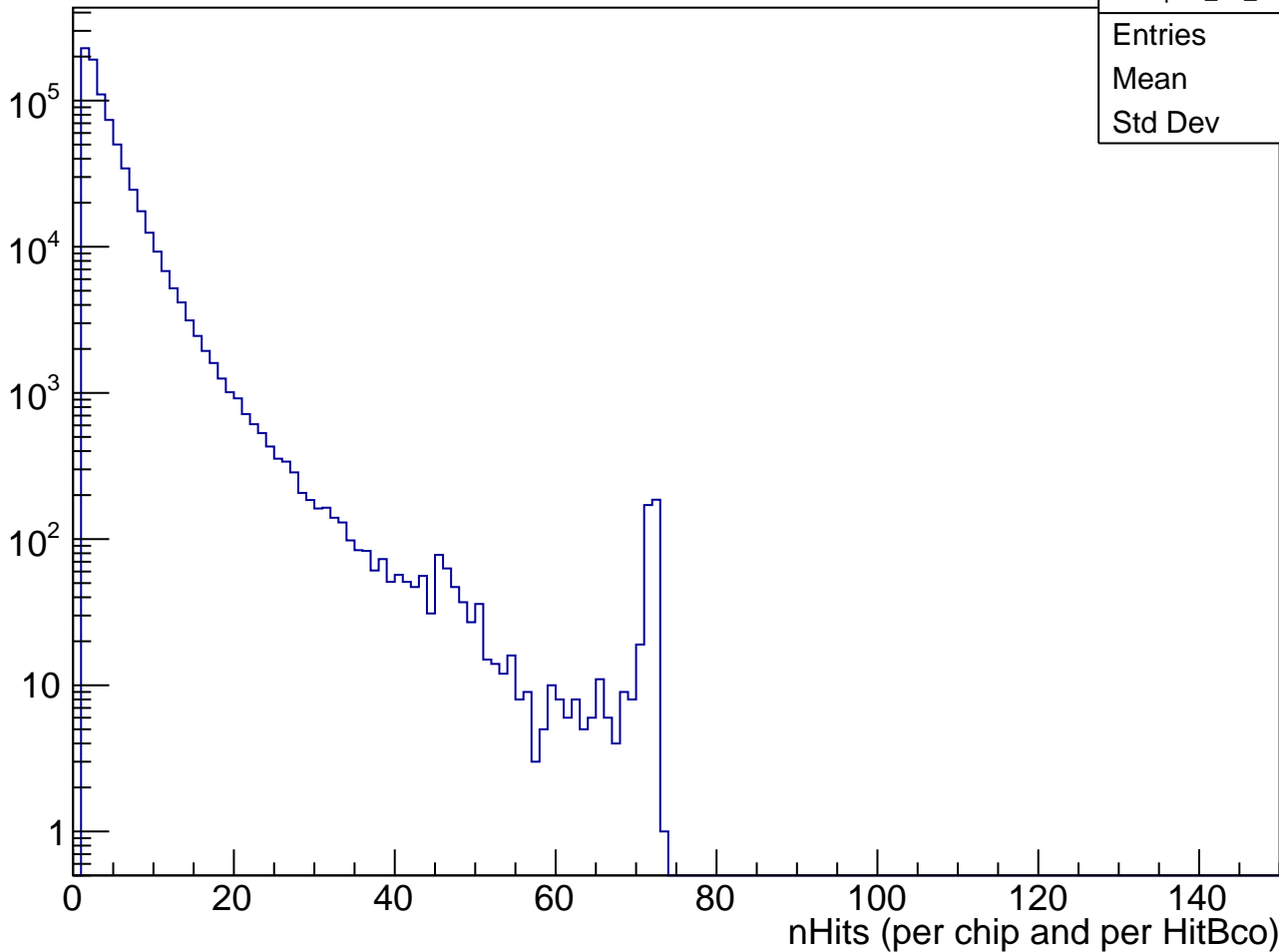
Entries 830657

Mean 3.048

Std Dev 3.496

nChipHit_F6_Fch5_Chip0

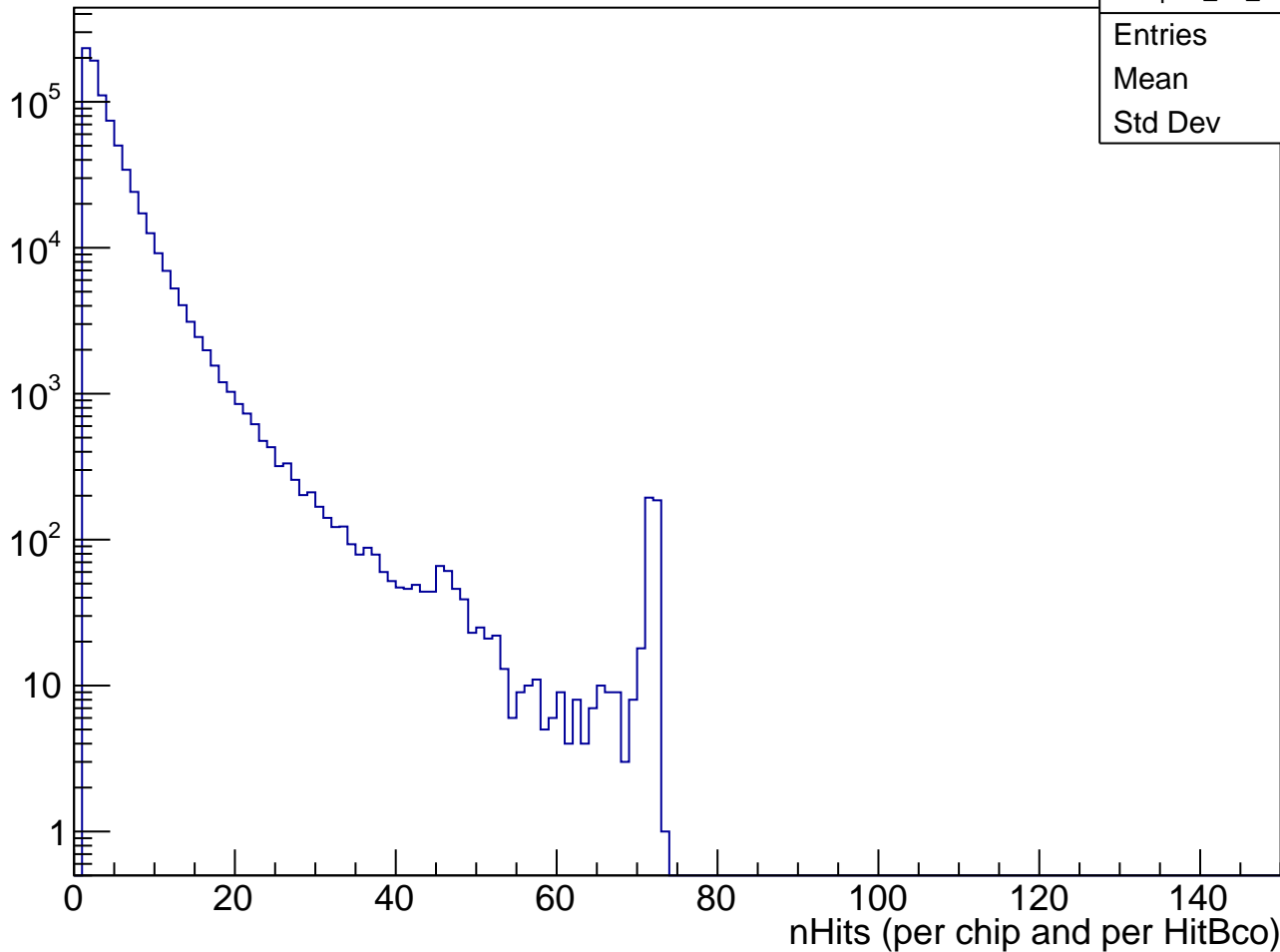
Entries



nChipHit_F6_Fch5_Chip0	
Entries	785354
Mean	3.542
Std Dev	3.981

nChipHit_F6_Fch5_Chip1

Entries

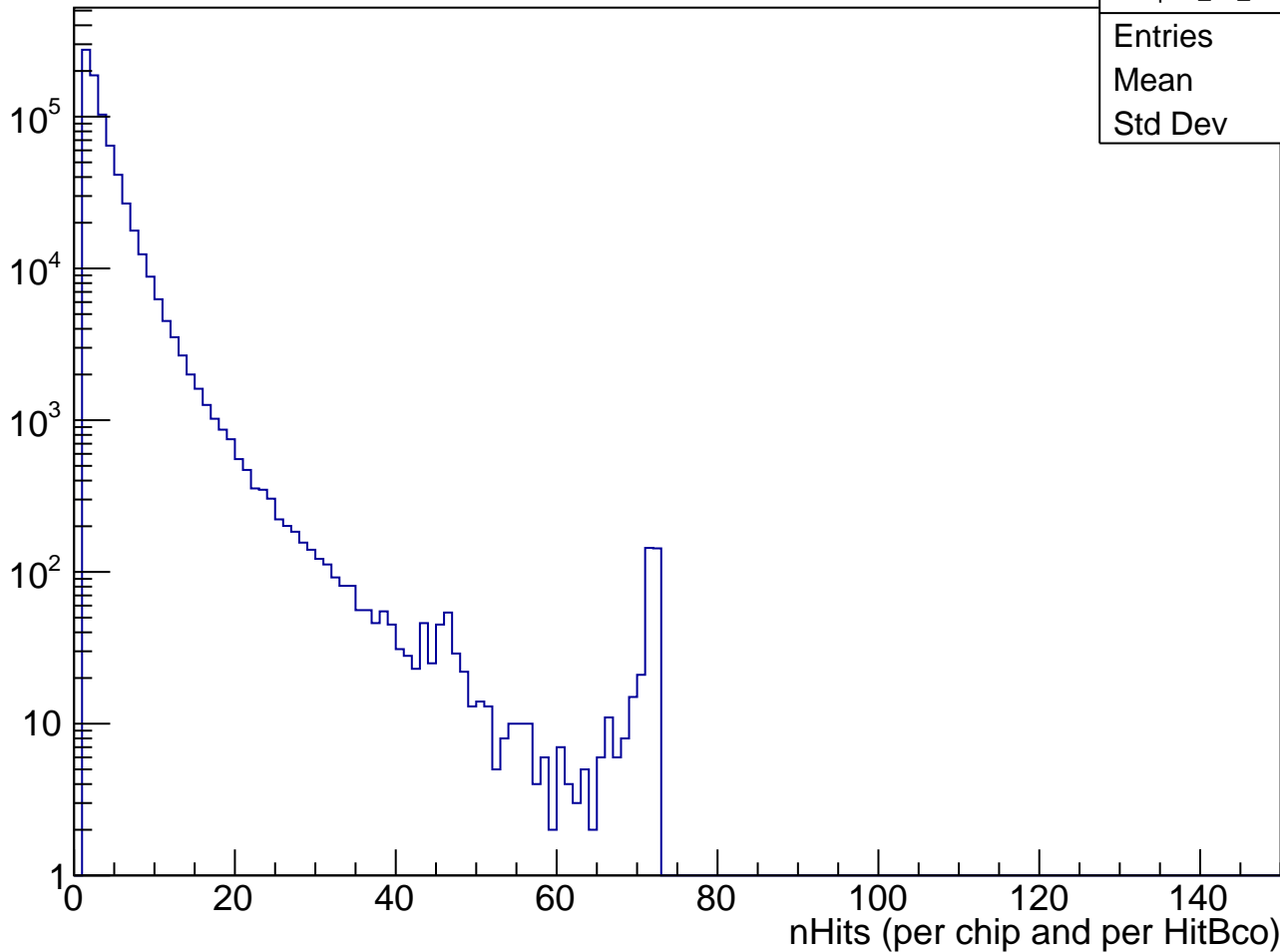


nChipHit_F6_Fch5_Chip1	
Entries	791396
Mean	3.515
Std Dev	3.963

nHits (per chip and per HitBco)

nChipHit_F6_Fch5_Chip10

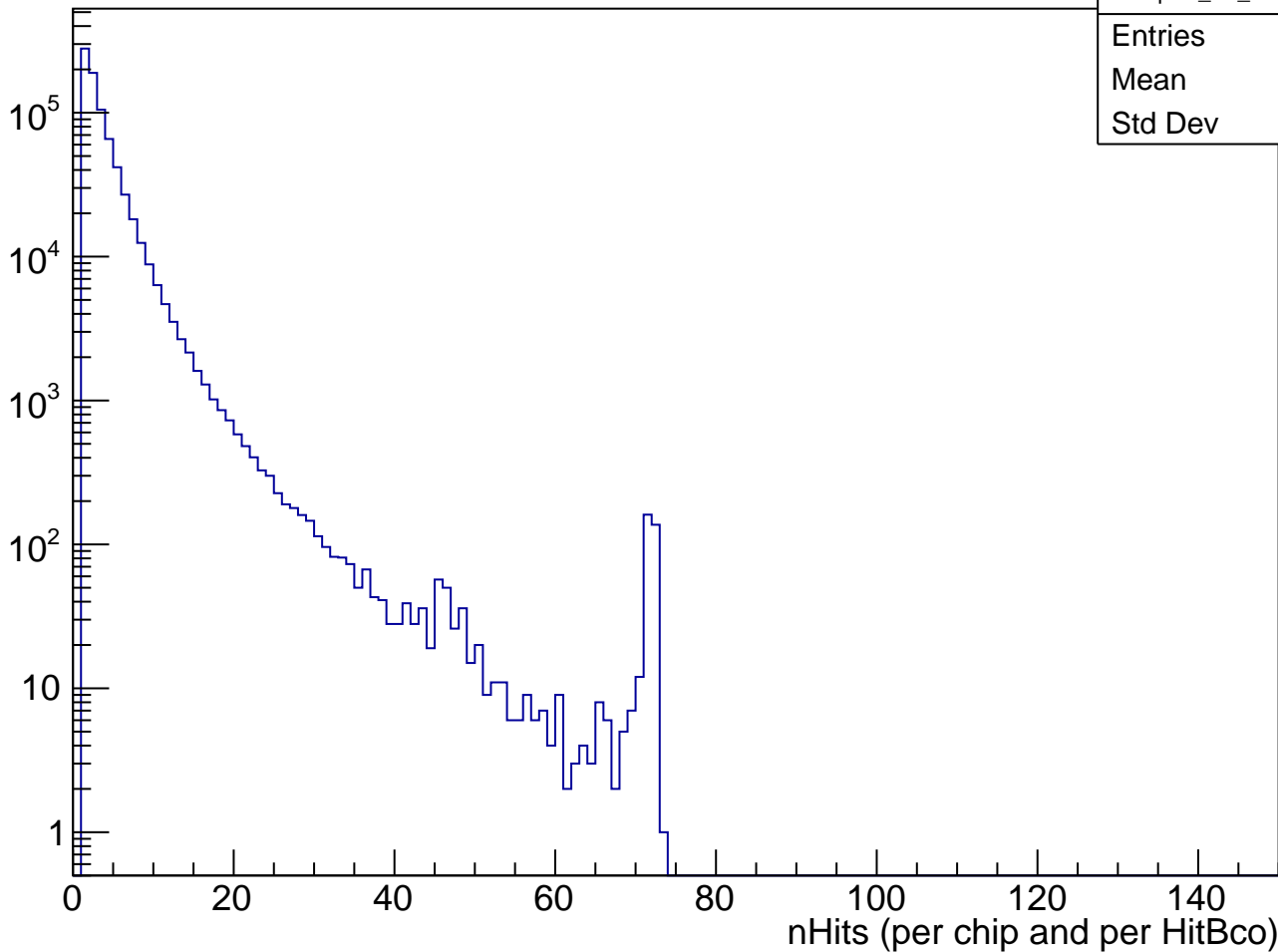
Entries



nChipHit_F6_Fch5_Chip10	
Entries	766861
Mean	3.045
Std Dev	3.515

nChipHit_F6_Fch5_Chip11

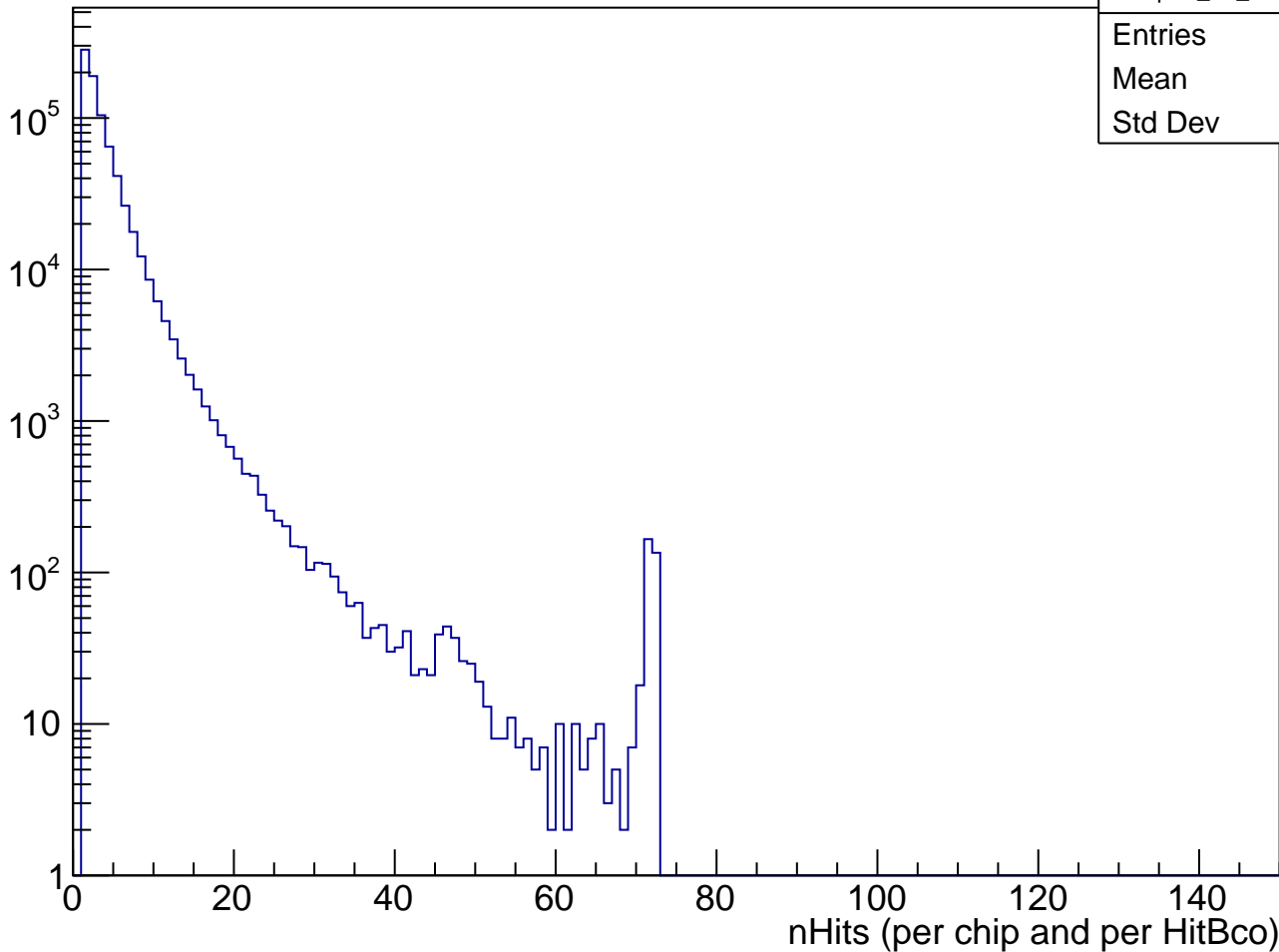
Entries



nChipHit_F6_Fch5_Chip11	
Entries	776516
Mean	3.043
Std Dev	3.492

nChipHit_F6_Fch5_Chip12

Entries

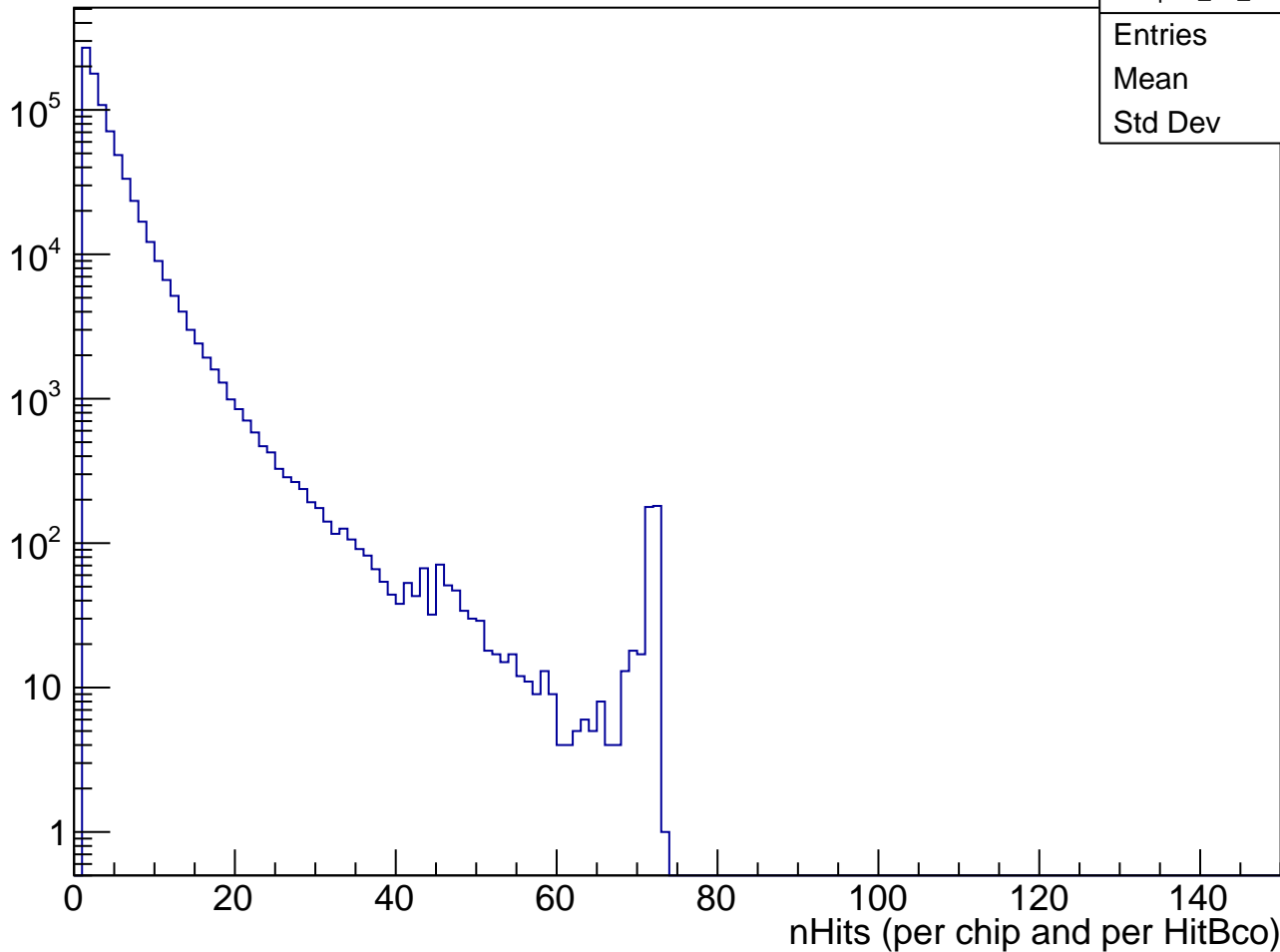


nChipHit_F6_Fch5_Chip12	
Entries	775475
Mean	3.01
Std Dev	3.469

nHits (per chip and per HitBco)

nChipHit_F6_Fch5_Chip13

Entries

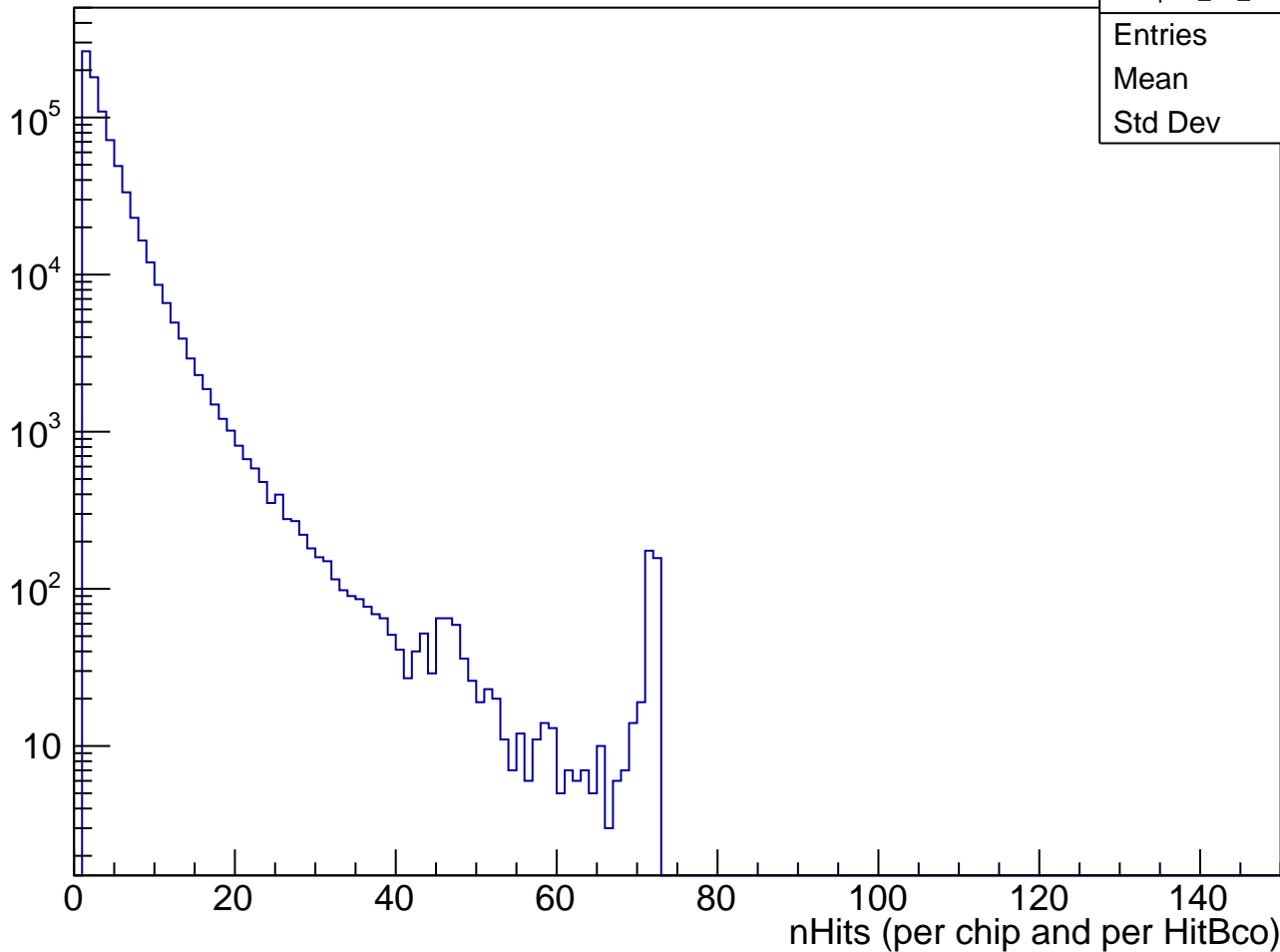


nChipHit_F6_Fch5_Chip13	
Entries	803614
Mean	3.404
Std Dev	3.938

nHits (per chip and per HitBco)

nChipHit_F6_Fch5_Chip14

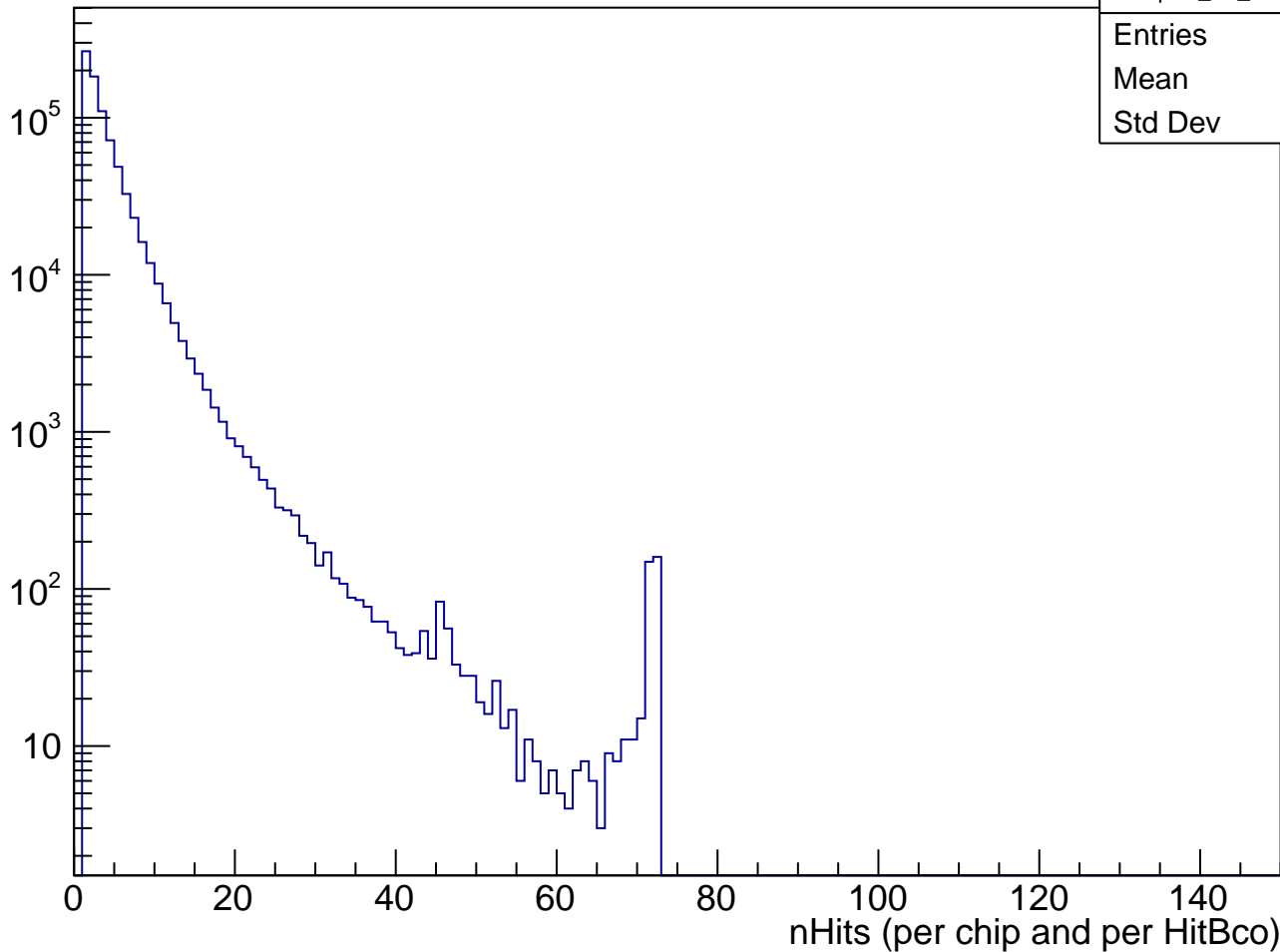
Entries



nChipHit_F6_Fch5_Chip14	
Entries	800259
Mean	3.391
Std Dev	3.881

nChipHit_F6_Fch5_Chip15

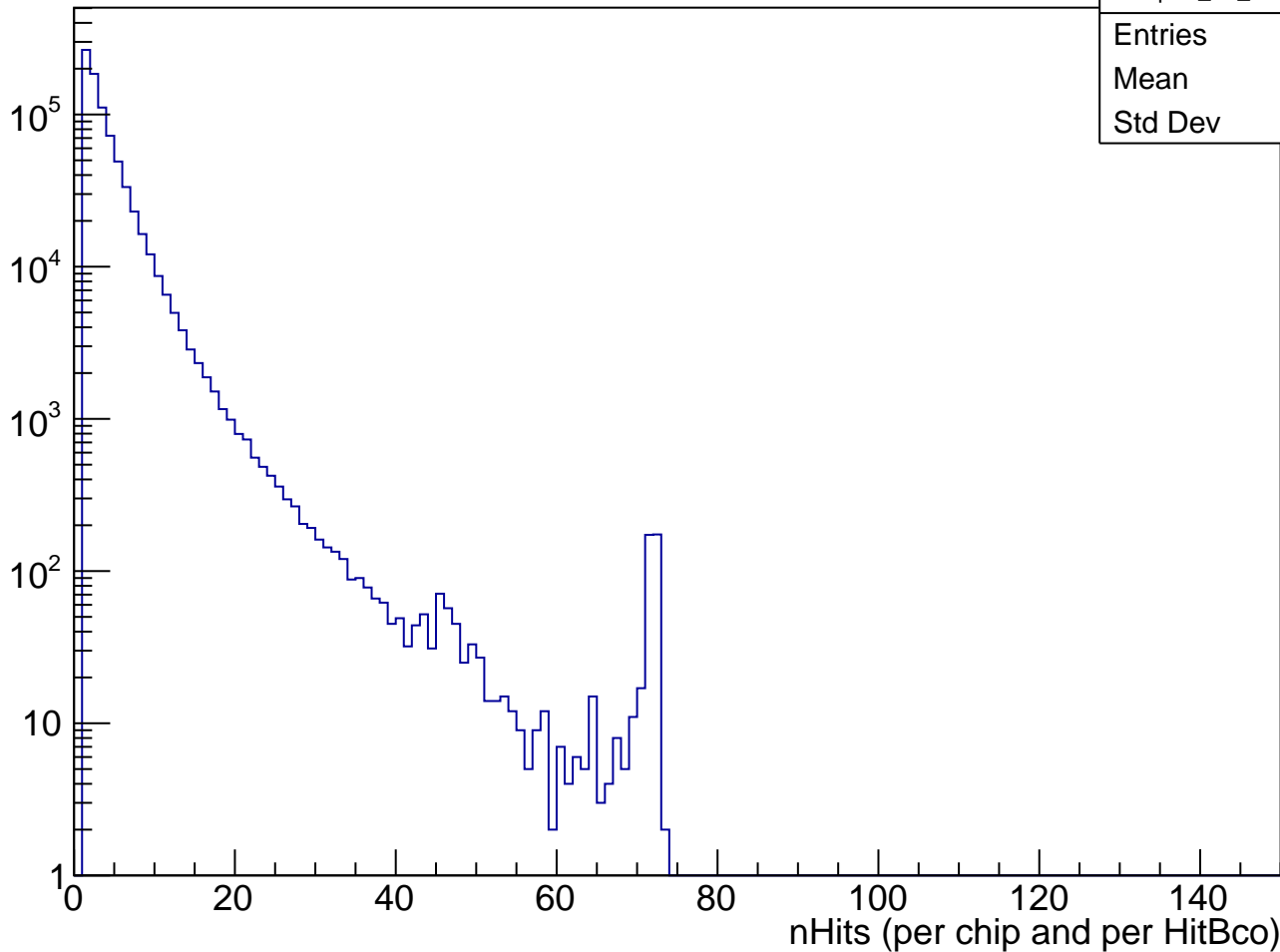
Entries



nChipHit_F6_Fch5_Chip15	
Entries	803526
Mean	3.375
Std Dev	3.85

nChipHit_F6_Fch5_Chip16

Entries

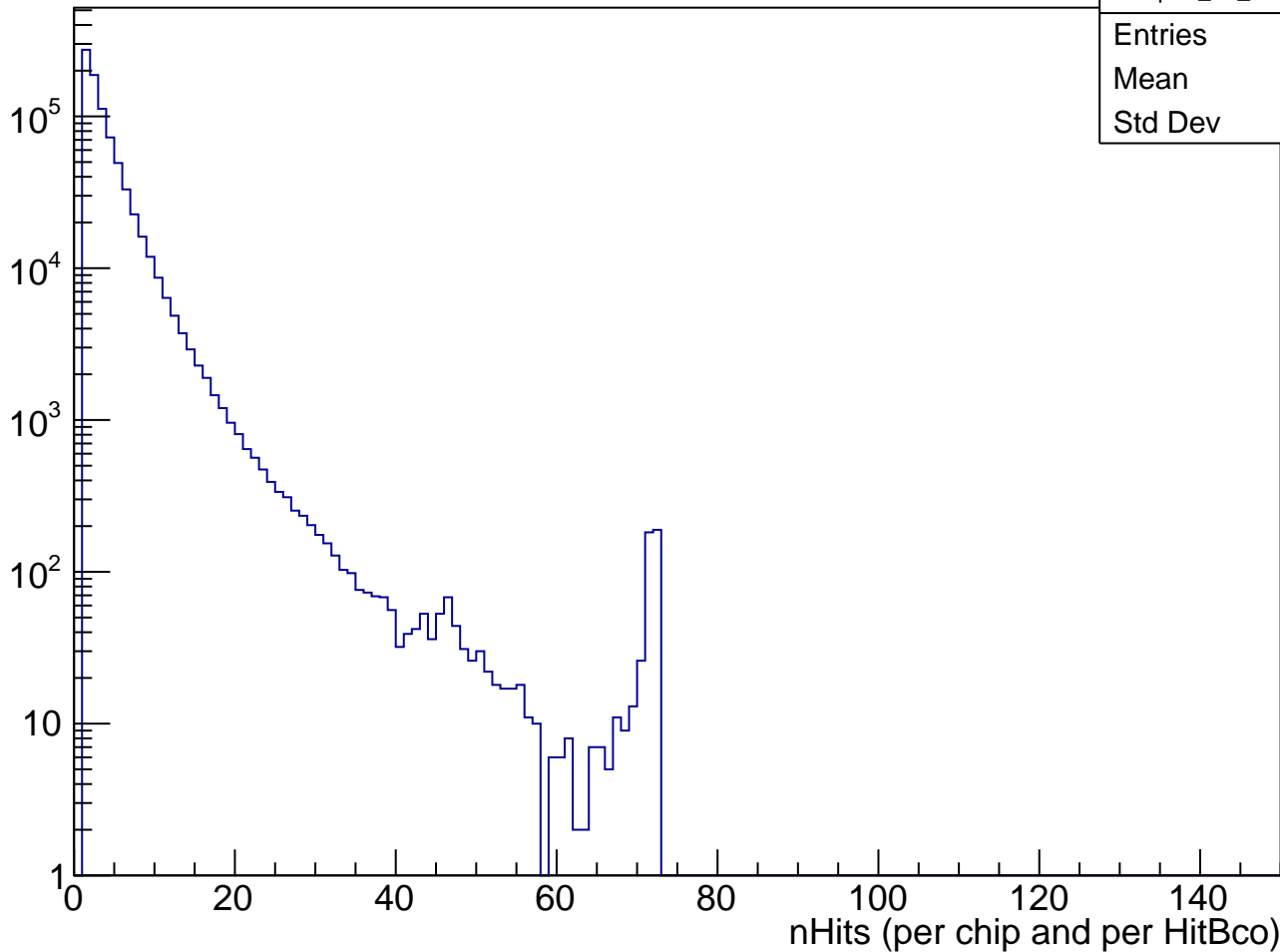


nChipHit_F6_Fch5_Chip16	
Entries	809352
Mean	3.377
Std Dev	3.865

nHits (per chip and per HitBco)

nChipHit_F6_Fch5_Chip17

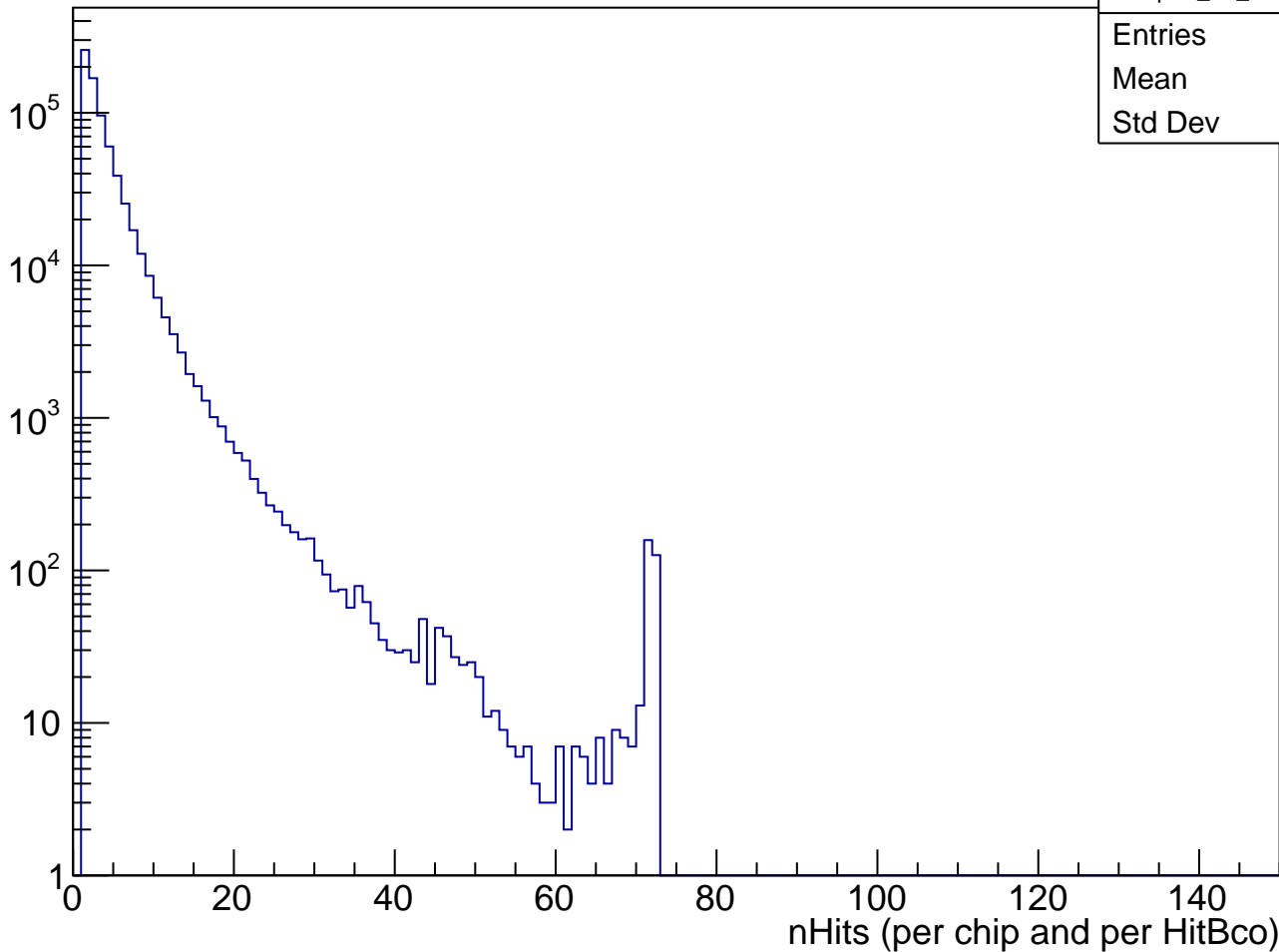
Entries



nChipHit_F6_Fch5_Chip17	
Entries	820319
Mean	3.341
Std Dev	3.87

nChipHit_F6_Fch5_Chip18

Entries

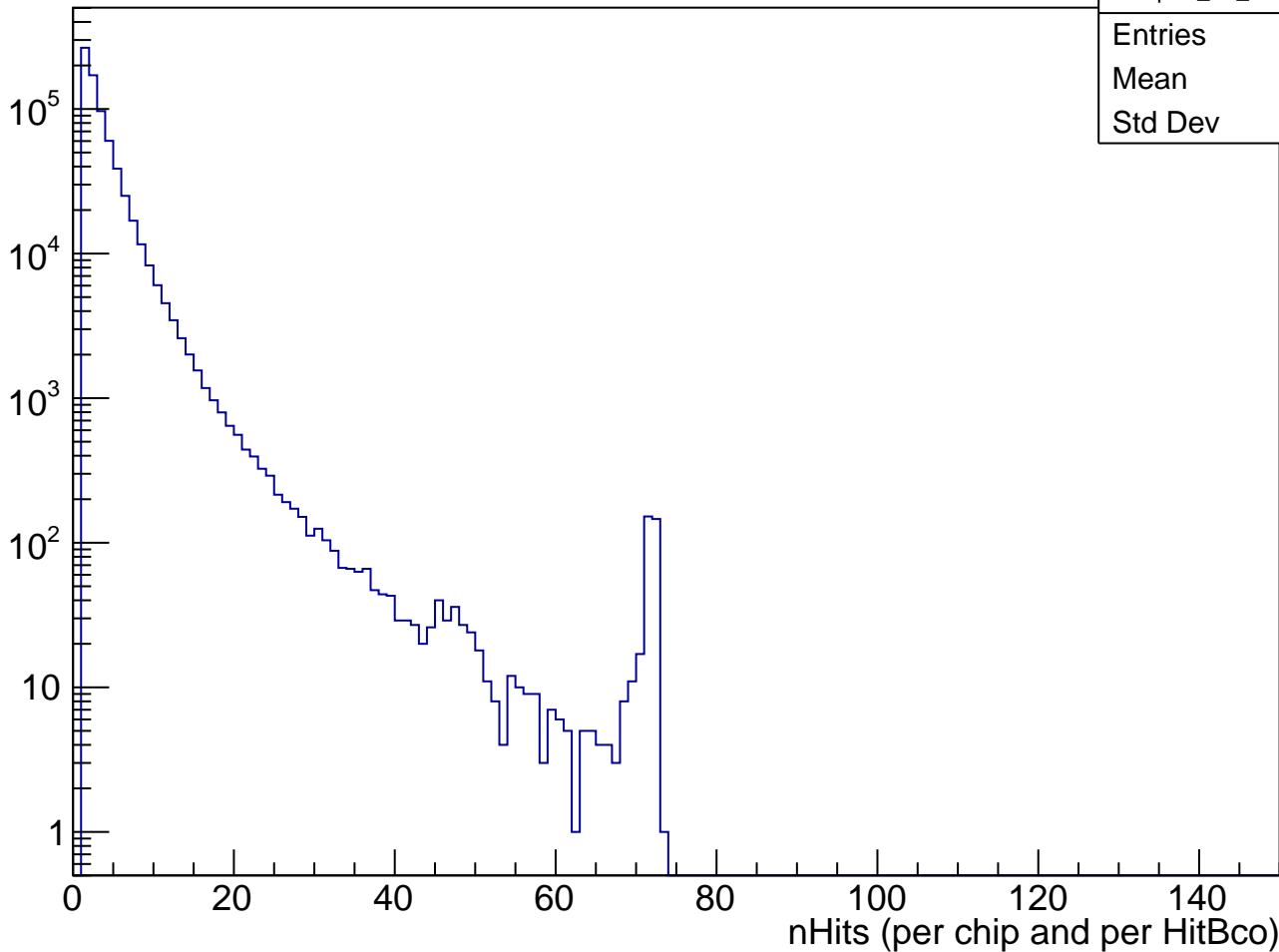


nChipHit_F6_Fch5_Chip18	
Entries	713752
Mean	3.09
Std Dev	3.585

nHits (per chip and per HitBco)

nChipHit_F6_Fch5_Chip19

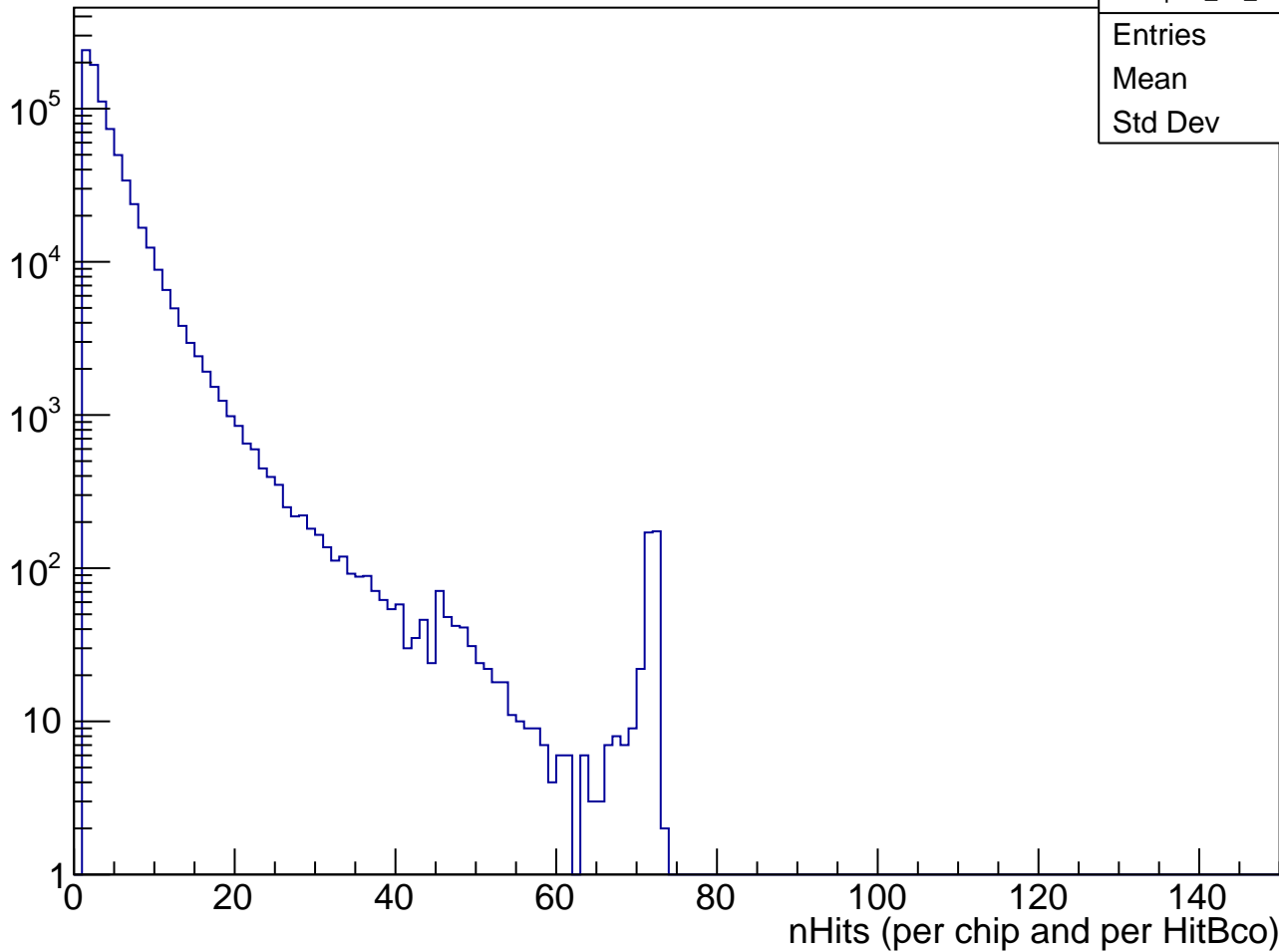
Entries



nChipHit_F6_Fch5_Chip19	
Entries	721838
Mean	3.048
Std Dev	3.554

nChipHit_F6_Fch5_Chip2

Entries



nChipHit_F6_Fch5_Chip2

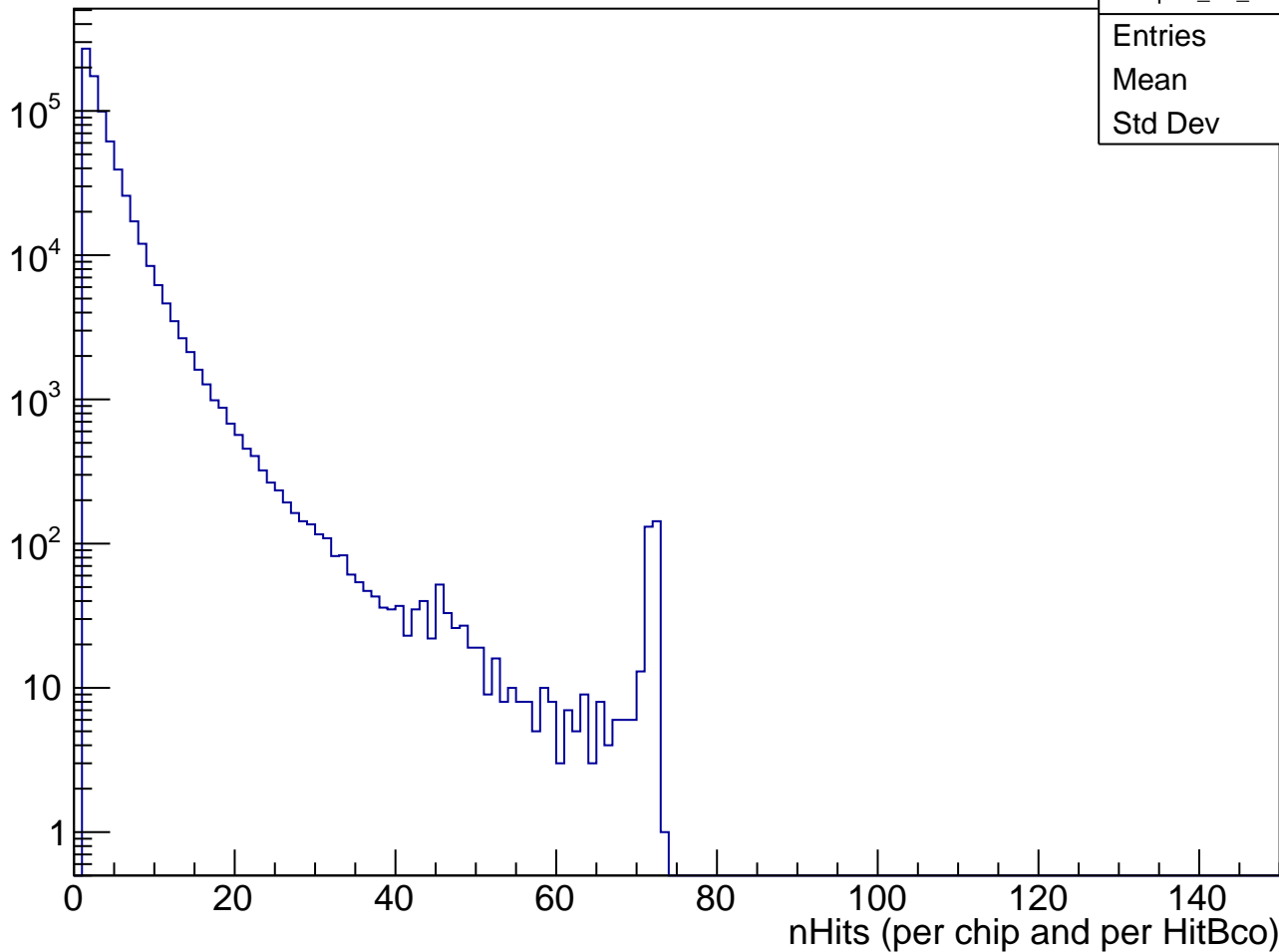
Entries 796827

Mean 3.451

Std Dev 3.875

nChipHit_F6_Fch5_Chip20

Entries

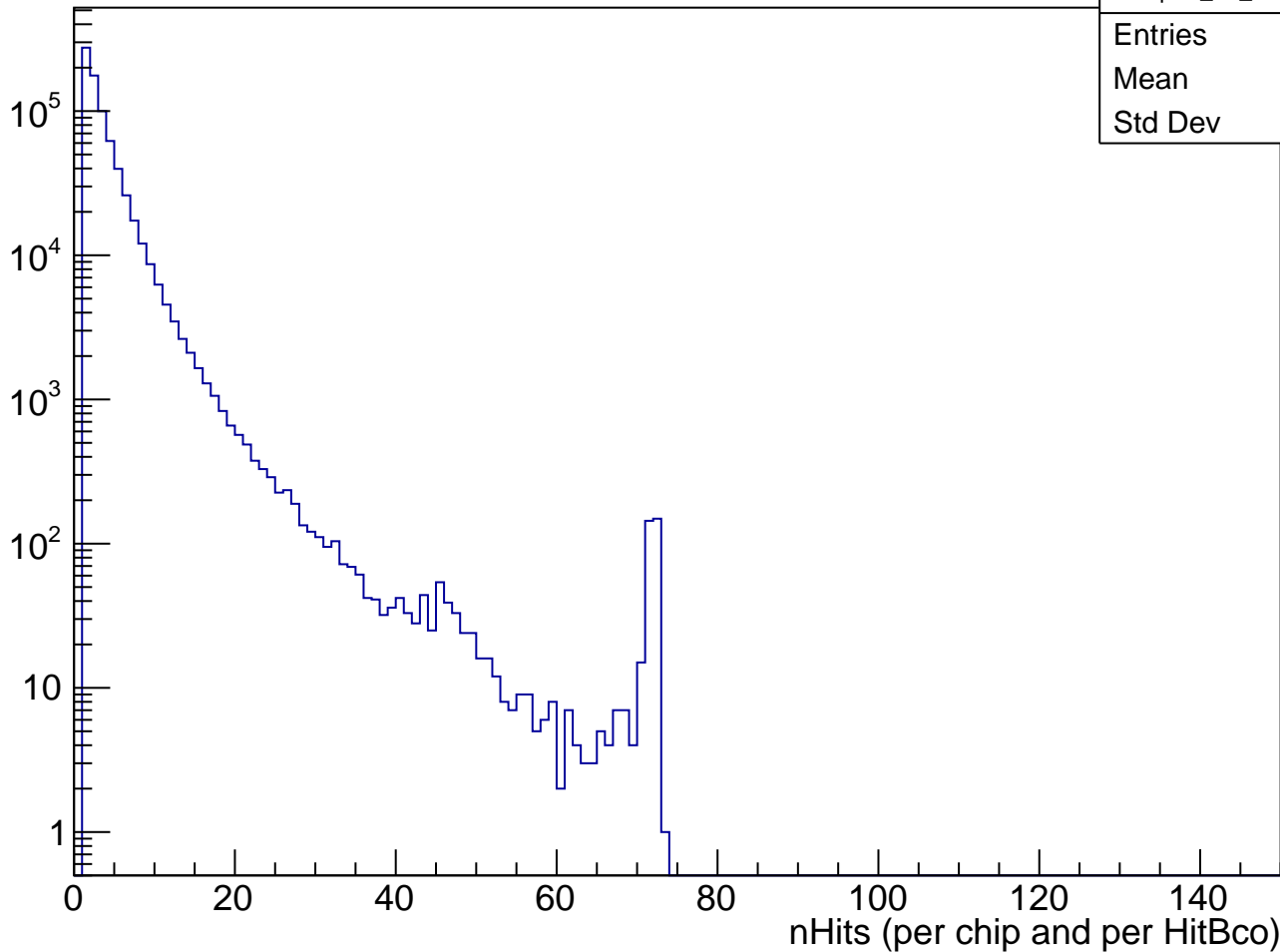


nChipHit_F6_Fch5_Chip20	
Entries	736332
Mean	3.05
Std Dev	3.529

nHits (per chip and per HitBco)

nChipHit_F6_Fch5_Chip21

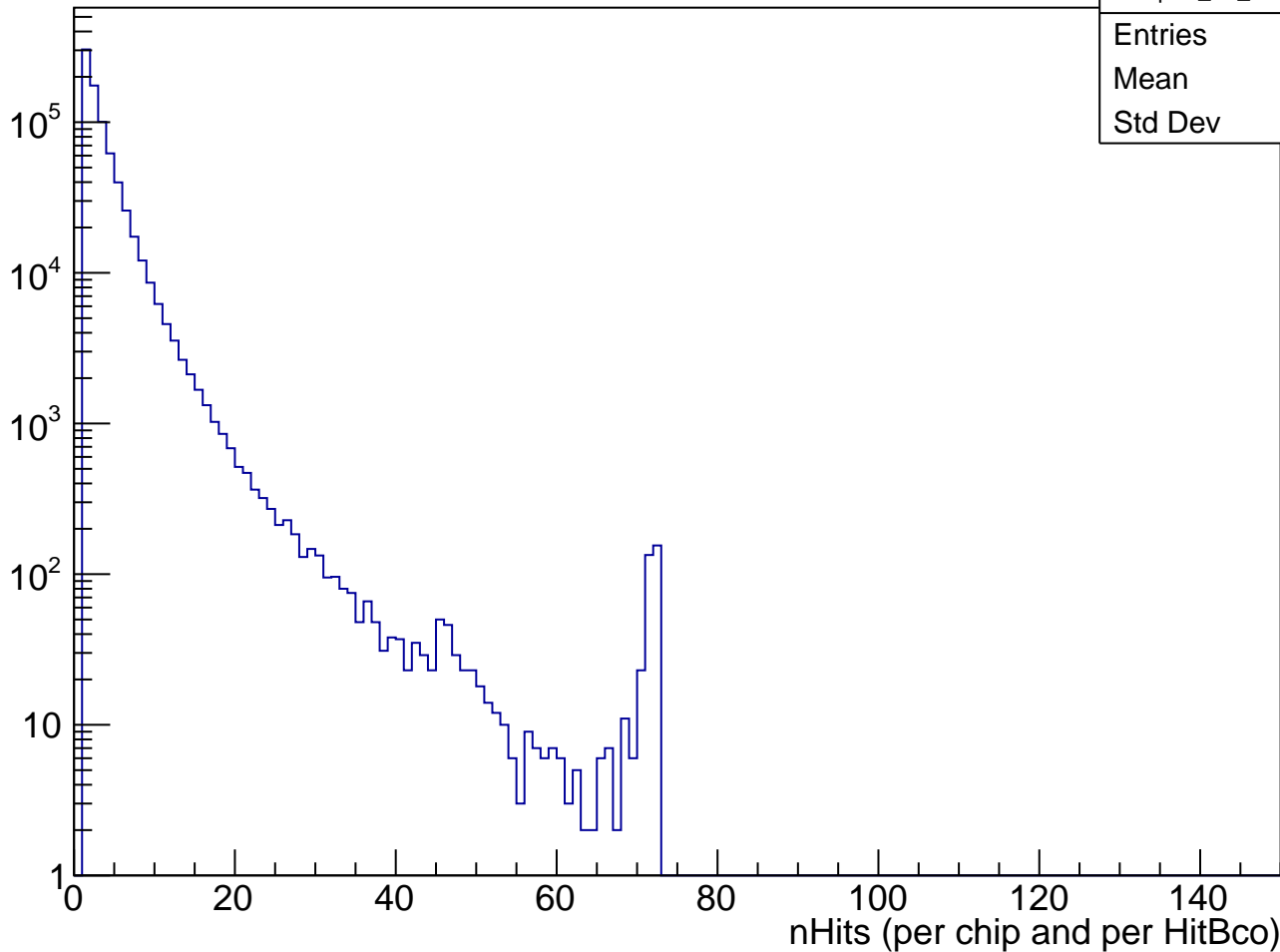
Entries



nChipHit_F6_Fch5_Chip21	
Entries	746454
Mean	3.044
Std Dev	3.538

nChipHit_F6_Fch5_Chip22

Entries

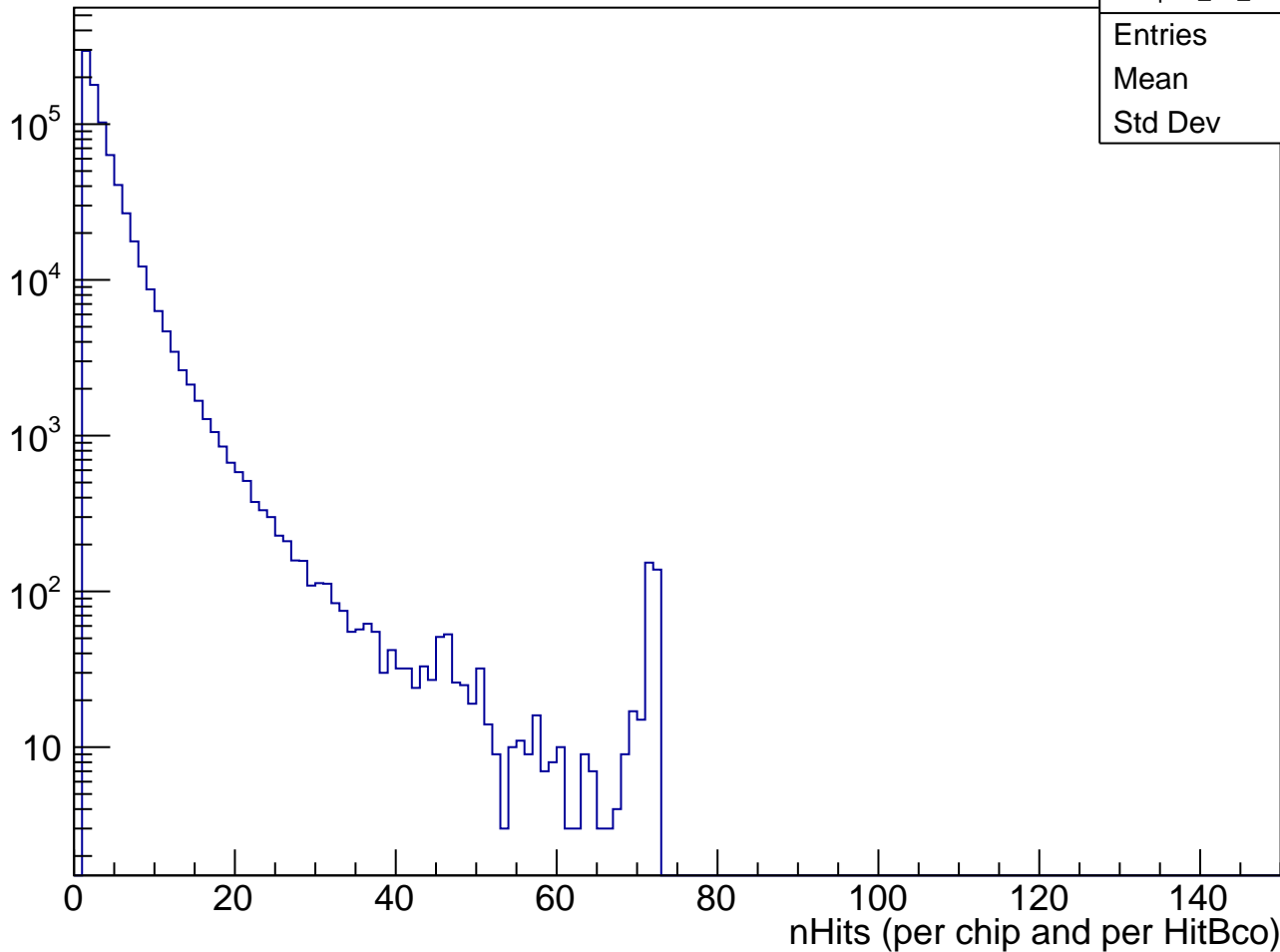


nChipHit_F6_Fch5_Chip22	
Entries	774314
Mean	2.97
Std Dev	3.495

nHits (per chip and per HitBco)

nChipHit_F6_Fch5_Chip23

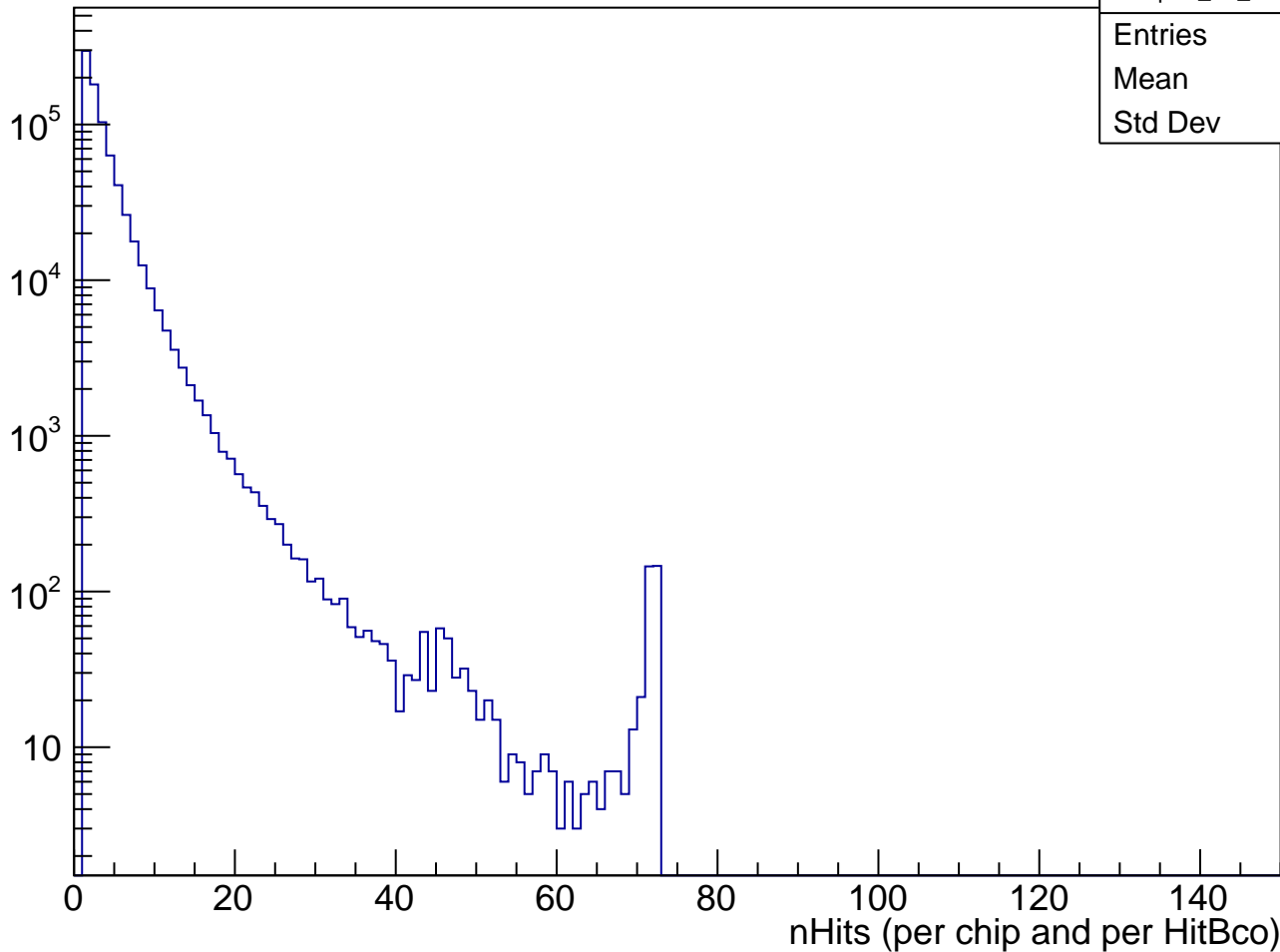
Entries



nChipHit_F6_Fch5_Chip23	
Entries	775234
Mean	3.002
Std Dev	3.515

nChipHit_F6_Fch5_Chip24

Entries

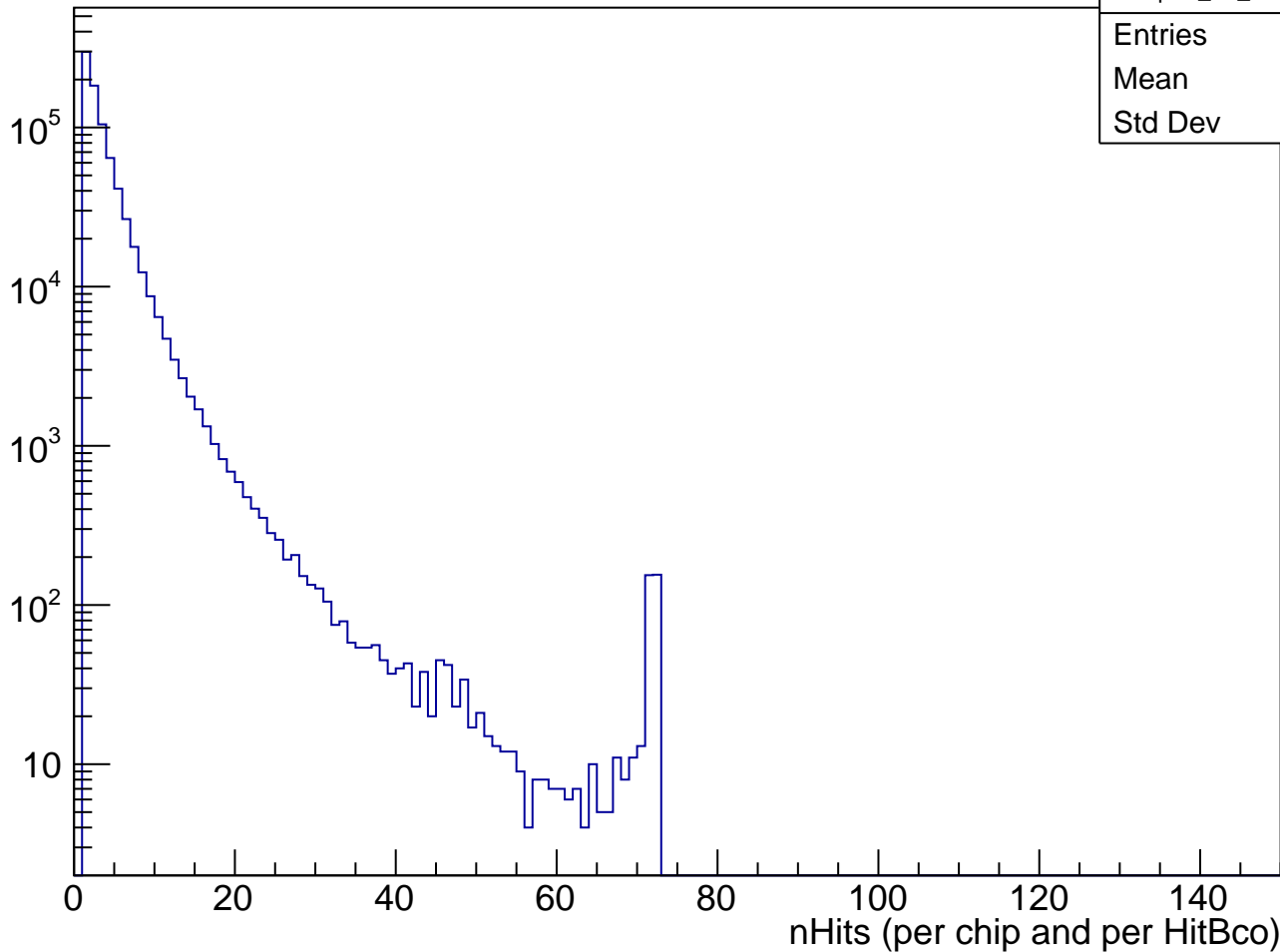


nChipHit_F6_Fch5_Chip24	
Entries	780426
Mean	3.002
Std Dev	3.517

nHits (per chip and per HitBco)

nChipHit_F6_Fch5_Chip25

Entries

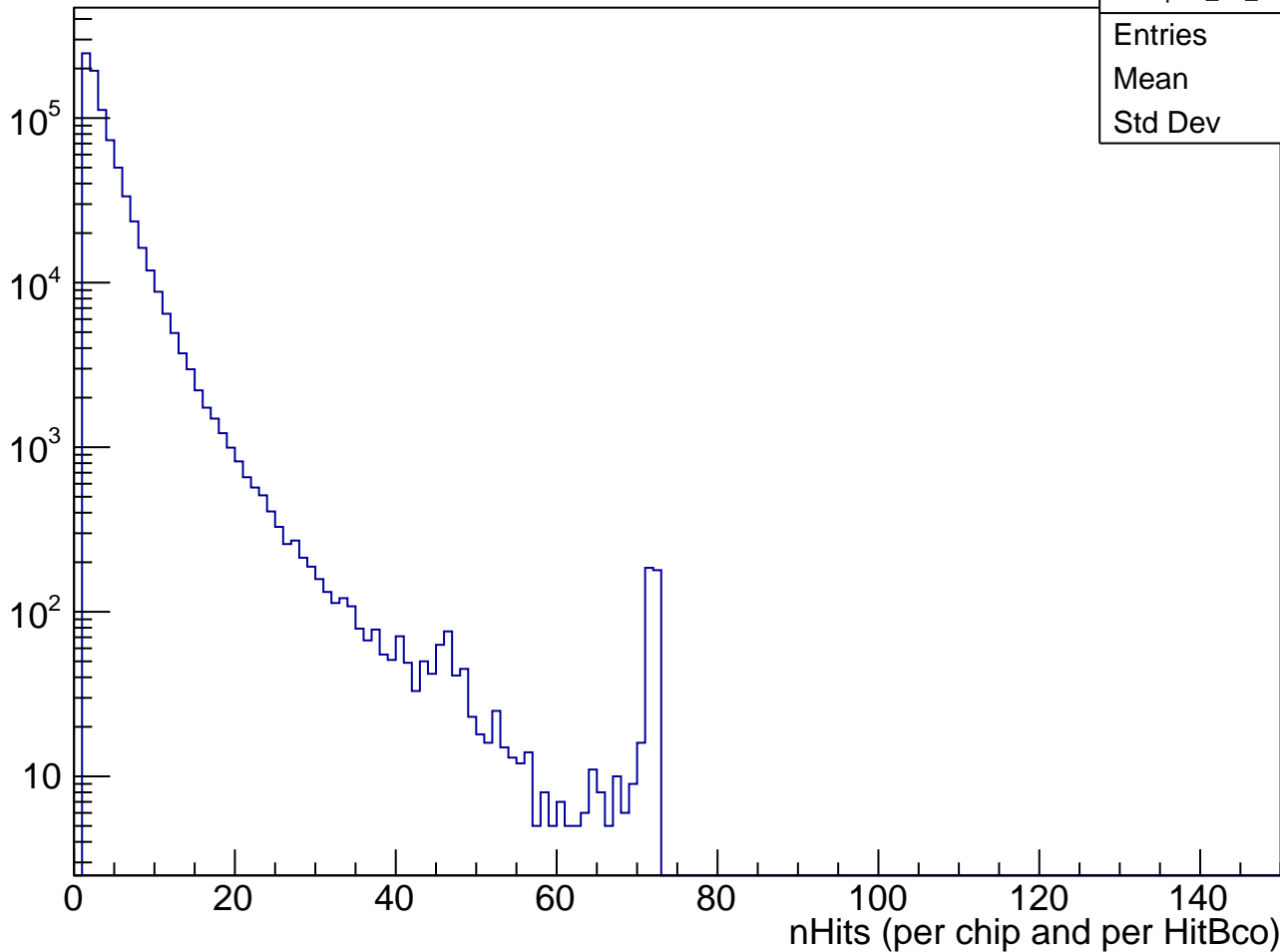


nChipHit_F6_Fch5_Chip25	
Entries	786774
Mean	2.997
Std Dev	3.517

nHits (per chip and per HitBco)

nChipHit_F6_Fch5_Chip3

Entries

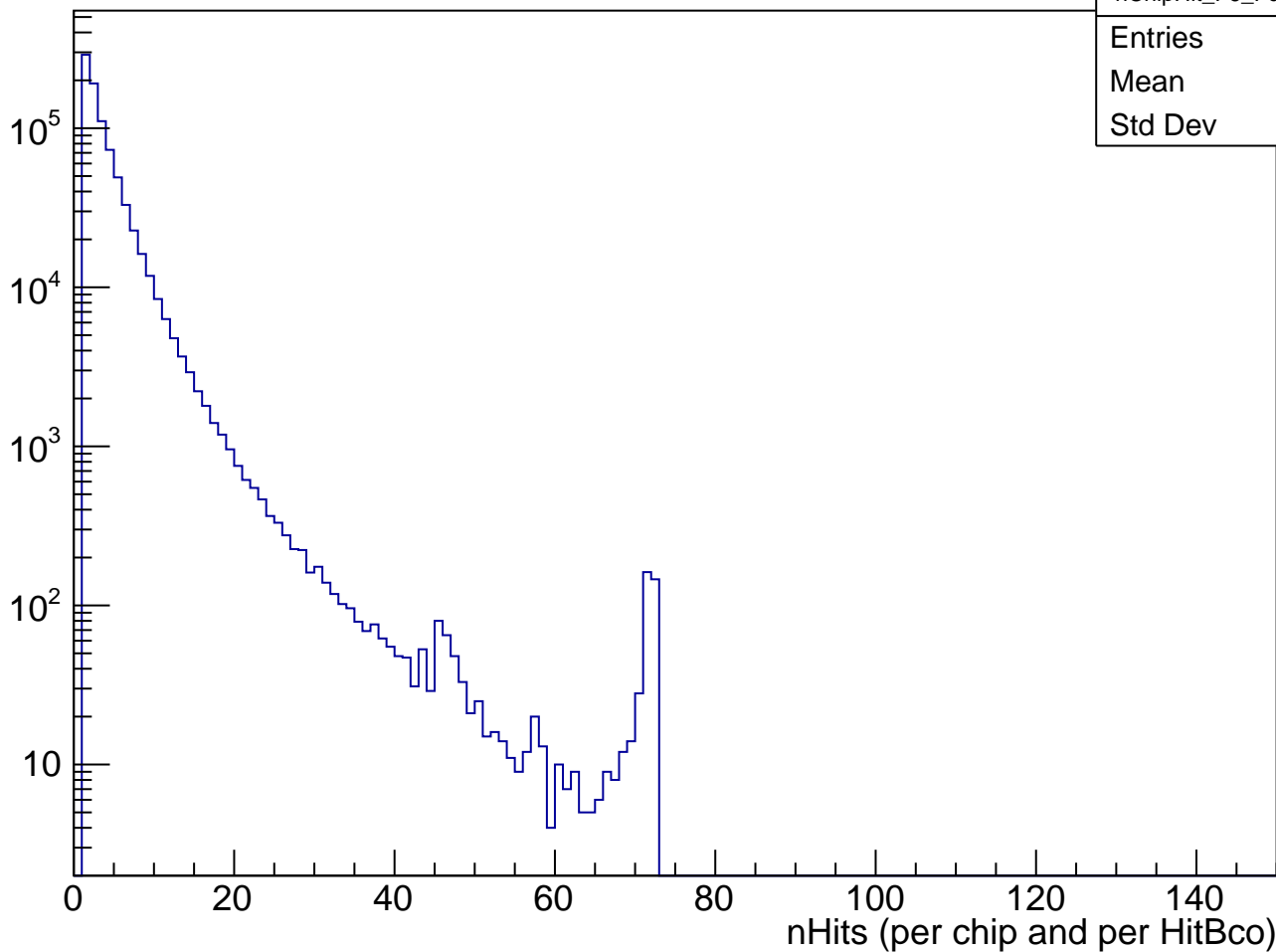


nChipHit_F6_Fch5_Chip3	
Entries	802592
Mean	3.418
Std Dev	3.886

nHits (per chip and per HitBco)

nChipHit_F6_Fch5_Chip4

Entries



nChipHit_F6_Fch5_Chip4

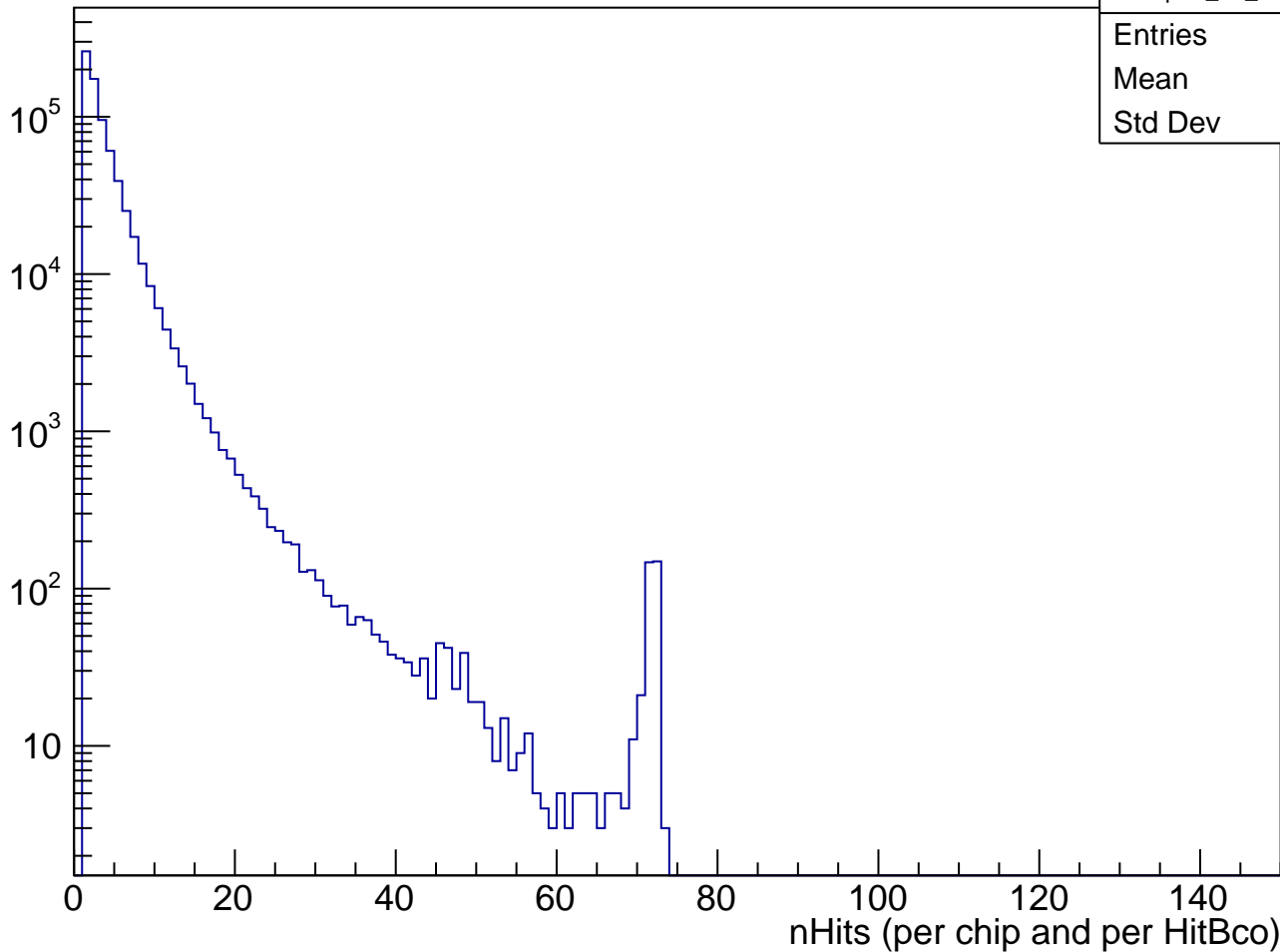
Entries 837020

Mean 3.275

Std Dev 3.783

nChipHit_F6_Fch5_Chip5

Entries

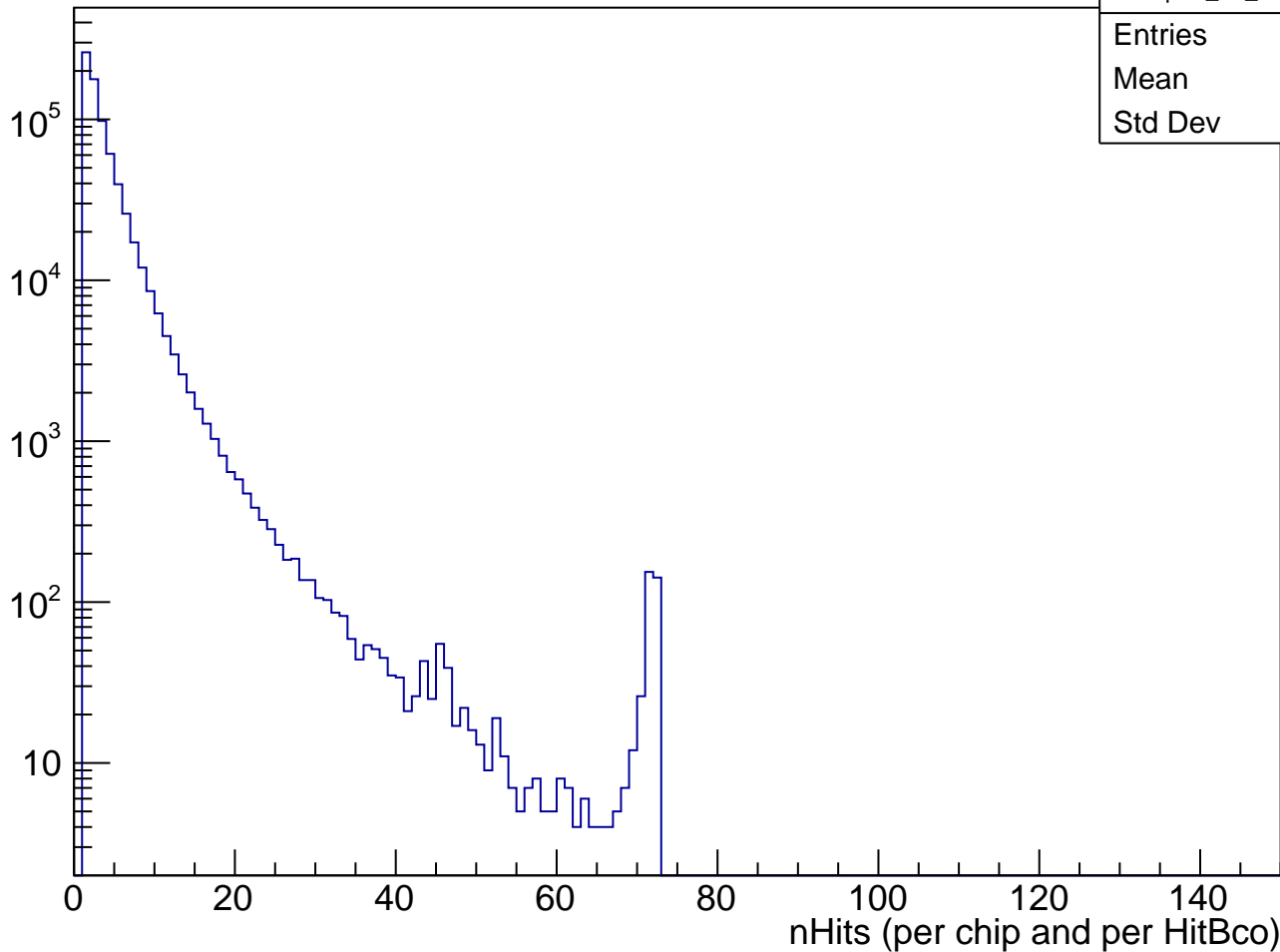


nChipHit_F6_Fch5_Chip5	
Entries	720804
Mean	3.059
Std Dev	3.561

nHits (per chip and per HitBco)

nChipHit_F6_Fch5_Chip6

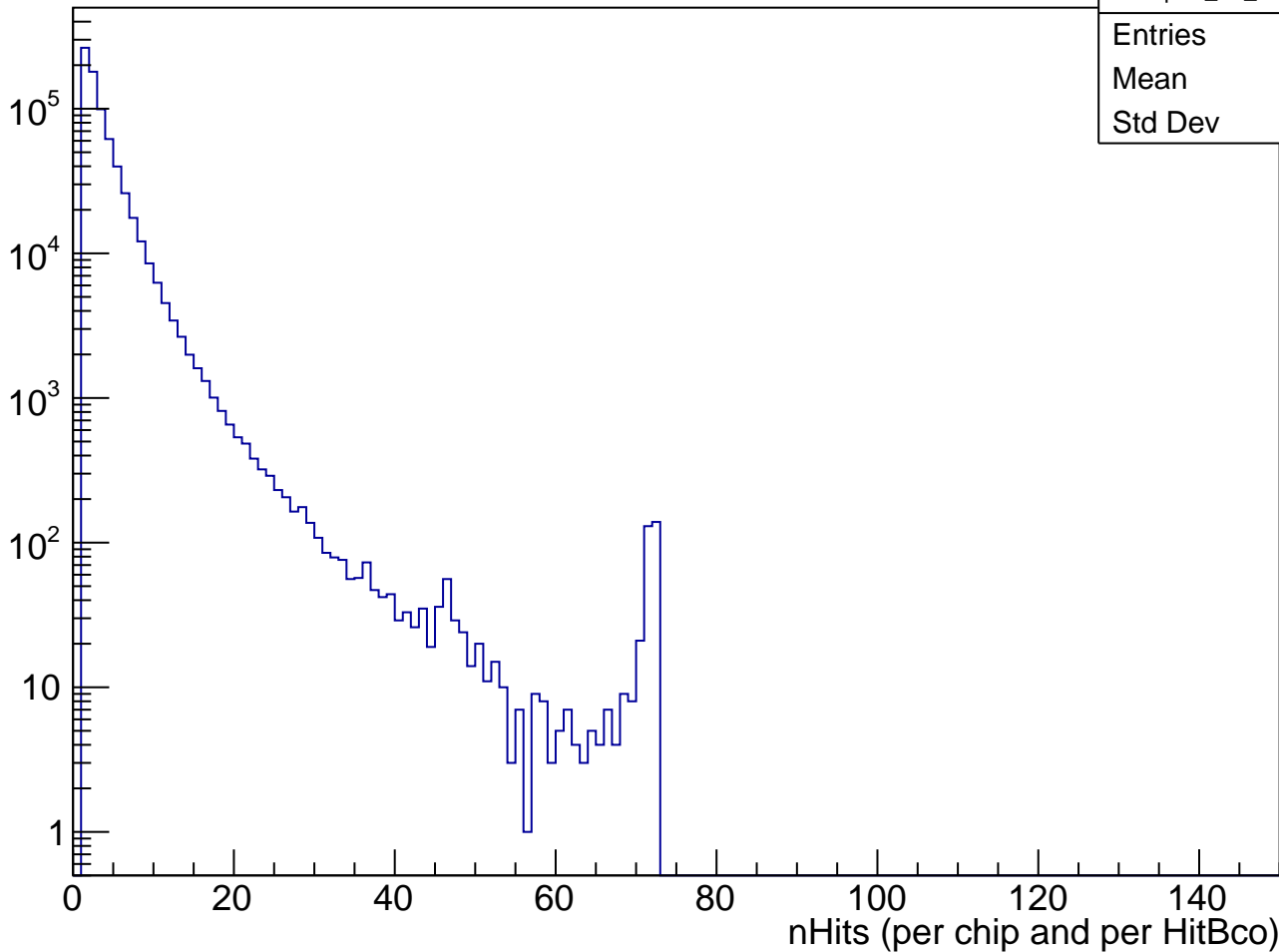
Entries



nChipHit_F6_Fch5_Chip6	
Entries	729396
Mean	3.07
Std Dev	3.558

nChipHit_F6_Fch5_Chip7

Entries

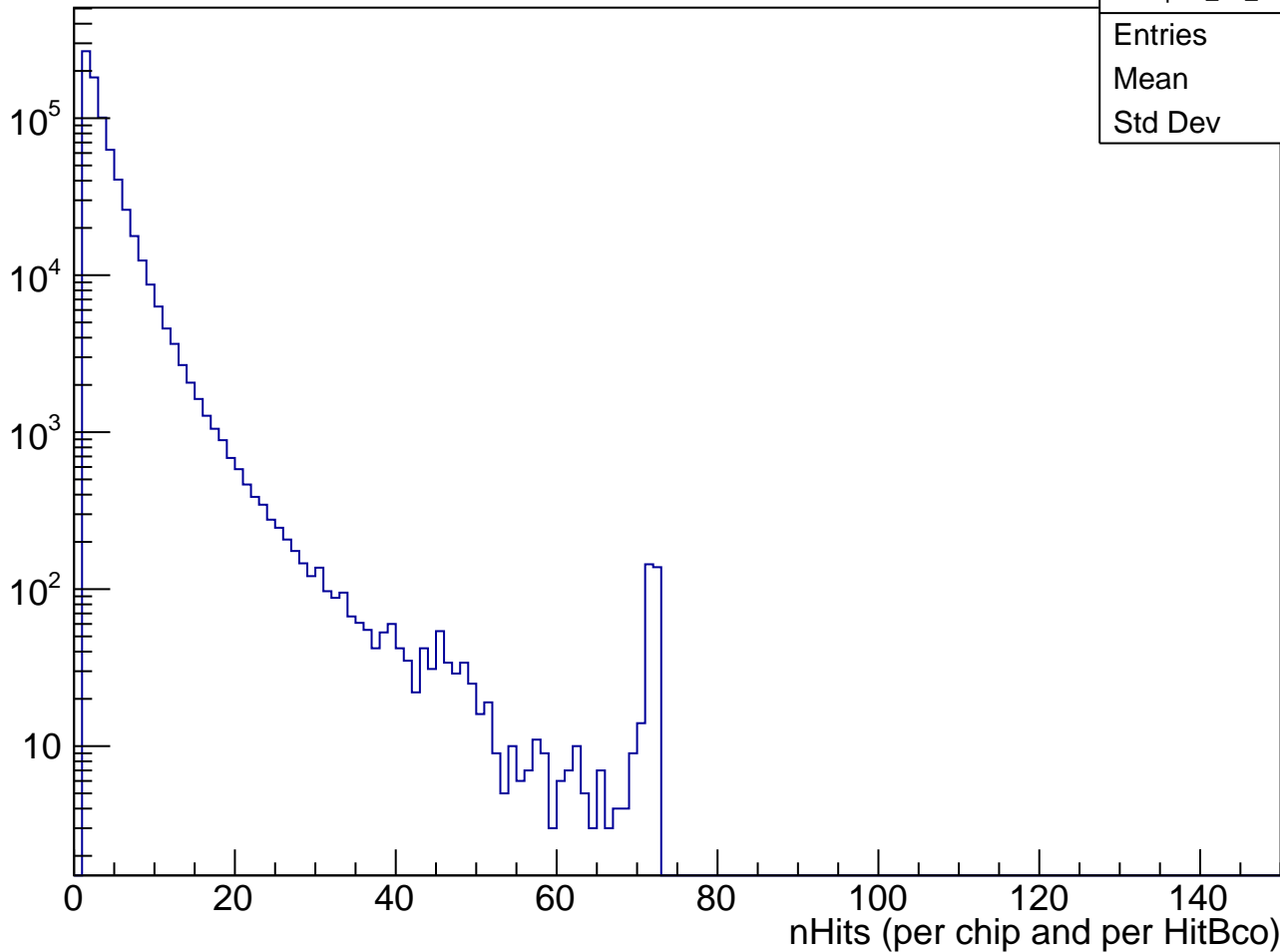


nChipHit_F6_Fch5_Chip7	
Entries	737458
Mean	3.063
Std Dev	3.518

nHits (per chip and per HitBco)

nChipHit_F6_Fch5_Chip8

Entries



nChipHit_F6_Fch5_Chip8

Entries 747778

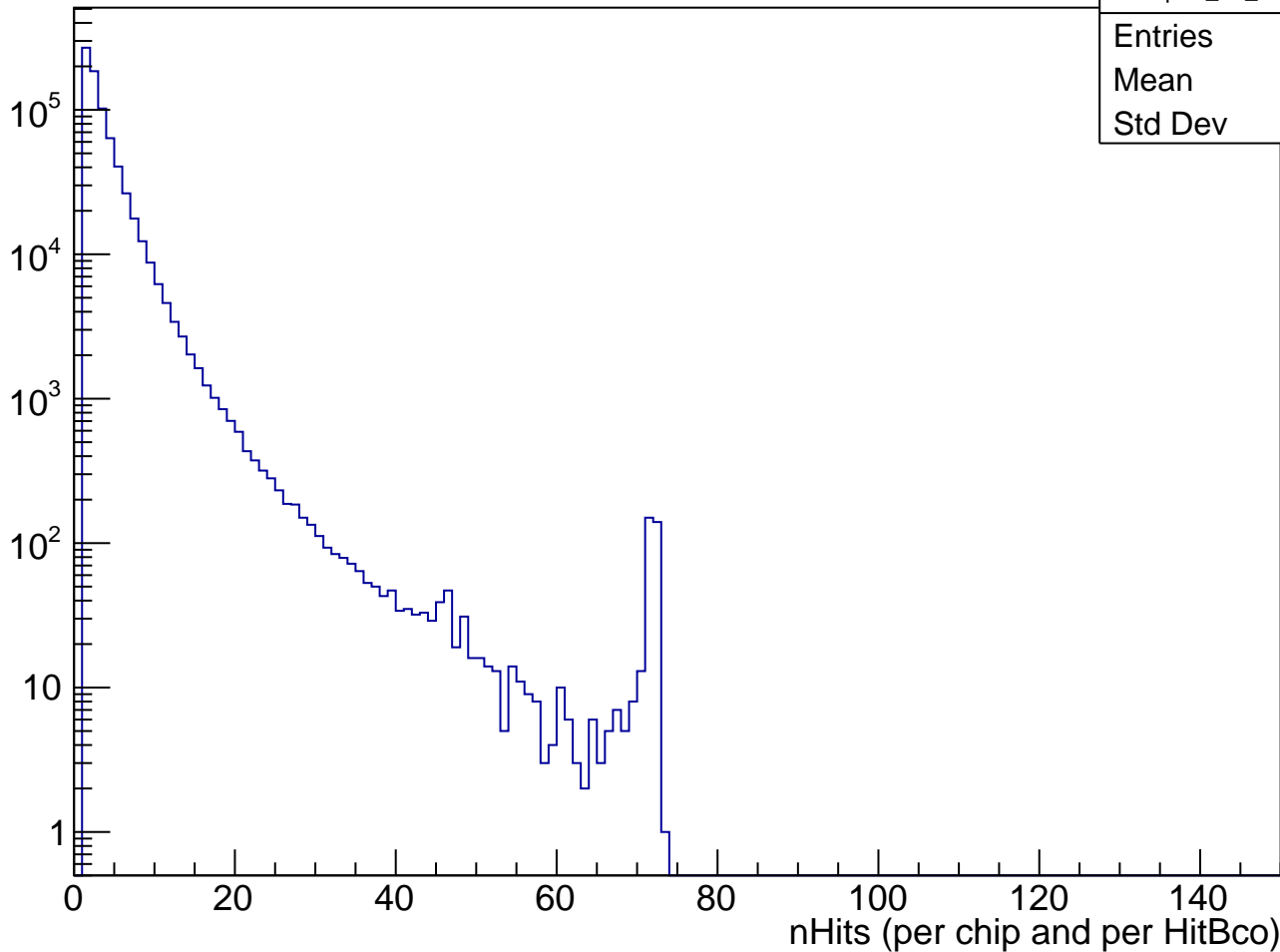
Mean 3.074

Std Dev 3.553

nHits (per chip and per HitBco)

nChipHit_F6_Fch5_Chip9

Entries

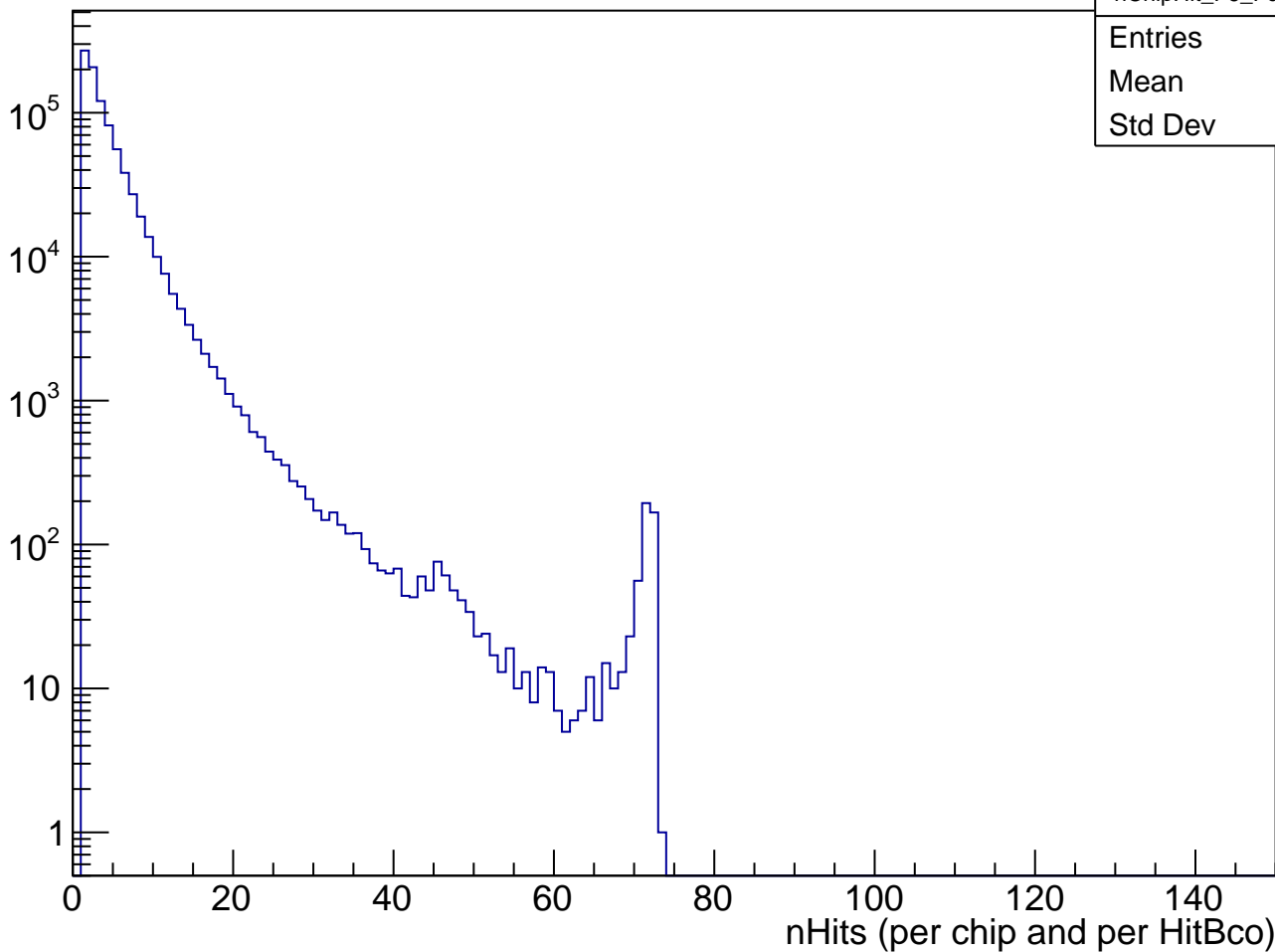


nChipHit_F6_Fch5_Chip9

Entries	754671
Mean	3.054
Std Dev	3.511

nChipHit_F6_Fch6_Chip0

Entries



nChipHit_F6_Fch6_Chip0

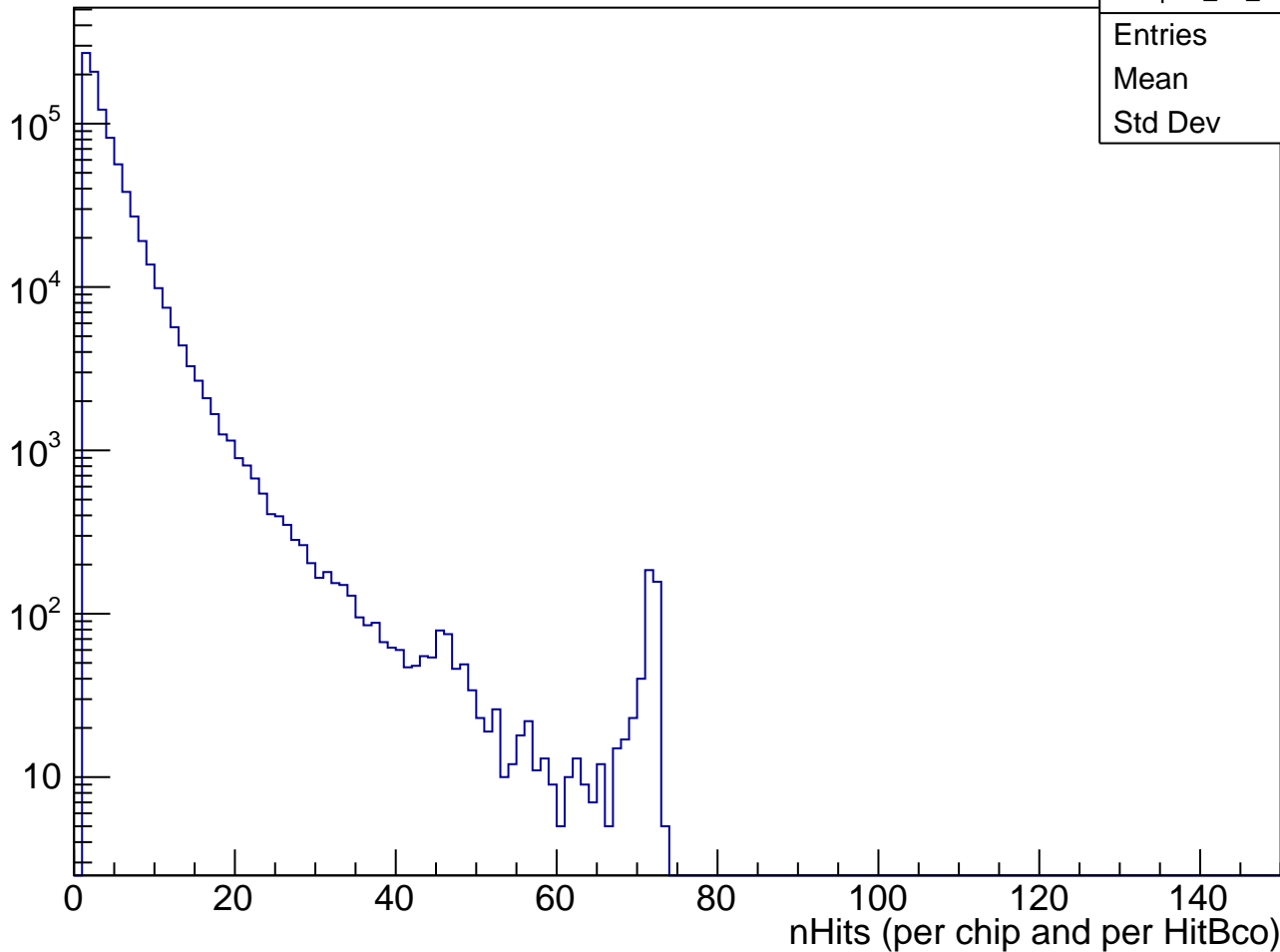
Entries 880949

Mean 3.483

Std Dev 3.959

nChipHit_F6_Fch6_Chip1

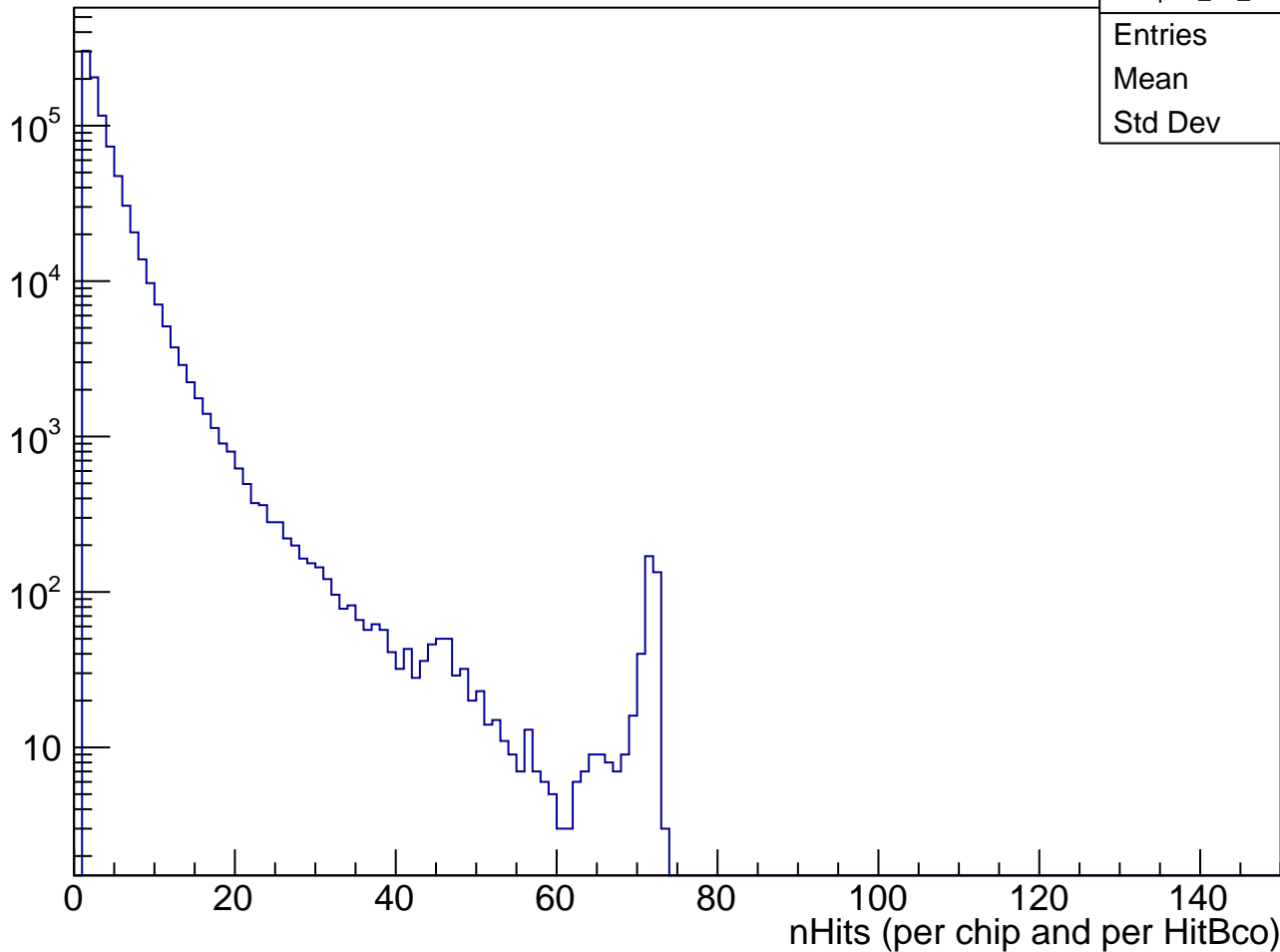
Entries



nChipHit_F6_Fch6_Chip1	
Entries	883537
Mean	3.479
Std Dev	3.948

nChipHit_F6_Fch6_Chip10

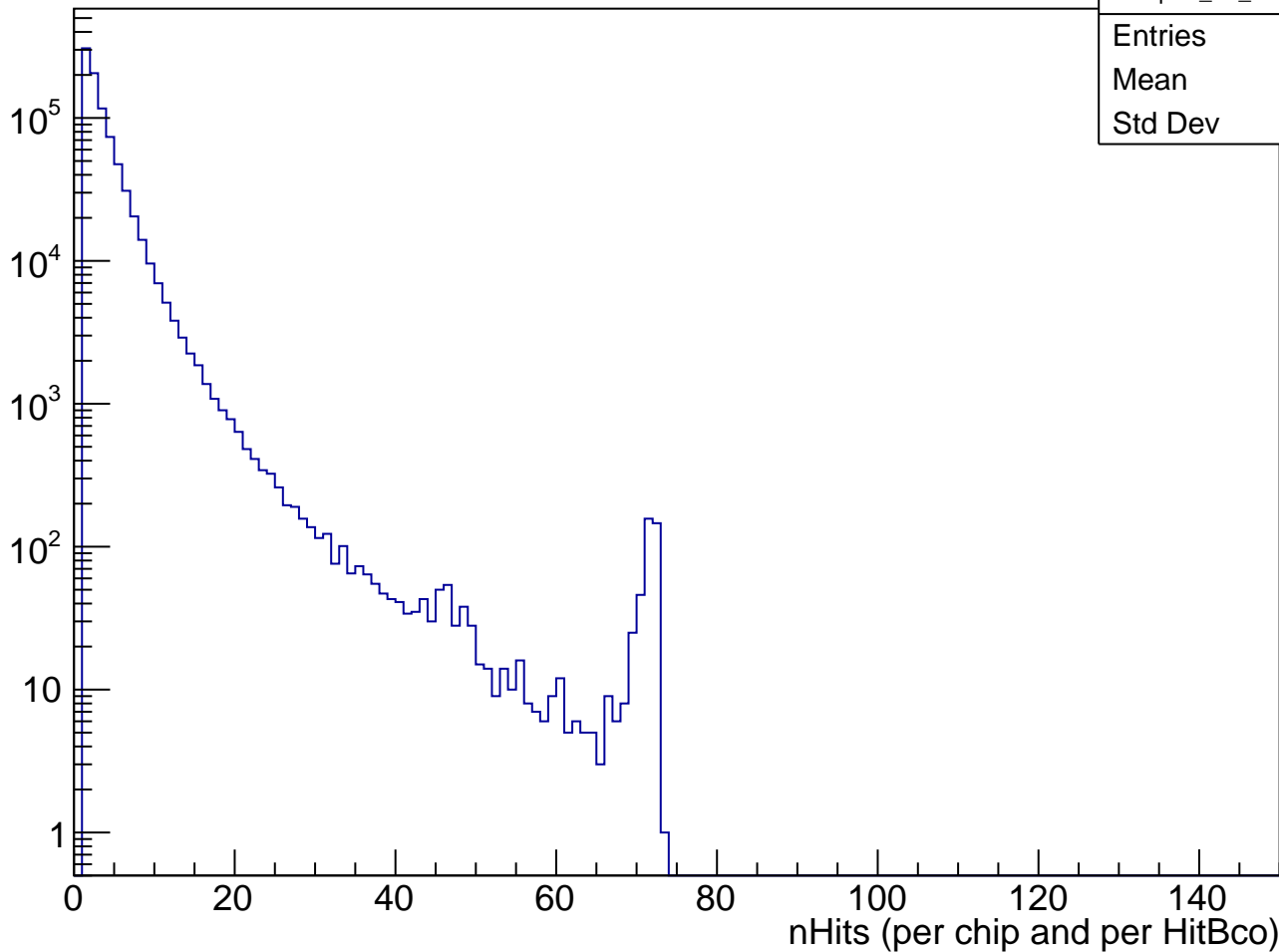
Entries



nChipHit_F6_Fch6_Chip10	
Entries	851436
Mean	3.064
Std Dev	3.517

nChipHit_F6_Fch6_Chip11

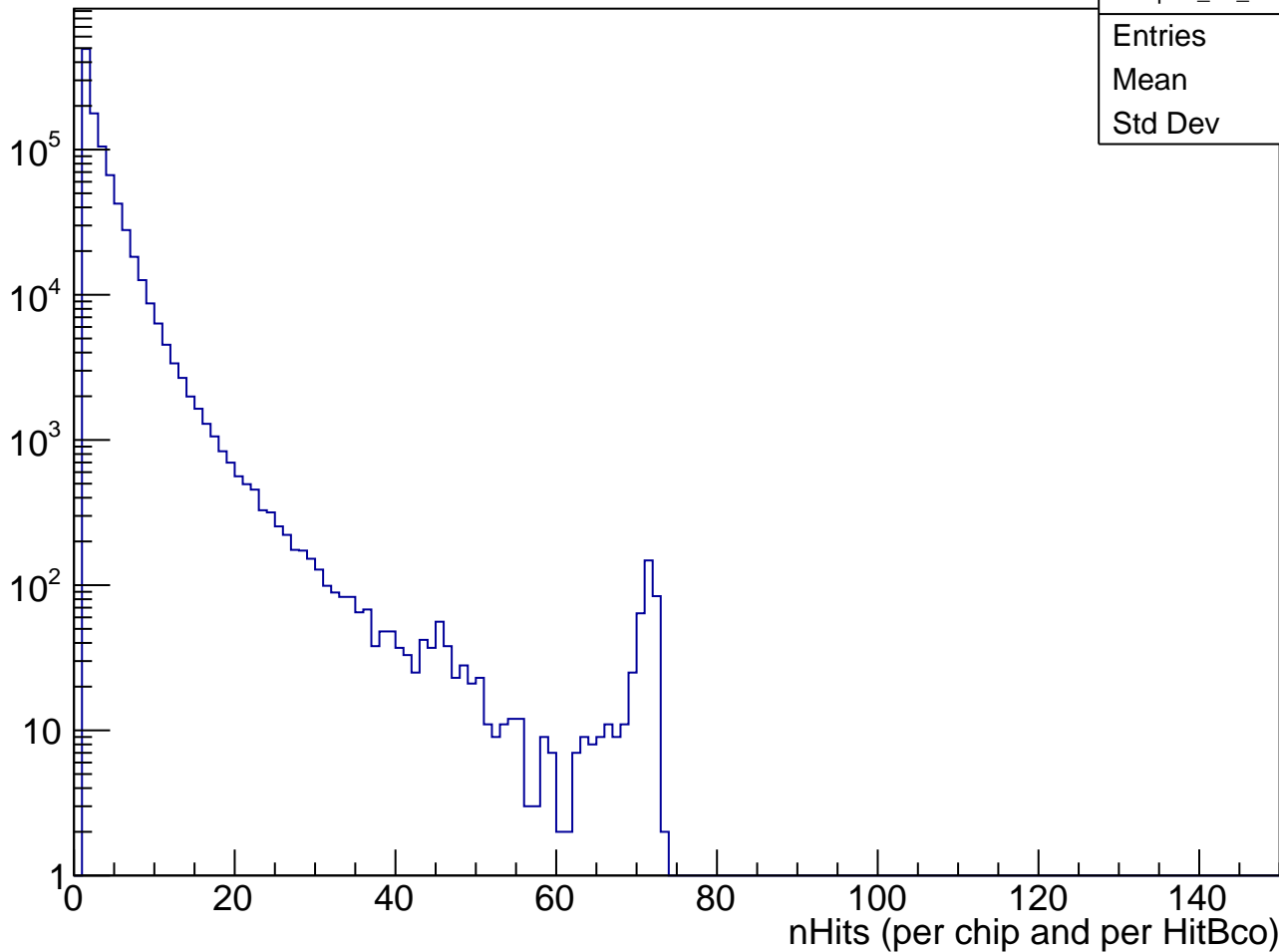
Entries



nChipHit_F6_Fch6_Chip11	
Entries	857564
Mean	3.054
Std Dev	3.51

nChipHit_F6_Fch6_Chip12

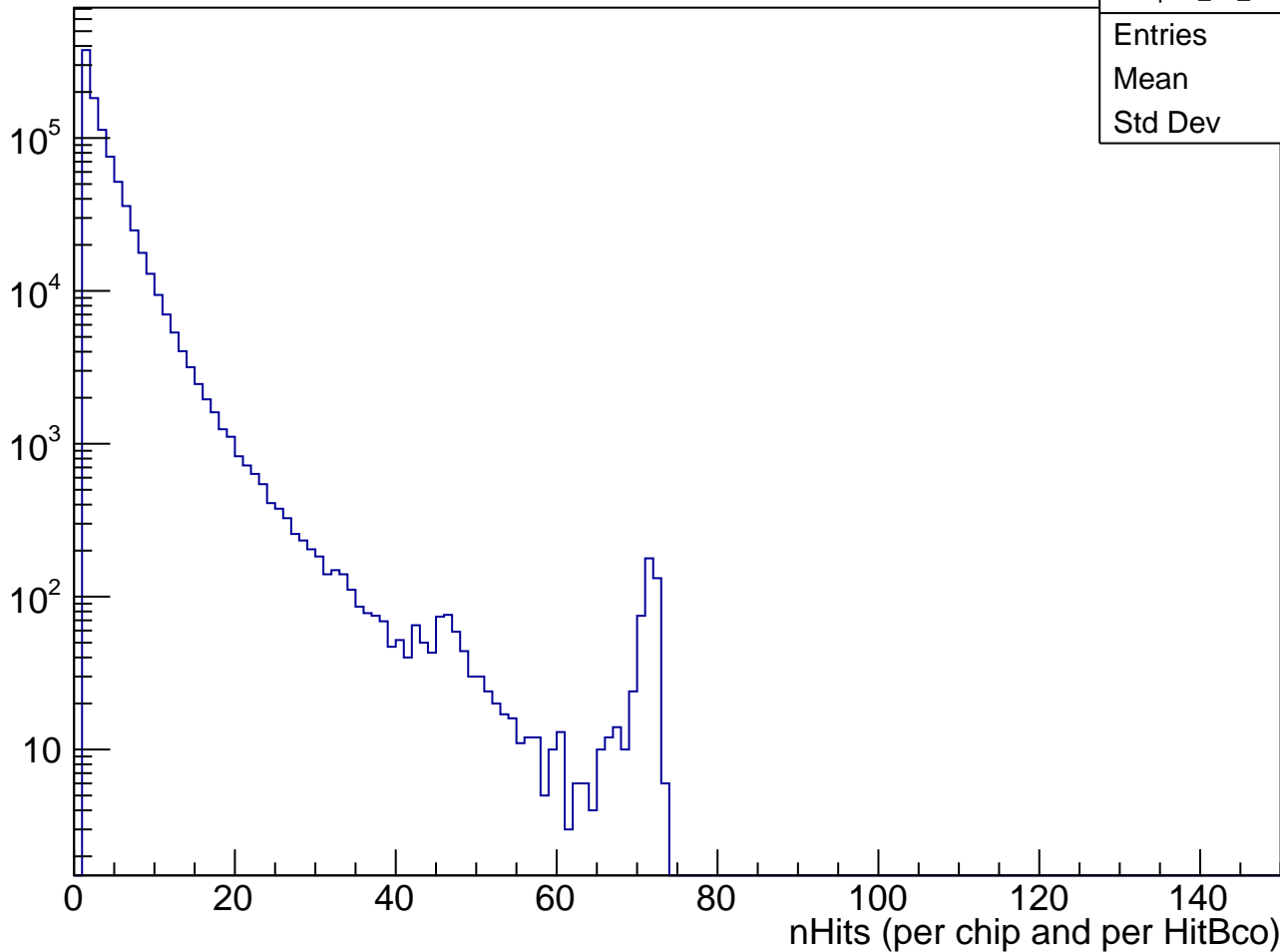
Entries



nChipHit_F6_Fch6_Chip12	
Entries	981886
Mean	2.618
Std Dev	3.26

nChipHit_F6_Fch6_Chip13

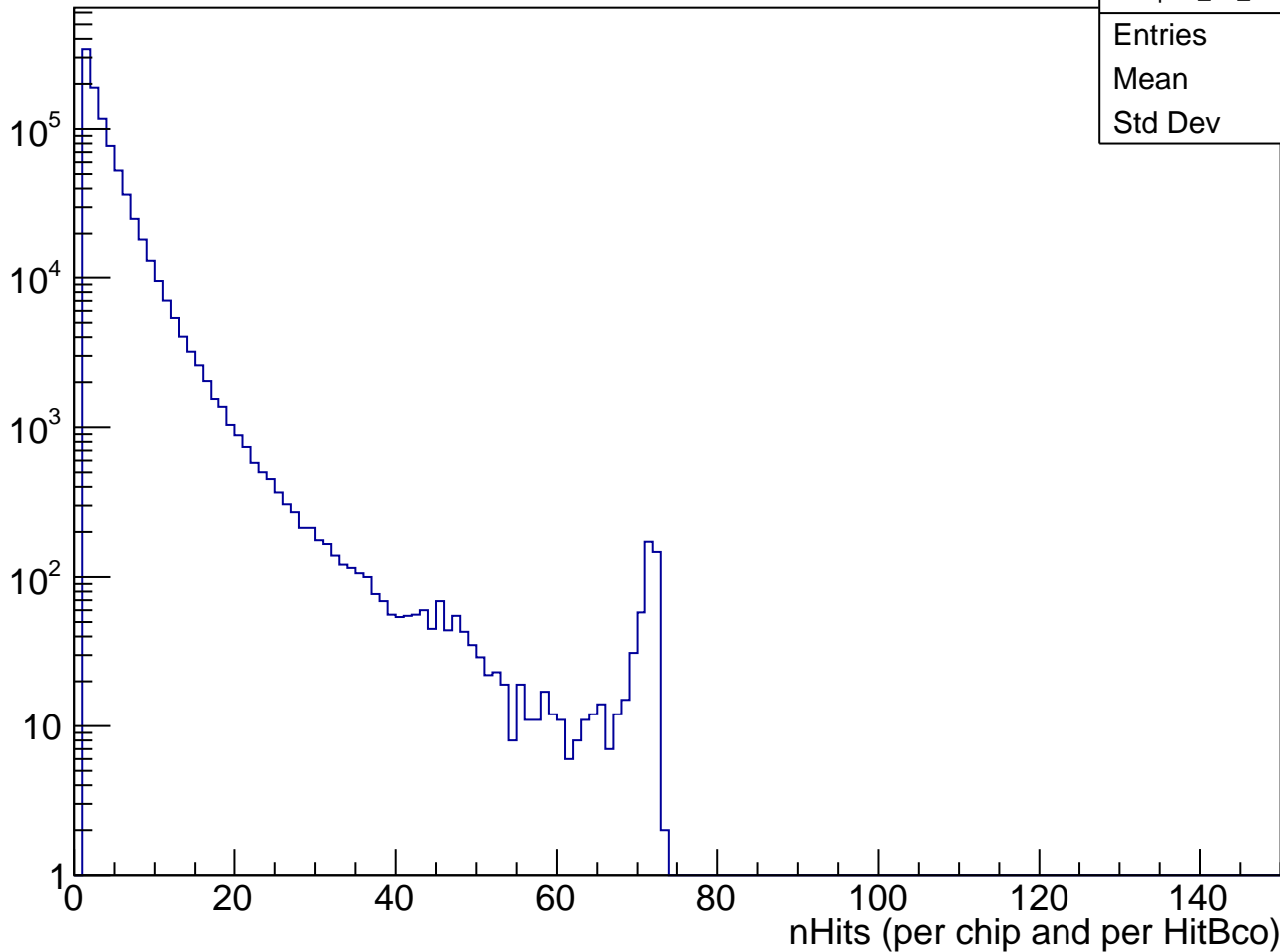
Entries



nChipHit_F6_Fch6_Chip13	
Entries	934507
Mean	3.176
Std Dev	3.827

nChipHit_F6_Fch6_Chip14

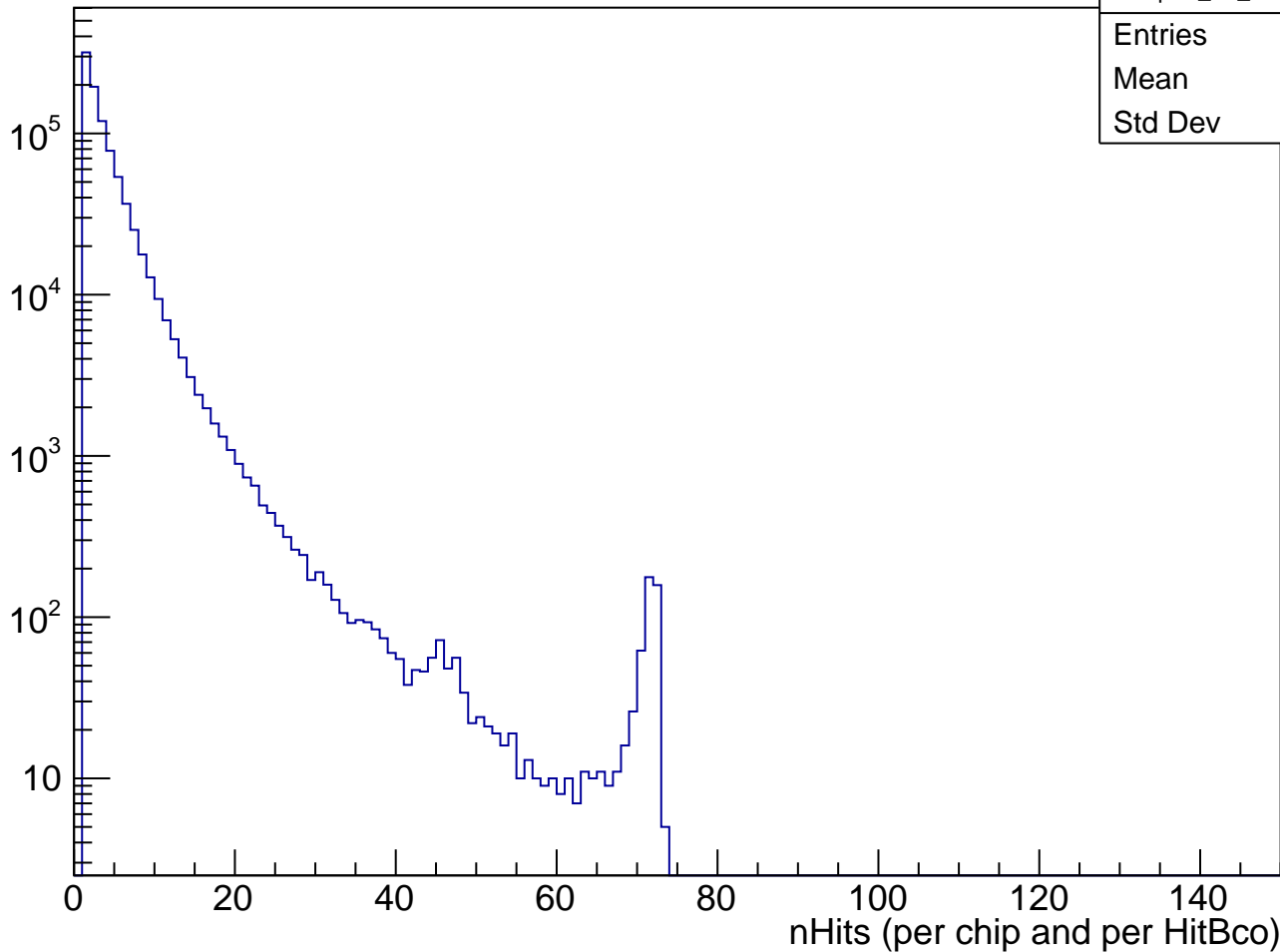
Entries



nChipHit_F6_Fch6_Chip14	
Entries	913955
Mean	3.264
Std Dev	3.869

nChipHit_F6_Fch6_Chip15

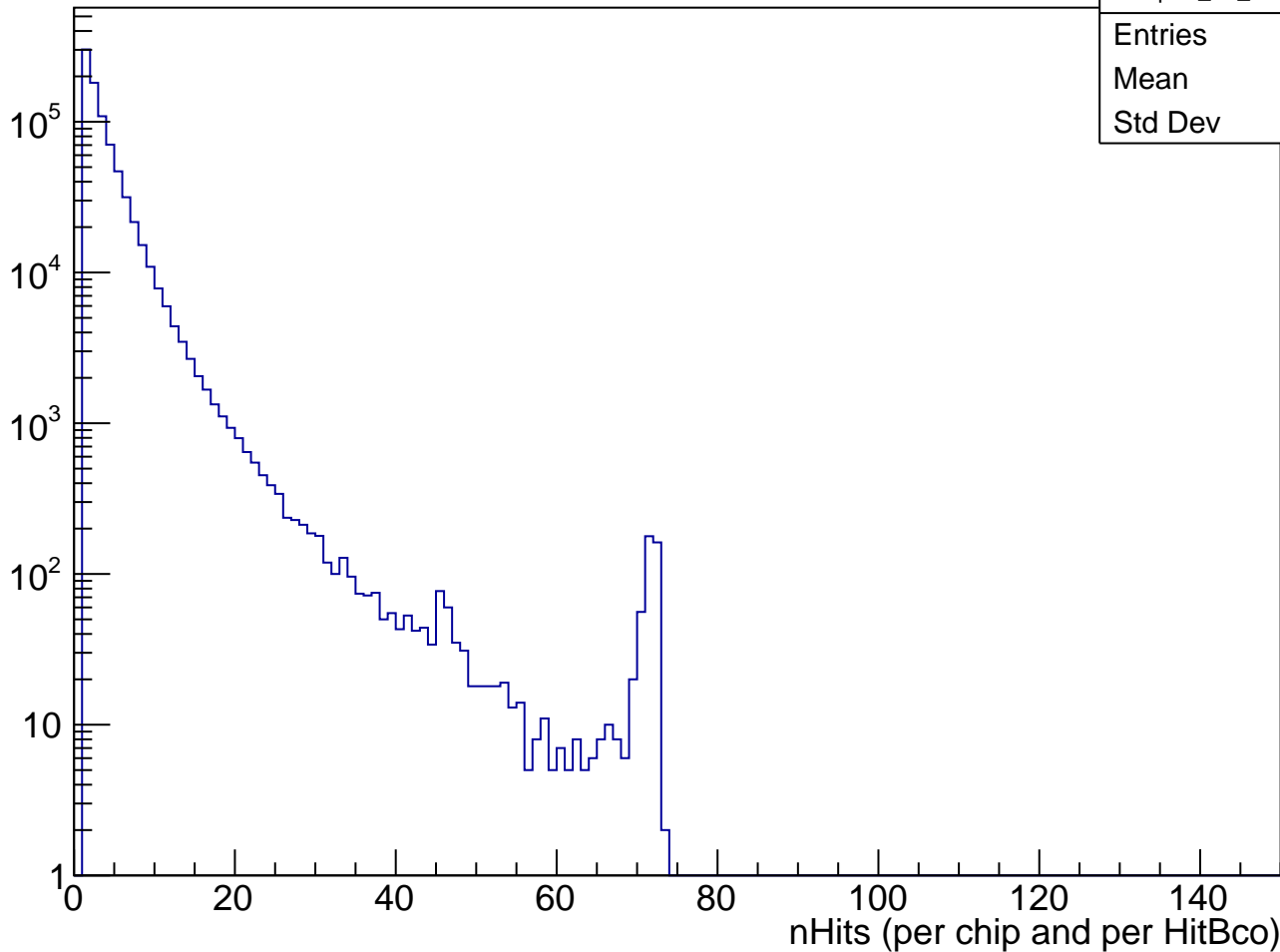
Entries



nChipHit_F6_Fch6_Chip15	
Entries	900487
Mean	3.306
Std Dev	3.863

nChipHit_F6_Fch6_Chip16

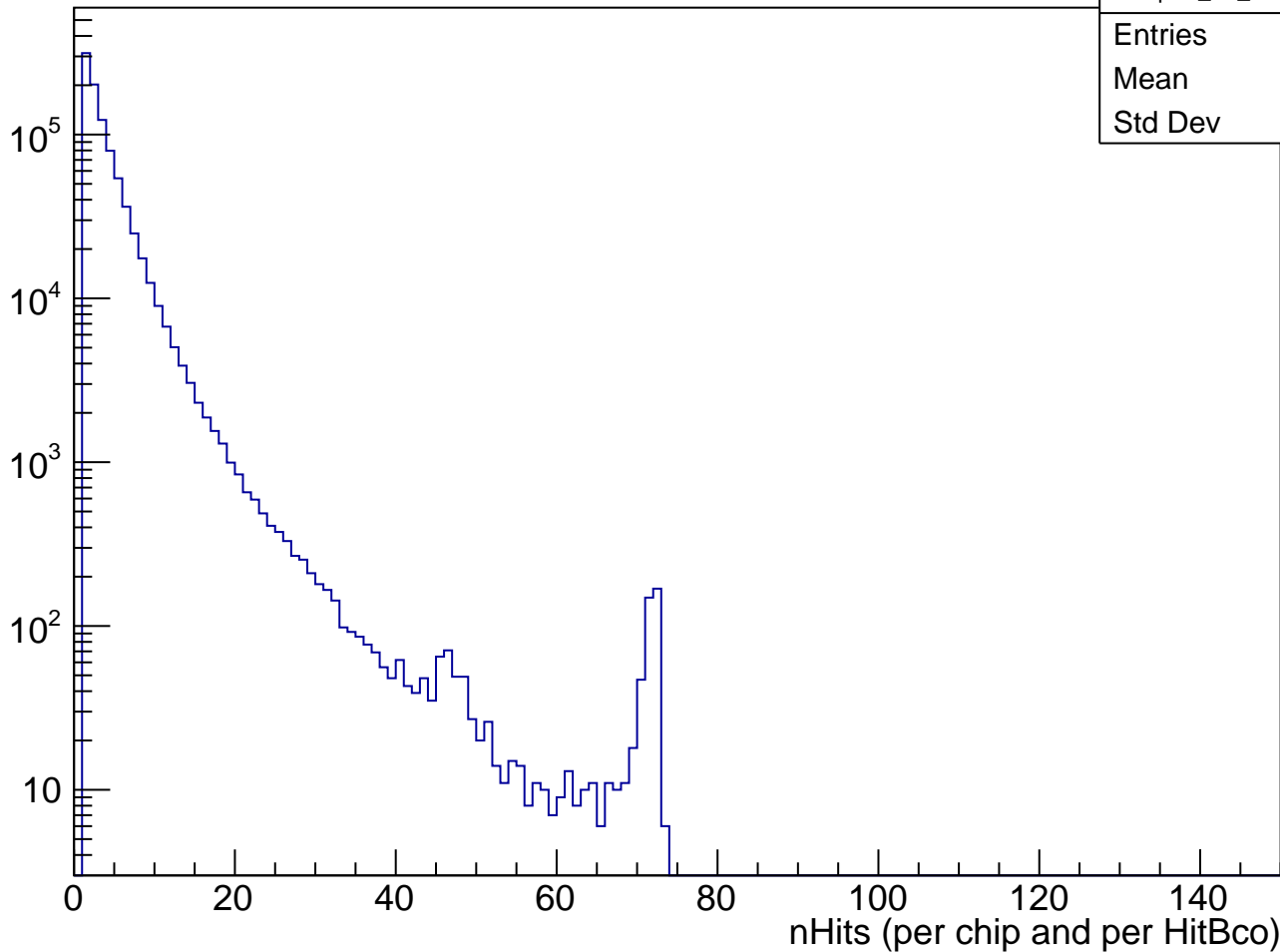
Entries



nChipHit_F6_Fch6_Chip16	
Entries	825883
Mean	3.208
Std Dev	3.802

nChipHit_F6_Fch6_Chip17

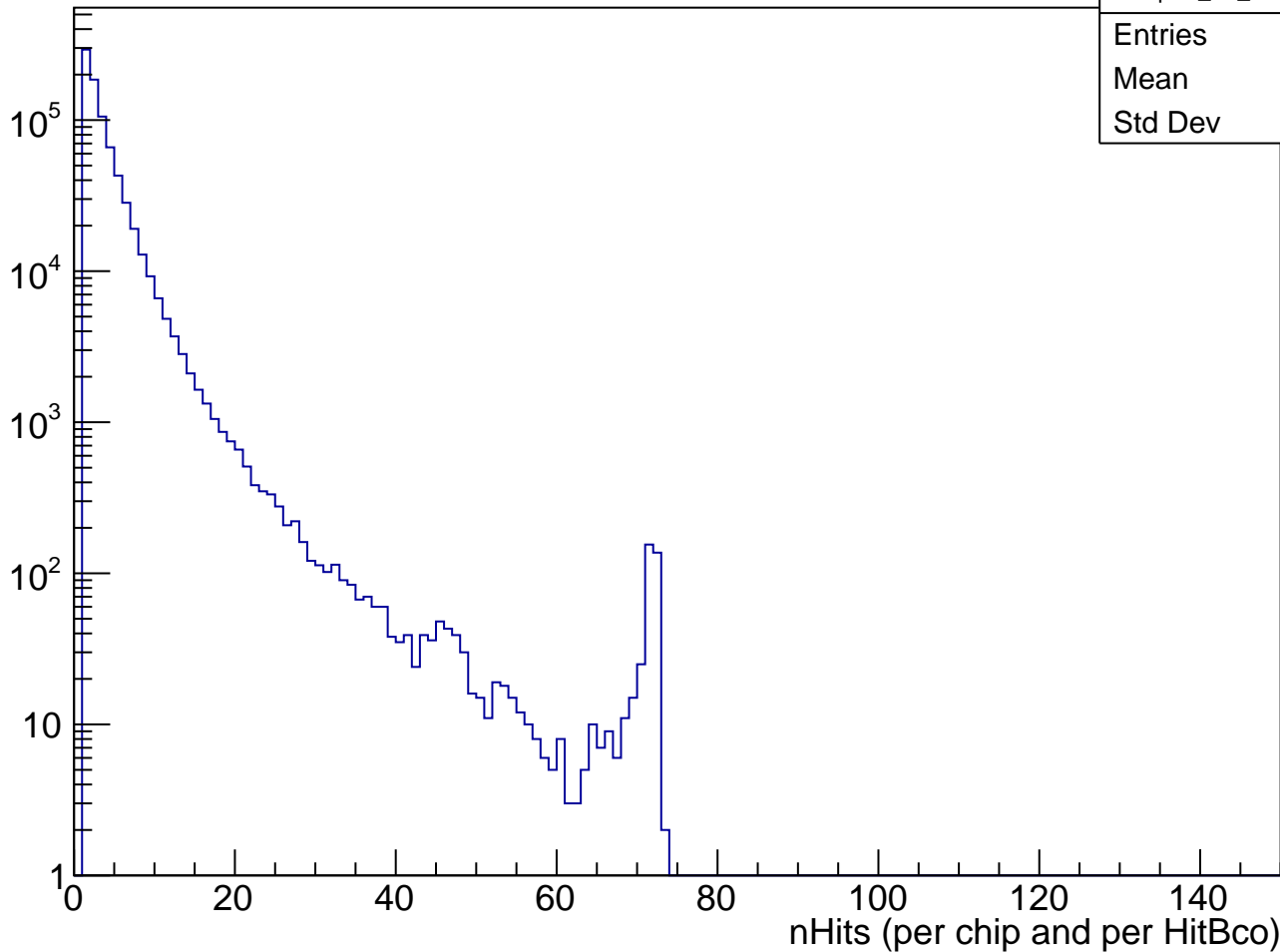
Entries



nChipHit_F6_Fch6_Chip17	
Entries	906188
Mean	3.277
Std Dev	3.782

nChipHit_F6_Fch6_Chip18

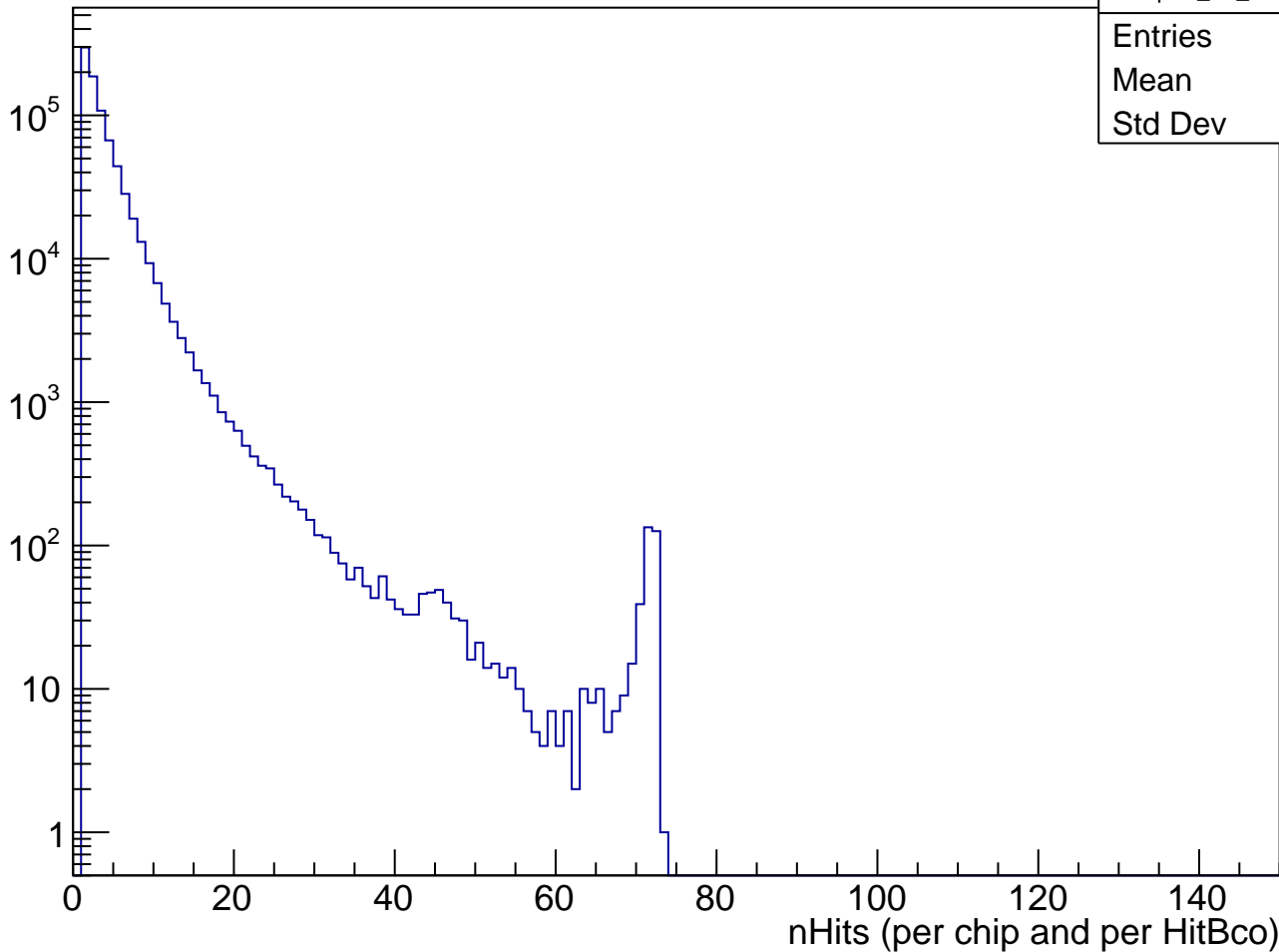
Entries



nChipHit_F6_Fch6_Chip18	
Entries	792648
Mean	3.057
Std Dev	3.572

nChipHit_F6_Fch6_Chip19

Entries

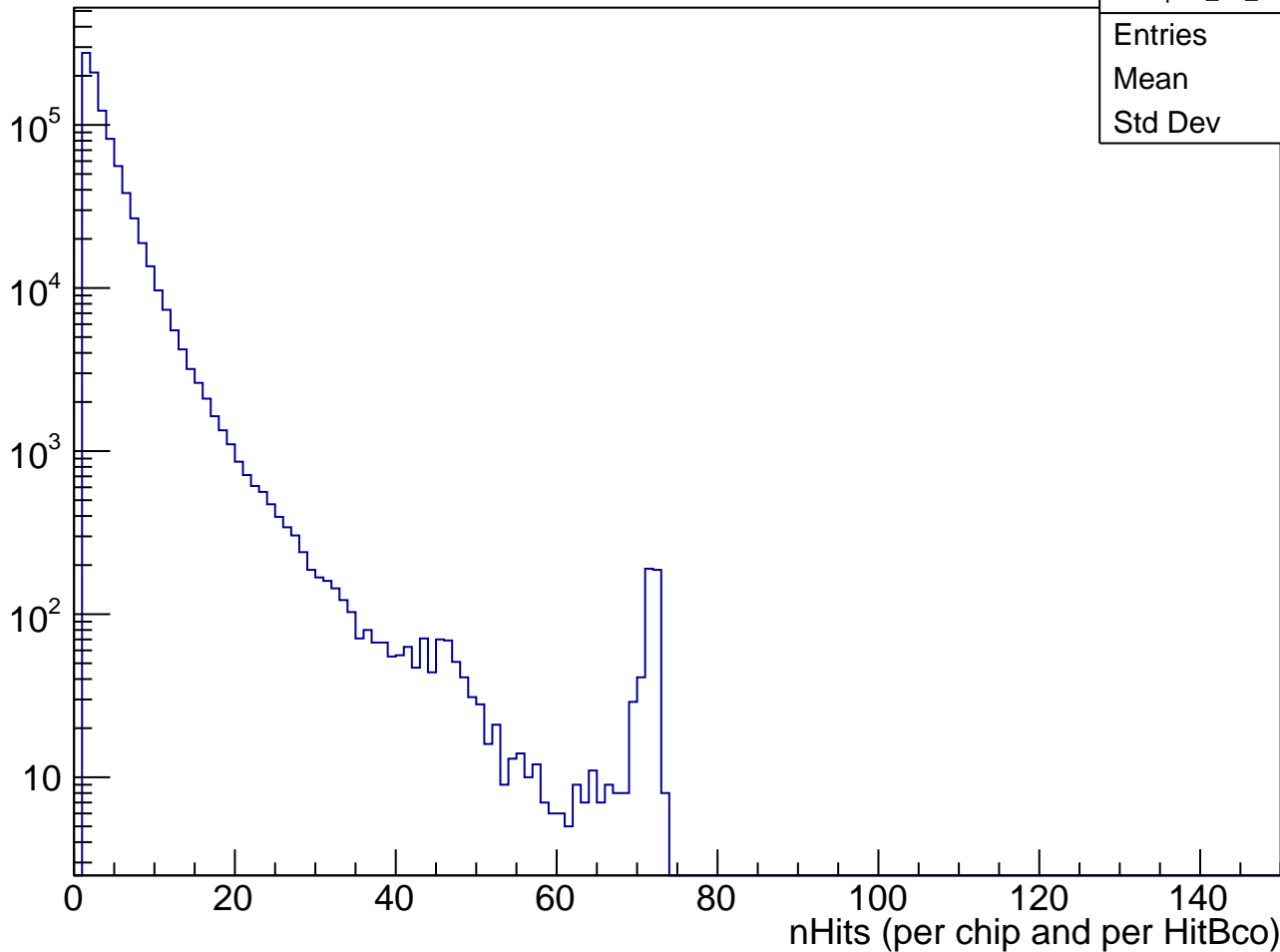


nChipHit_F6_Fch6_Chip19	
Entries	803901
Mean	3.048
Std Dev	3.532

nHits (per chip and per HitBco)

nChipHit_F6_Fch6_Chip2

Entries



nChipHit_F6_Fch6_Chip2

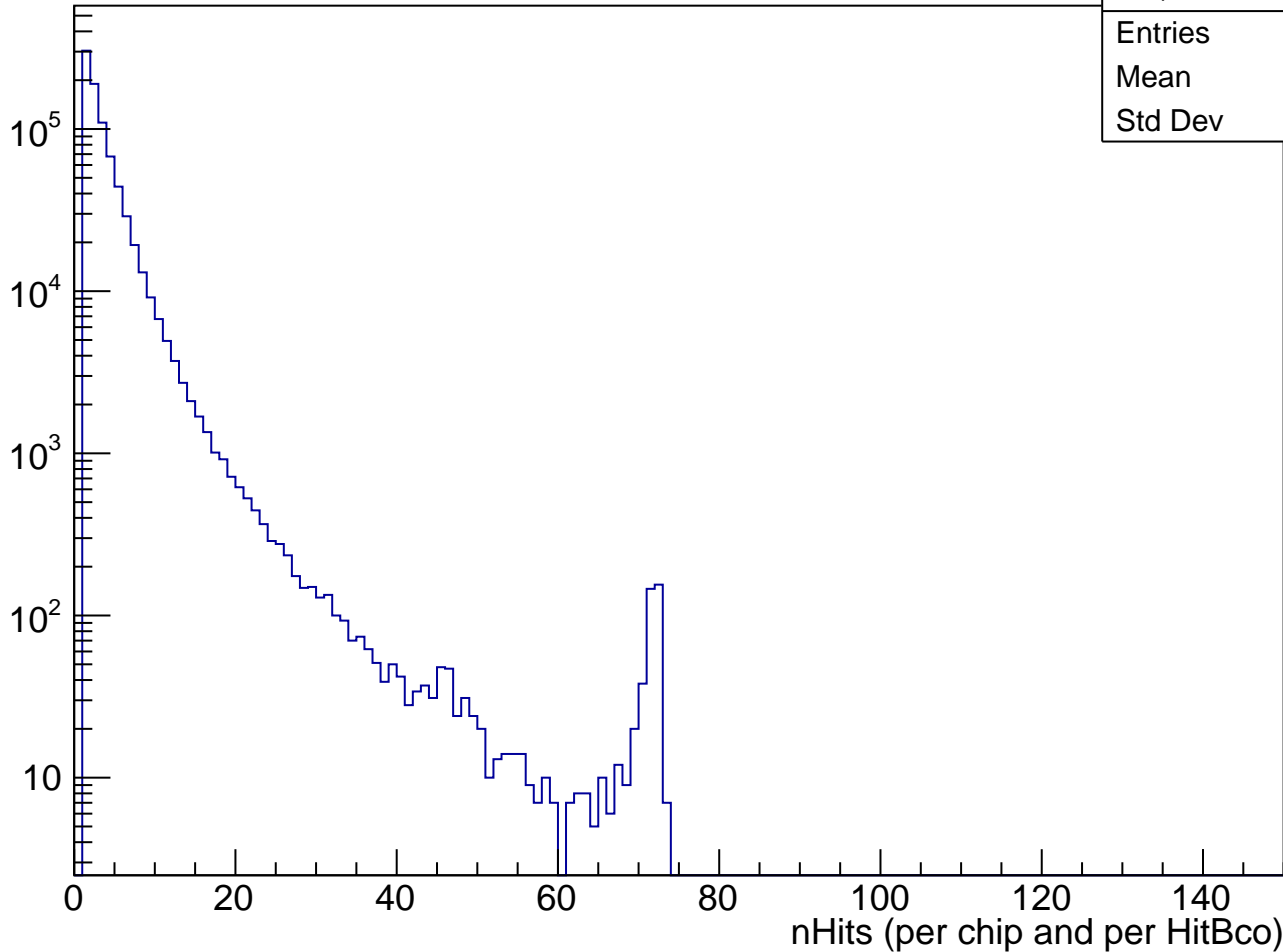
Entries 889530

Mean 3.441

Std Dev 3.909

nChipHit_F6_Fch6_Chip20

Entries

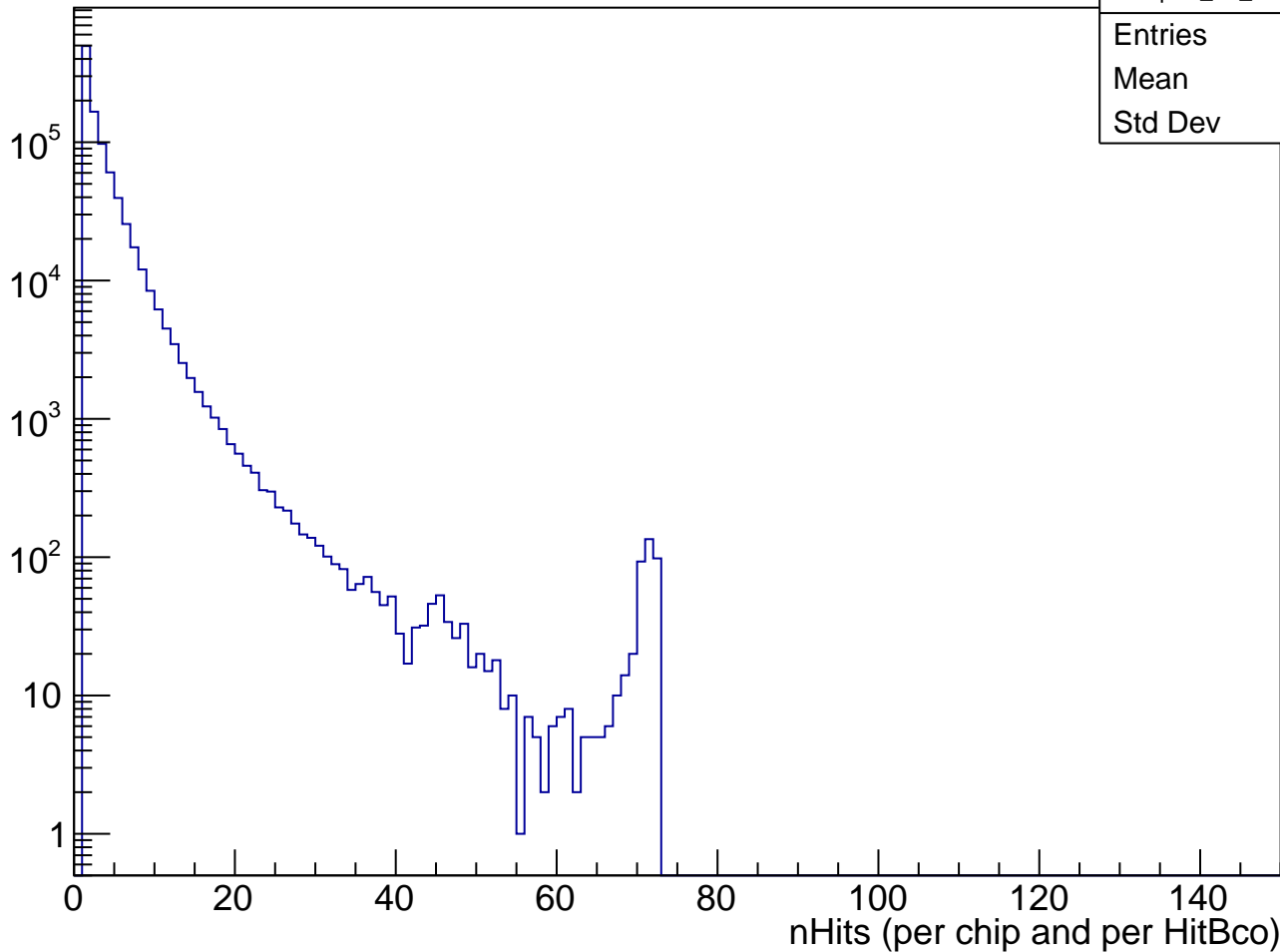


nChipHit_F6_Fch6_Chip20	
Entries	816504
Mean	3.035
Std Dev	3.559

nHits (per chip and per HitBco)

nChipHit_F6_Fch6_Chip21

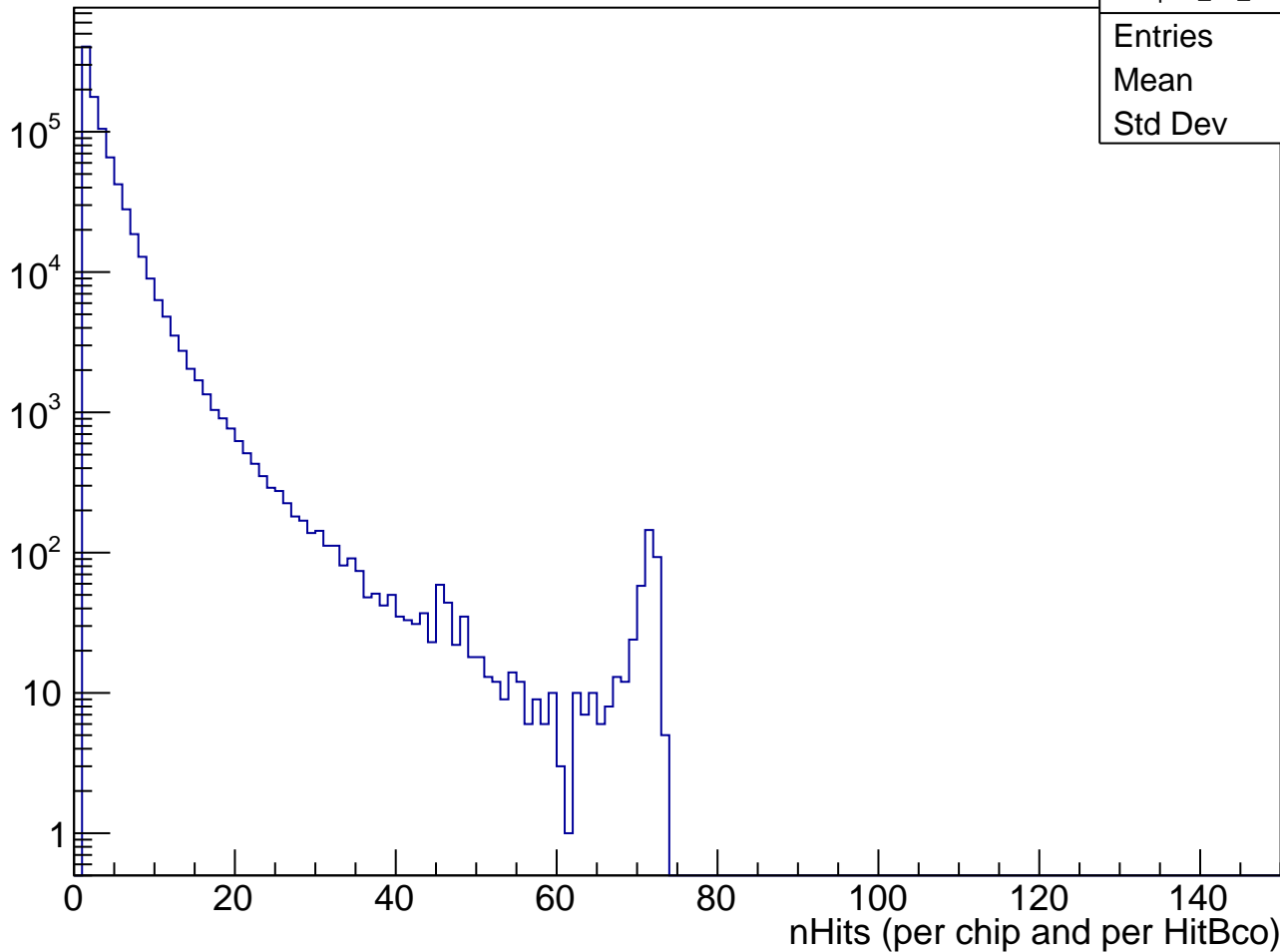
Entries



nChipHit_F6_Fch6_Chip21	
Entries	951326
Mean	2.575
Std Dev	3.274

nChipHit_F6_Fch6_Chip22

Entries

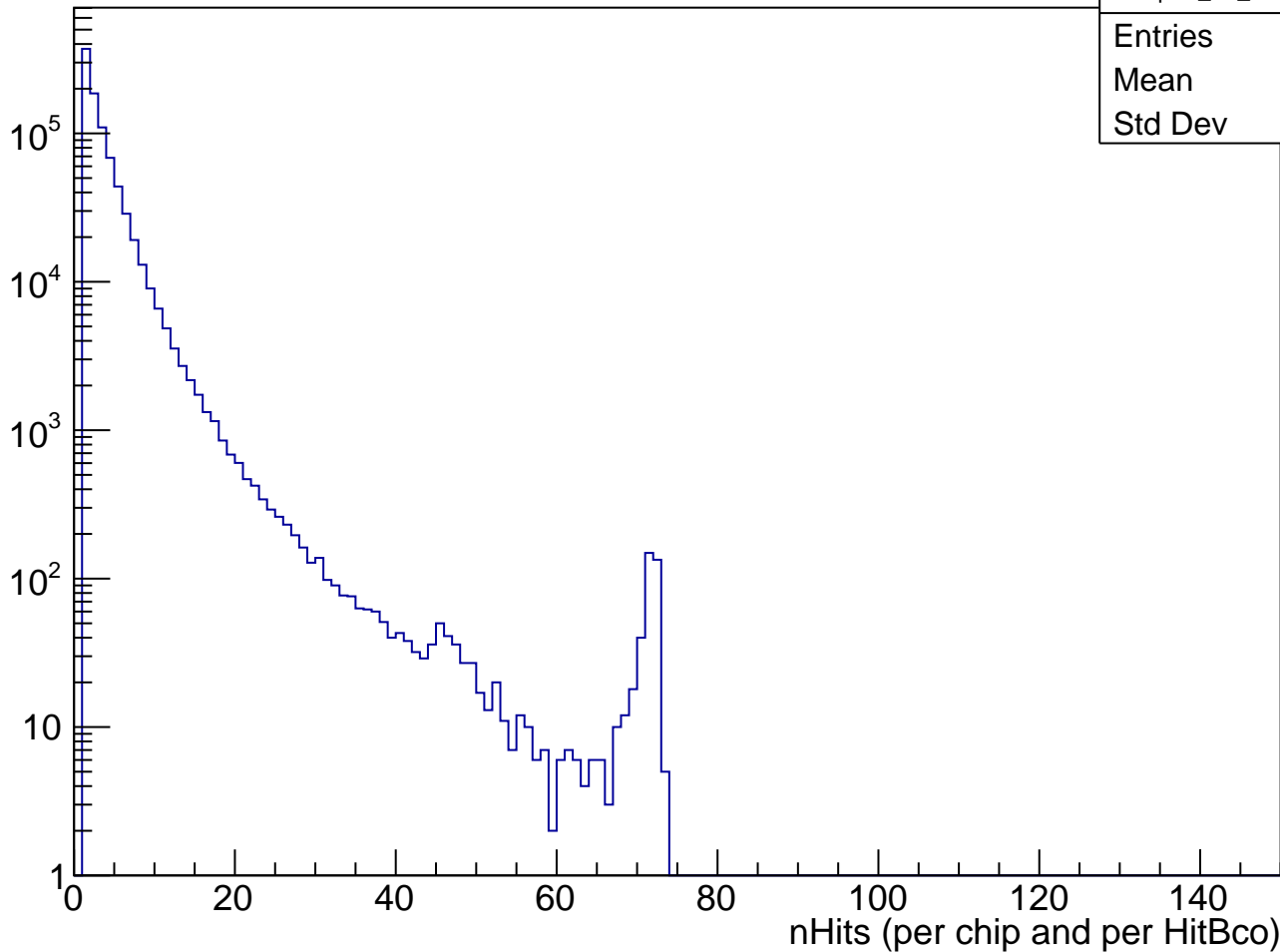


nChipHit_F6_Fch6_Chip22	
Entries	893261
Mean	2.796
Std Dev	3.411

nHits (per chip and per HitBco)

nChipHit_F6_Fch6_Chip23

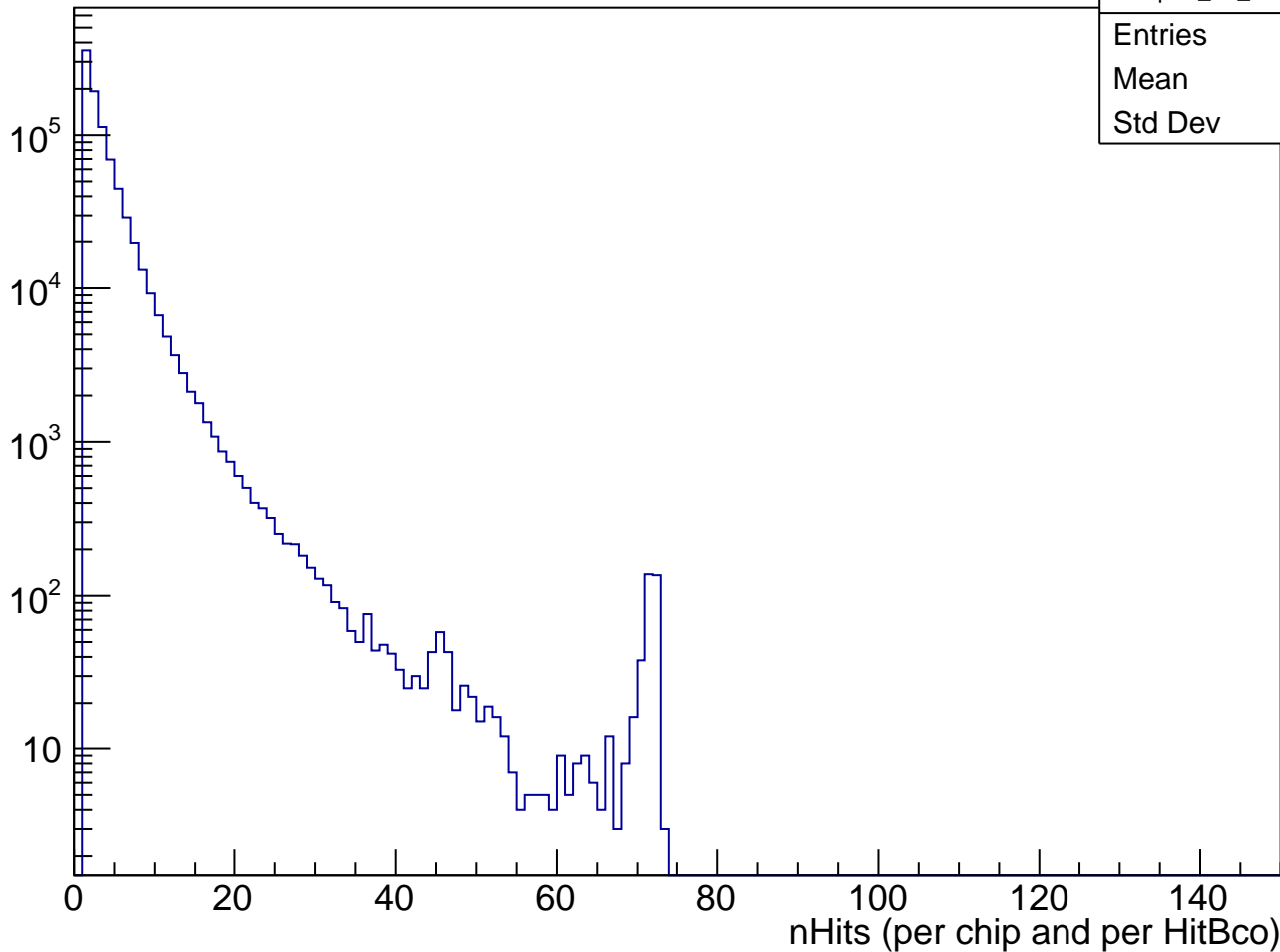
Entries



nChipHit_F6_Fch6_Chip23	
Entries	879411
Mean	2.874
Std Dev	3.432

nChipHit_F6_Fch6_Chip24

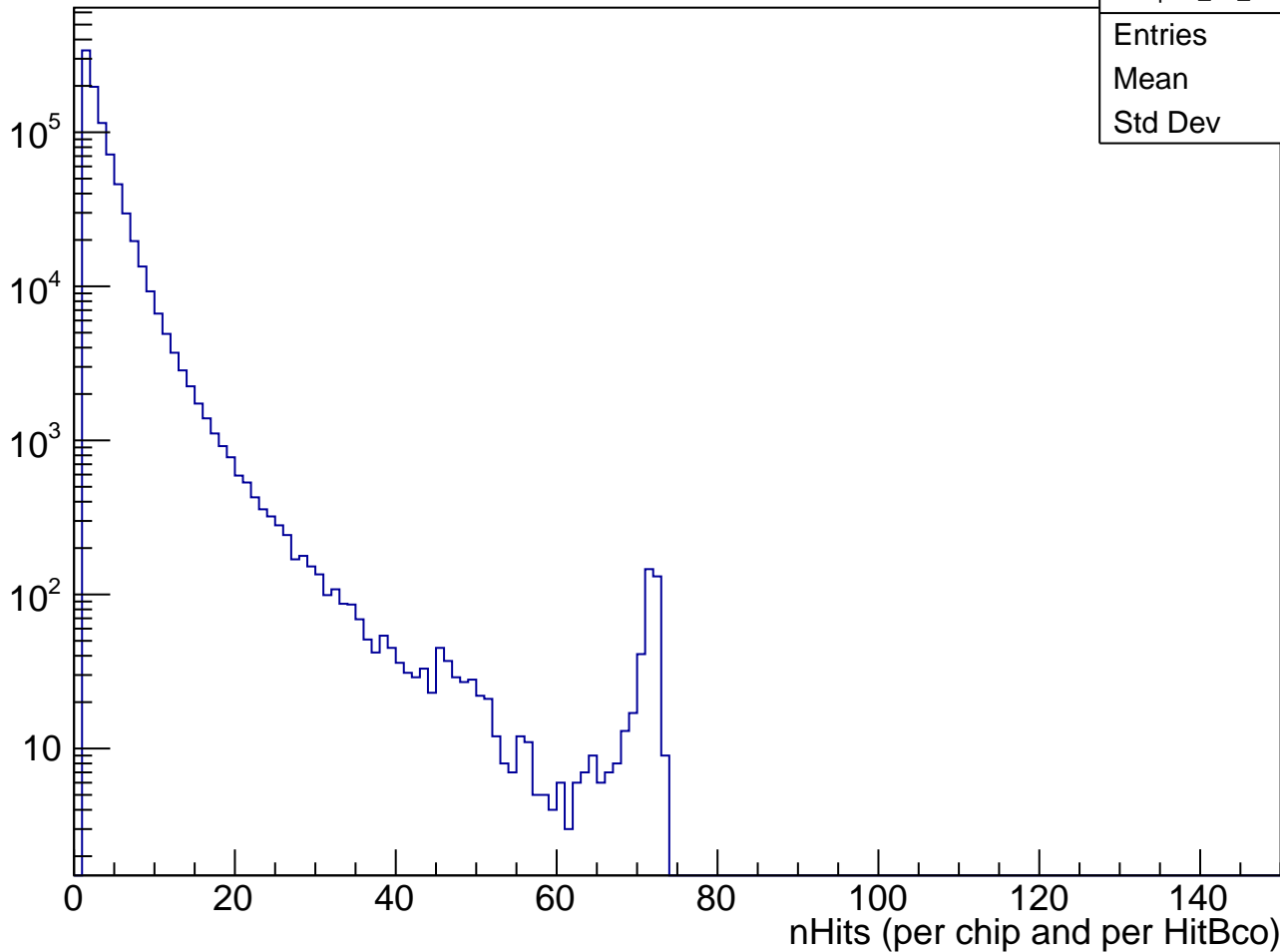
Entries



nChipHit_F6_Fch6_Chip24	
Entries	877896
Mean	2.911
Std Dev	3.417

nChipHit_F6_Fch6_Chip25

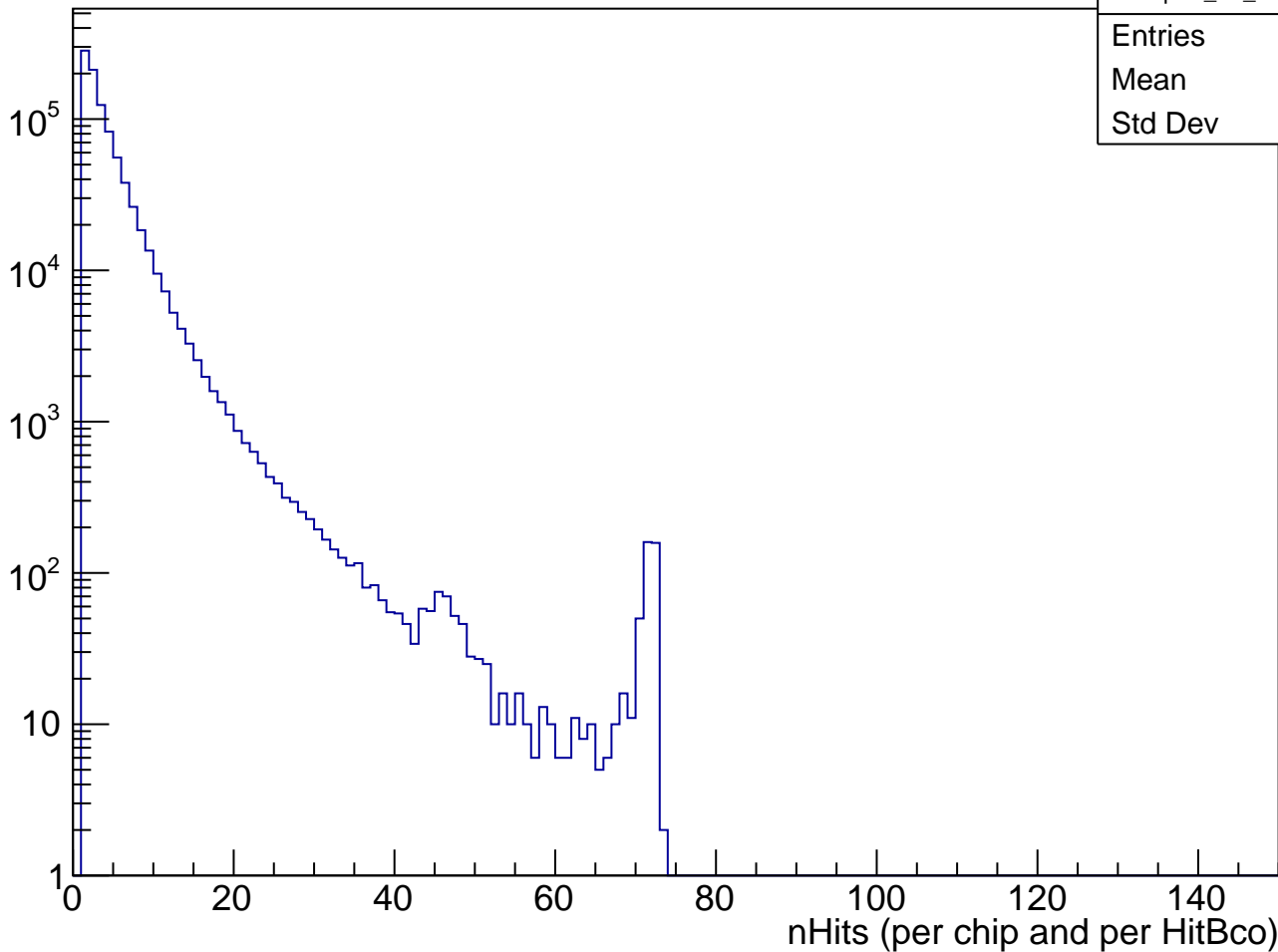
Entries



nChipHit_F6_Fch6_Chip25	
Entries	872949
Mean	2.961
Std Dev	3.455

nChipHit_F6_Fch6_Chip3

Entries



nChipHit_F6_Fch6_Chip3

Entries 898298

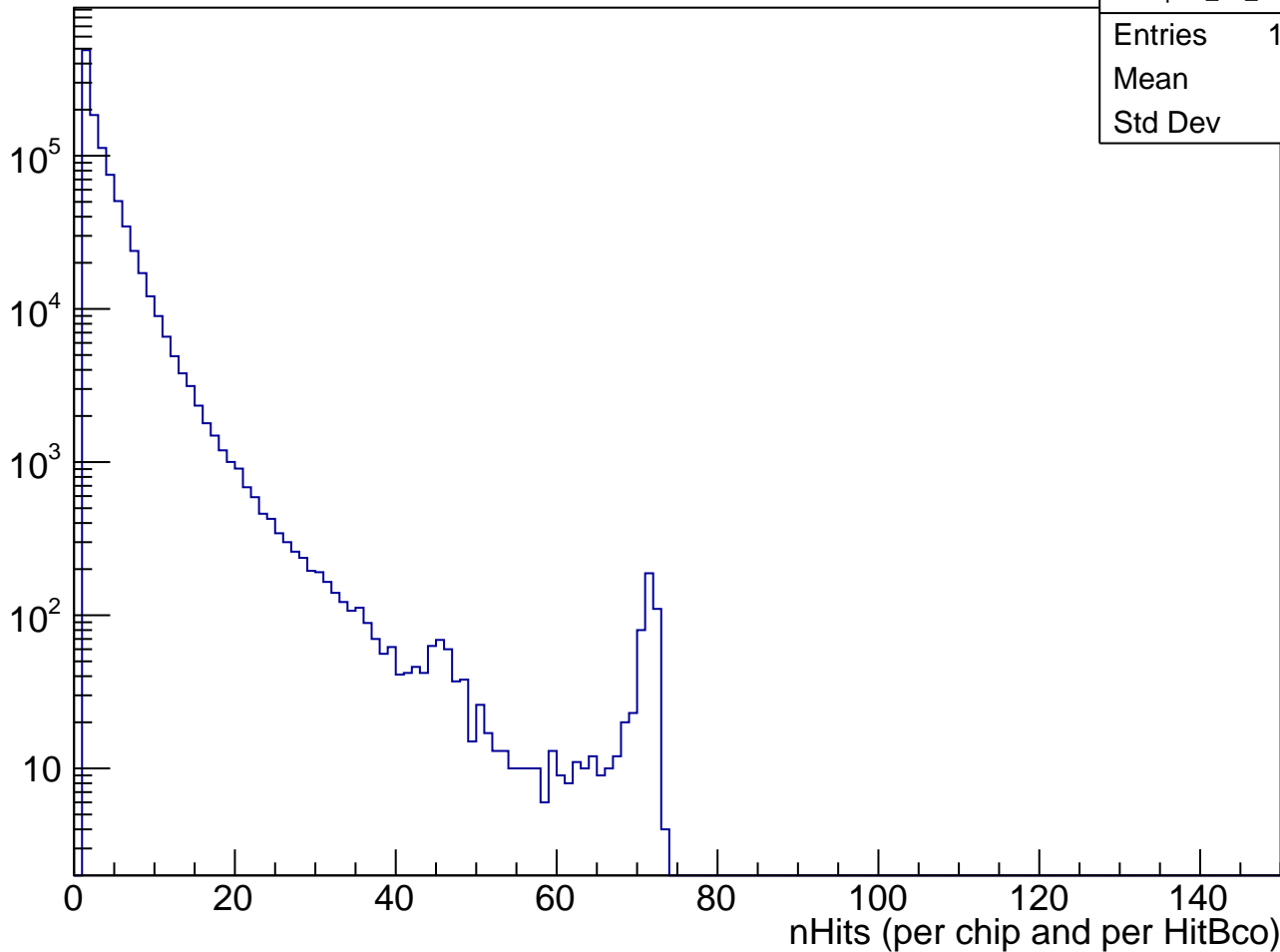
Mean 3.403

Std Dev 3.852

nHits (per chip and per HitBco)

nChipHit_F6_Fch6_Chip4

Entries



nChipHit_F6_Fch6_Chip4

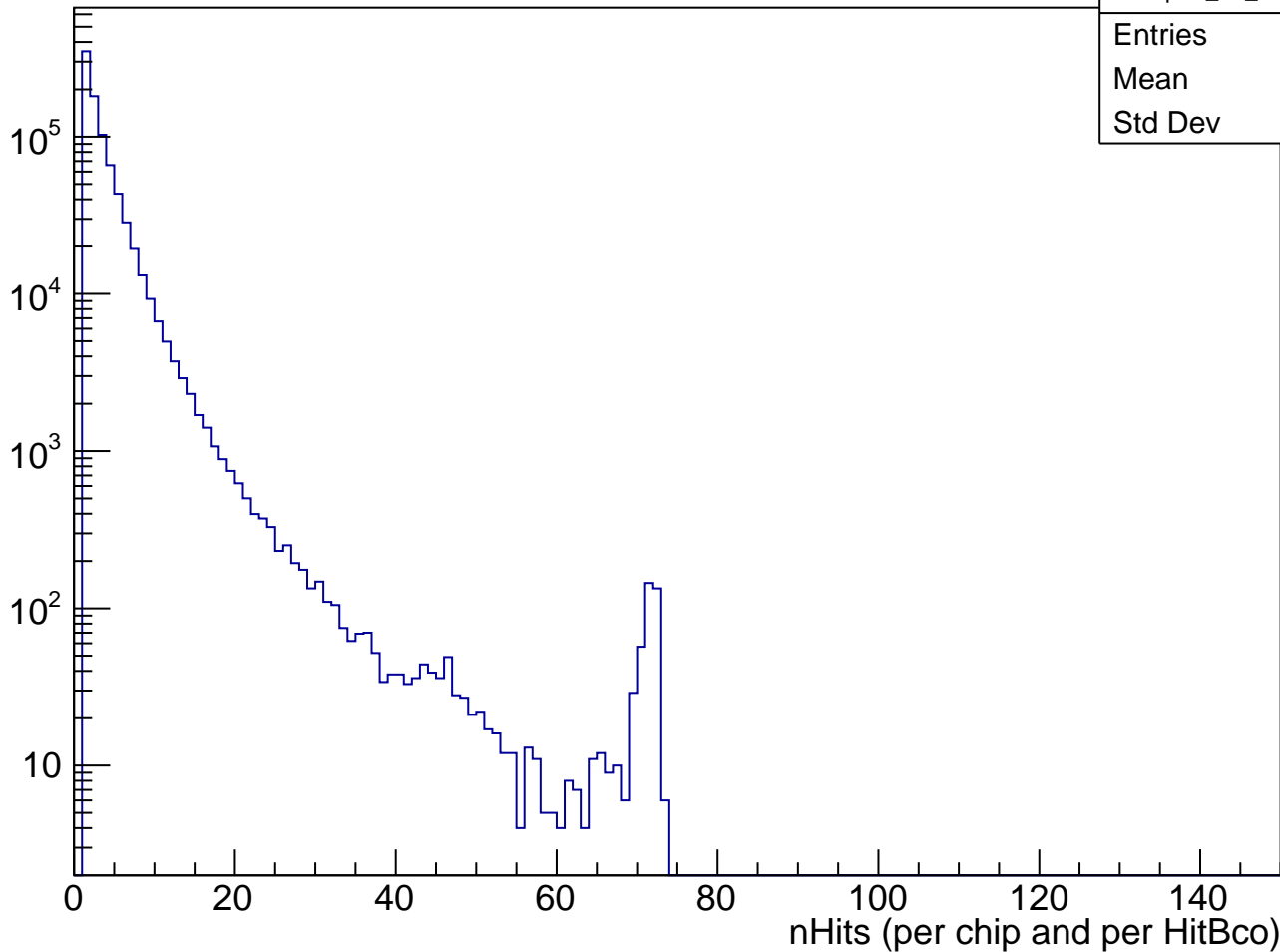
Entries 1041468

Mean 2.895

Std Dev 3.622

nChipHit_F6_Fch6_Chip5

Entries



nChipHit_F6_Fch6_Chip5

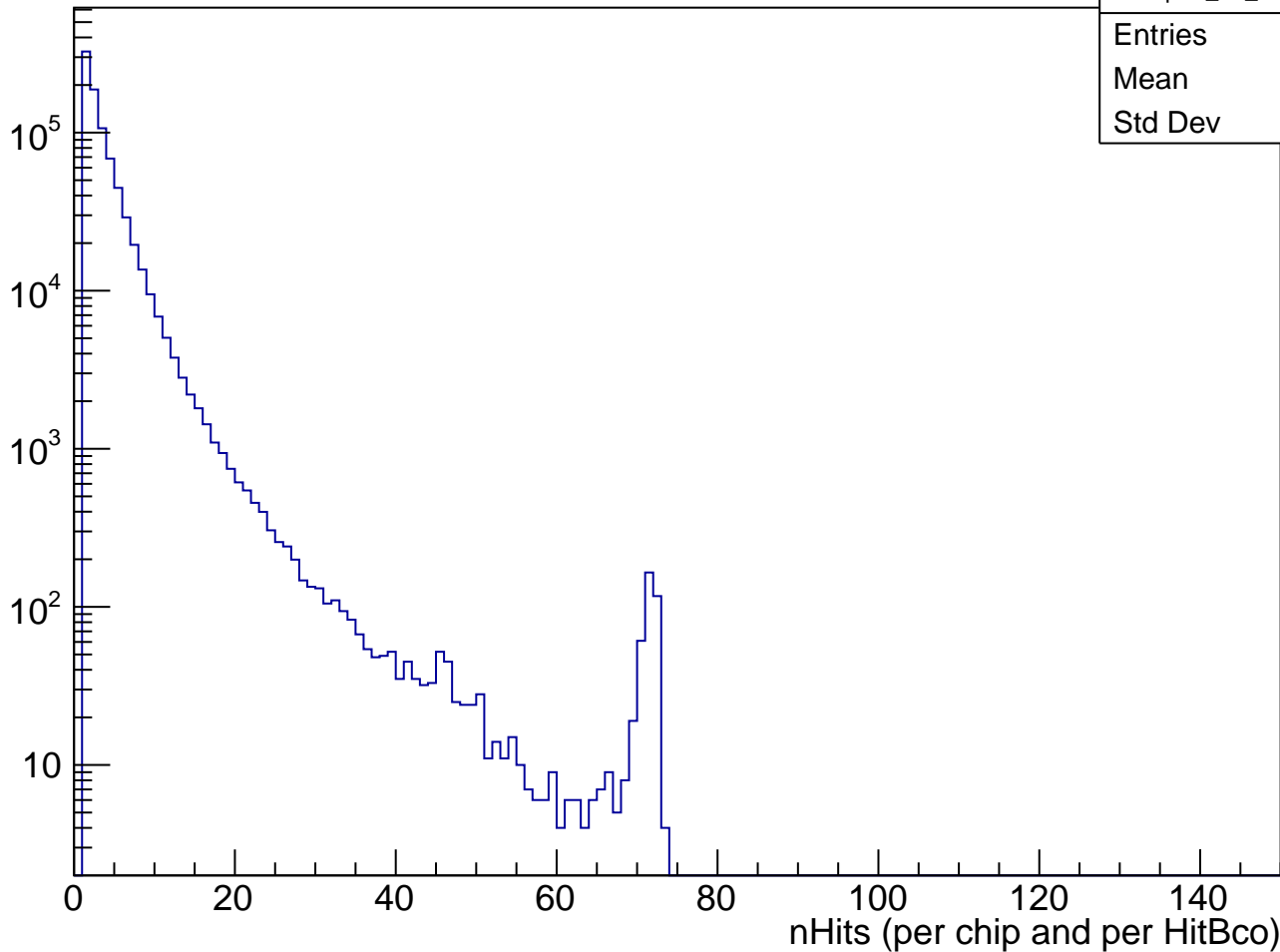
Entries 843830

Mean 2.939

Std Dev 3.539

nChipHit_F6_Fch6_Chip6

Entries



nChipHit_F6_Fch6_Chip6

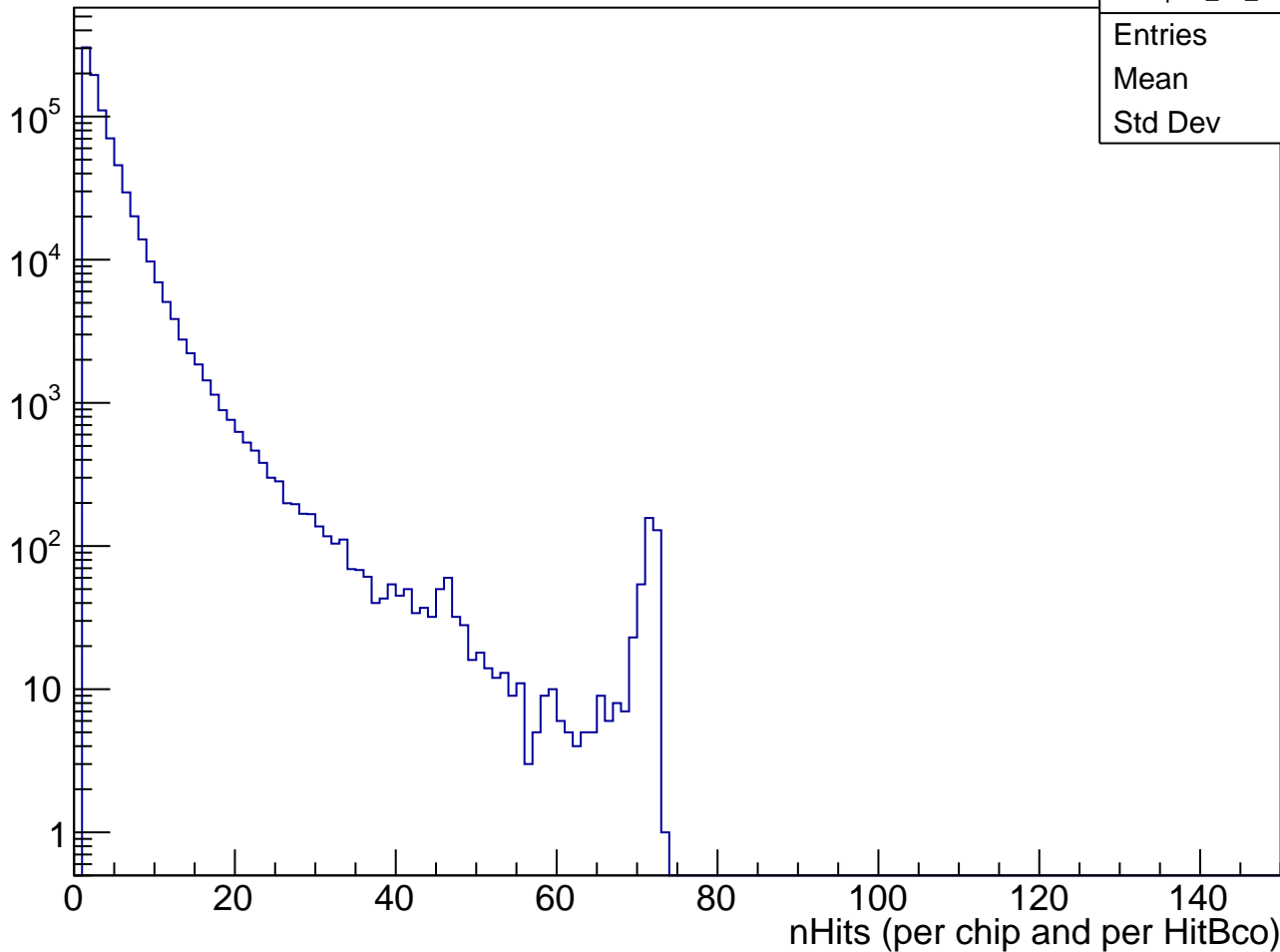
Entries 836430

Mean 3.006

Std Dev 3.548

nChipHit_F6_Fch6_Chip7

Entries

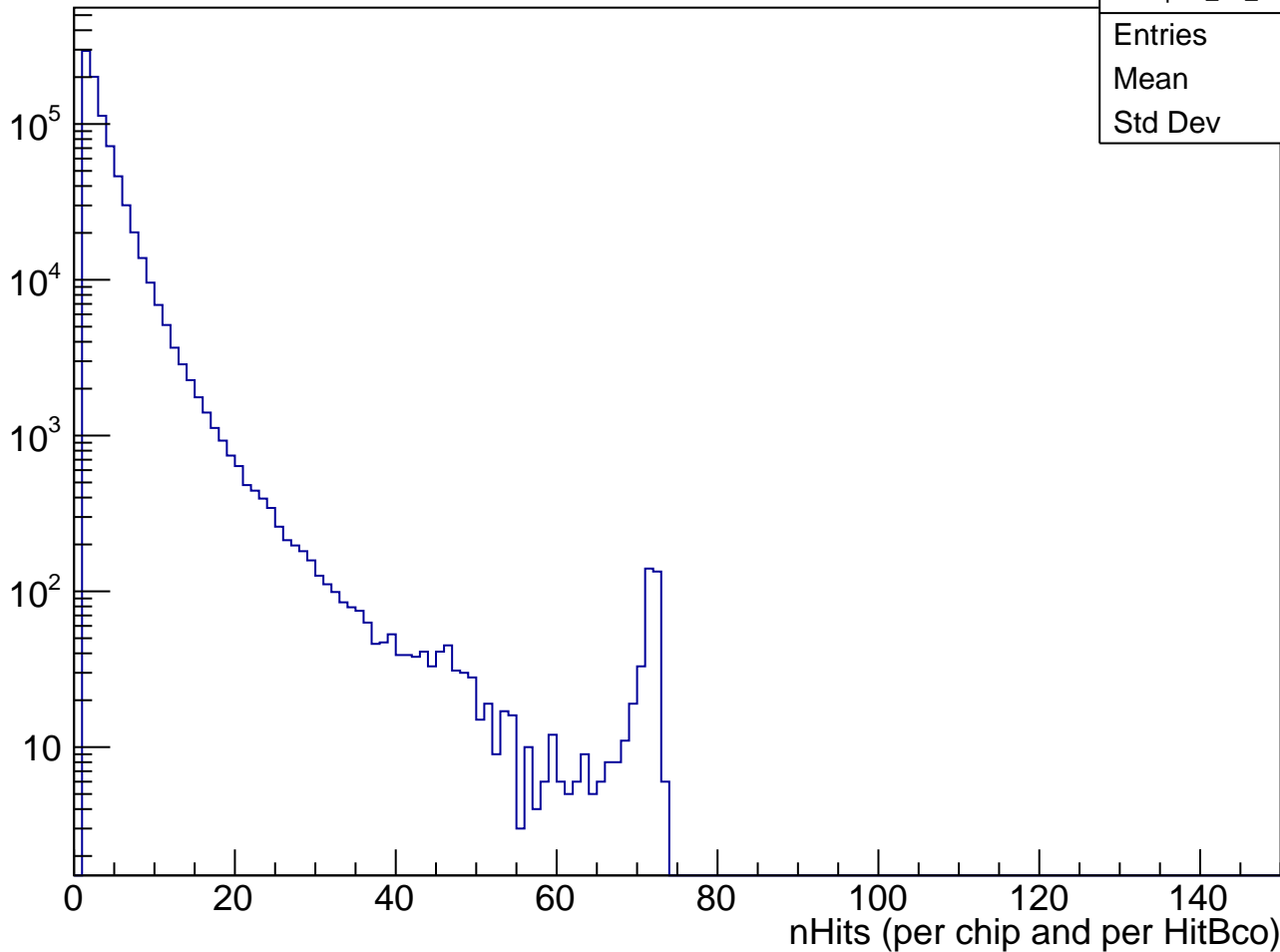


nChipHit_F6_Fch6_Chip7	
Entries	830929
Mean	3.065
Std Dev	3.561

nHits (per chip and per HitBco)

nChipHit_F6_Fch6_Chip8

Entries



nChipHit_F6_Fch6_Chip8

Entries 832434

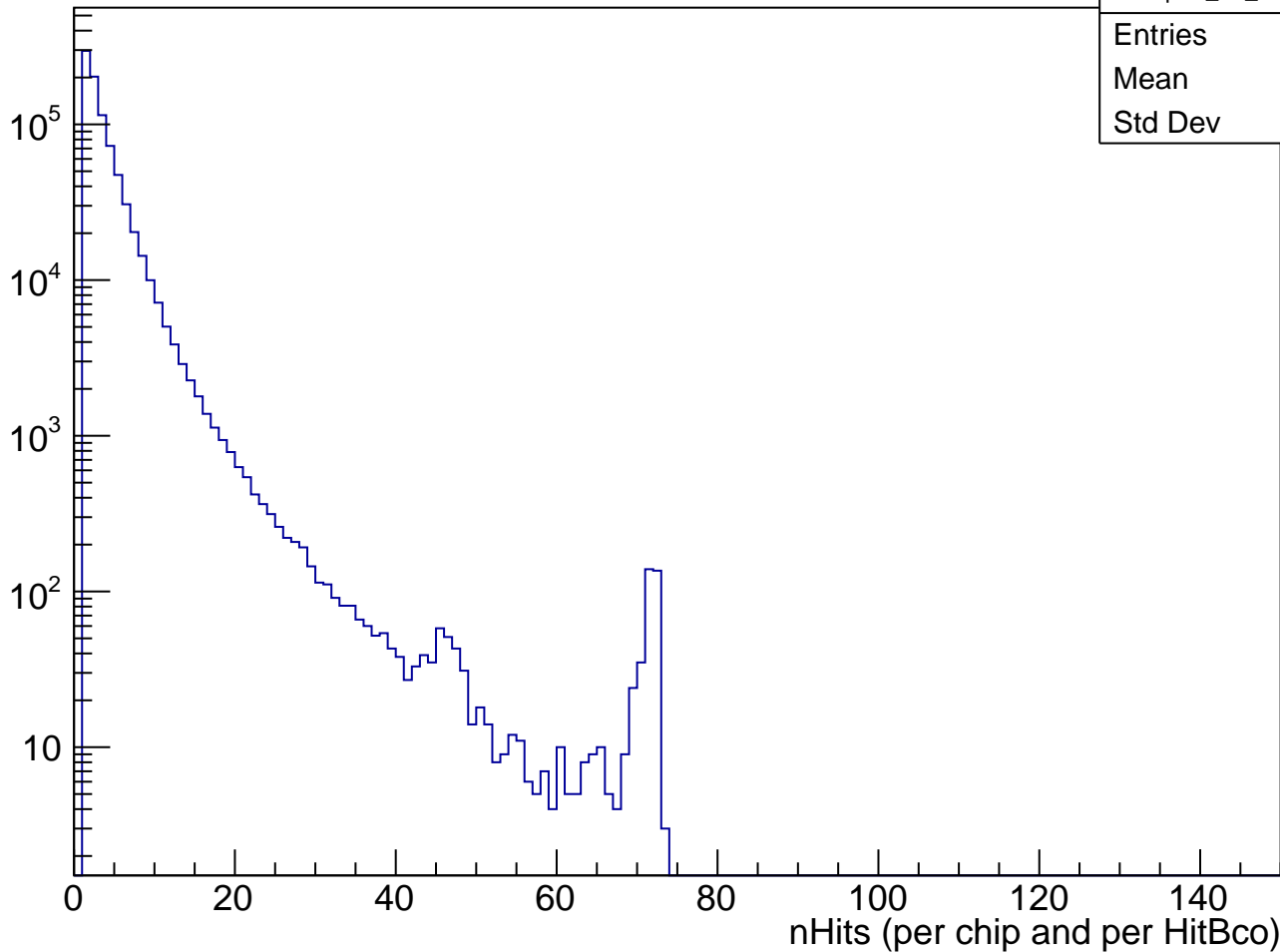
Mean 3.08

Std Dev 3.529

nHits (per chip and per HitBco)

nChipHit_F6_Fch6_Chip9

Entries



nChipHit_F6_Fch6_Chip9

Entries 841169

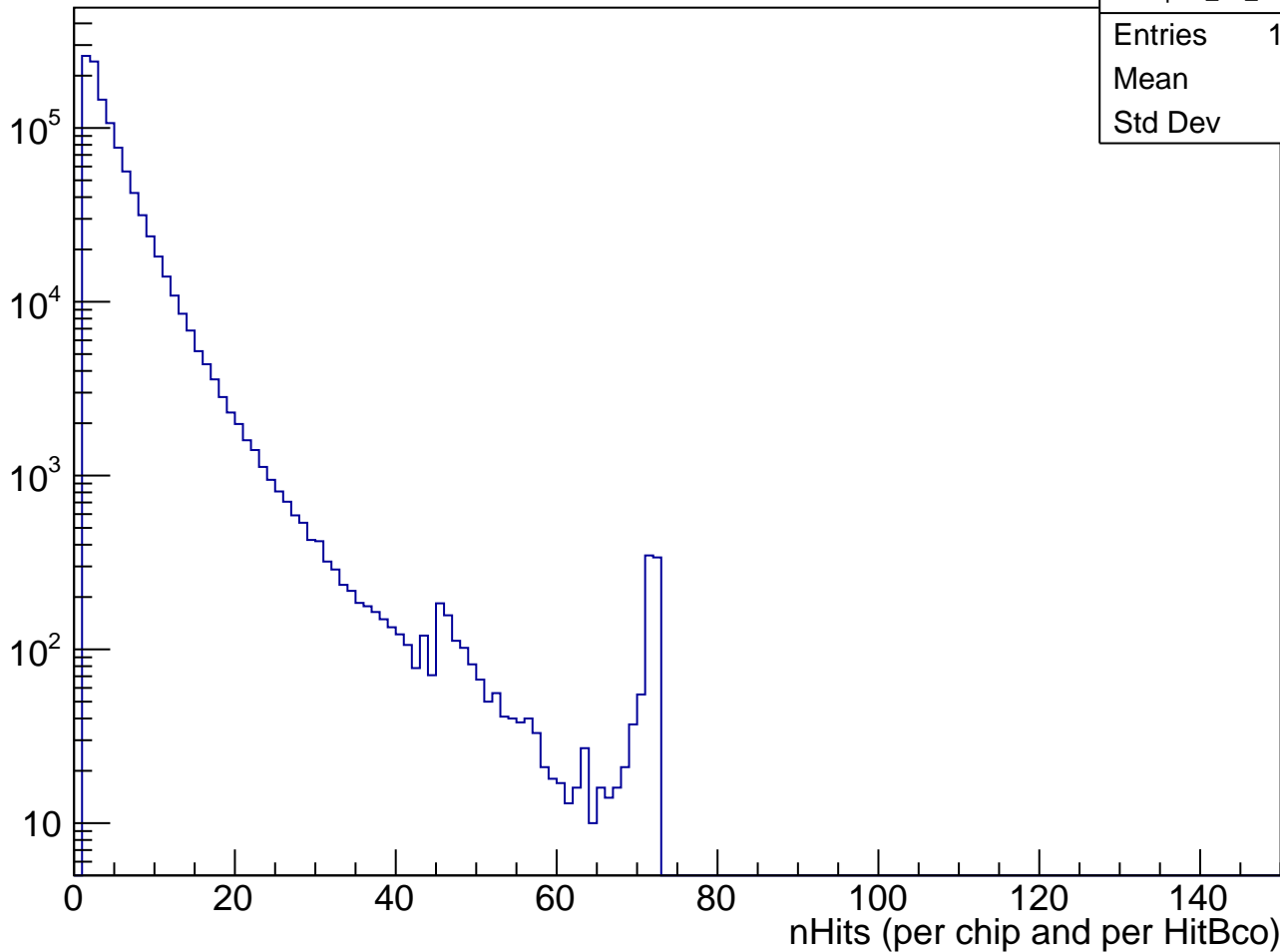
Mean 3.088

Std Dev 3.517

nHits (per chip and per HitBco)

nChipHit_F6_Fch7_Chip0

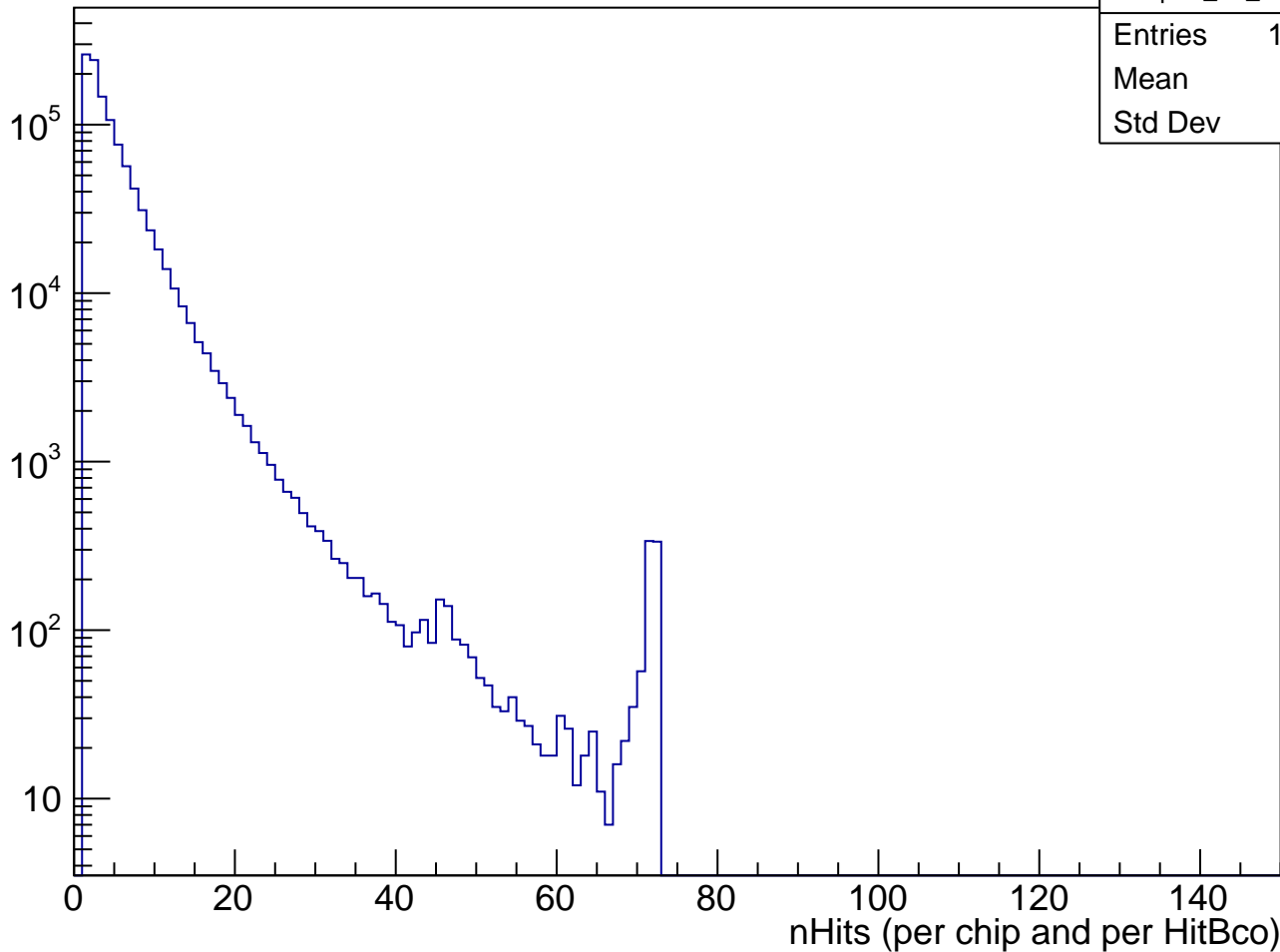
Entries



nChipHit_F6_Fch7_Chip0	
Entries	1075776
Mean	4.196
Std Dev	4.748

nChipHit_F6_Fch7_Chip1

Entries

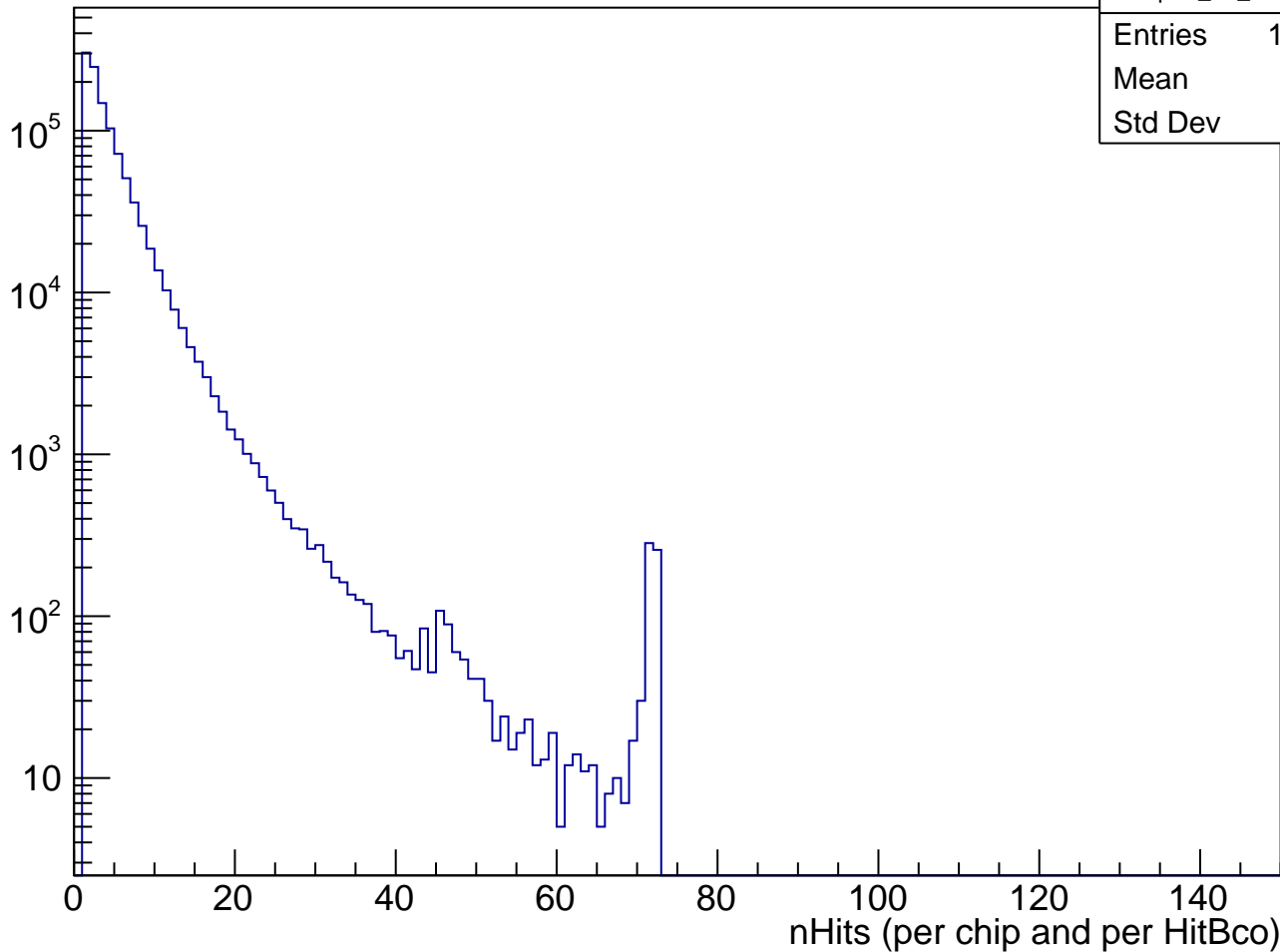


nChipHit_F6_Fch7_Chip1	
Entries	1075595
Mean	4.168
Std Dev	4.69

nHits (per chip and per HitBco)

nChipHit_F6_Fch7_Chip10

Entries

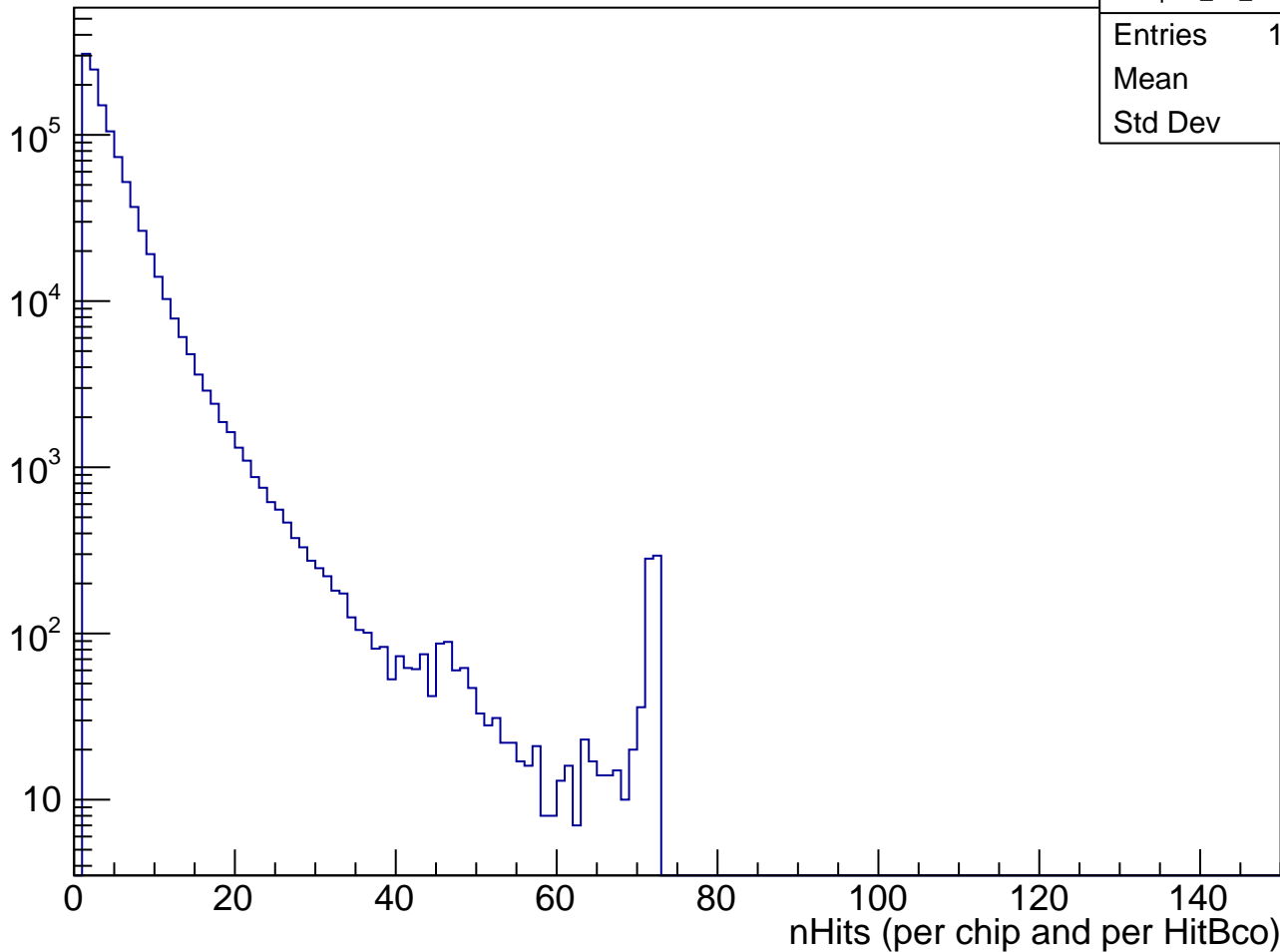


nChipHit_F6_Fch7_Chip10	
Entries	1070185
Mean	3.662
Std Dev	4.077

nHits (per chip and per HitBco)

nChipHit_F6_Fch7_Chip11

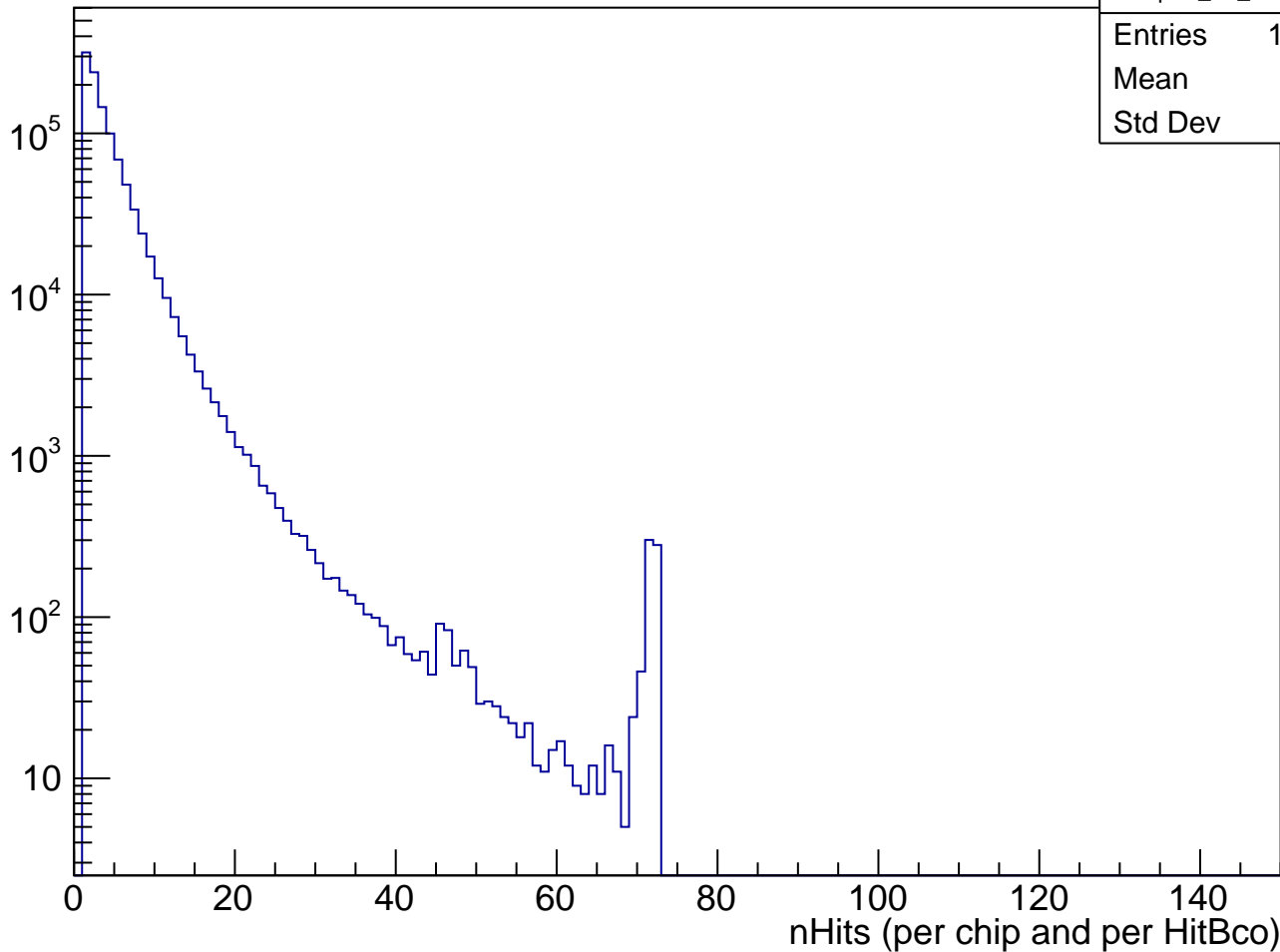
Entries



nChipHit_F6_Fch7_Chip11	
Entries	1083859
Mean	3.68
Std Dev	4.117

nChipHit_F6_Fch7_Chip12

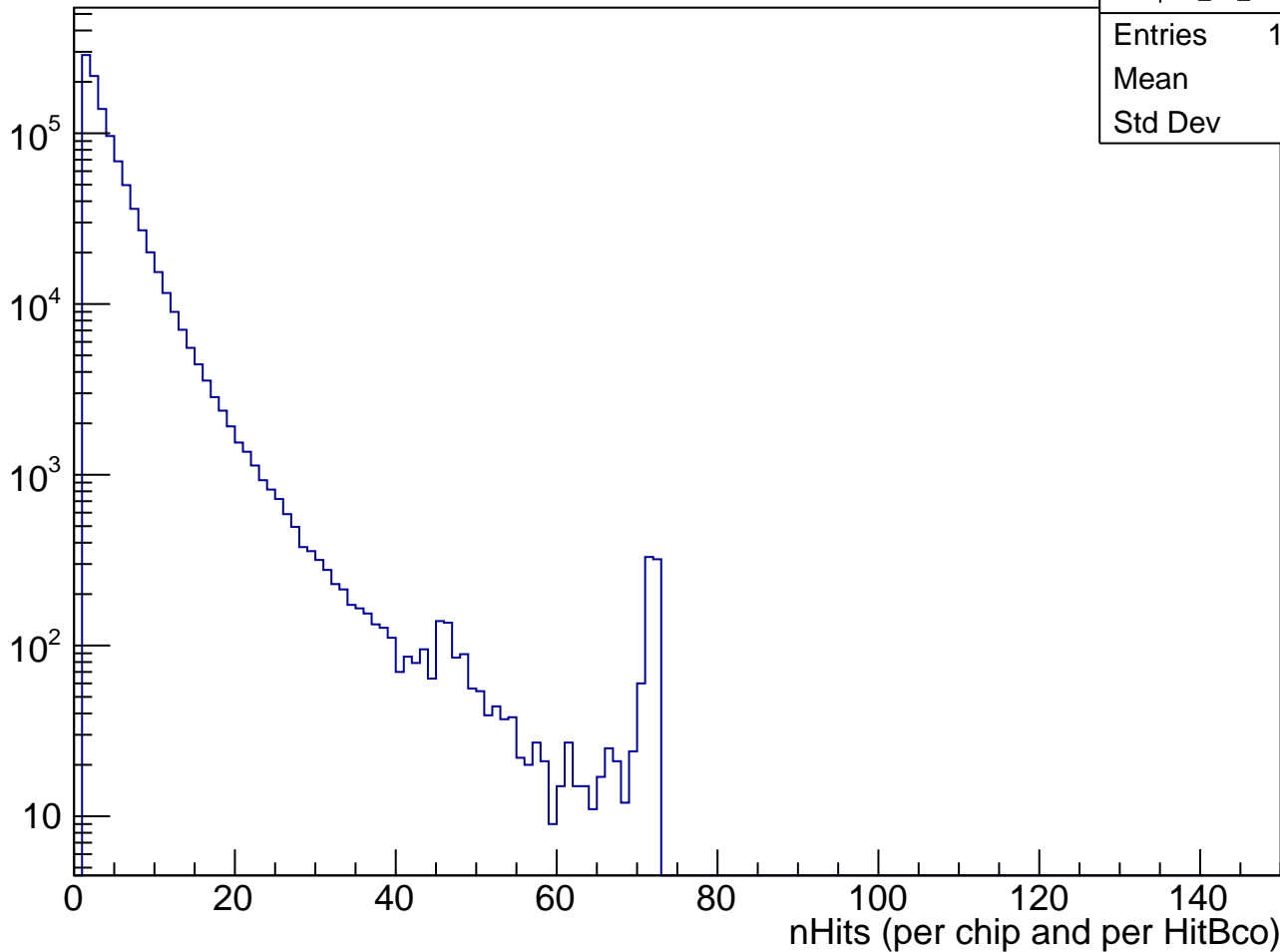
Entries



nChipHit_F6_Fch7_Chip12	
Entries	1053385
Mean	3.568
Std Dev	4.074

nChipHit_F6_Fch7_Chip13

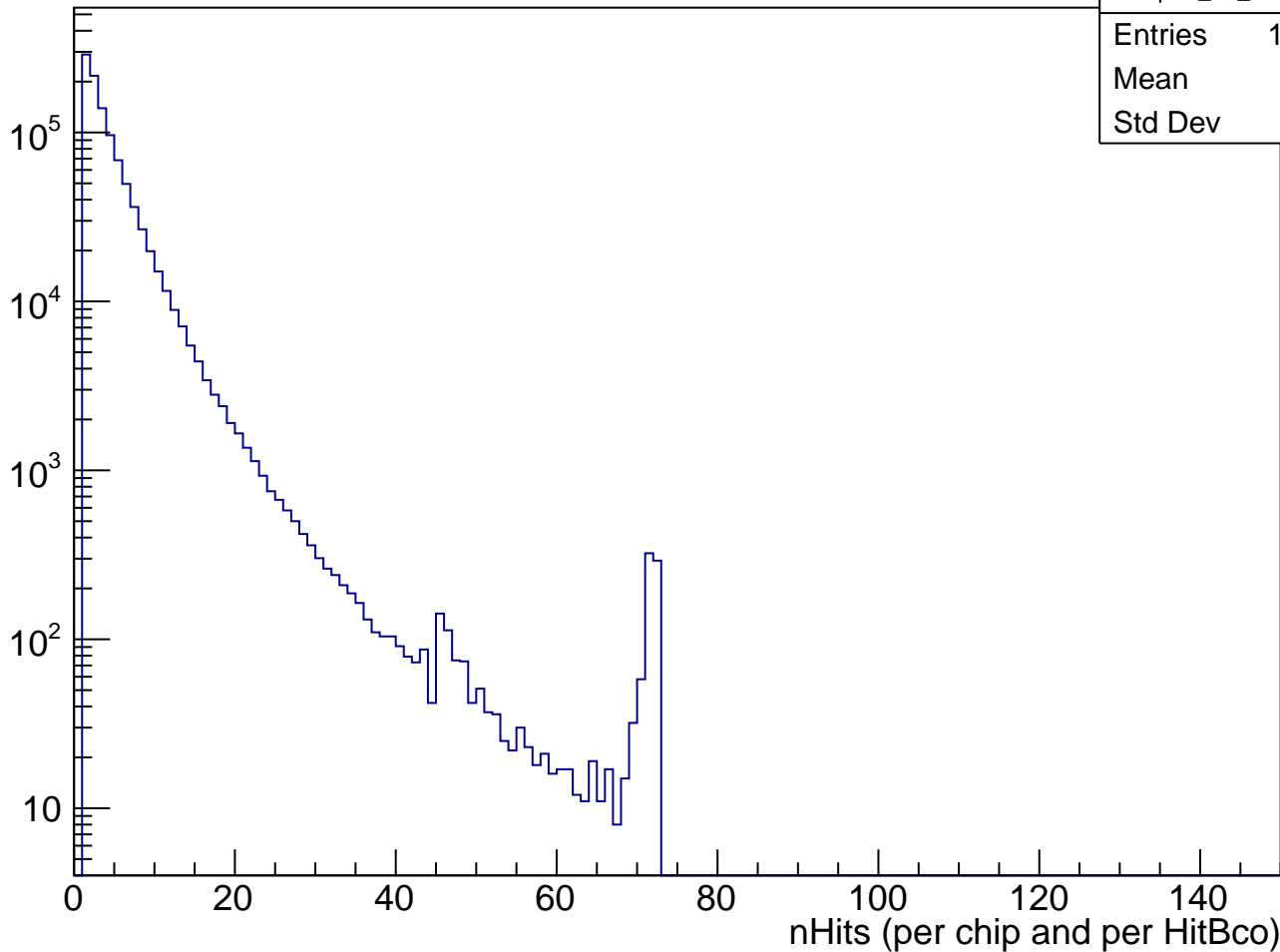
Entries



nChipHit_F6_Fch7_Chip13	
Entries	1016414
Mean	3.934
Std Dev	4.578

nChipHit_F6_Fch7_Chip14

Entries

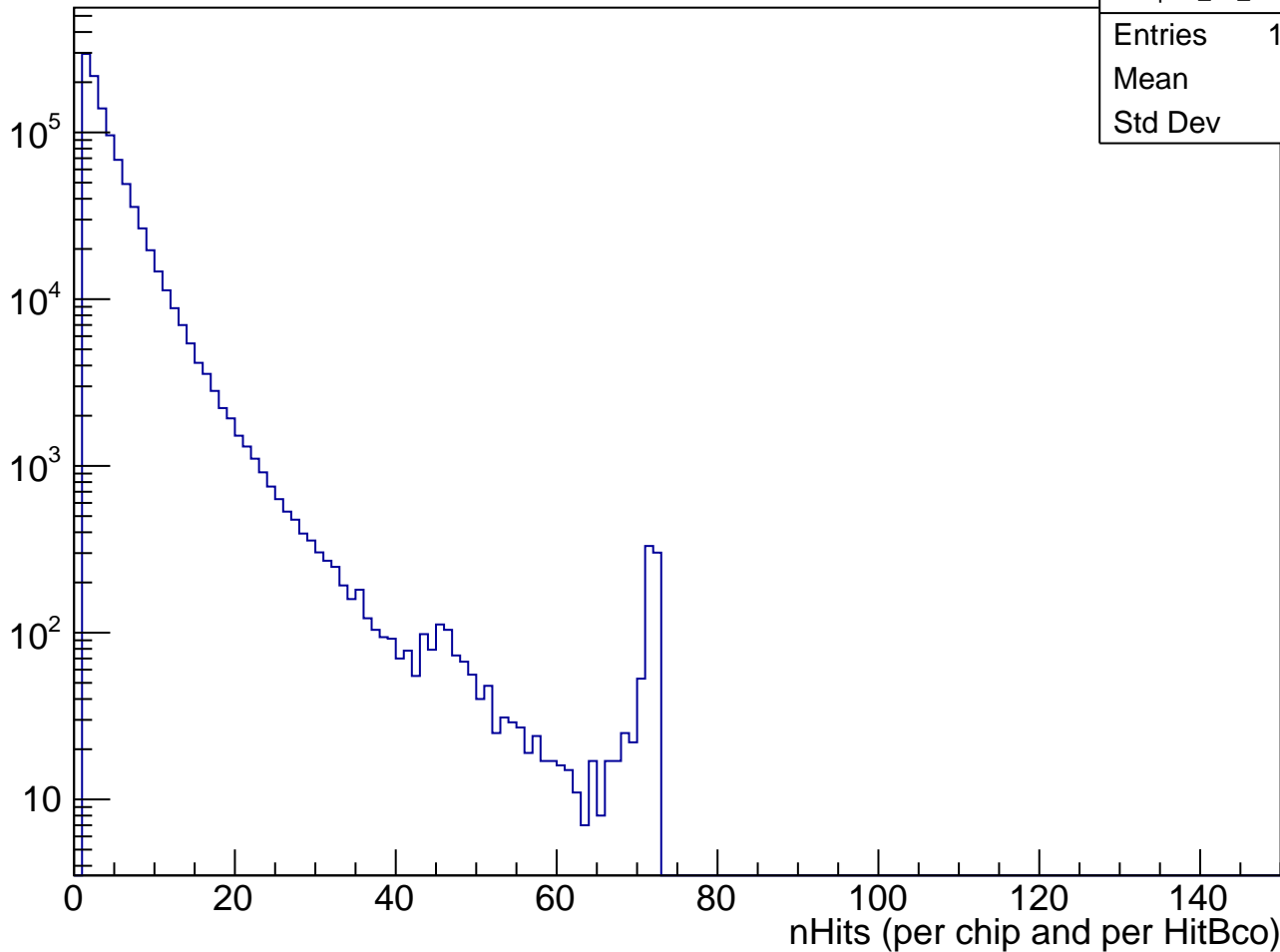


nChipHit_F6_Fch7_Chip14	
Entries	1017355
Mean	3.908
Std Dev	4.51

nHits (per chip and per HitBco)

nChipHit_F6_Fch7_Chip15

Entries

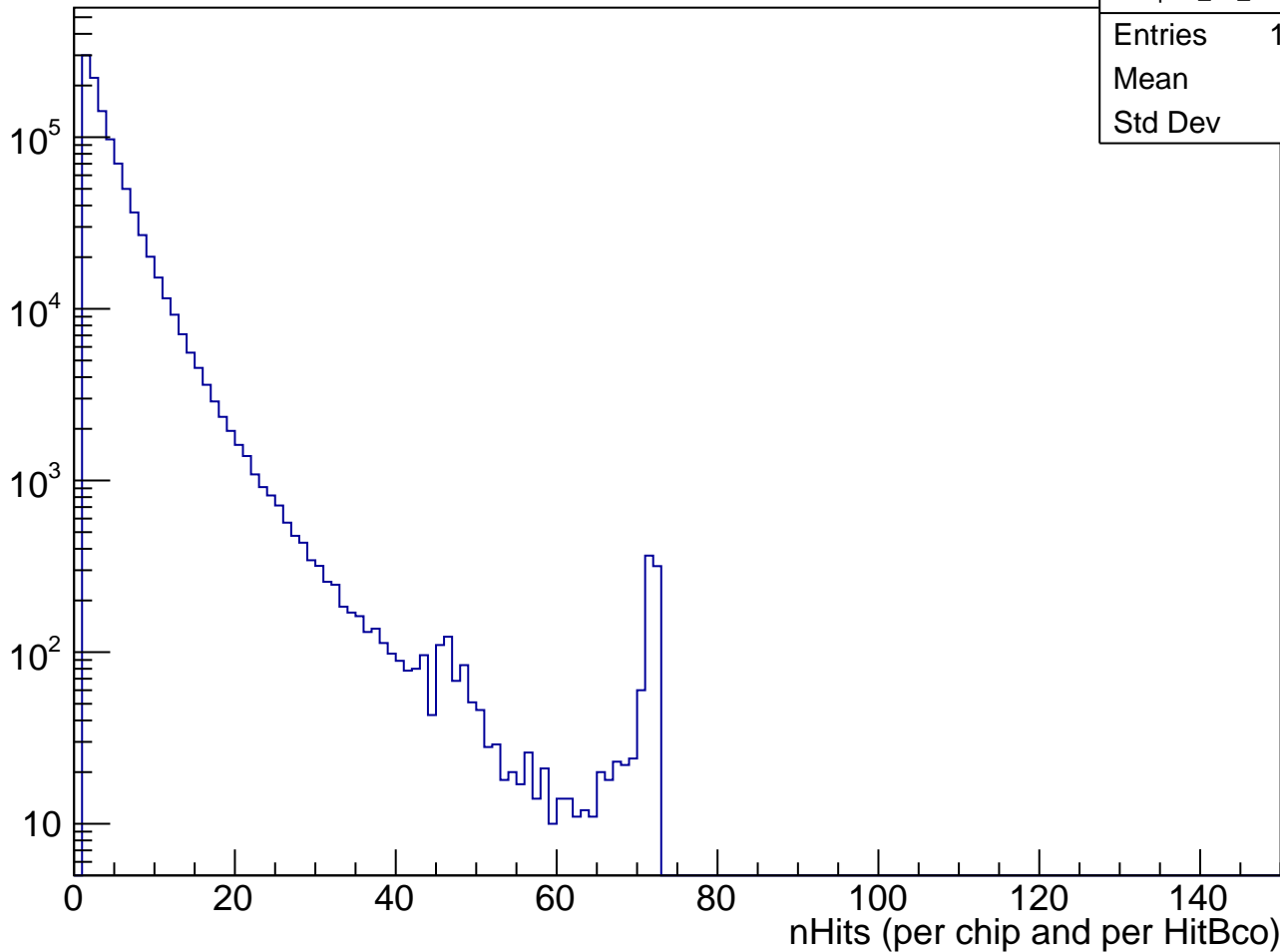


nChipHit_F6_Fch7_Chip15	
Entries	1021864
Mean	3.866
Std Dev	4.477

nHits (per chip and per HitBco)

nChipHit_F6_Fch7_Chip16

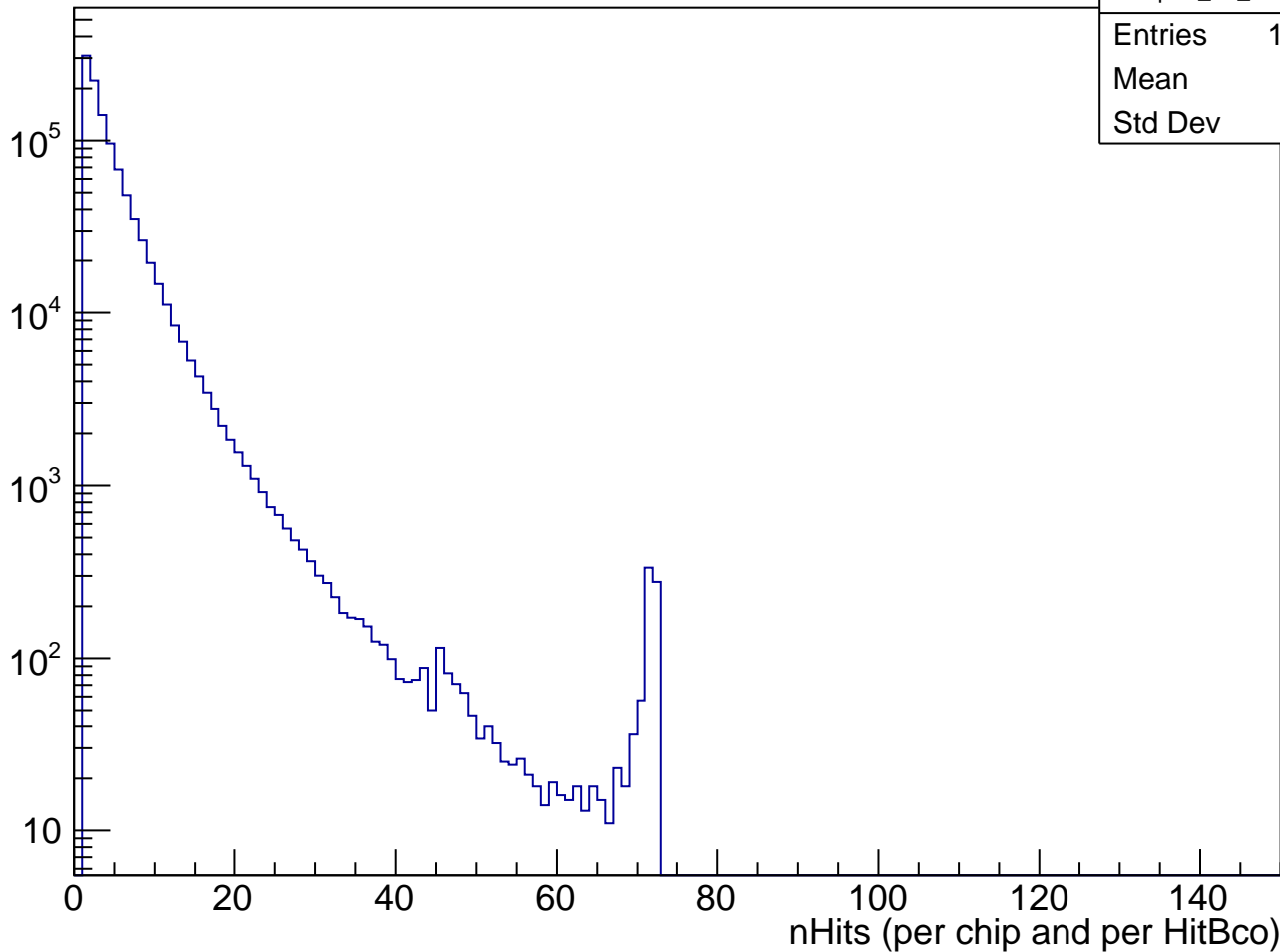
Entries



nChipHit_F6_Fch7_Chip16	
Entries	1040493
Mean	3.884
Std Dev	4.51

nChipHit_F6_Fch7_Chip17

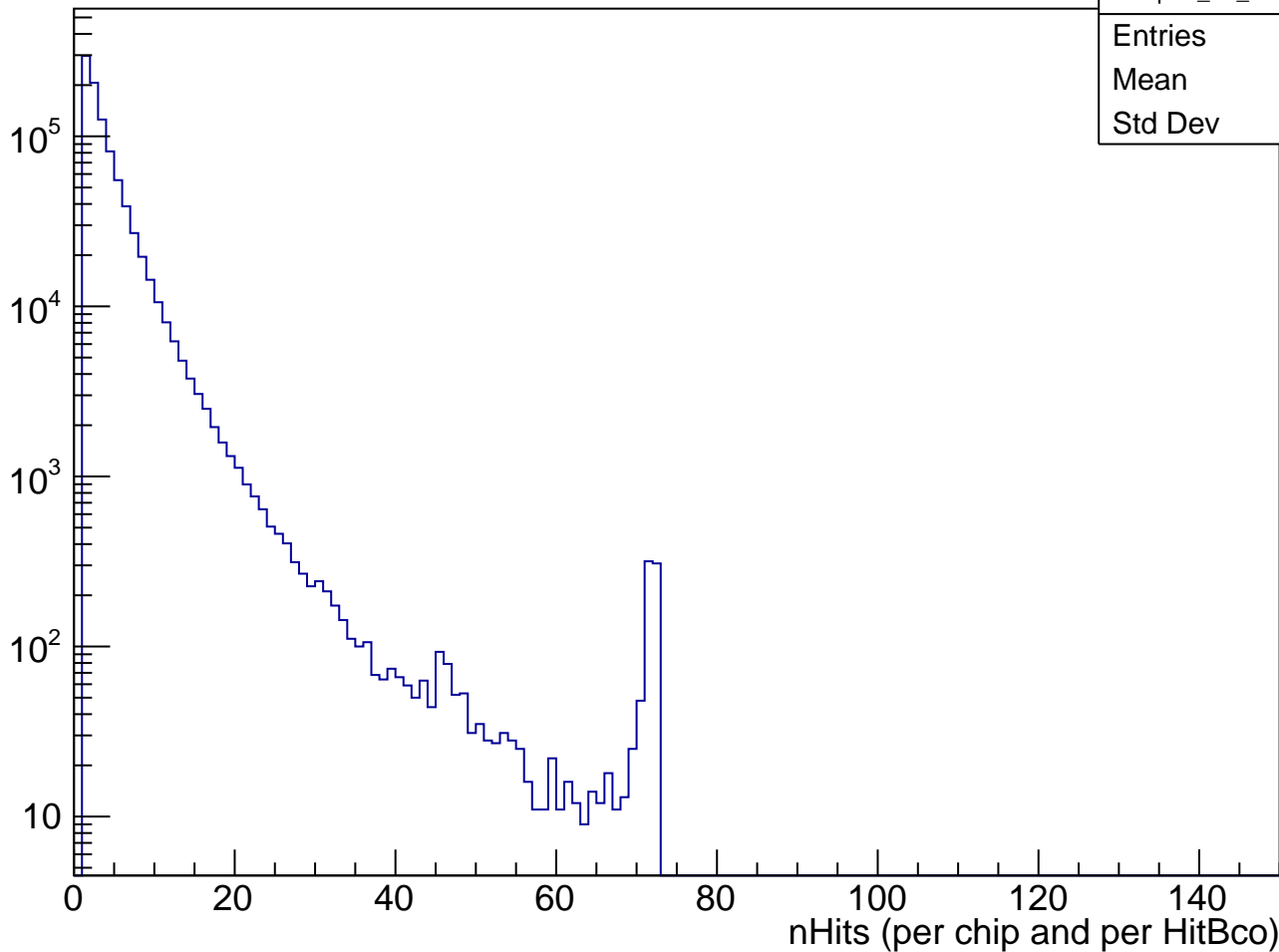
Entries



nChipHit_F6_Fch7_Chip17	
Entries	1038814
Mean	3.803
Std Dev	4.441

nChipHit_F6_Fch7_Chip18

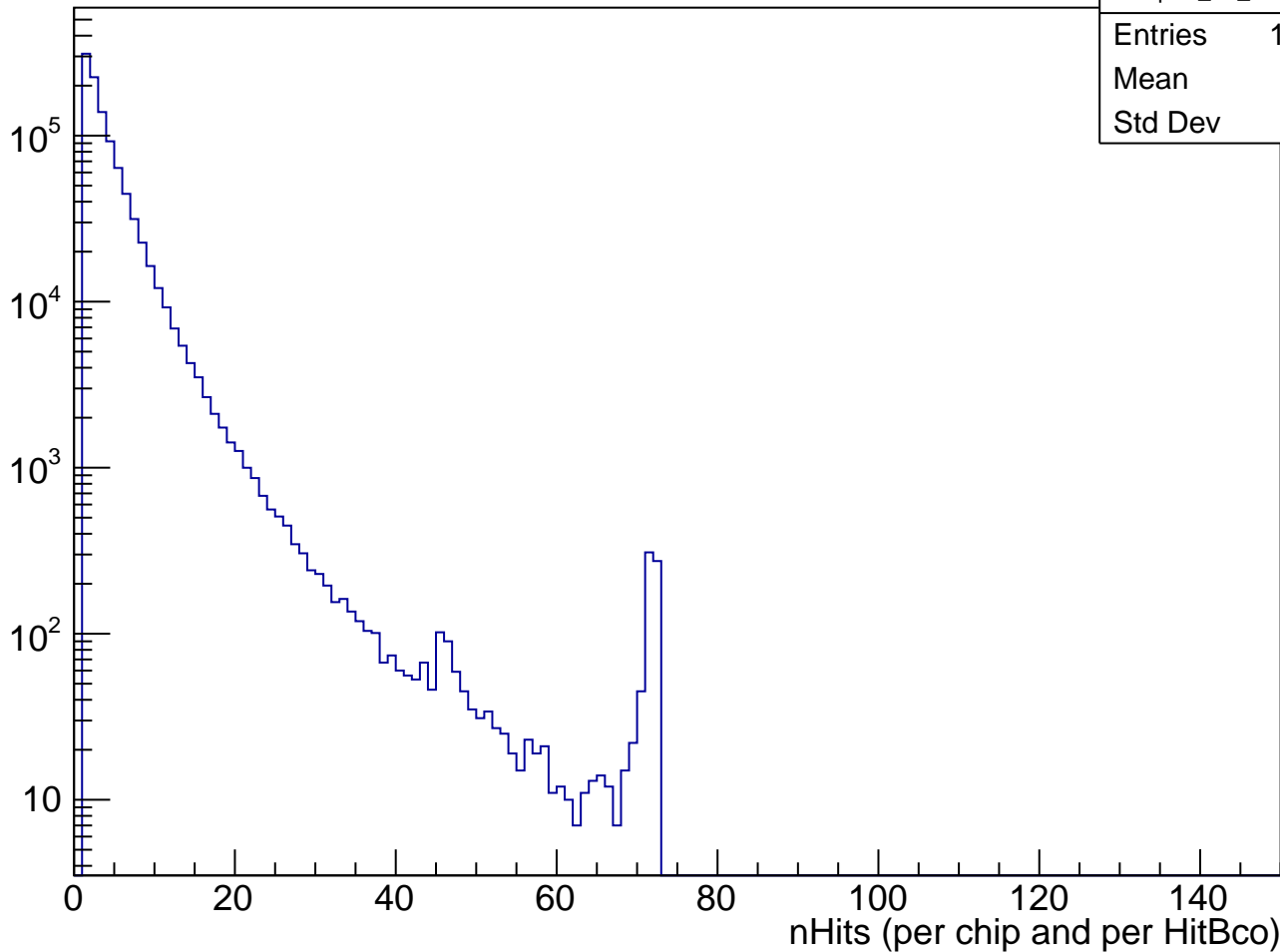
Entries



nChipHit_F6_Fch7_Chip18	
Entries	918084
Mean	3.509
Std Dev	4.235

nChipHit_F6_Fch7_Chip19

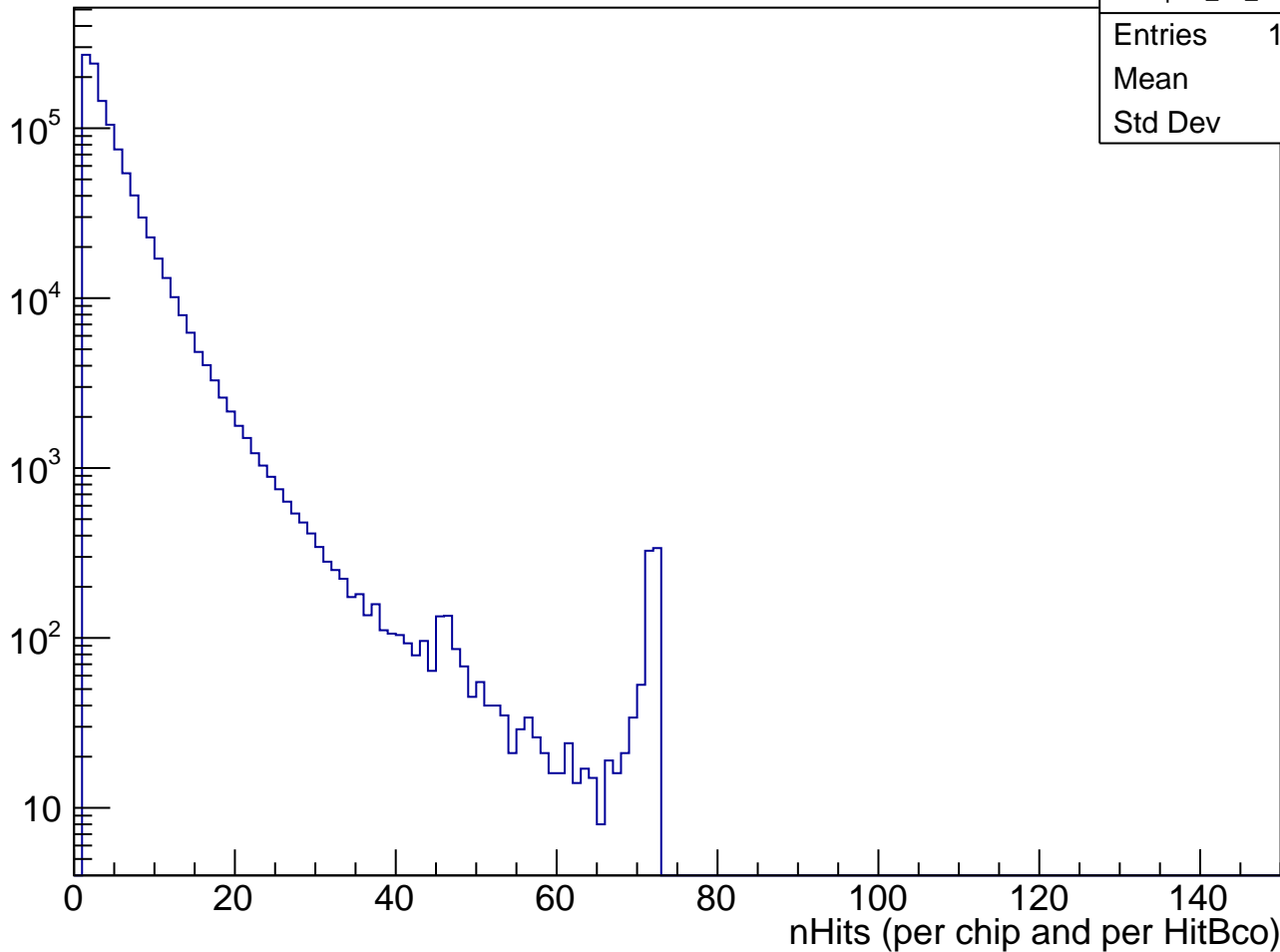
Entries



nChipHit_F6_Fch7_Chip19	
Entries	1004813
Mean	3.571
Std Dev	4.153

nChipHit_F6_Fch7_Chip2

Entries



nChipHit_F6_Fch7_Chip2

Entries 1067339

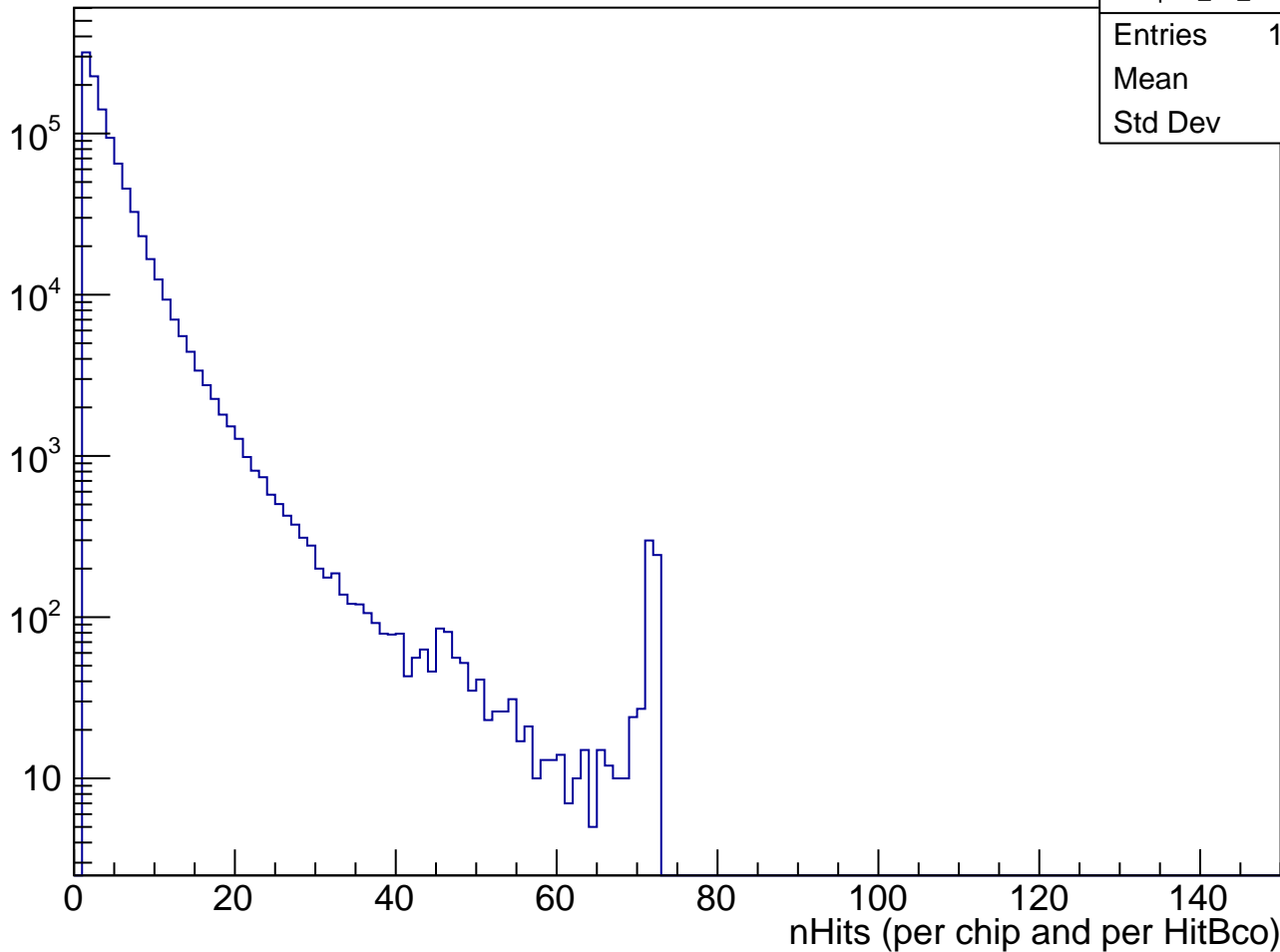
Mean 4.065

Std Dev 4.592

nHits (per chip and per HitBco)

nChipHit_F6_Fch7_Chip20

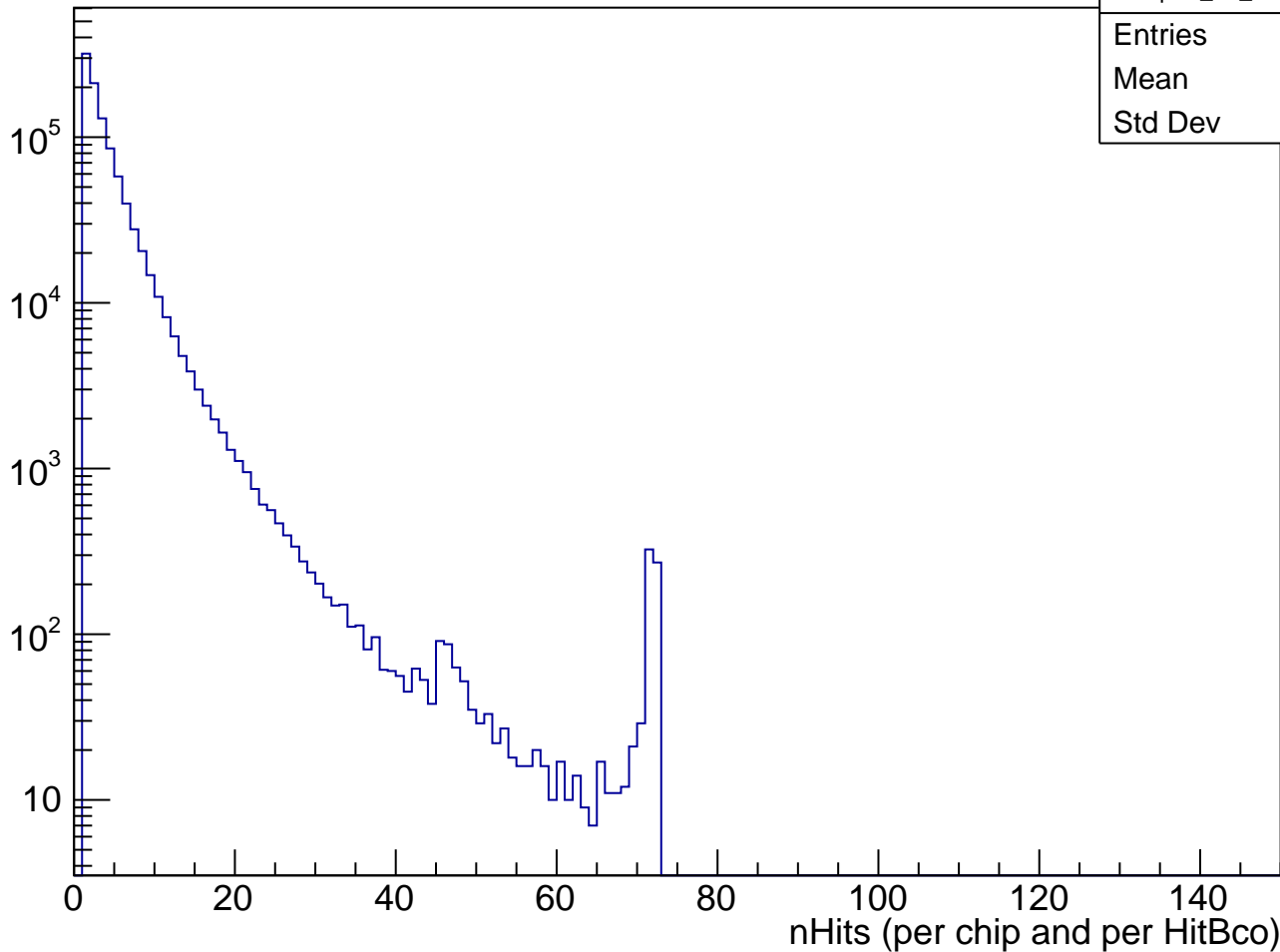
Entries



nChipHit_F6_Fch7_Chip20	
Entries	1022294
Mean	3.567
Std Dev	4.105

nChipHit_F6_Fch7_Chip21

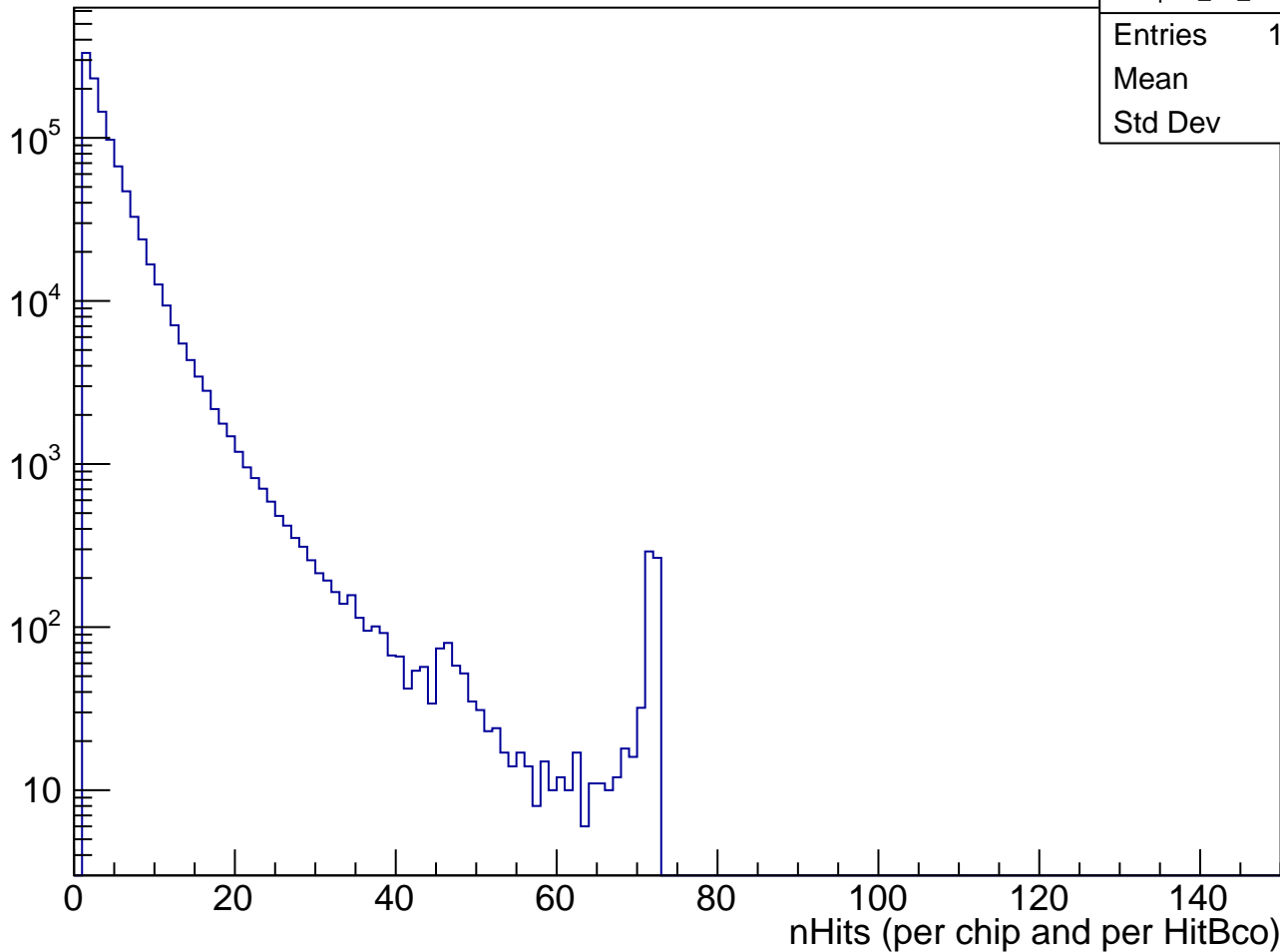
Entries



nChipHit_F6_Fch7_Chip21	
Entries	959987
Mean	3.455
Std Dev	4.122

nChipHit_F6_Fch7_Chip22

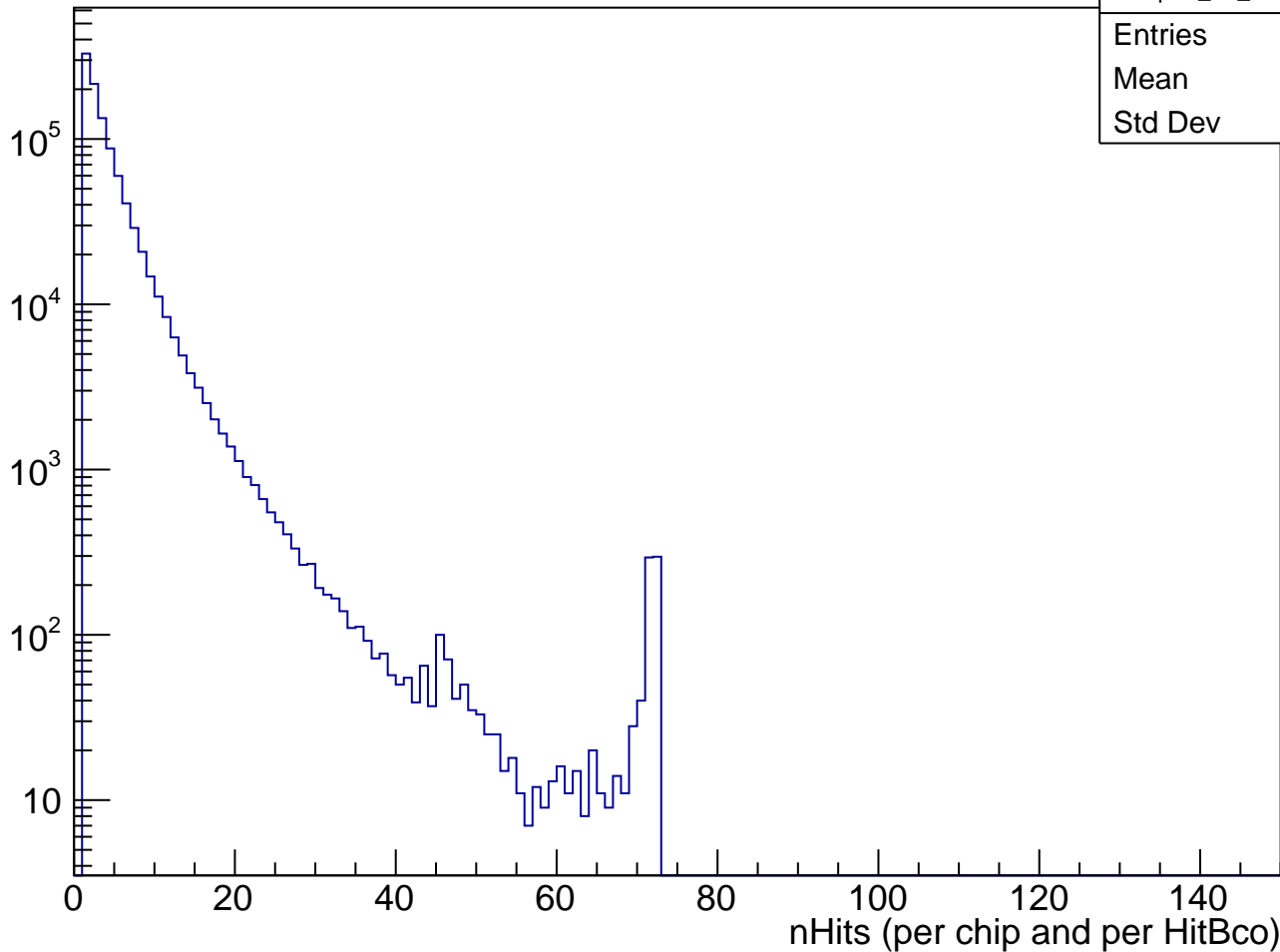
Entries



nChipHit_F6_Fch7_Chip22	
Entries	1052299
Mean	3.531
Std Dev	4.044

nChipHit_F6_Fch7_Chip23

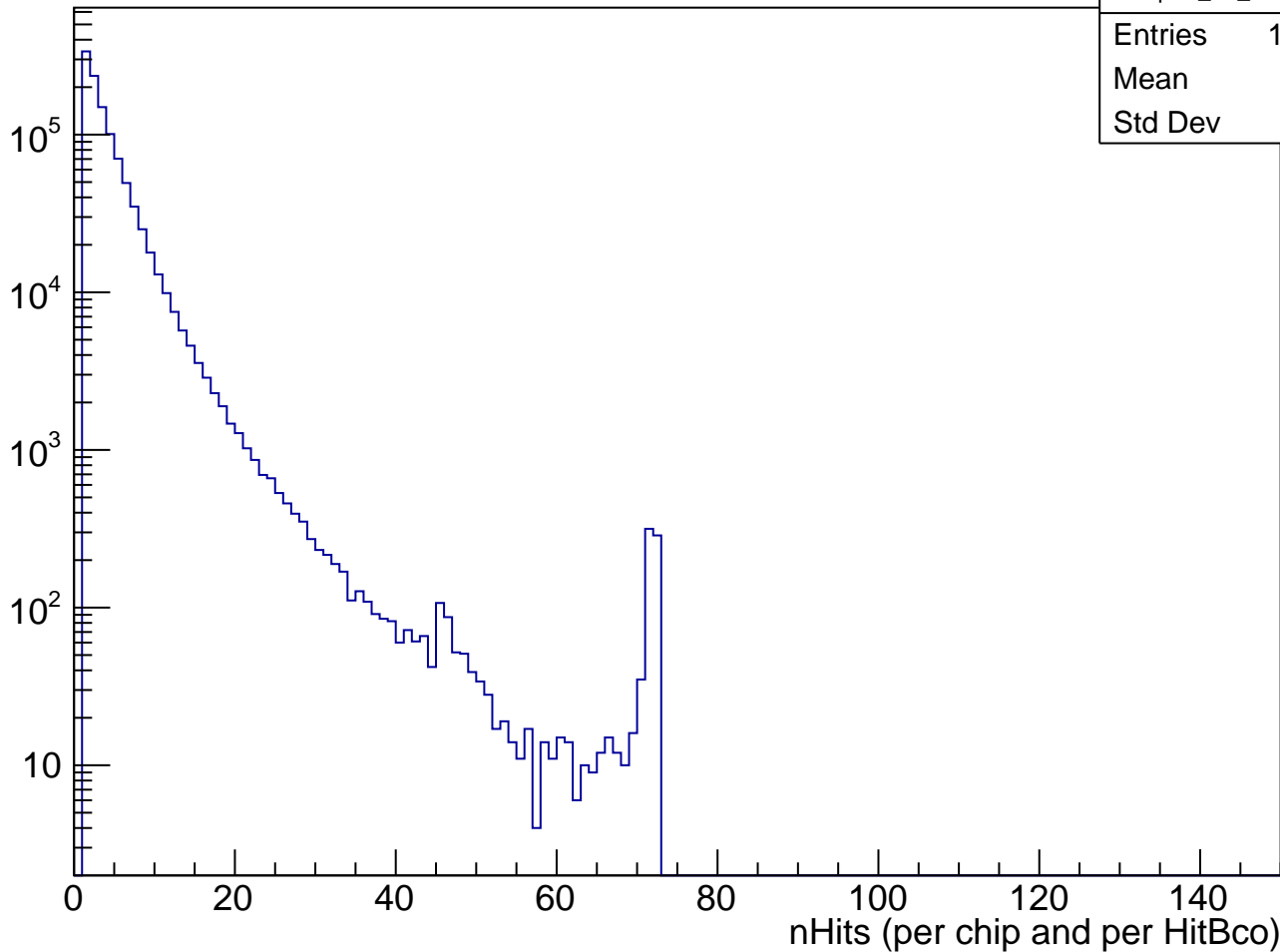
Entries



nChipHit_F6_Fch7_Chip23	
Entries	985037
Mean	3.445
Std Dev	4.088

nChipHit_F6_Fch7_Chip24

Entries



nChipHit_F6_Fch7_Chip24

Entries 1083276

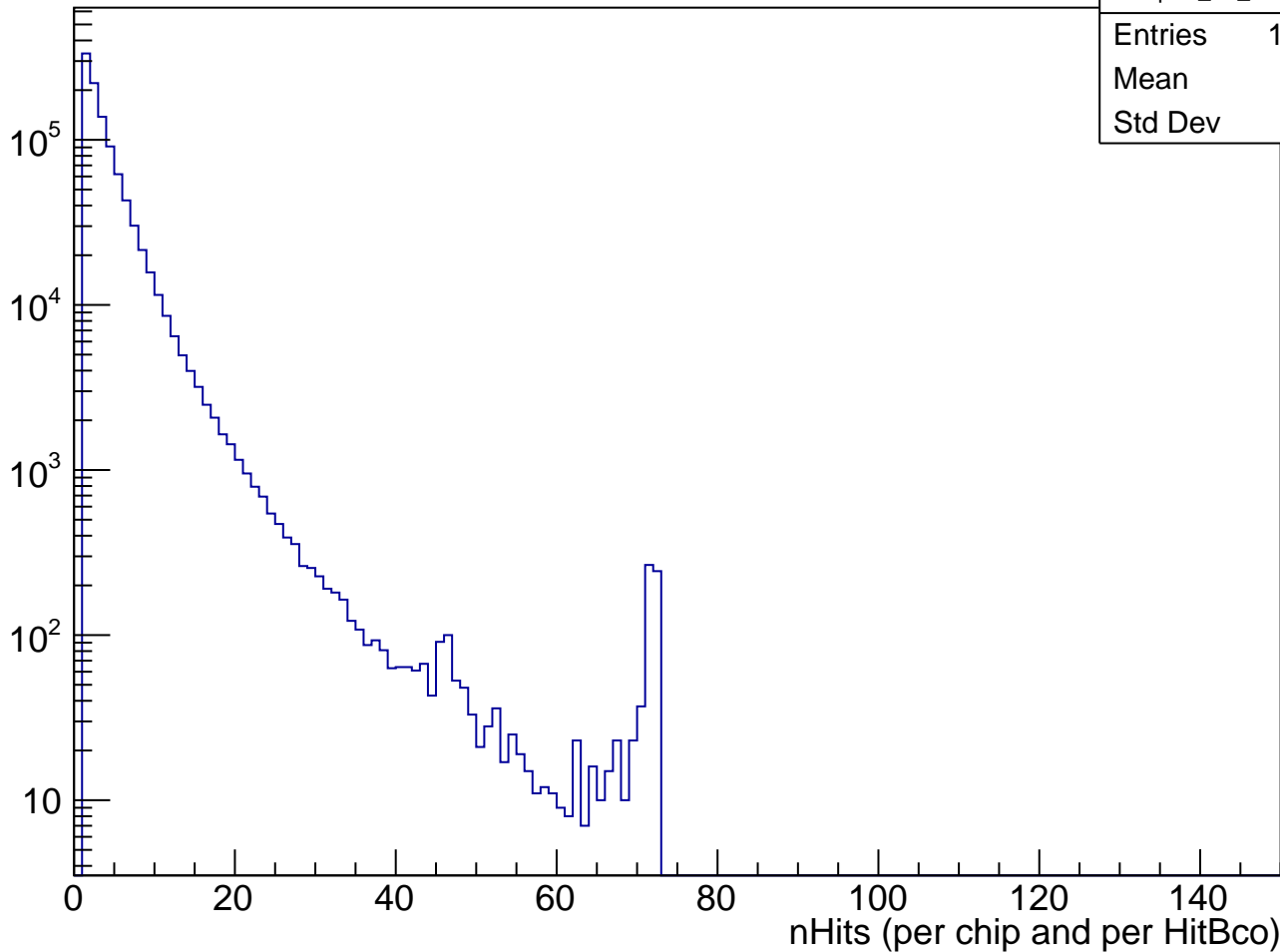
Mean 3.574

Std Dev 4.091

nHits (per chip and per HitBco)

nChipHit_F6_Fch7_Chip25

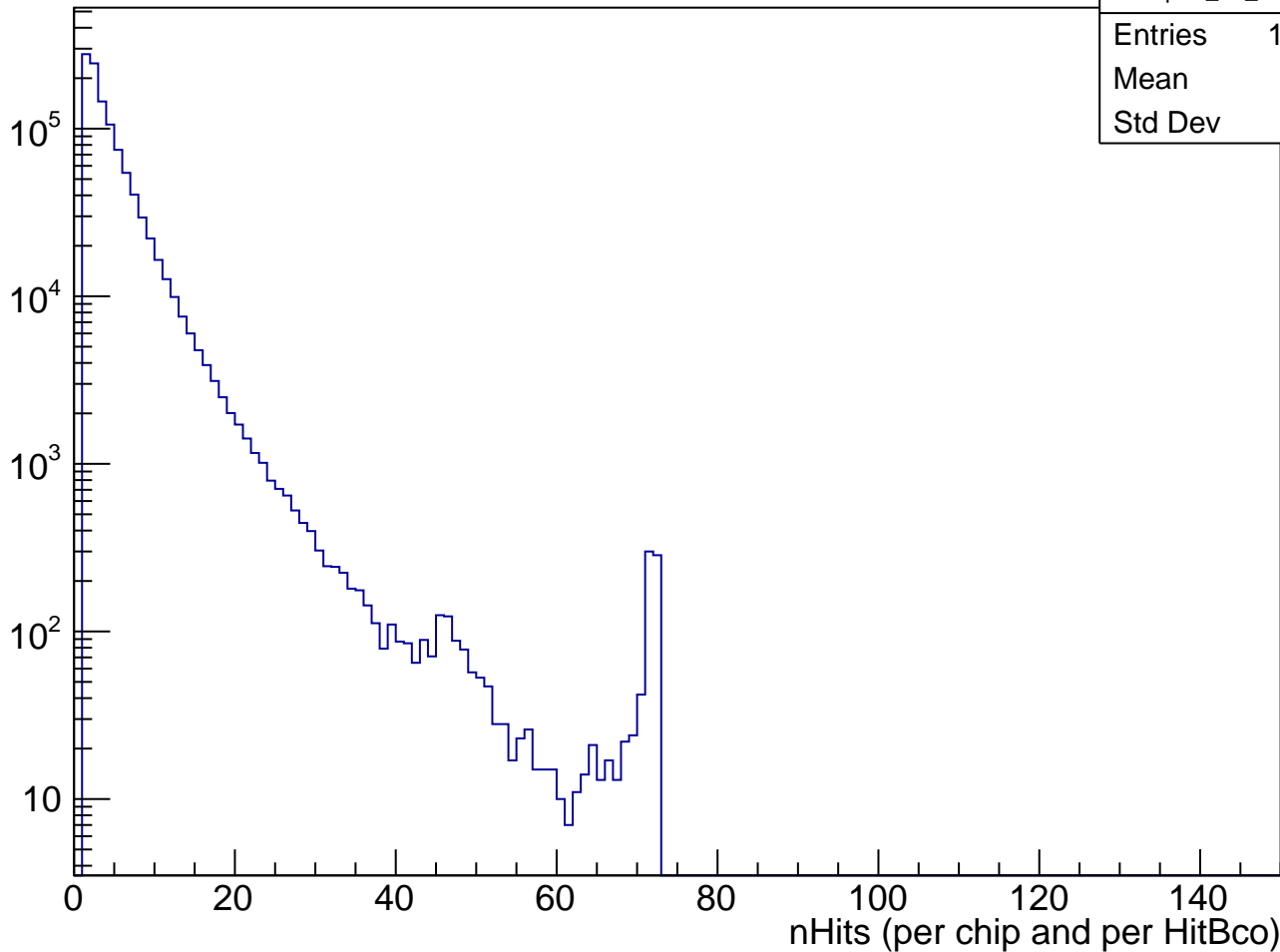
Entries



nChipHit_F6_Fch7_Chip25	
Entries	1010625
Mean	3.462
Std Dev	4.055

nChipHit_F6_Fch7_Chip3

Entries



nChipHit_F6_Fch7_Chip3

Entries 1077362

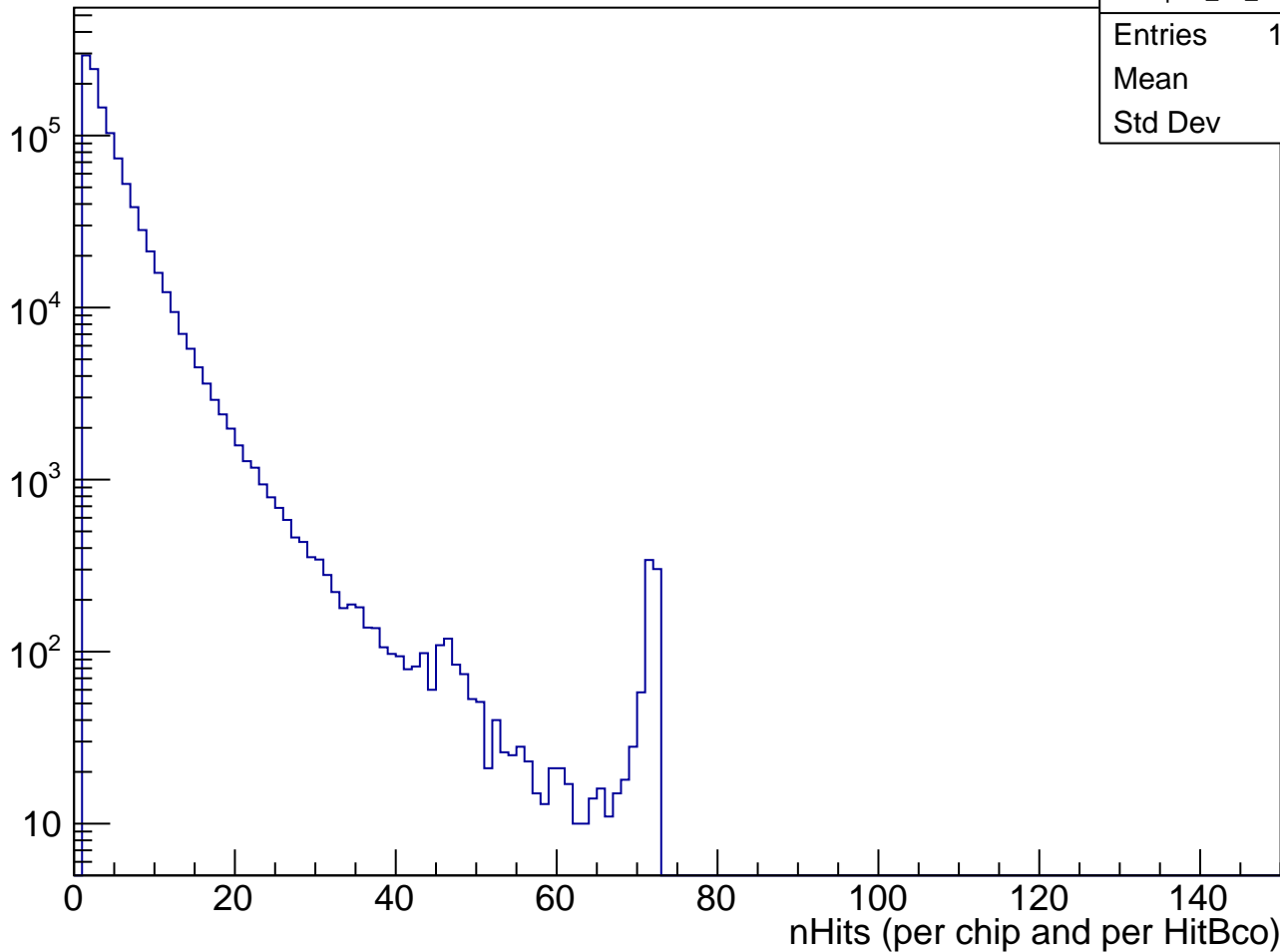
Mean 3.986

Std Dev 4.455

nHits (per chip and per HitBco)

nChipHit_F6_Fch7_Chip4

Entries



nChipHit_F6_Fch7_Chip4

Entries 1076465

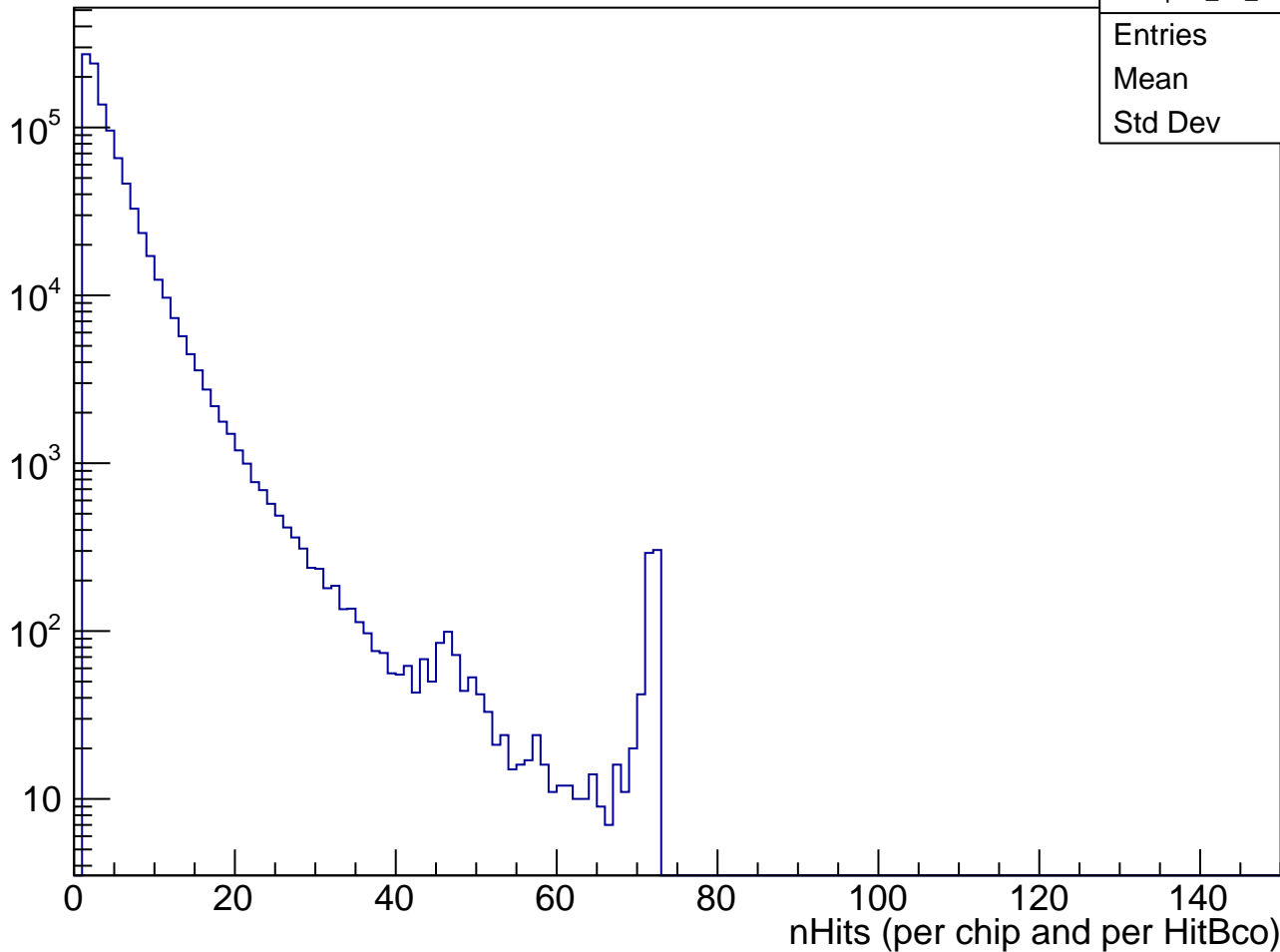
Mean 3.9

Std Dev 4.449

nHits (per chip and per HitBco)

nChipHit_F6_Fch7_Chip5

Entries



nChipHit_F6_Fch7_Chip5

Entries 992074

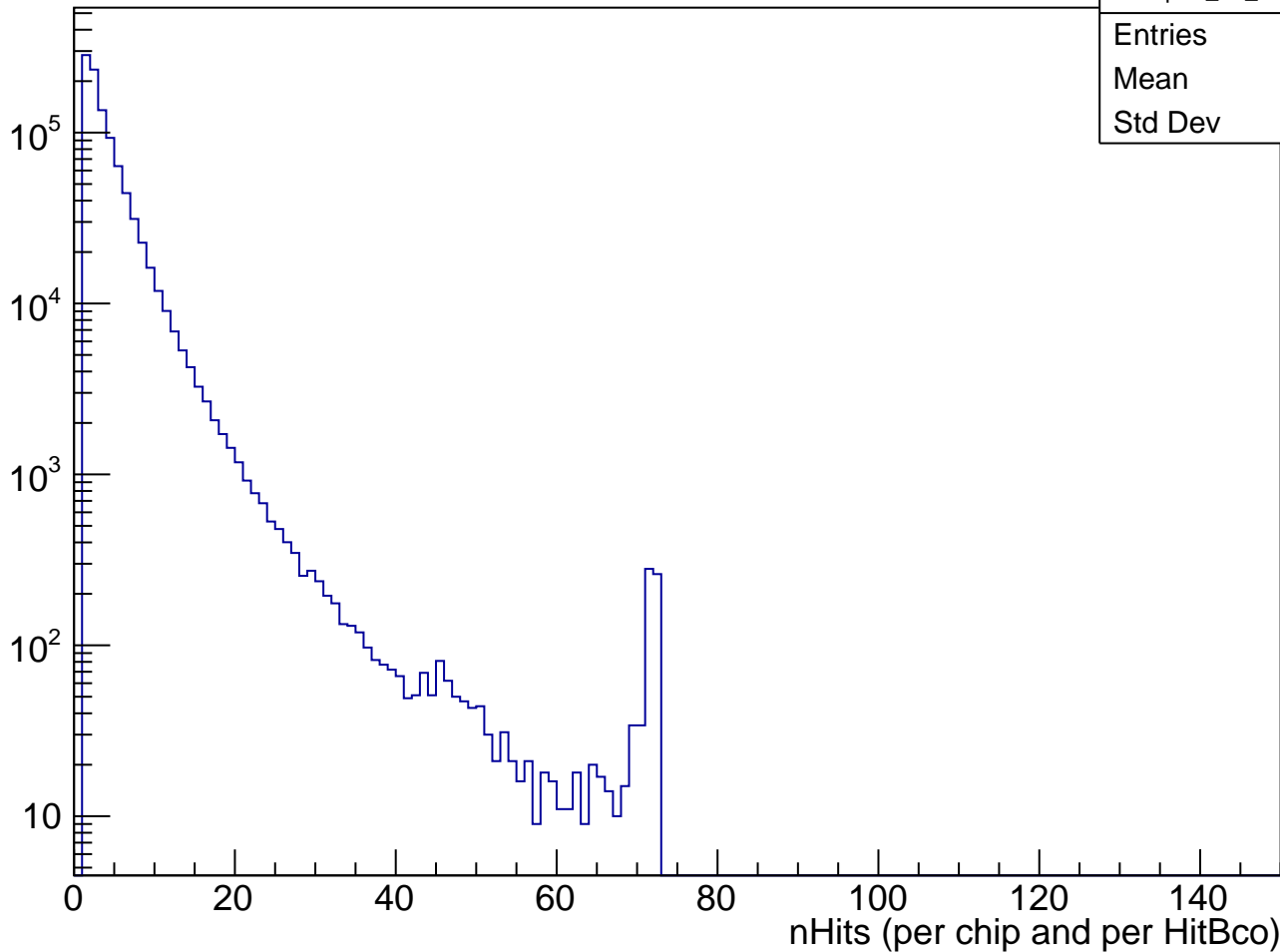
Mean 3.681

Std Dev 4.174

nHits (per chip and per HitBco)

nChipHit_F6_Fch7_Chip6

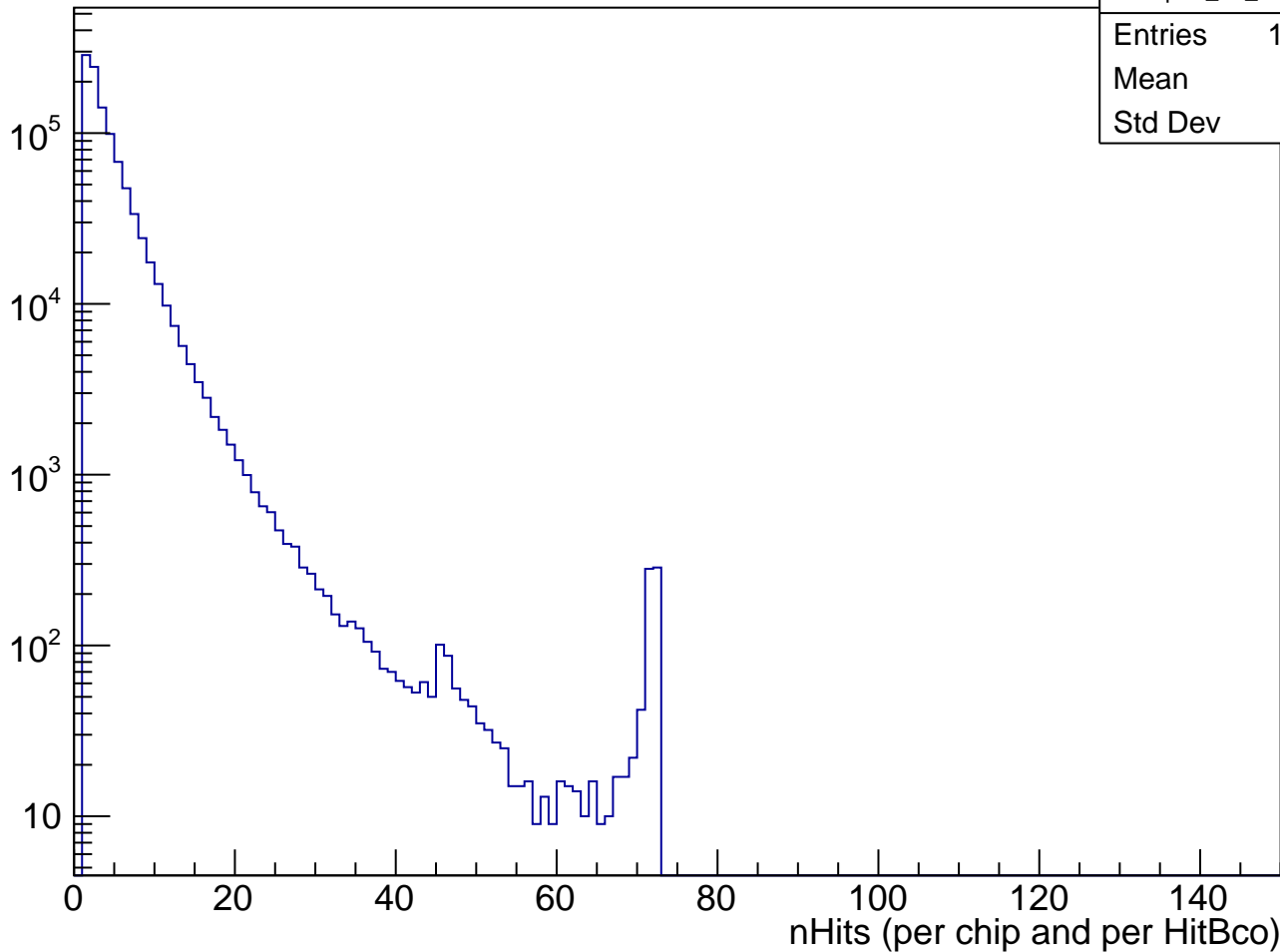
Entries



nChipHit_F6_Fch7_Chip6	
Entries	981483
Mean	3.608
Std Dev	4.131

nChipHit_F6_Fch7_Chip7

Entries

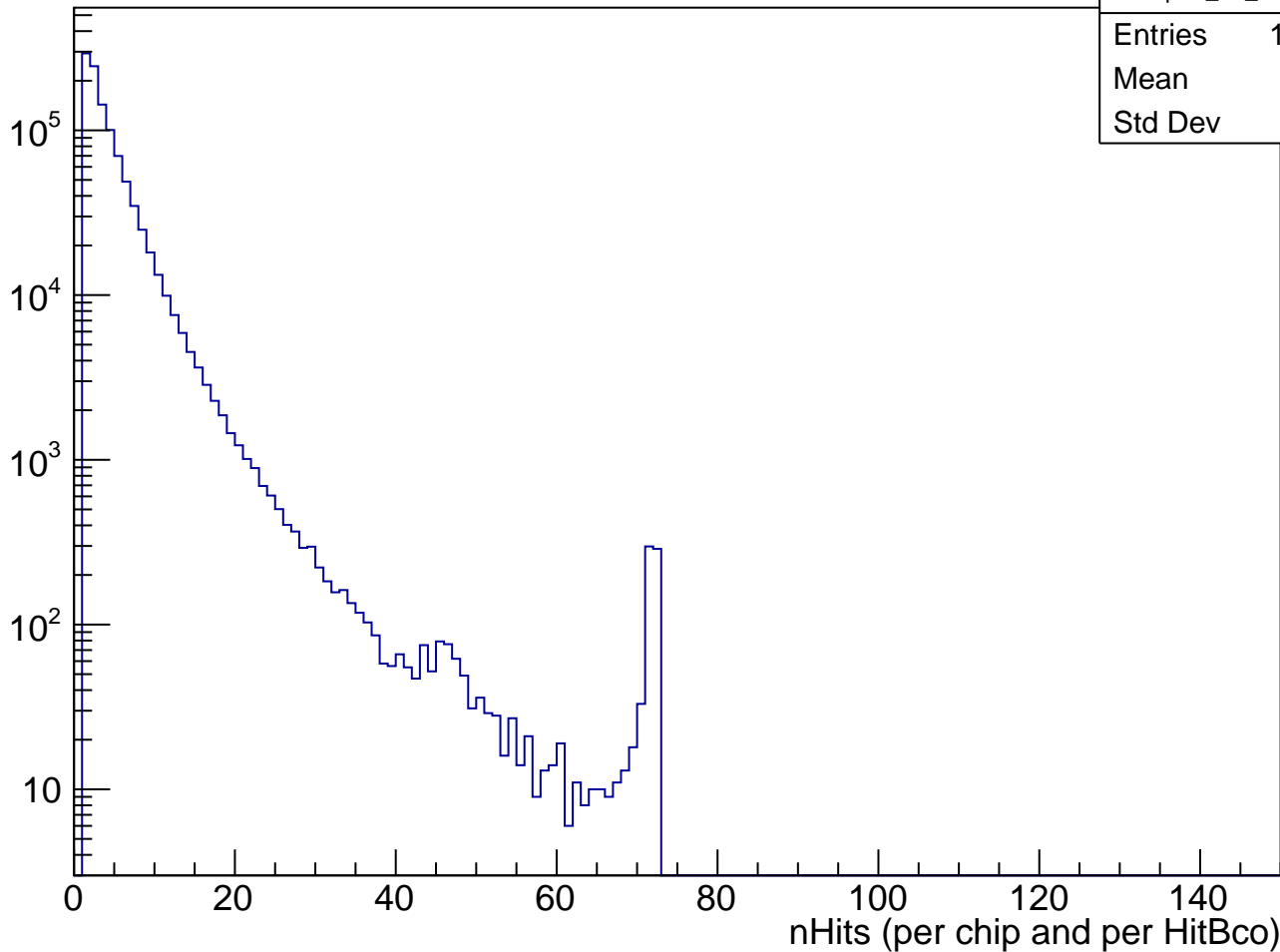


nChipHit_F6_Fch7_Chip7	
Entries	1022642
Mean	3.656
Std Dev	4.122

nHits (per chip and per HitBco)

nChipHit_F6_Fch7_Chip8

Entries



nChipHit_F6_Fch7_Chip8

Entries 1040810

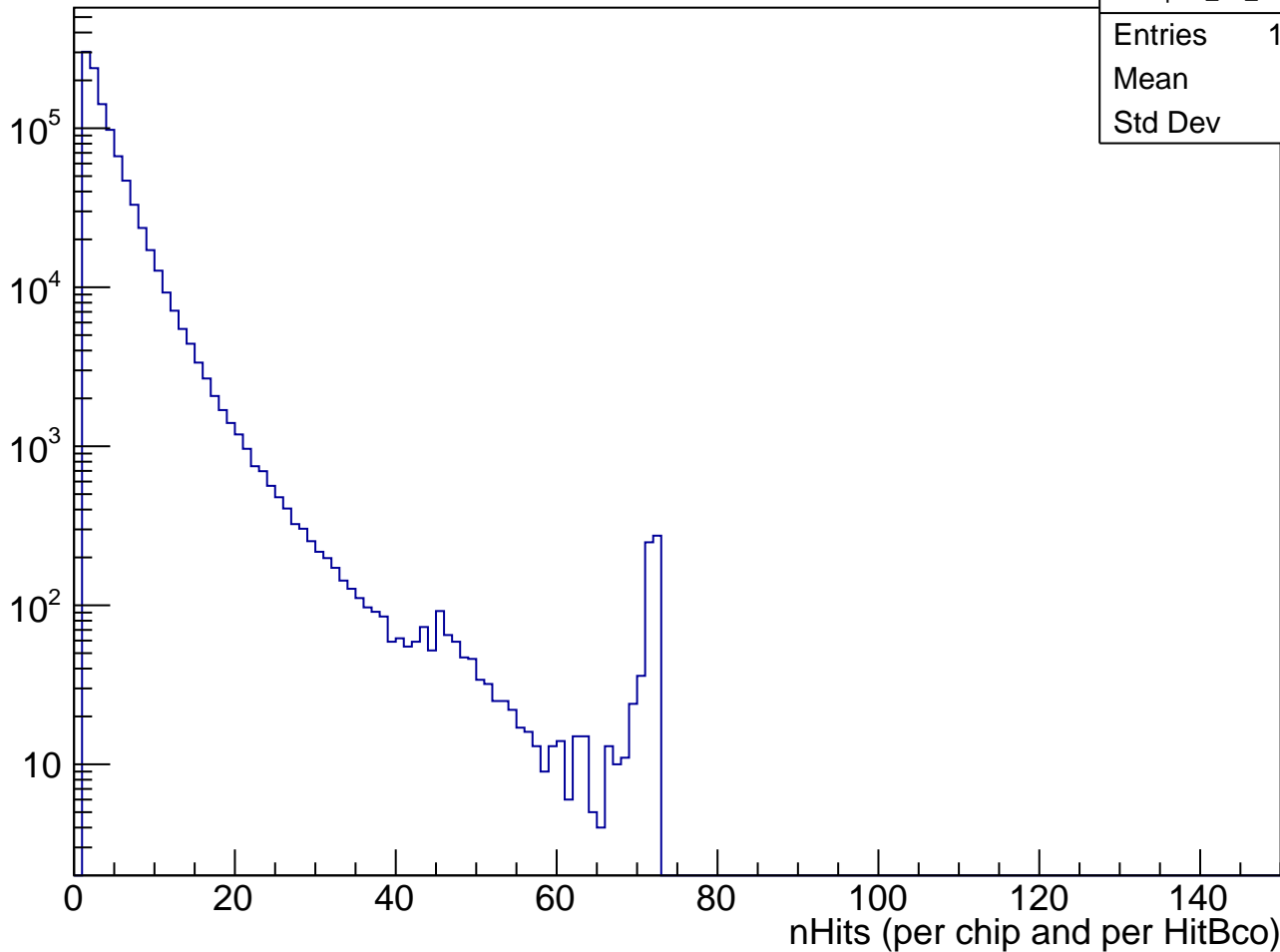
Mean 3.663

Std Dev 4.107

nHits (per chip and per HitBco)

nChipHit_F6_Fch7_Chip9

Entries



nChipHit_F6_Fch7_Chip9

Entries 1026854

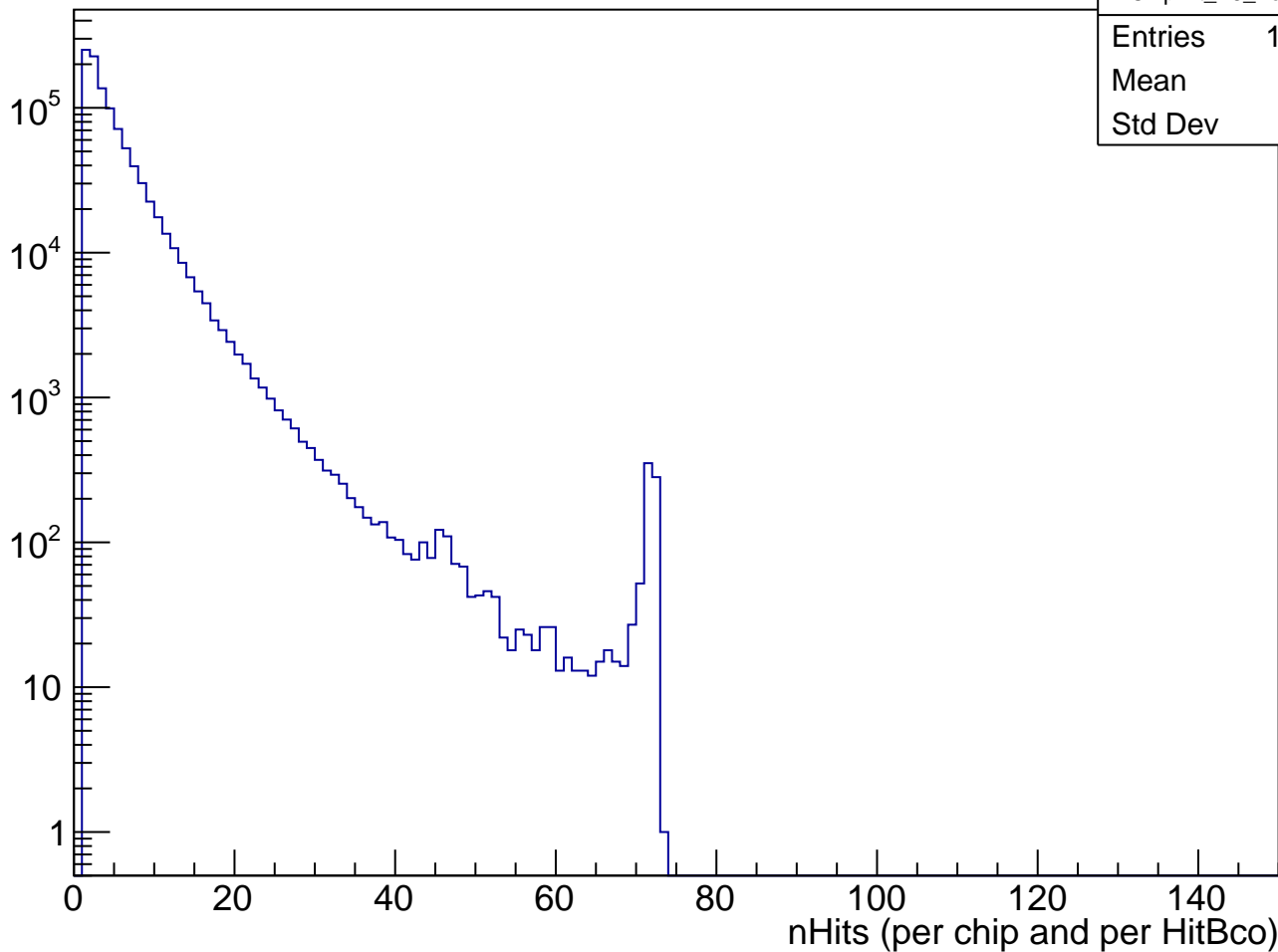
Mean 3.587

Std Dev 4.053

nHits (per chip and per HitBco)

nChipHit_F6_Fch8_Chip0

Entries



nChipHit_F6_Fch8_Chip0

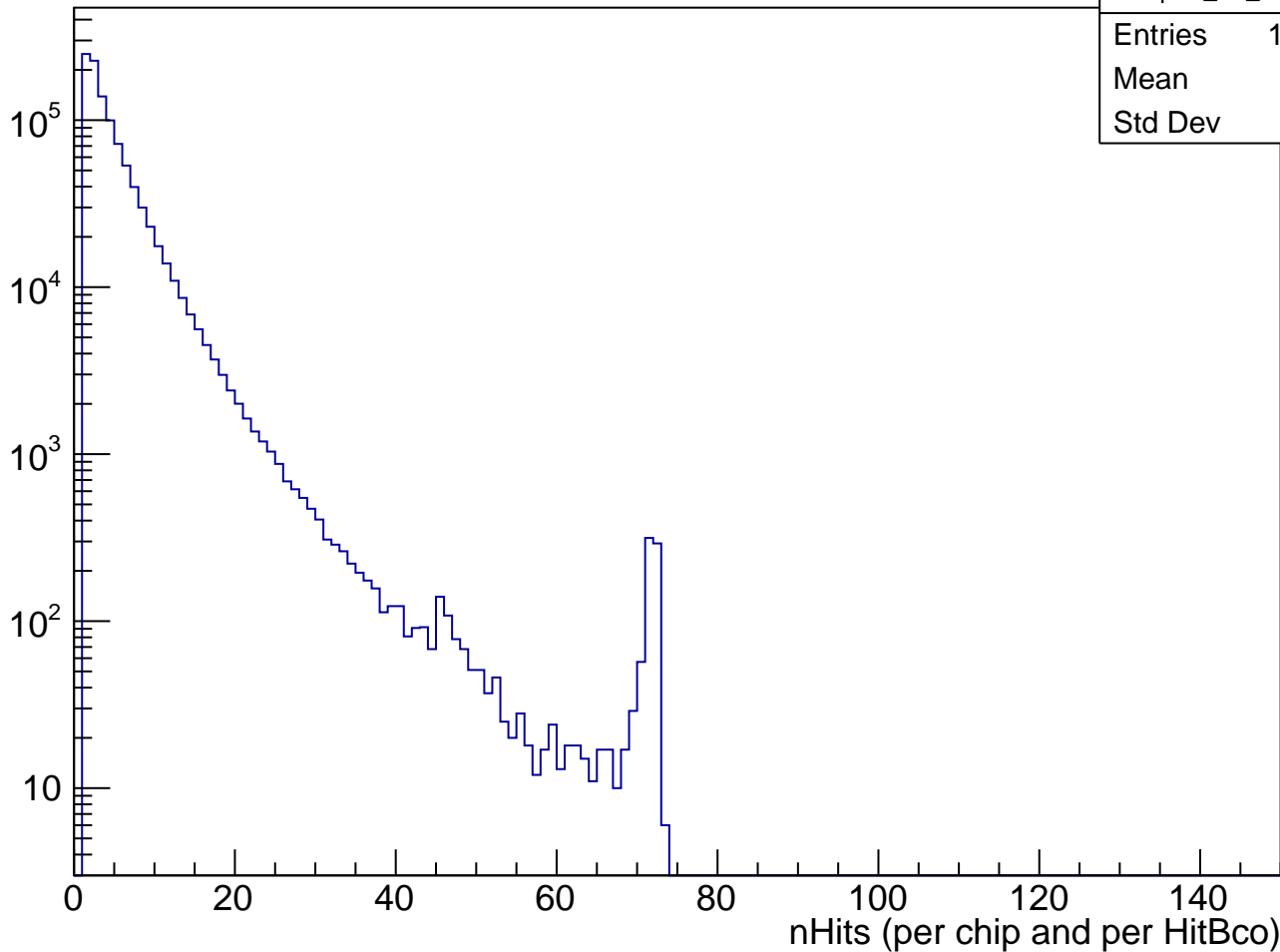
Entries 1020182

Mean 4.209

Std Dev 4.726

nChipHit_F6_Fch8_Chip1

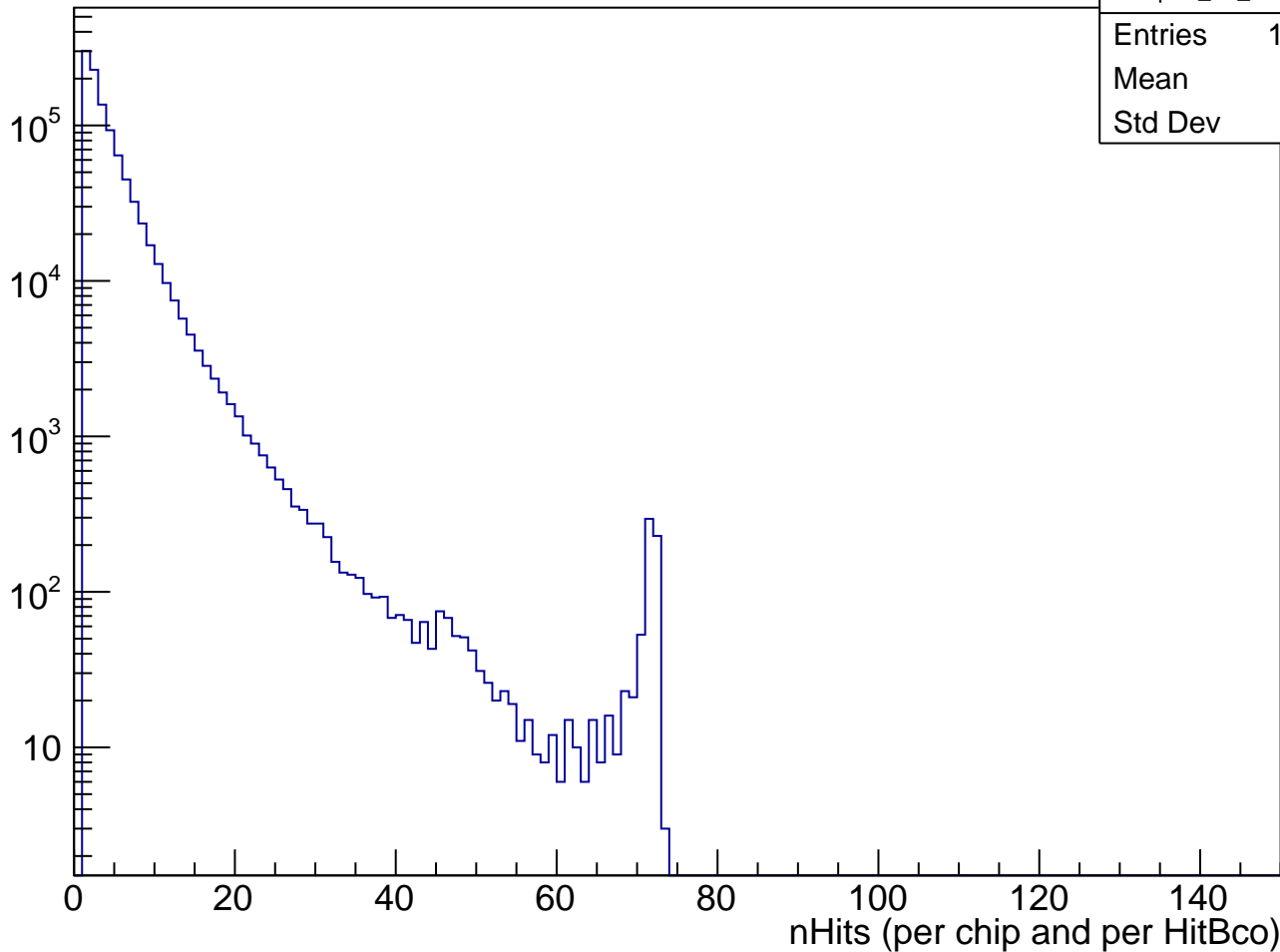
Entries



nChipHit_F6_Fch8_Chip1	
Entries	1024118
Mean	4.237
Std Dev	4.747

nChipHit_F6_Fch8_Chip10

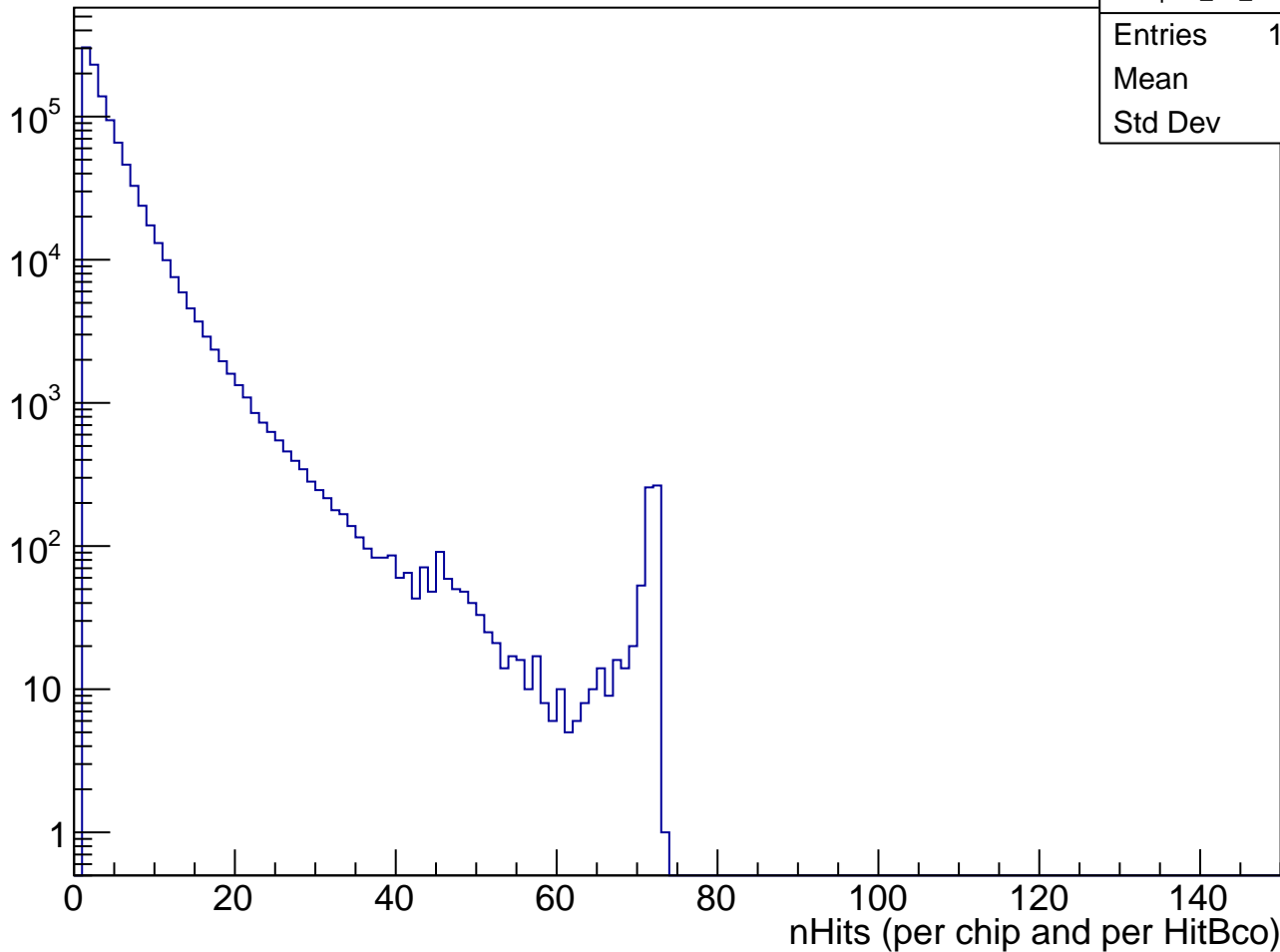
Entries



nChipHit_F6_Fch8_Chip10	
Entries	1003098
Mean	3.634
Std Dev	4.168

nChipHit_F6_Fch8_Chip11

Entries

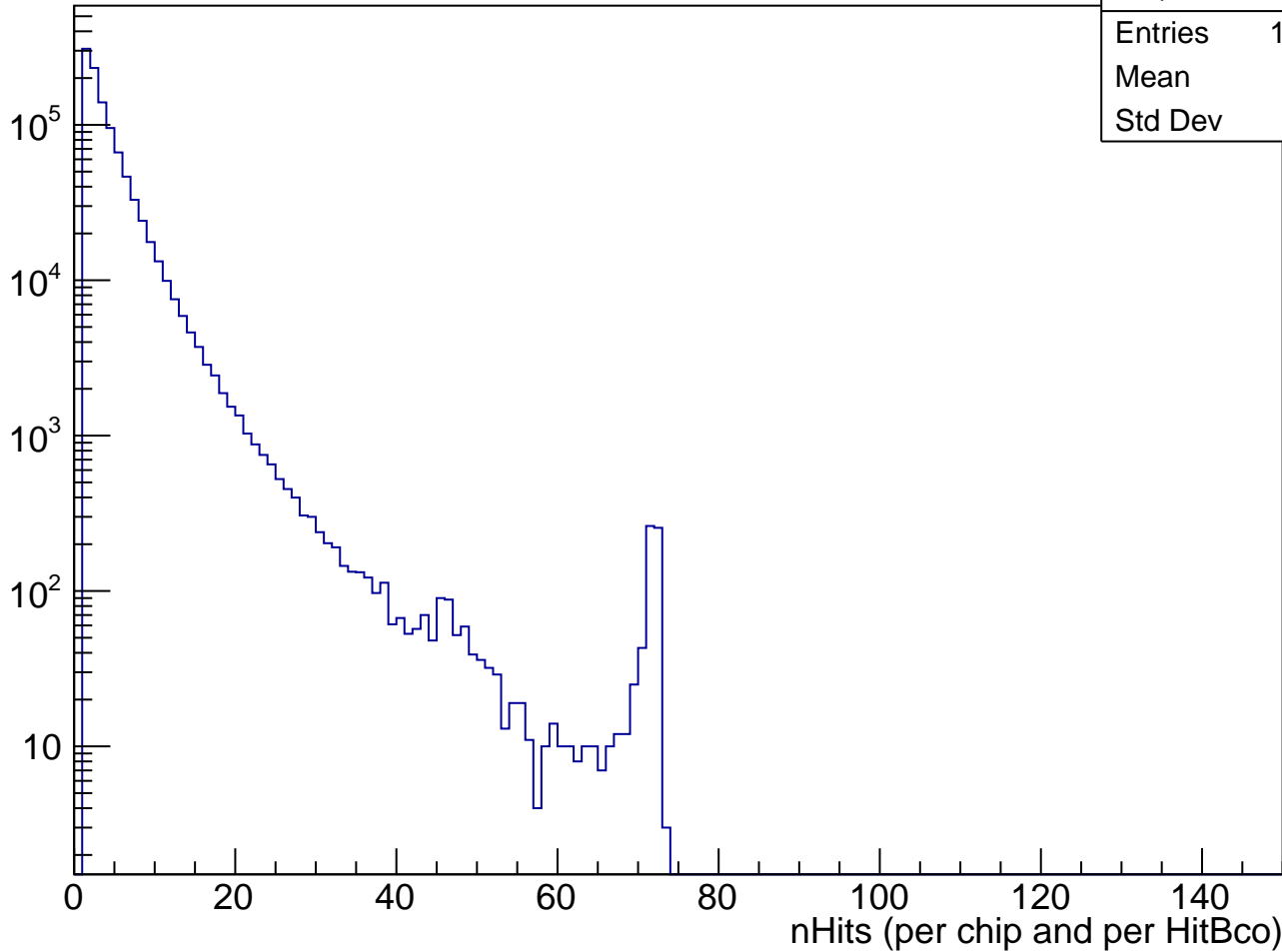


nChipHit_F6_Fch8_Chip11	
Entries	1016894
Mean	3.643
Std Dev	4.151

nHits (per chip and per HitBco)

nChipHit_F6_Fch8_Chip12

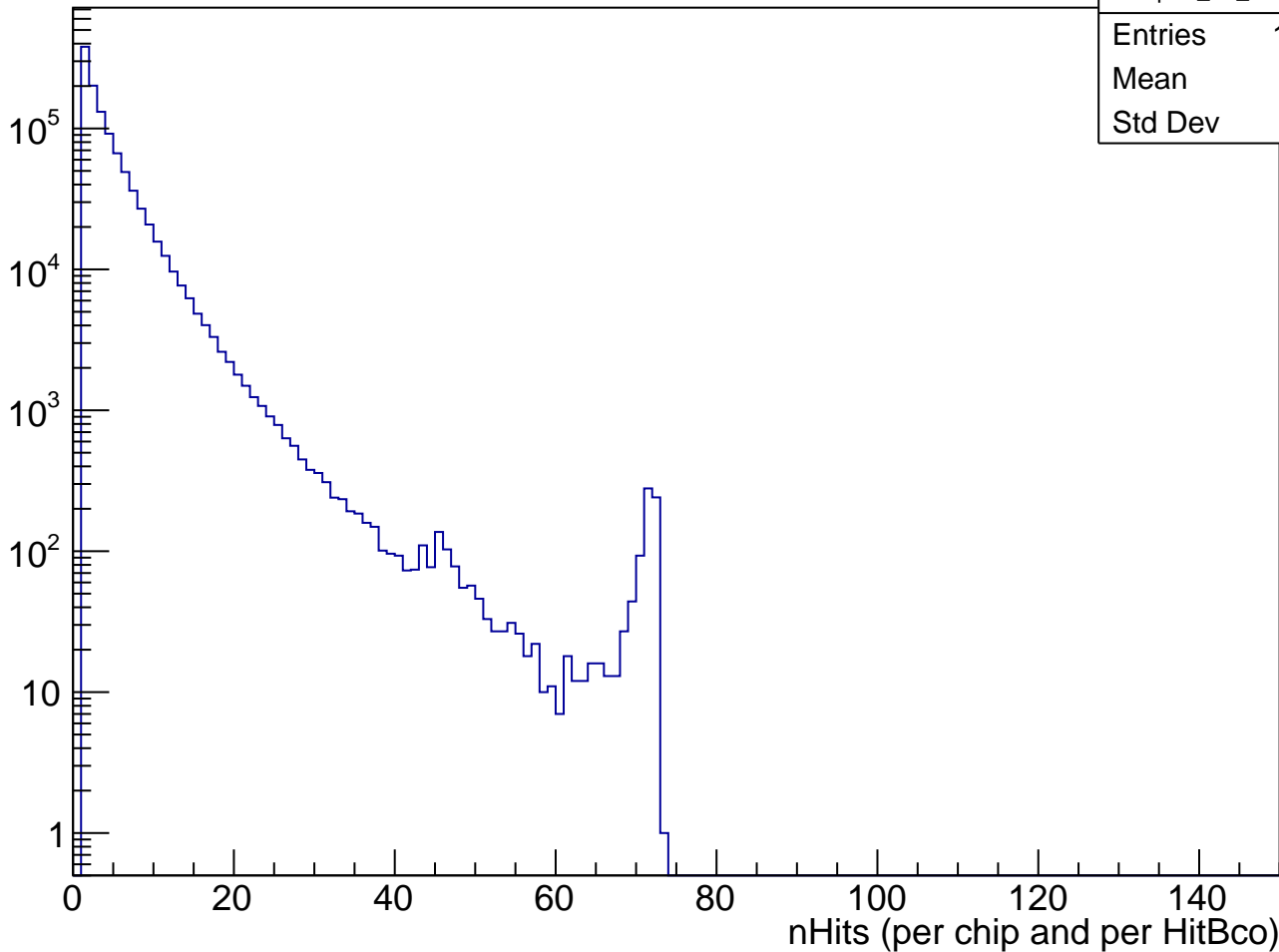
Entries



nChipHit_F6_Fch8_Chip12	
Entries	1026722
Mean	3.635
Std Dev	4.148

nChipHit_F6_Fch8_Chip13

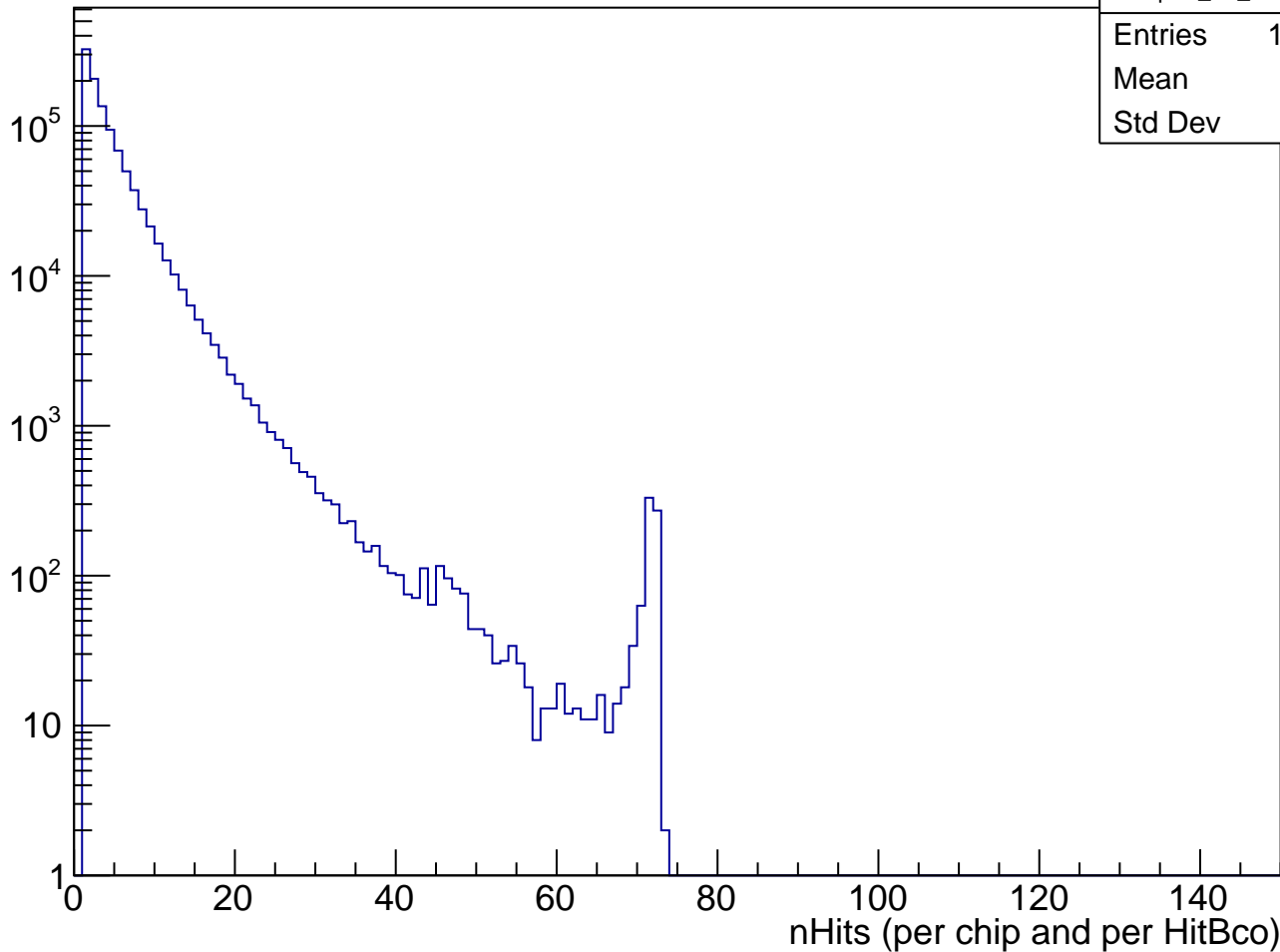
Entries



nChipHit_F6_Fch8_Chip13	
Entries	1086291
Mean	3.772
Std Dev	4.534

nChipHit_F6_Fch8_Chip14

Entries

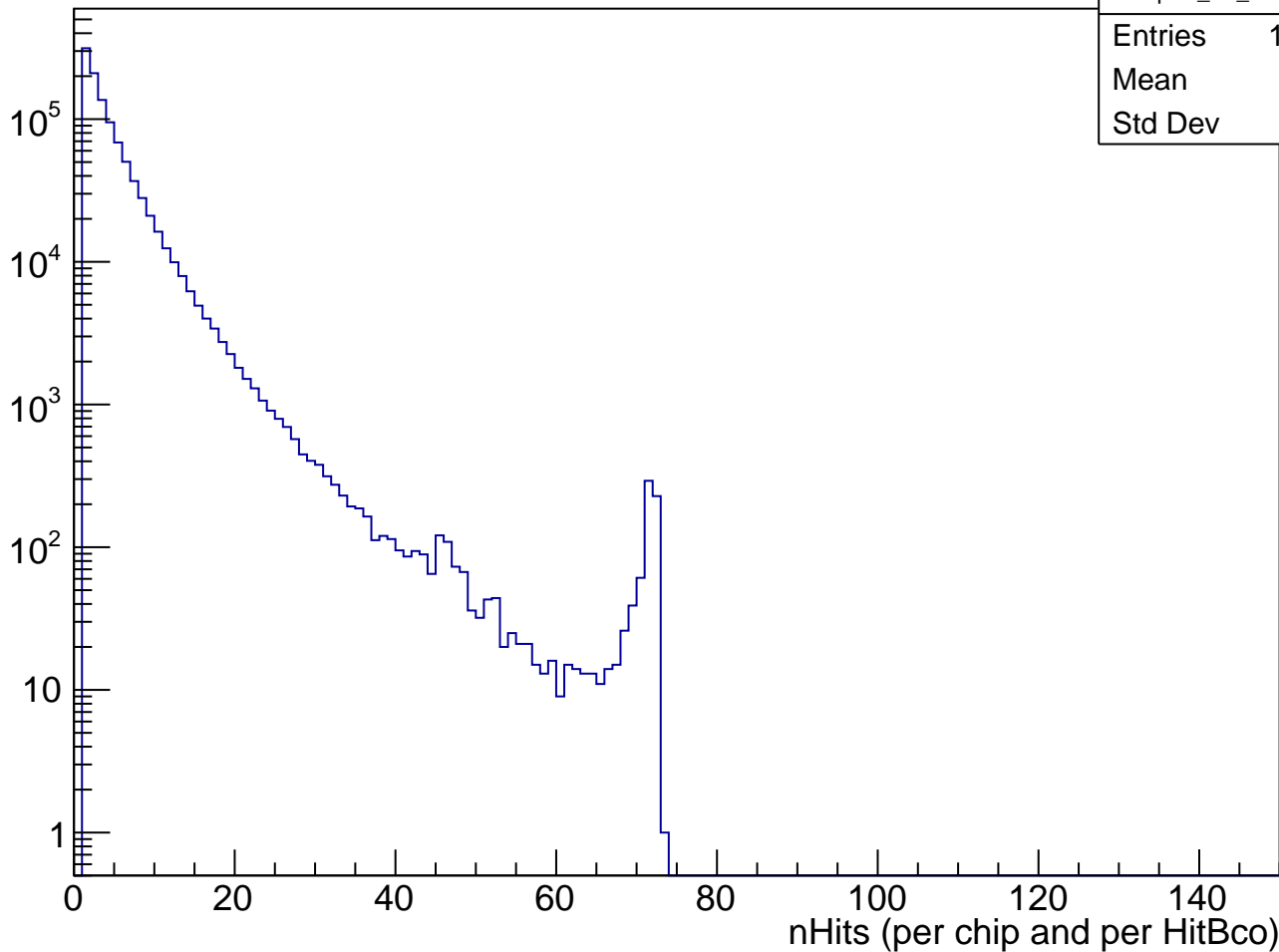


nChipHit_F6_Fch8_Chip14	
Entries	1051634
Mean	3.956
Std Dev	4.633

nHits (per chip and per HitBco)

nChipHit_F6_Fch8_Chip15

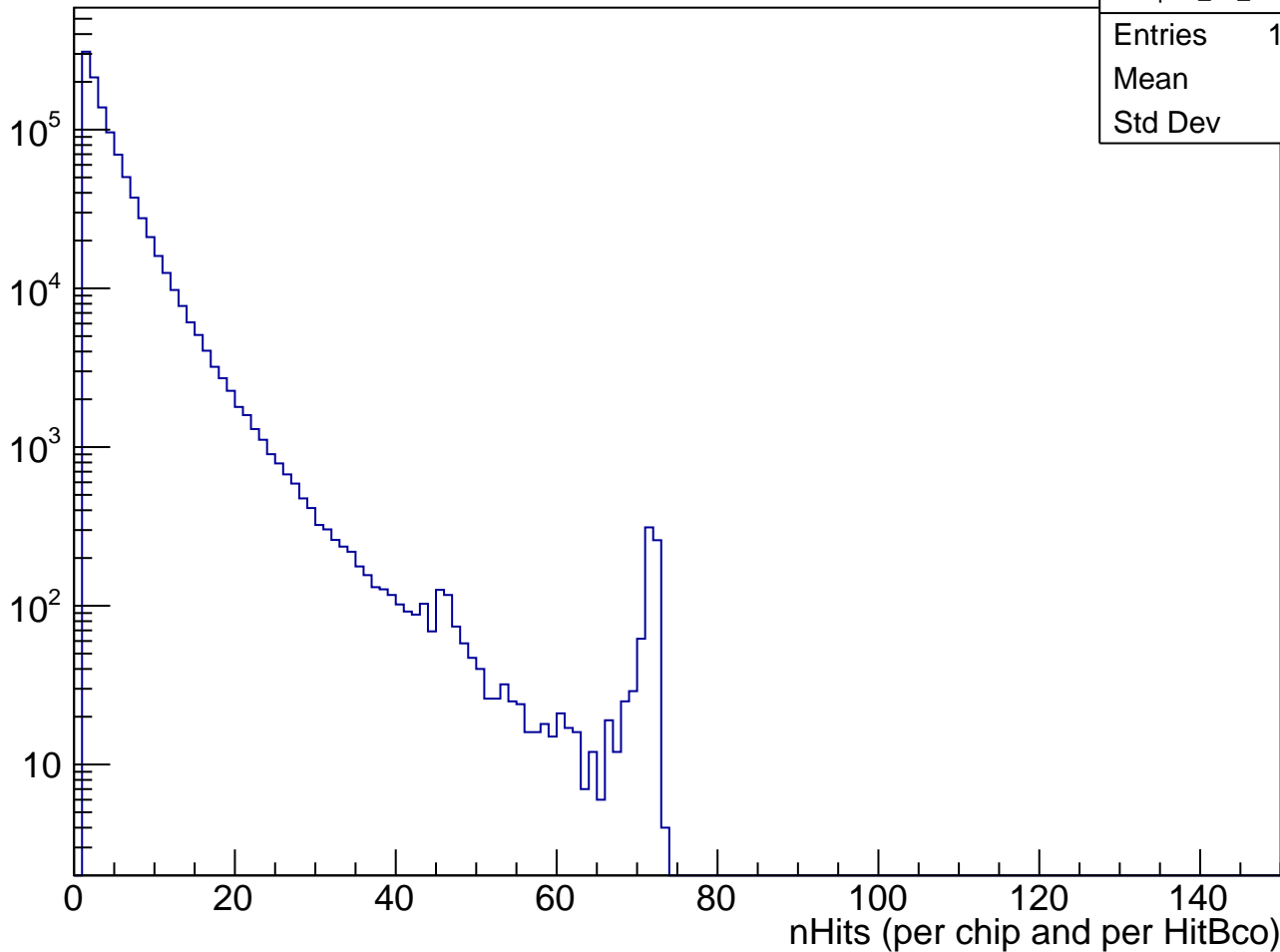
Entries



nChipHit_F6_Fch8_Chip15	
Entries	1043290
Mean	3.956
Std Dev	4.58

nChipHit_F6_Fch8_Chip16

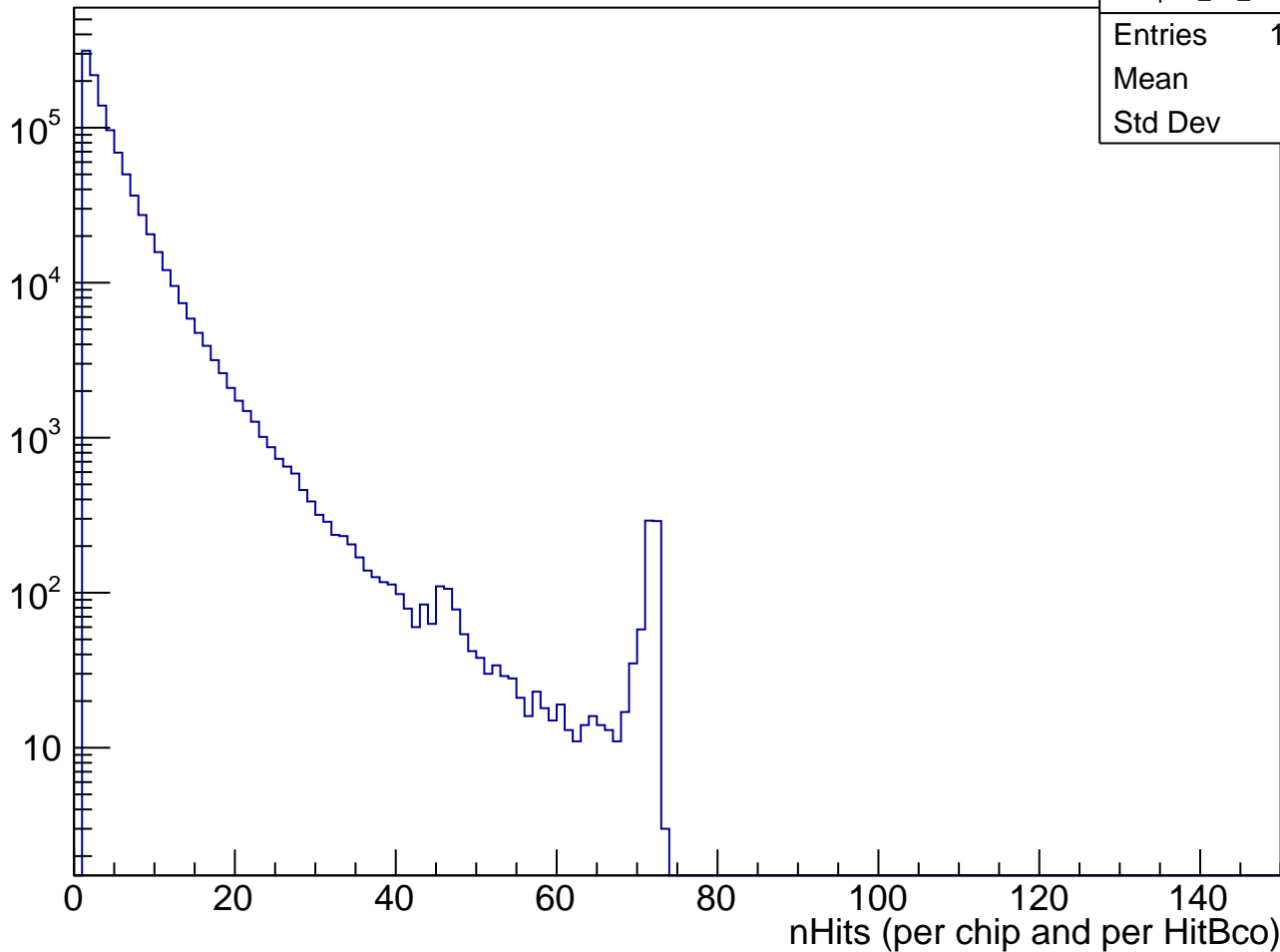
Entries



nChipHit_F6_Fch8_Chip16	
Entries	1046422
Mean	3.961
Std Dev	4.596

nChipHit_F6_Fch8_Chip17

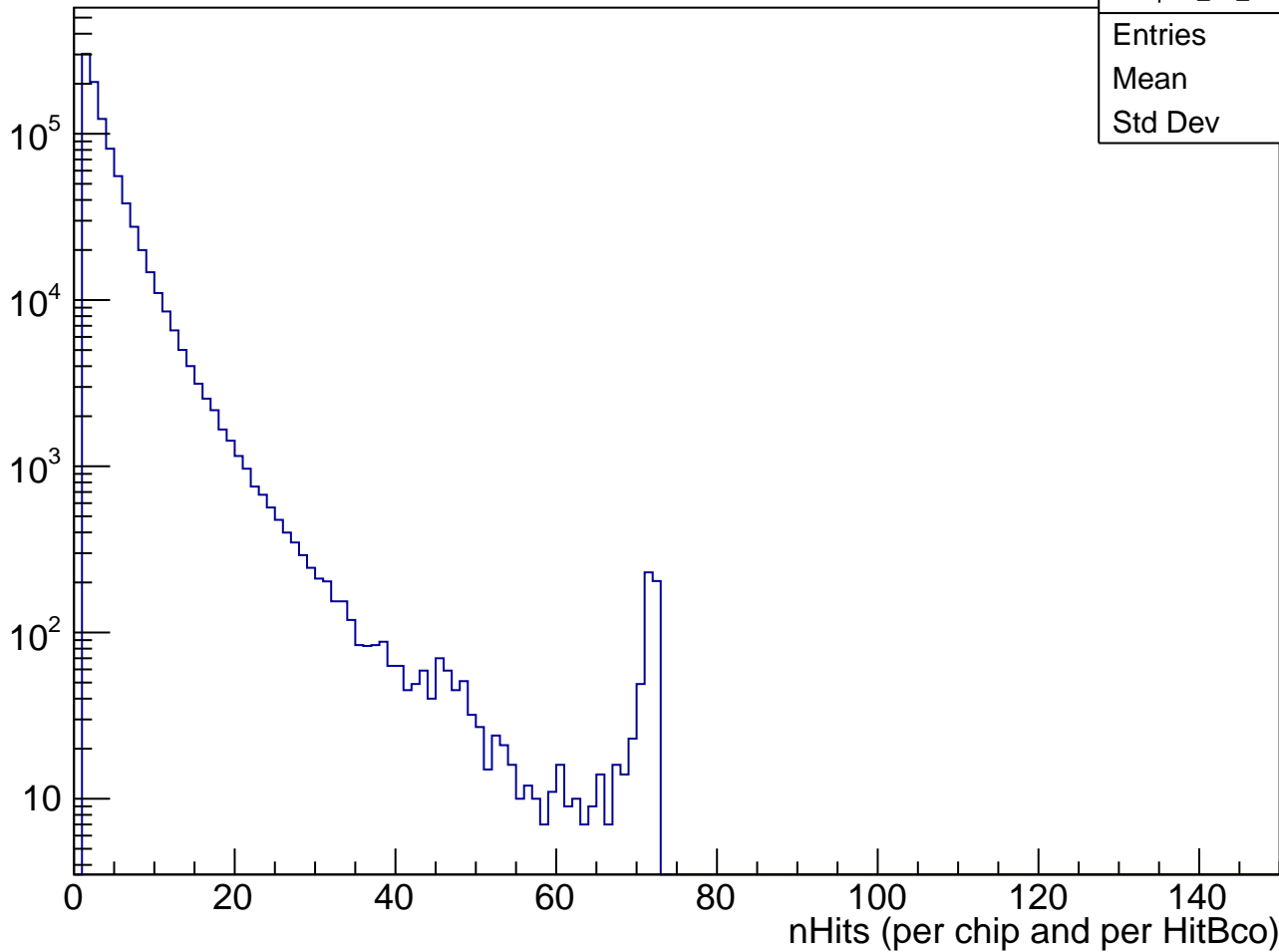
Entries



nChipHit_F6_Fch8_Chip17	
Entries	1050993
Mean	3.897
Std Dev	4.528

nChipHit_F6_Fch8_Chip18

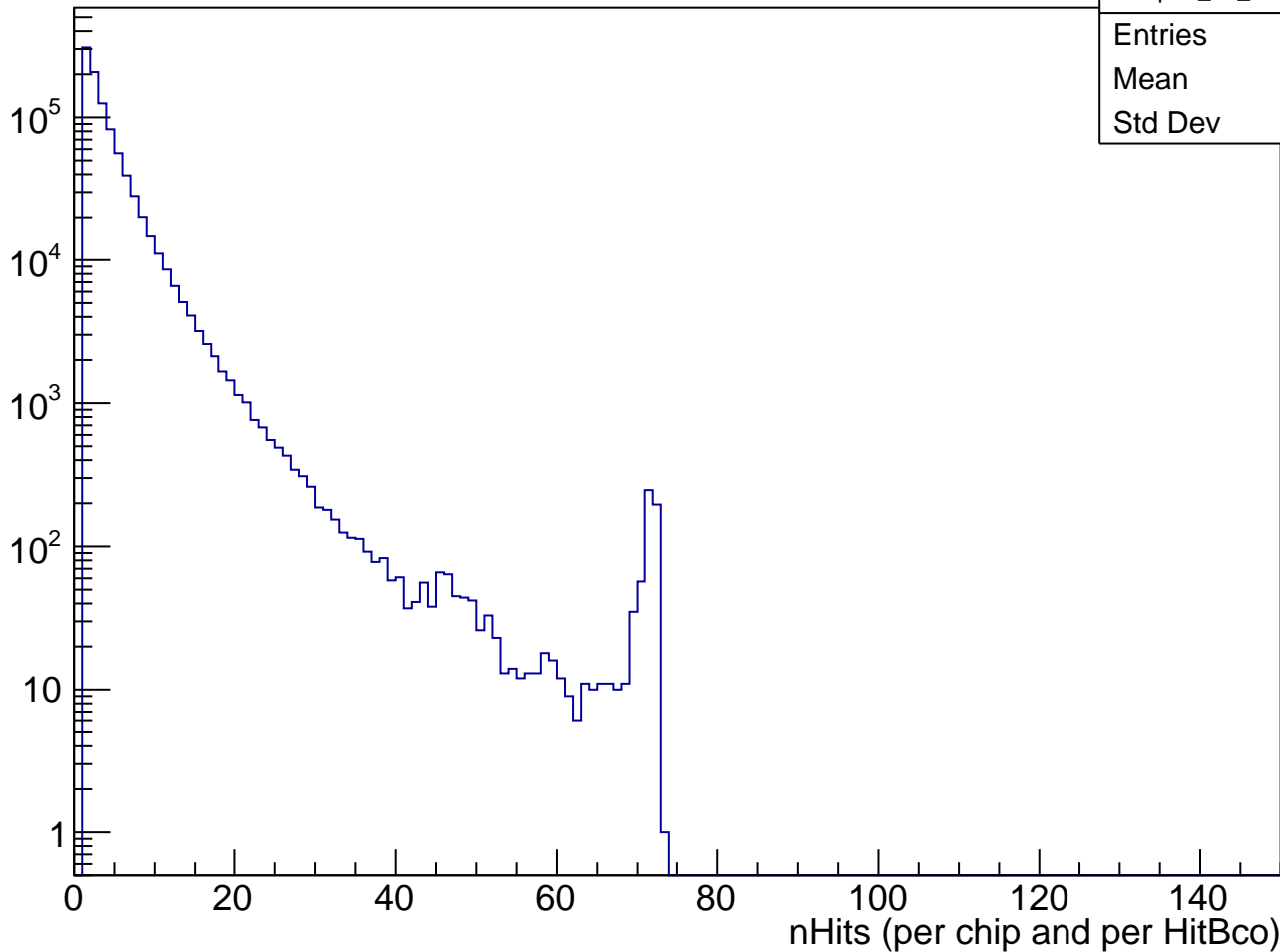
Entries



nChipHit_F6_Fch8_Chip18	
Entries	922583
Mean	3.507
Std Dev	4.104

nChipHit_F6_Fch8_Chip19

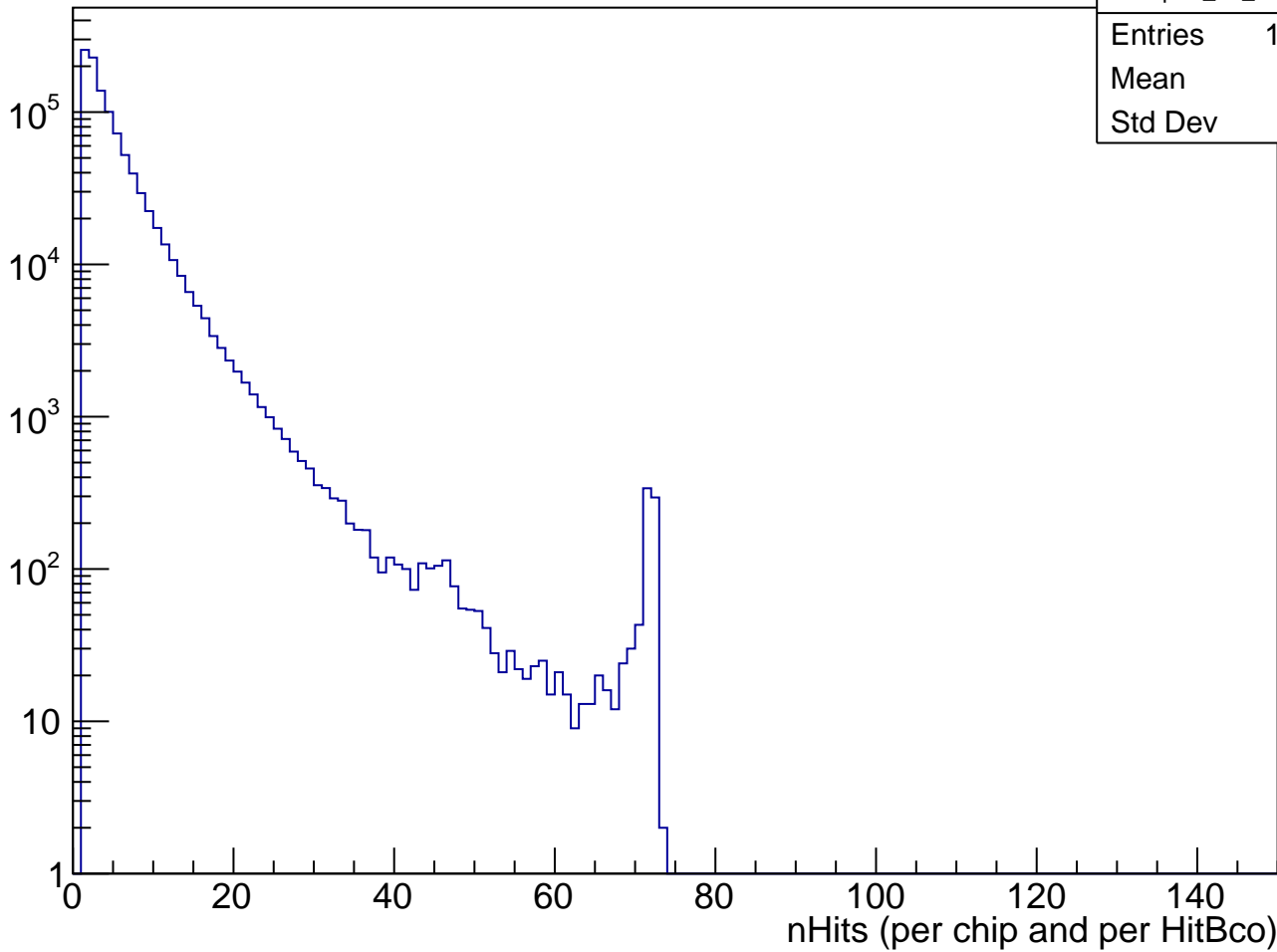
Entries



nChipHit_F6_Fch8_Chip19	
Entries	936464
Mean	3.504
Std Dev	4.104

nChipHit_F6_Fch8_Chip2

Entries



nChipHit_F6_Fch8_Chip2

Entries 1028056

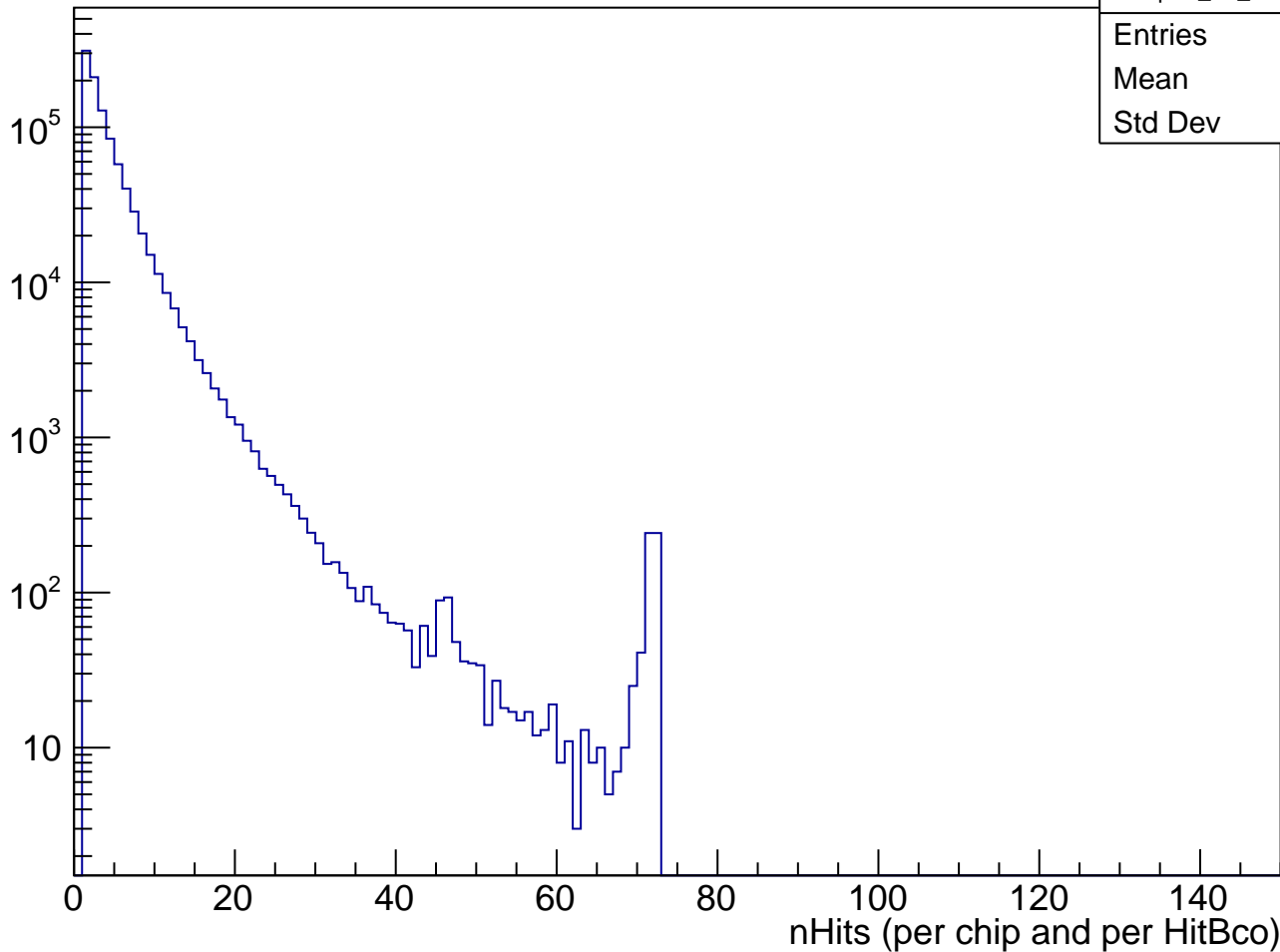
Mean 4.181

Std Dev 4.712

nHits (per chip and per HitBco)

nChipHit_F6_Fch8_Chip20

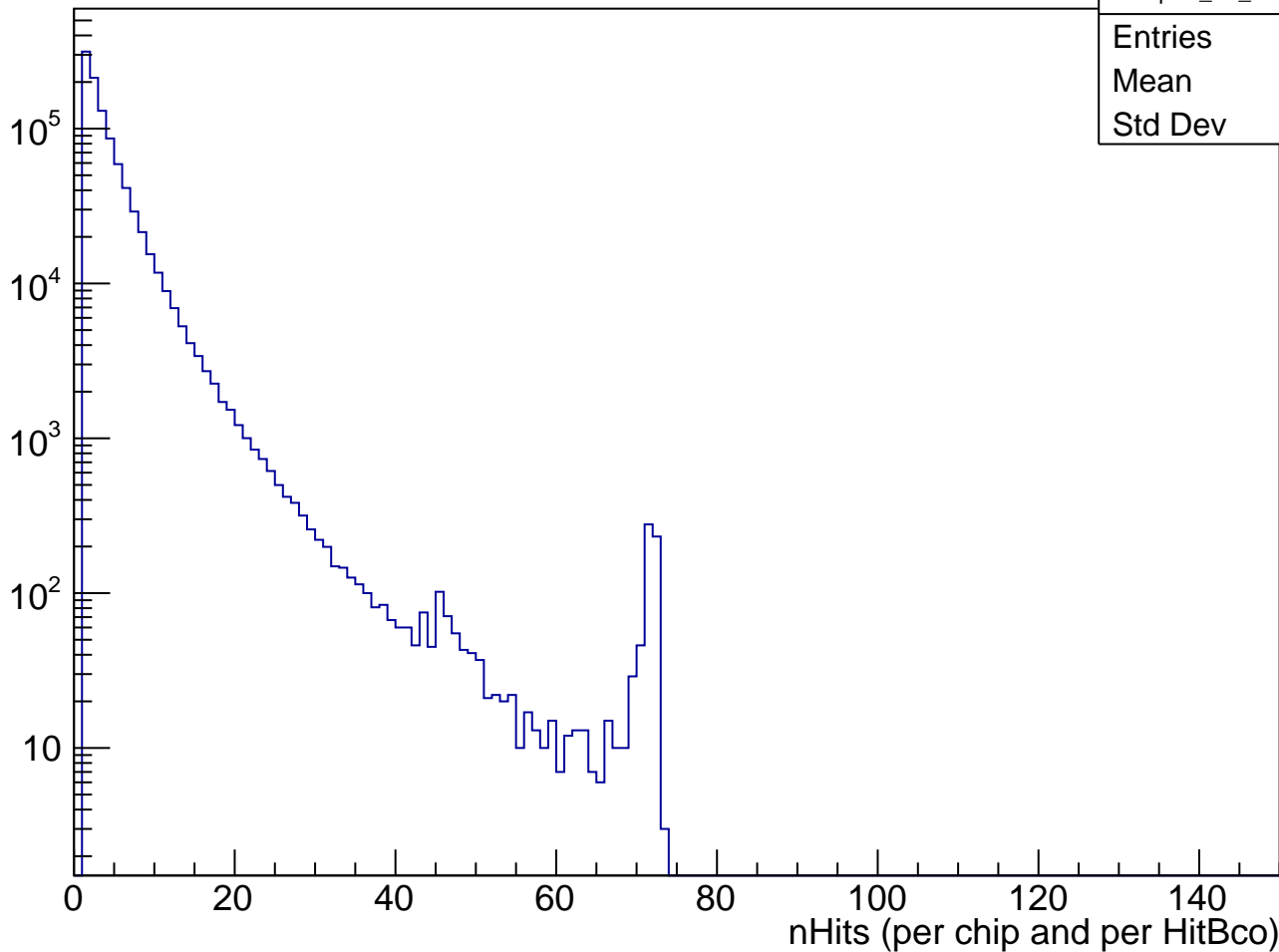
Entries



nChipHit_F6_Fch8_Chip20	
Entries	952186
Mean	3.505
Std Dev	4.097

nChipHit_F6_Fch8_Chip21

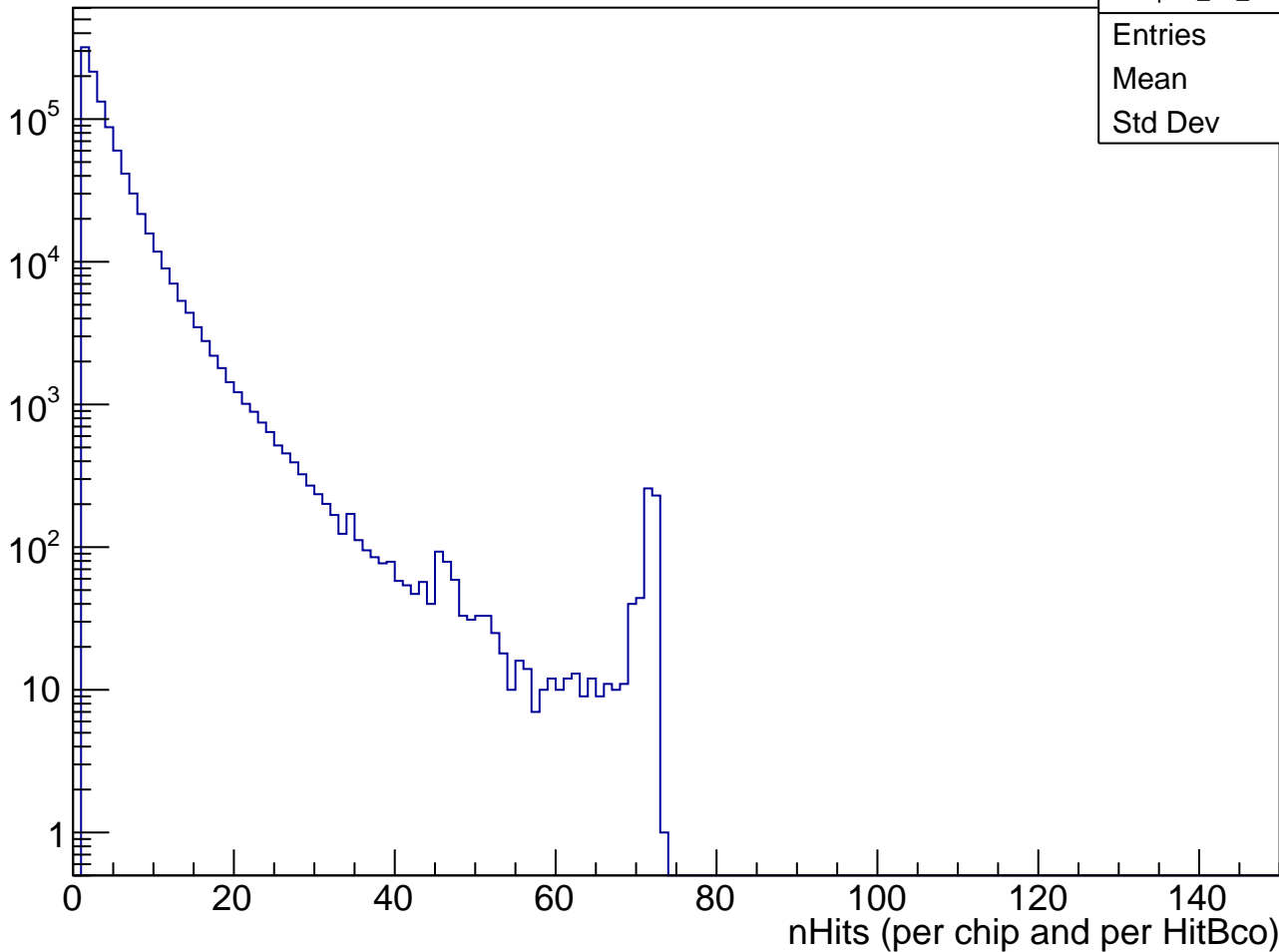
Entries



nChipHit_F6_Fch8_Chip21	
Entries	968471
Mean	3.538
Std Dev	4.158

nChipHit_F6_Fch8_Chip22

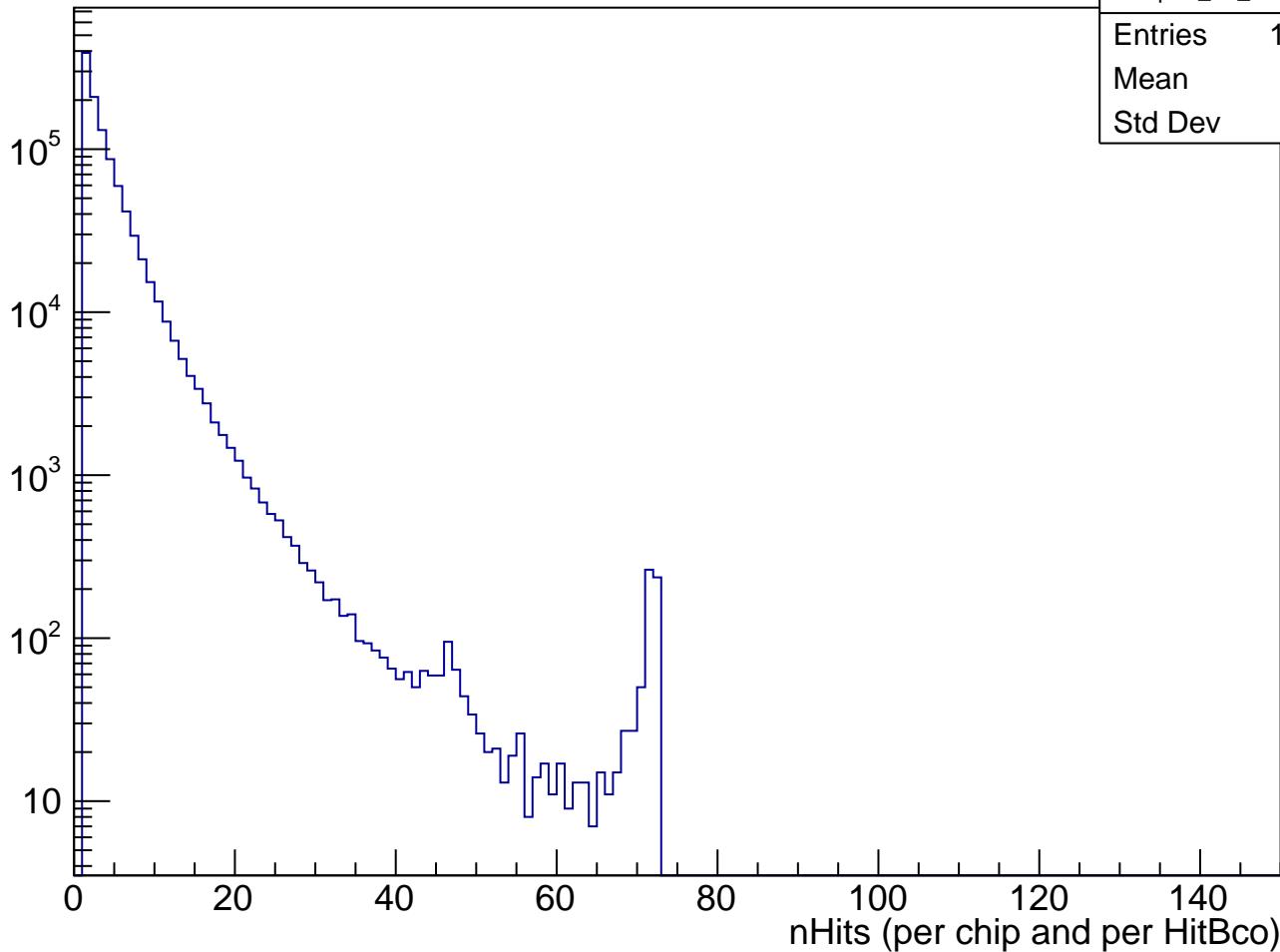
Entries



nChipHit_F6_Fch8_Chip22	
Entries	981808
Mean	3.539
Std Dev	4.139

nChipHit_F6_Fch8_Chip23

Entries

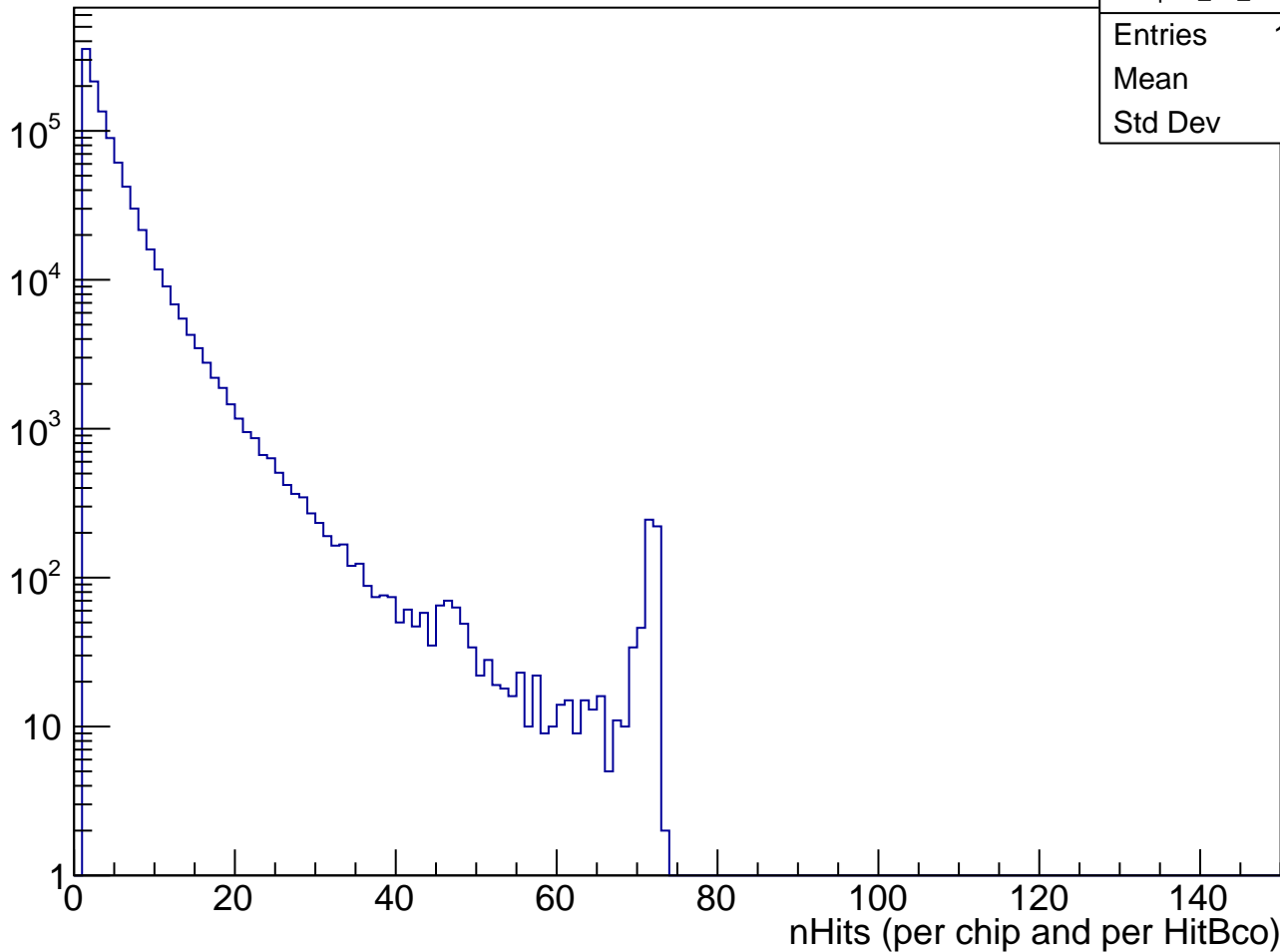


nChipHit_F6_Fch8_Chip23	
Entries	1040143
Mean	3.348
Std Dev	4.048

nHits (per chip and per HitBco)

nChipHit_F6_Fch8_Chip24

Entries

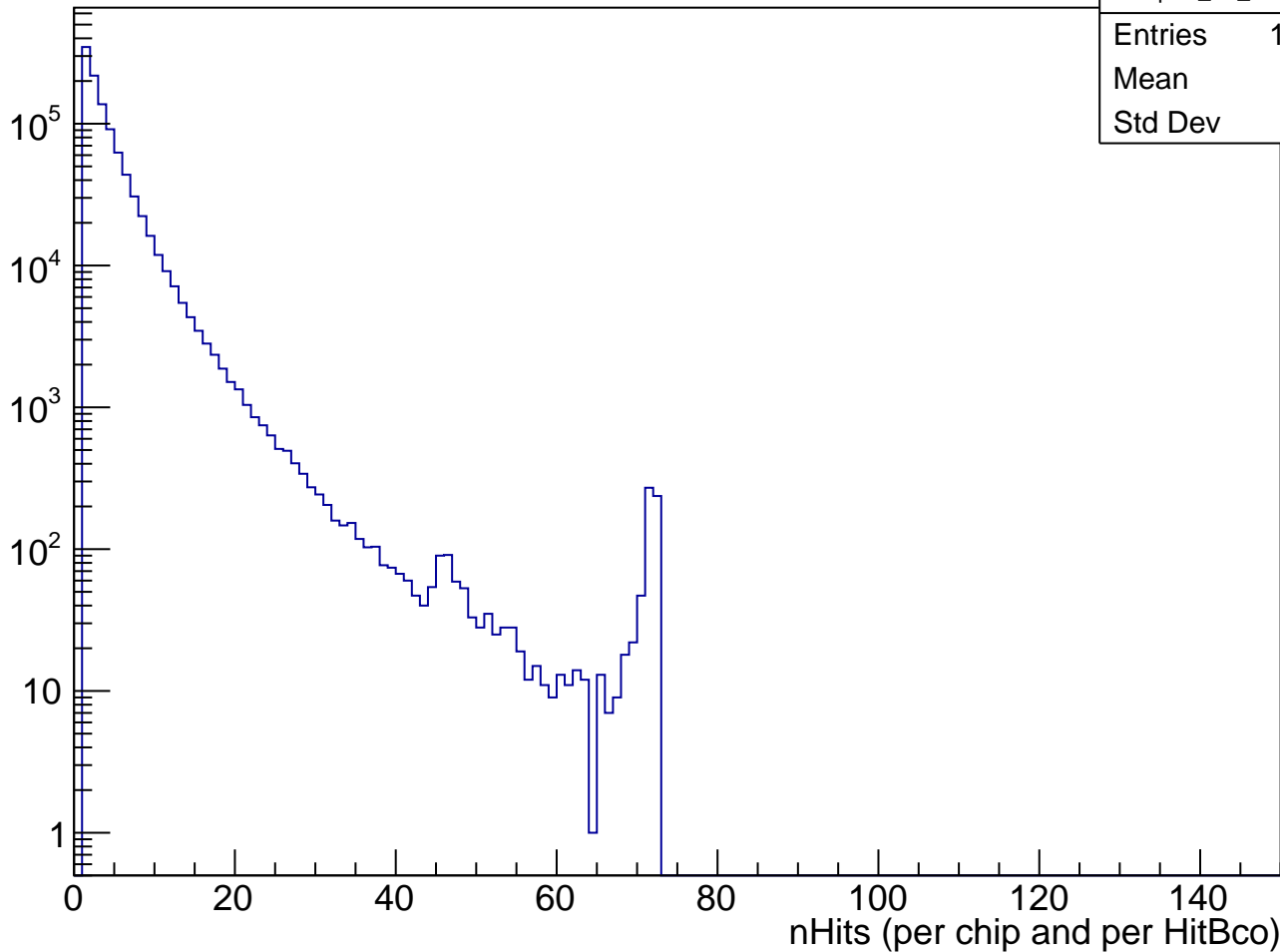


nChipHit_F6_Fch8_Chip24	
Entries	1023321
Mean	3.446
Std Dev	4.055

nHits (per chip and per HitBco)

nChipHit_F6_Fch8_Chip25

Entries

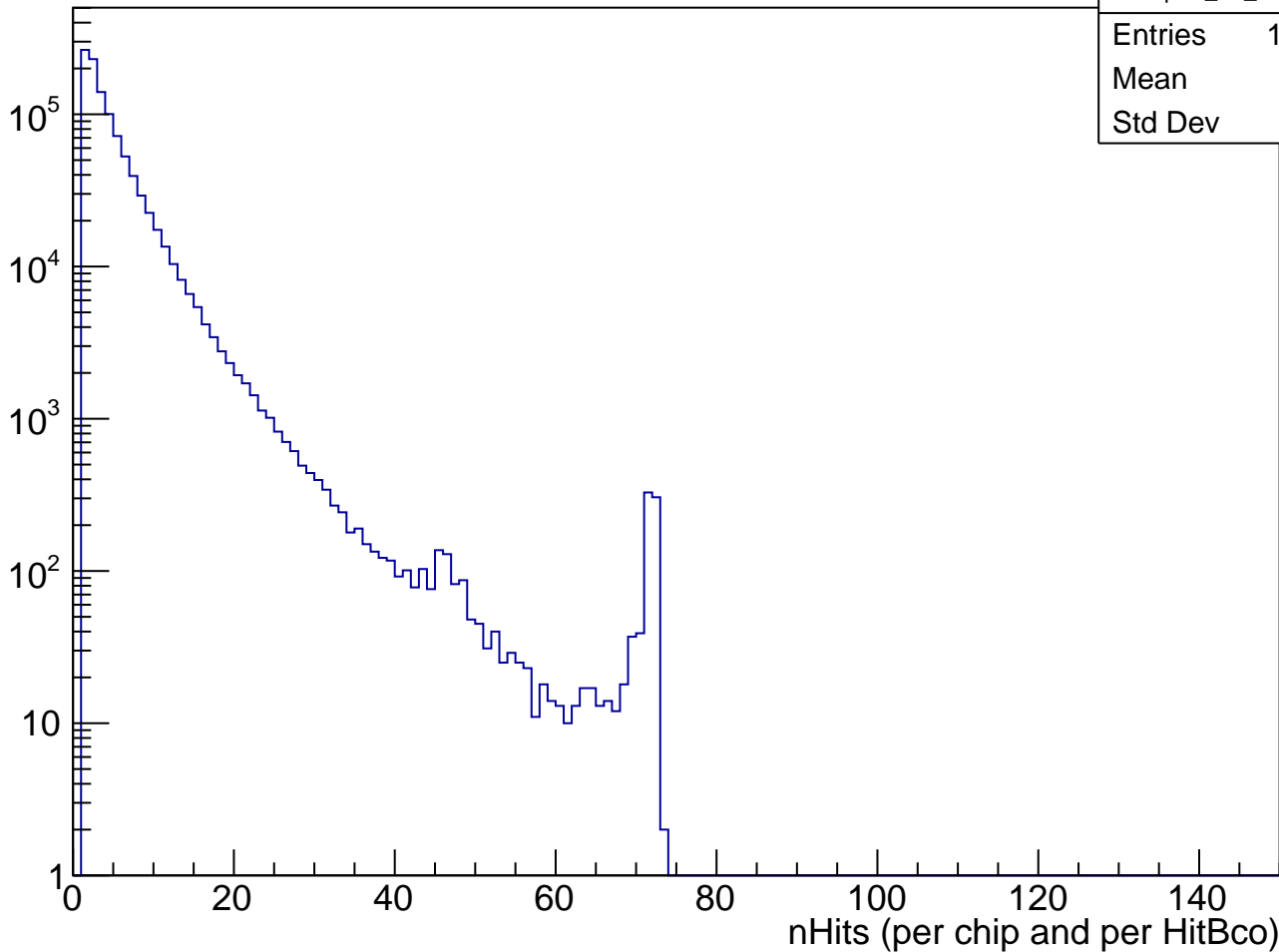


nChipHit_F6_Fch8_Chip25	
Entries	1029200
Mean	3.493
Std Dev	4.116

nHits (per chip and per HitBco)

nChipHit_F6_Fch8_Chip3

Entries



nChipHit_F6_Fch8_Chip3

Entries 1040088

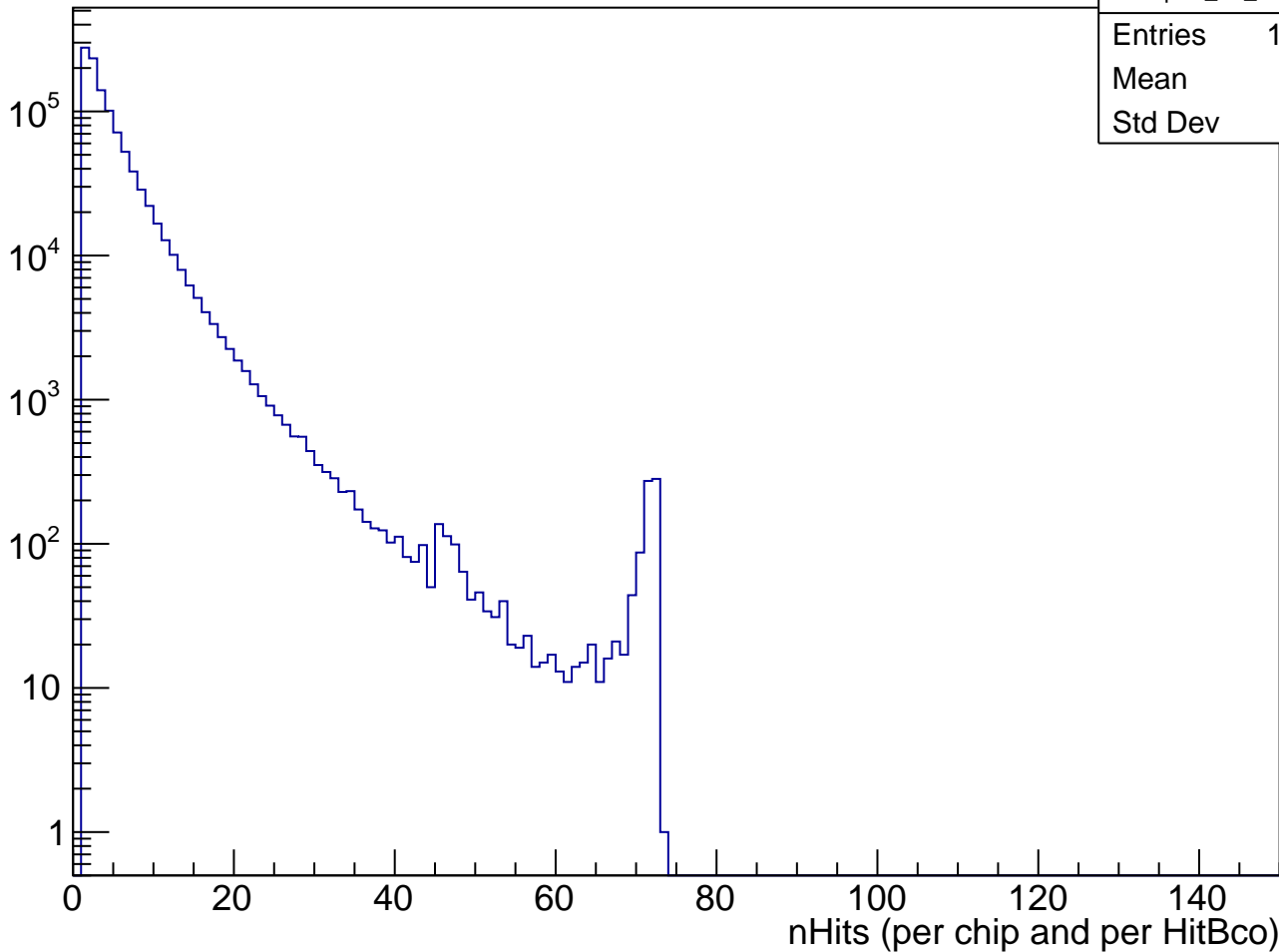
Mean 4.14

Std Dev 4.685

nHits (per chip and per HitBco)

nChipHit_F6_Fch8_Chip4

Entries



nChipHit_F6_Fch8_Chip4

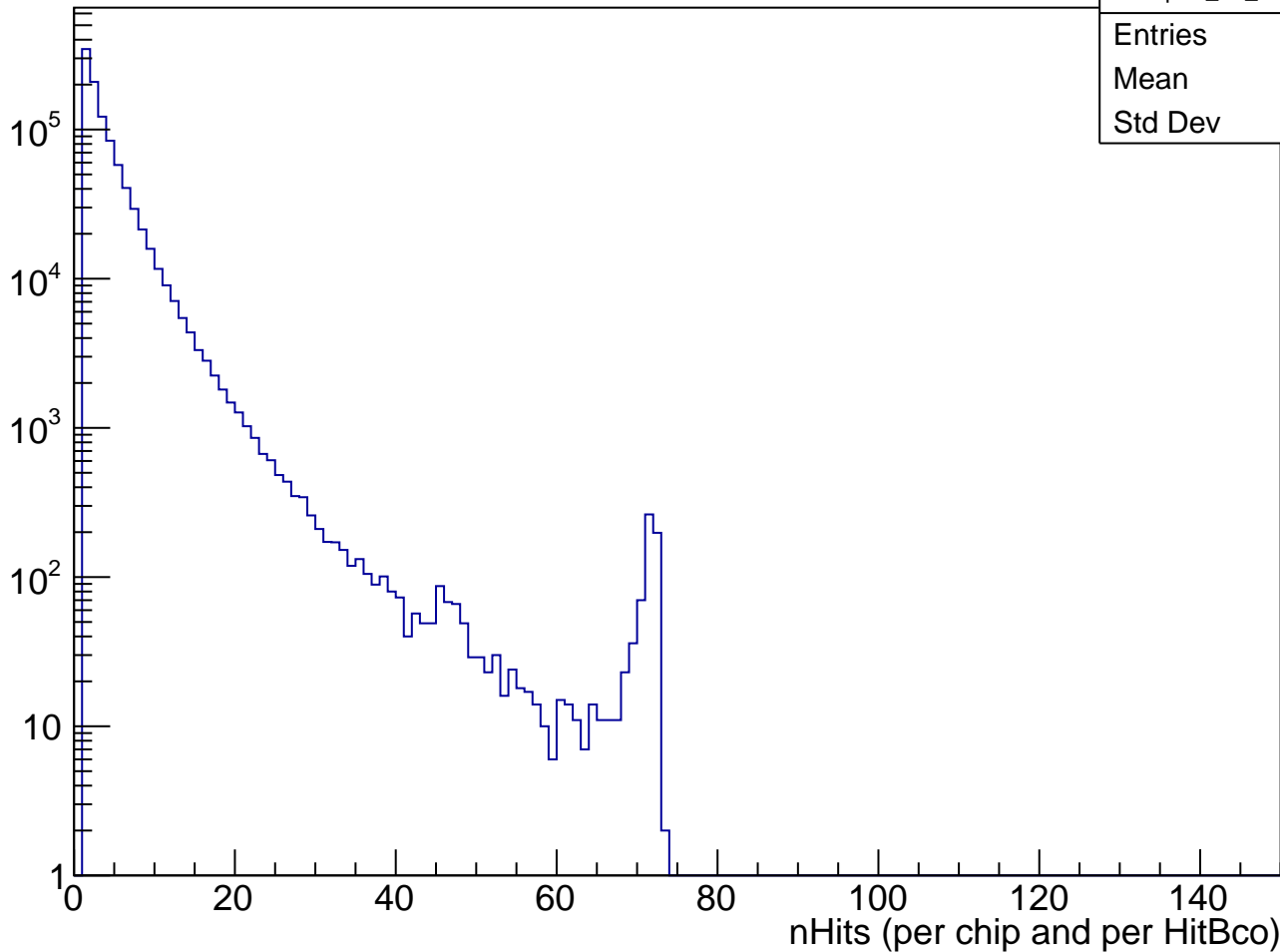
Entries 1049894

Mean 4.049

Std Dev 4.604

nChipHit_F6_Fch8_Chip5

Entries



nChipHit_F6_Fch8_Chip5

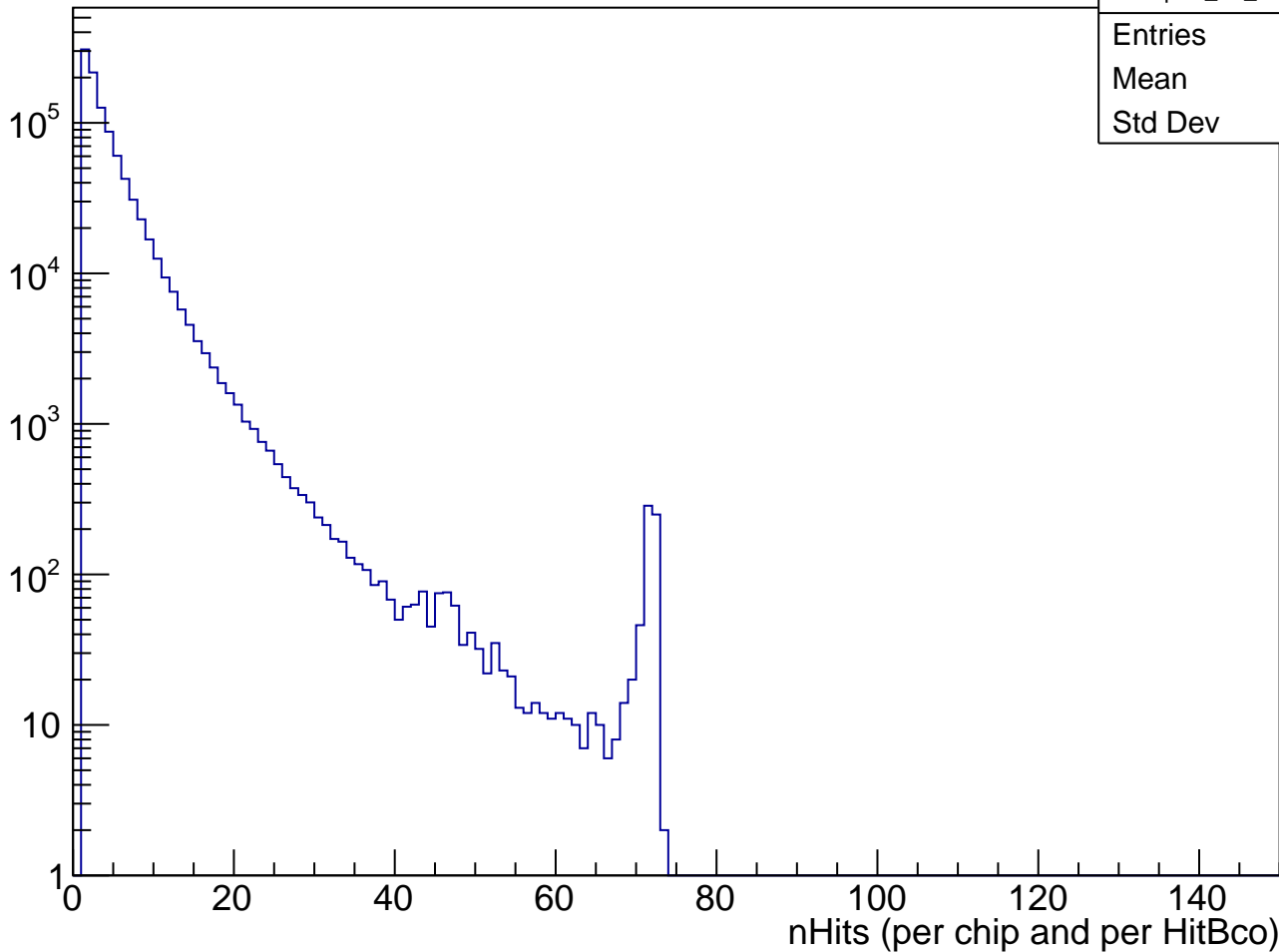
Entries 984042

Mean 3.473

Std Dev 4.156

nChipHit_F6_Fch8_Chip6

Entries



nChipHit_F6_Fch8_Chip6

Entries 972239

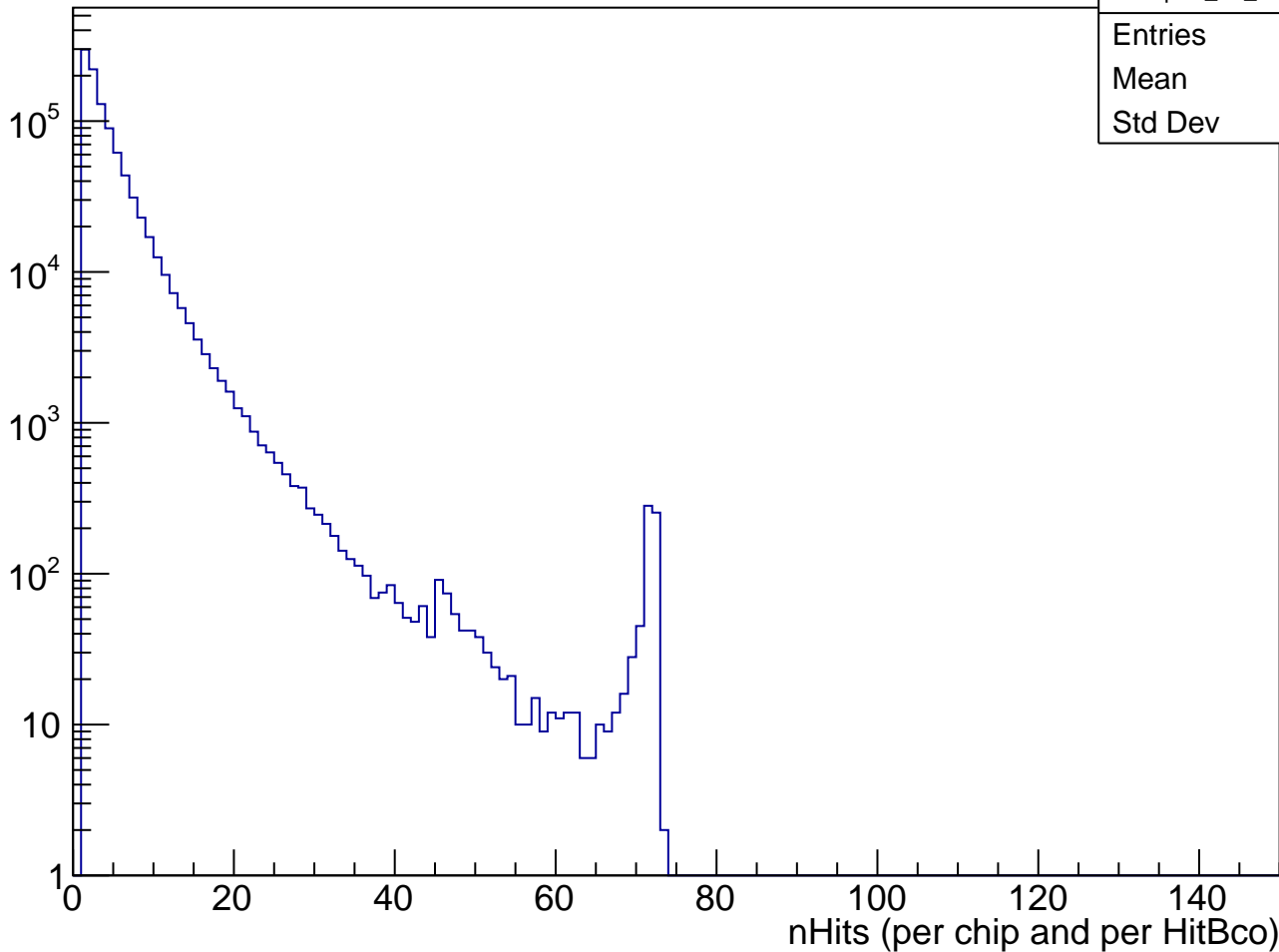
Mean 3.625

Std Dev 4.233

nHits (per chip and per HitBco)

nChipHit_F6_Fch8_Chip7

Entries

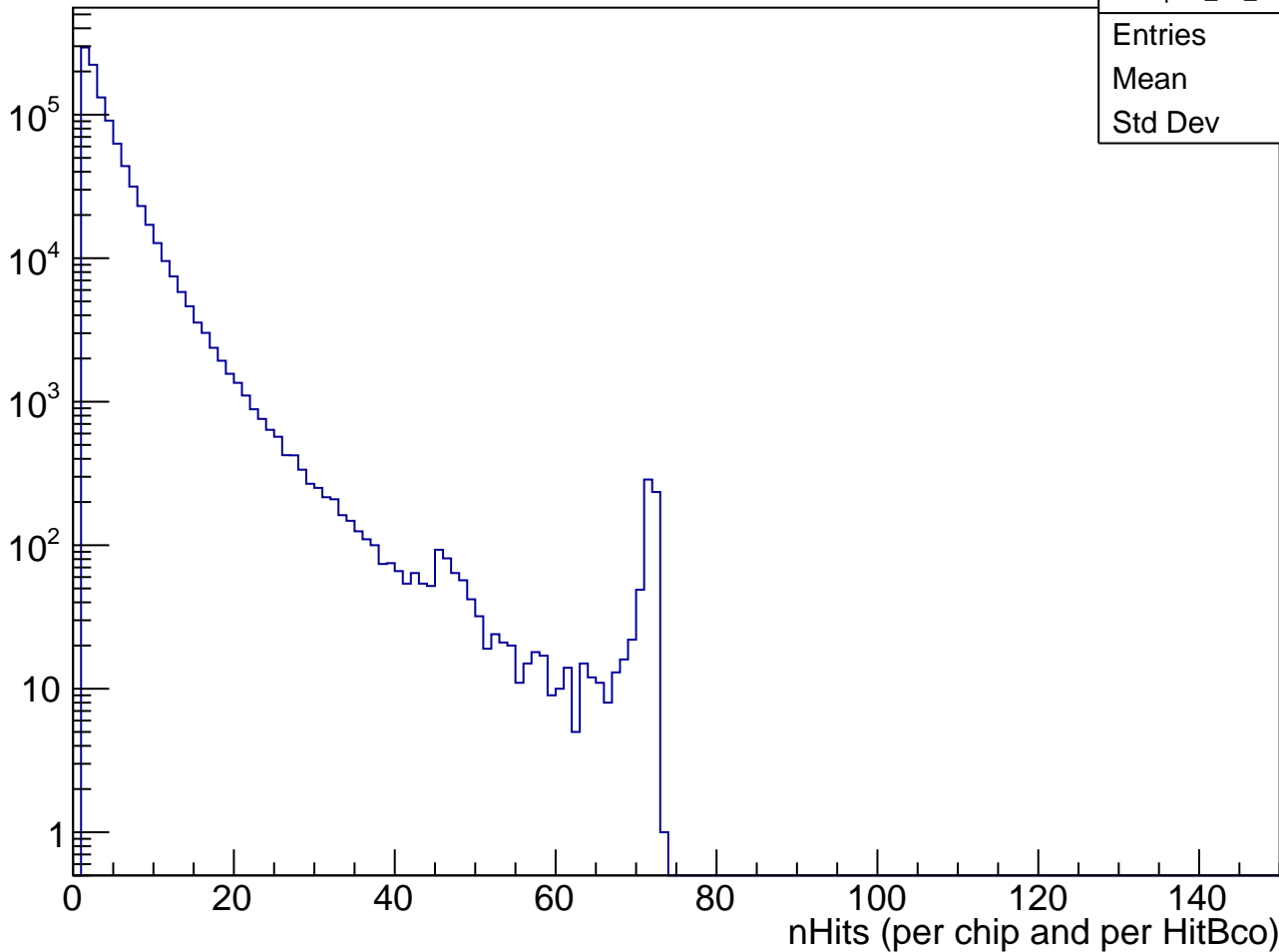


nChipHit_F6_Fch8_Chip7	
Entries	975426
Mean	3.642
Std Dev	4.21

nHits (per chip and per HitBco)

nChipHit_F6_Fch8_Chip8

Entries



nChipHit_F6_Fch8_Chip8

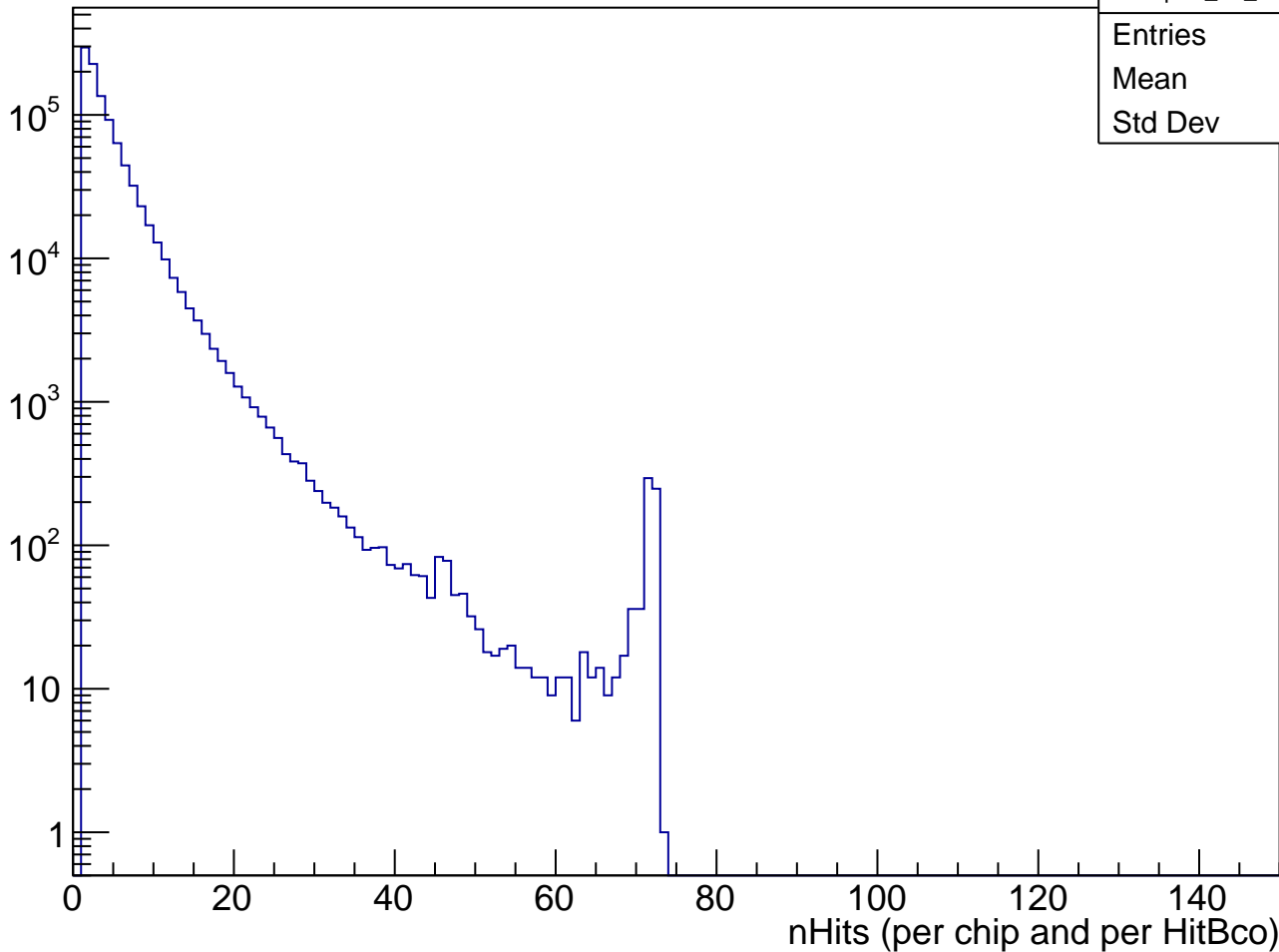
Entries 980433

Mean 3.669

Std Dev 4.234

nChipHit_F6_Fch8_Chip9

Entries



nChipHit_F6_Fch8_Chip9

Entries 991973

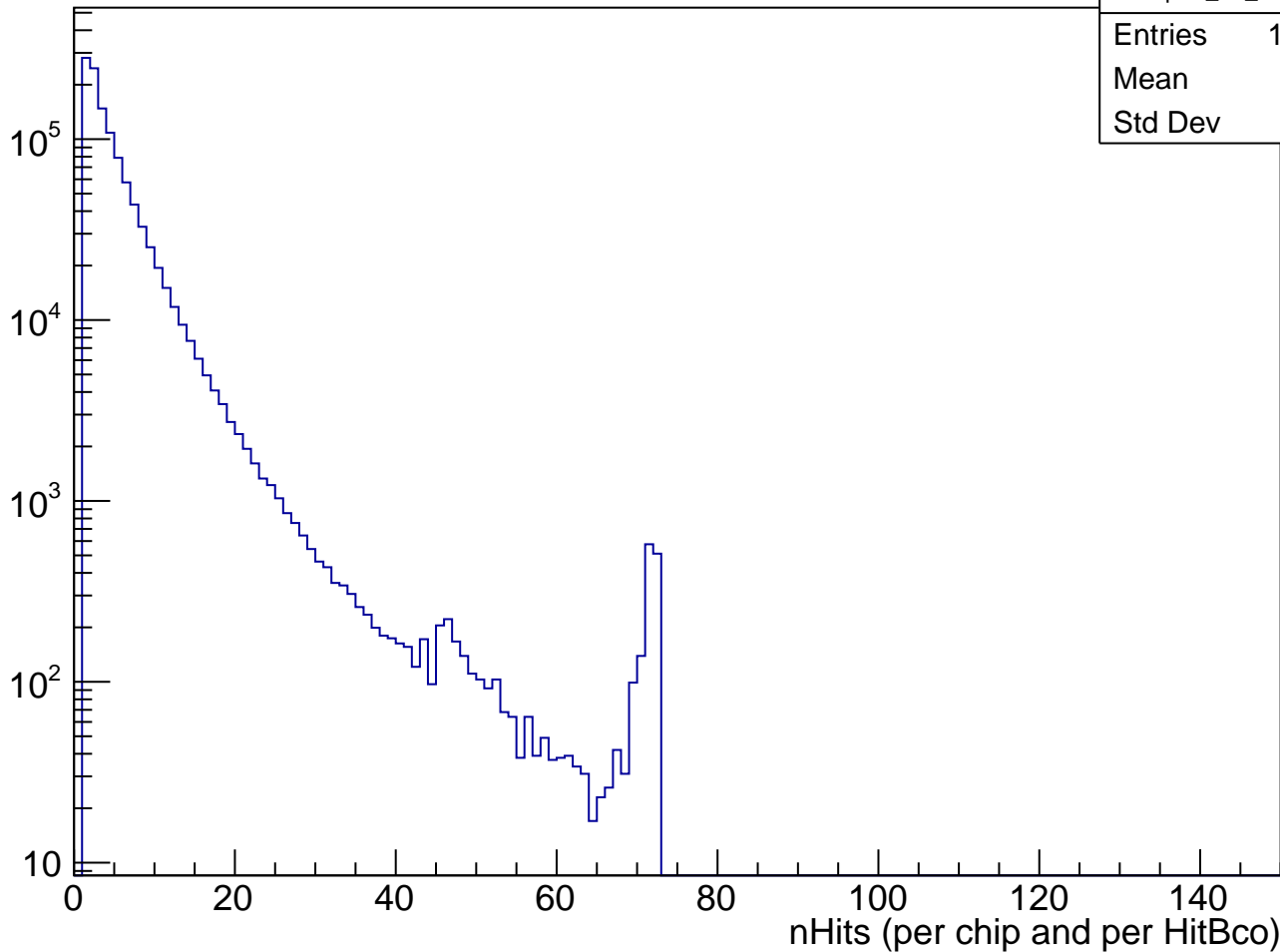
Mean 3.659

Std Dev 4.205

nHits (per chip and per HitBco)

nChipHit_F6_Fch9_Chip0

Entries



nChipHit_F6_Fch9_Chip0

Entries 1126138

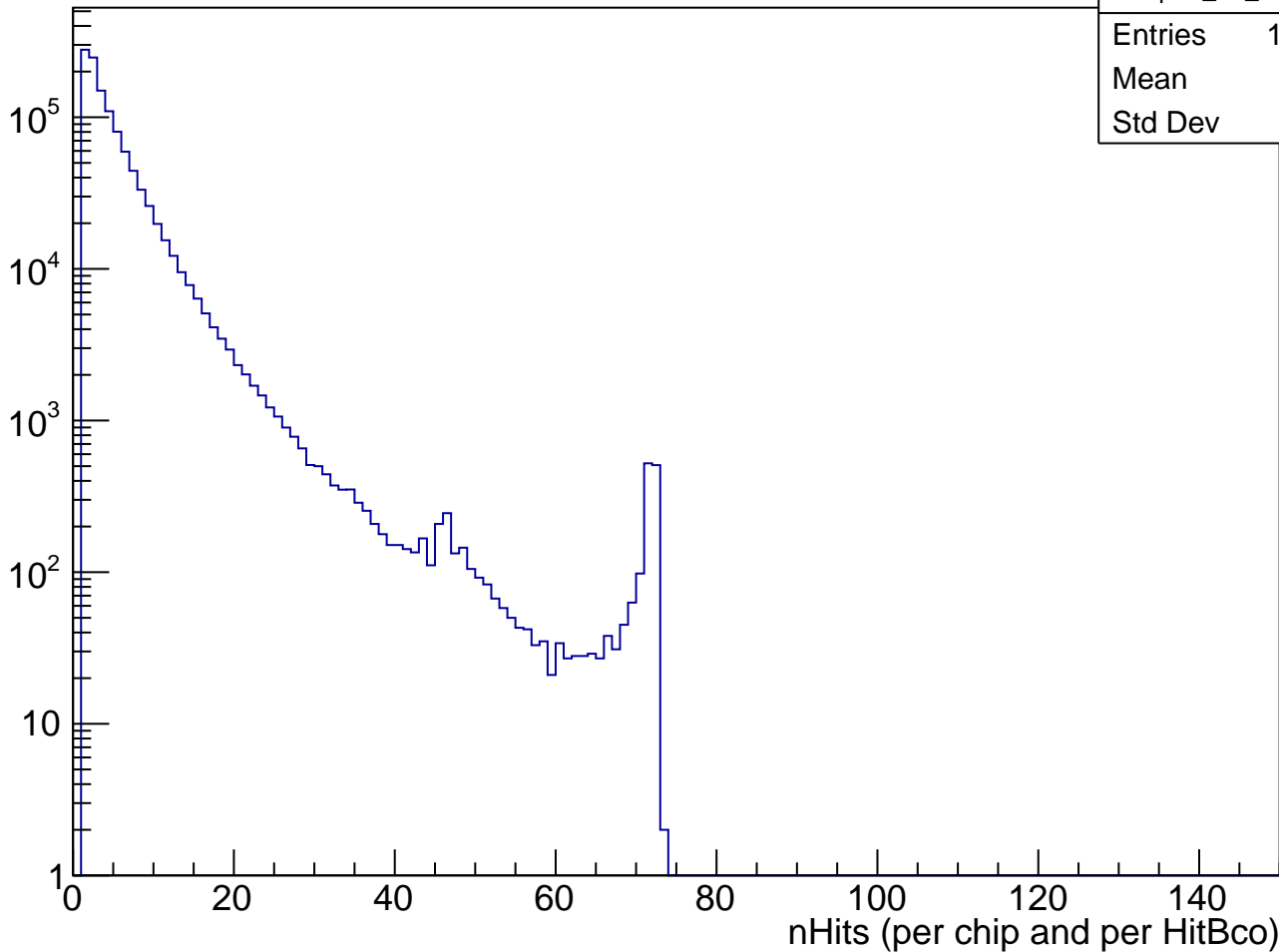
Mean 4.326

Std Dev 5.241

nHits (per chip and per HitBco)

nChipHit_F6_Fch9_Chip1

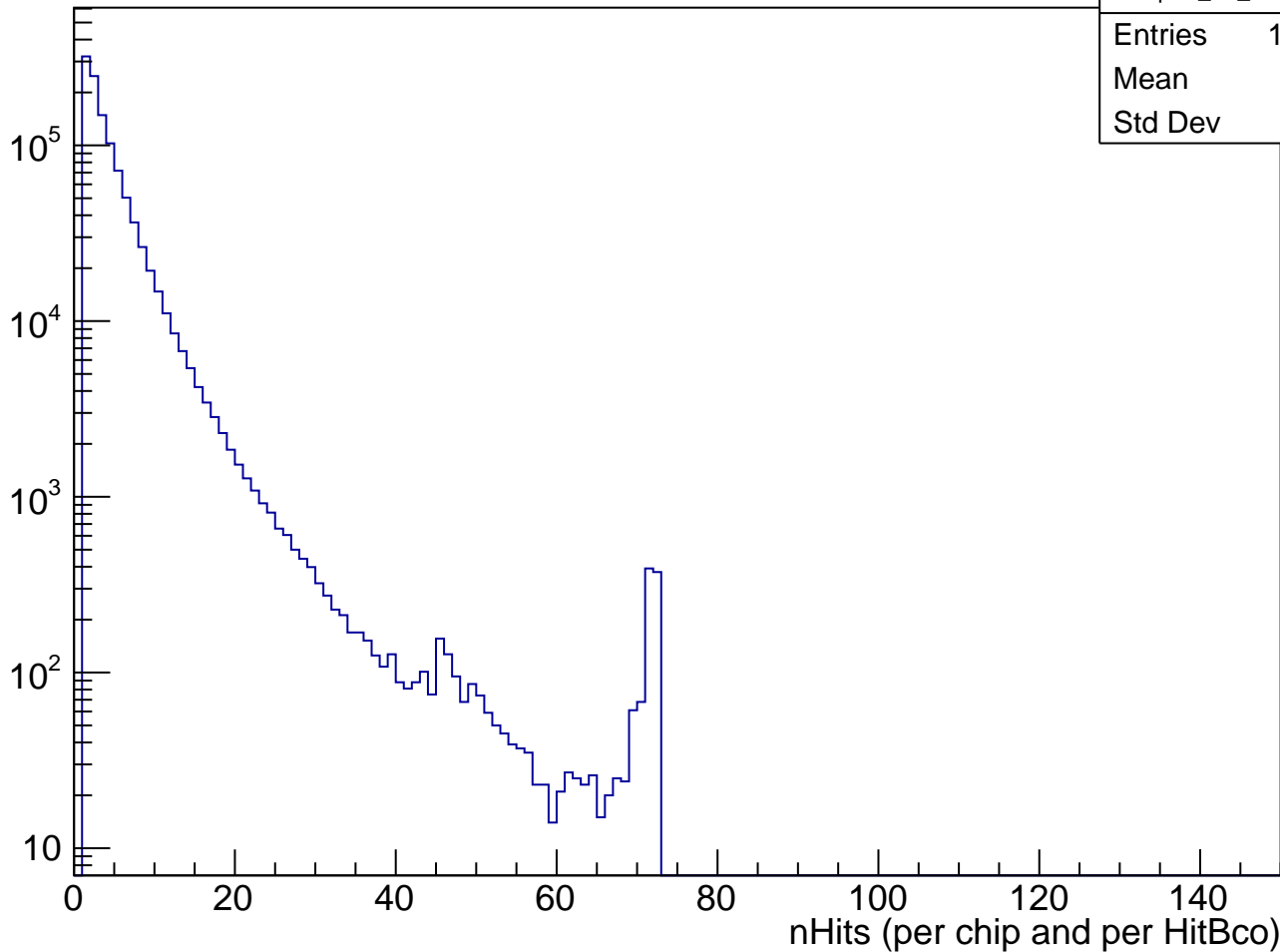
Entries



nChipHit_F6_Fch9_Chip1	
Entries	1134774
Mean	4.351
Std Dev	5.182

nChipHit_F6_Fch9_Chip10

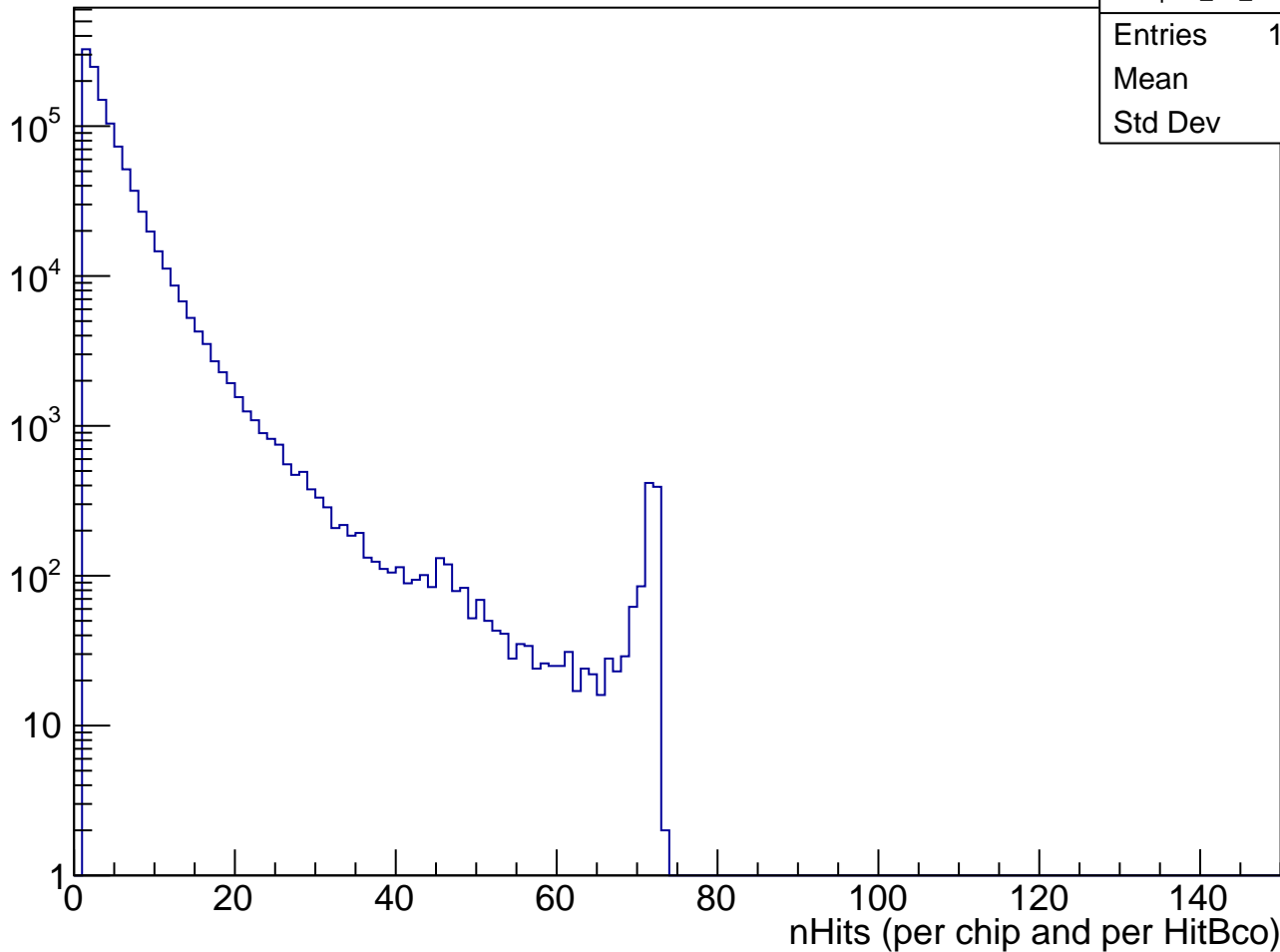
Entries



nChipHit_F6_Fch9_Chip10	
Entries	1097672
Mean	3.778
Std Dev	4.543

nChipHit_F6_Fch9_Chip11

Entries



nChipHit_F6_Fch9_Chip11

Entries 1110425

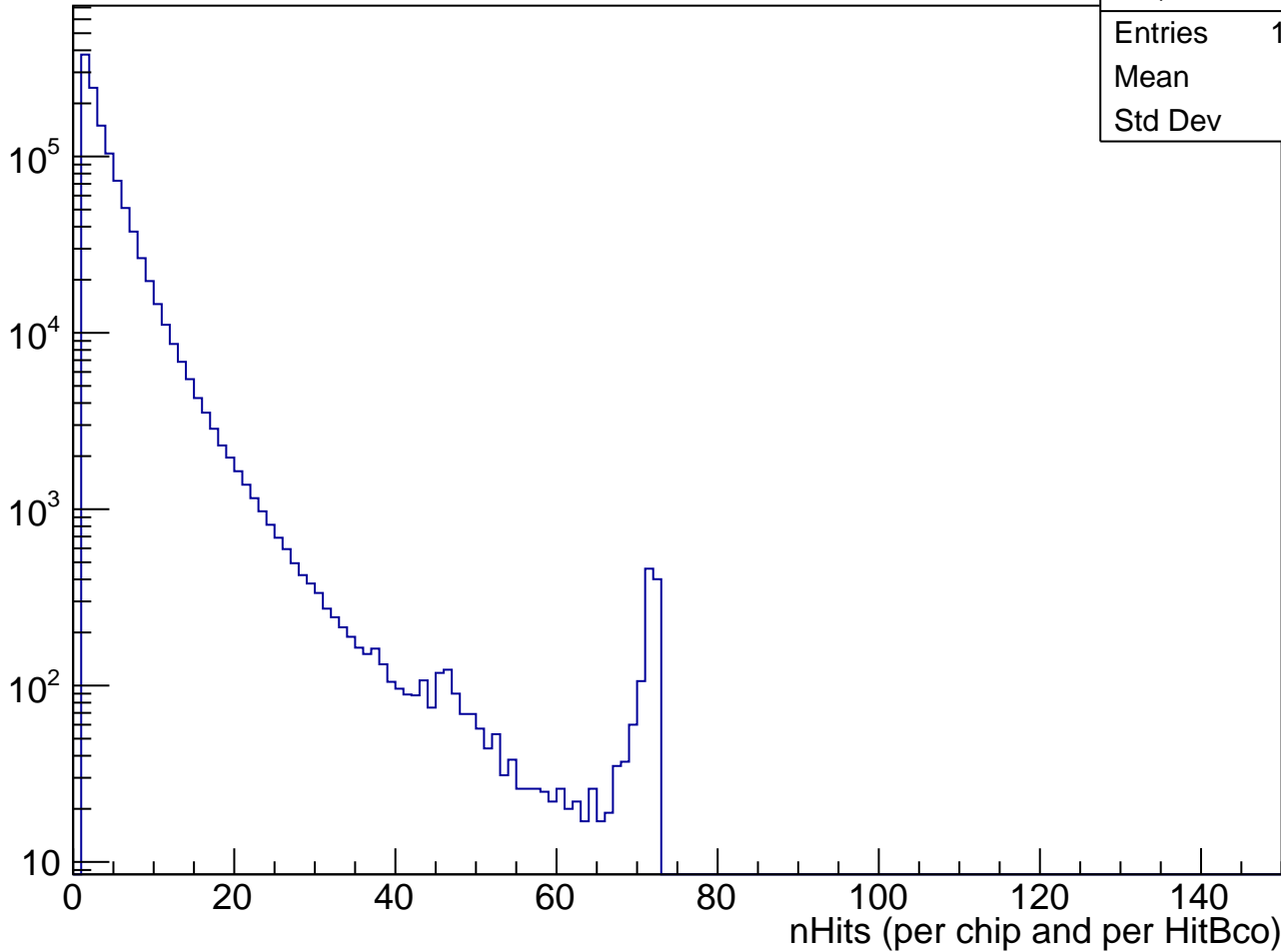
Mean 3.776

Std Dev 4.548

nHits (per chip and per HitBco)

nChipHit_F6_Fch9_Chip12

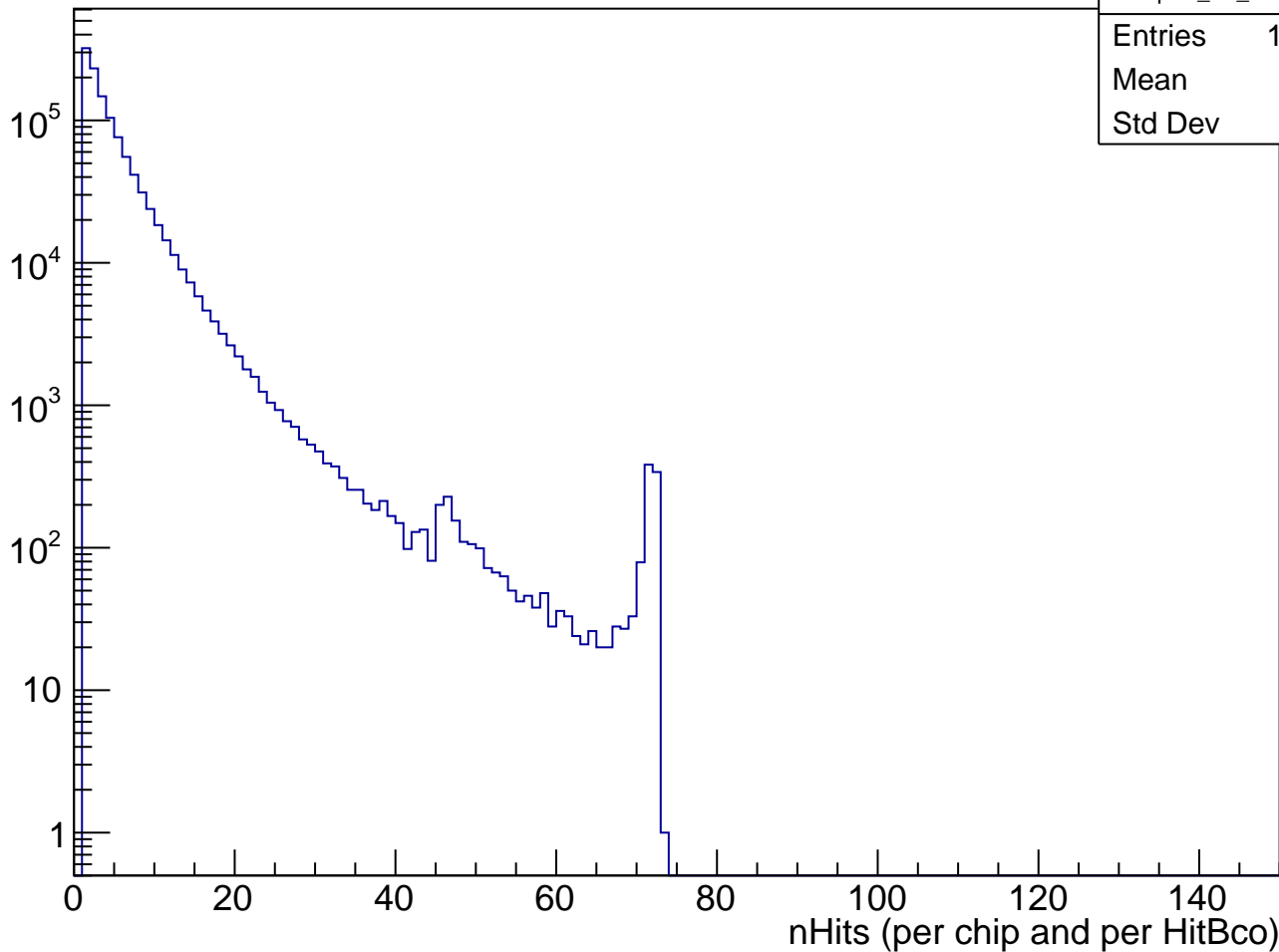
Entries



nChipHit_F6_Fch9_Chip12	
Entries	1159153
Mean	3.668
Std Dev	4.532

nChipHit_F6_Fch9_Chip13

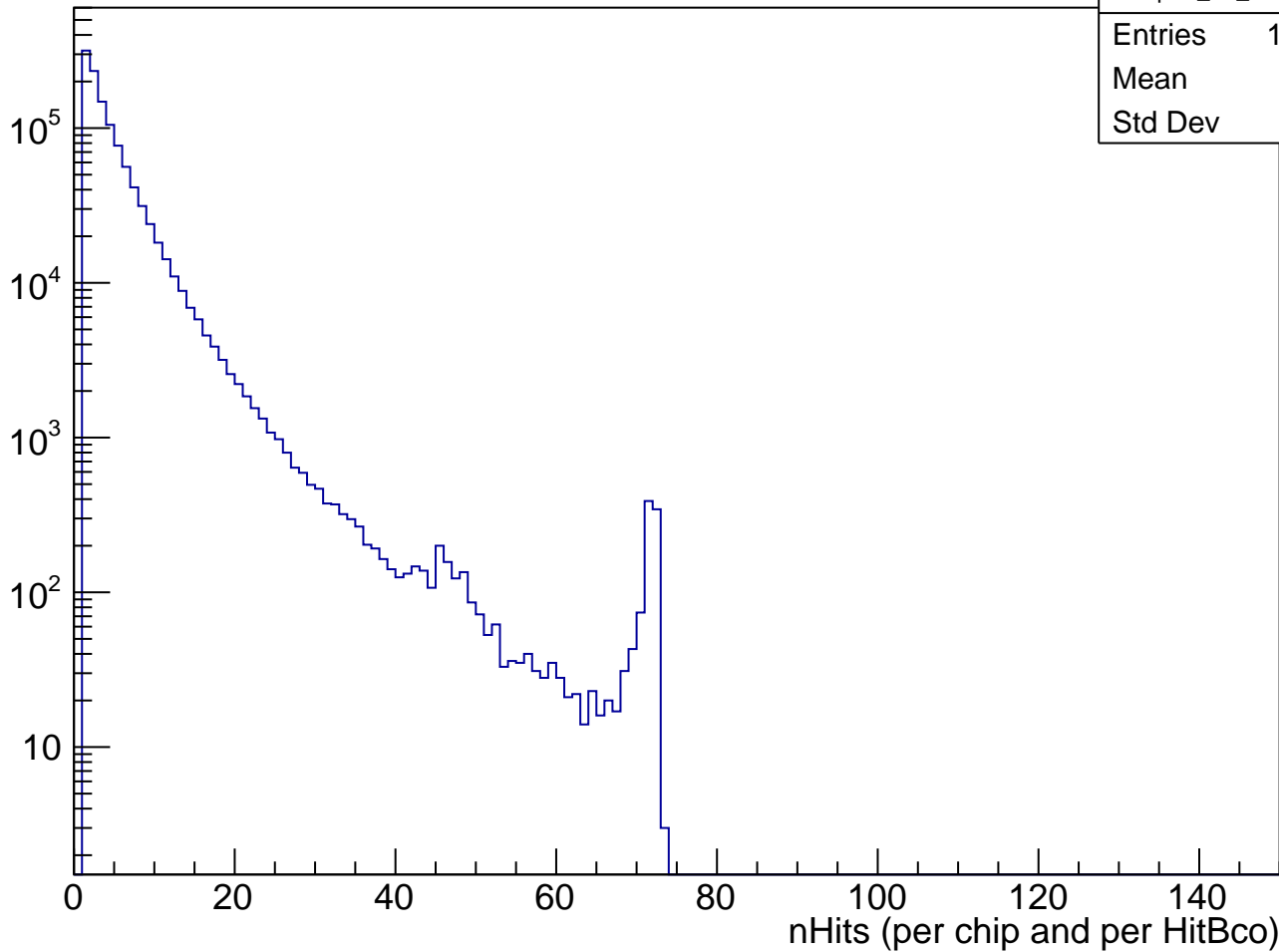
Entries



nChipHit_F6_Fch9_Chip13	
Entries	1130588
Mean	4.129
Std Dev	4.937

nChipHit_F6_Fch9_Chip14

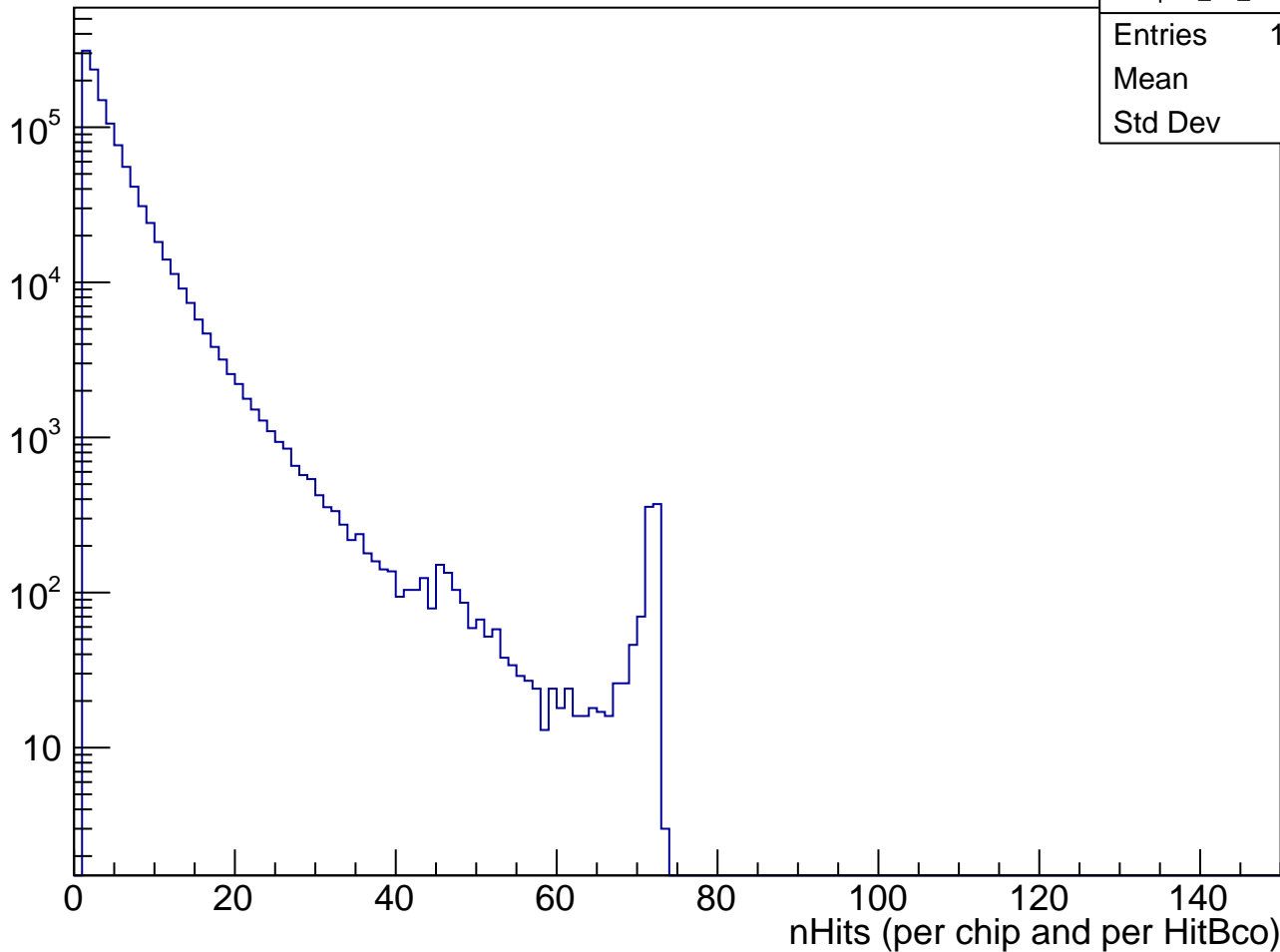
Entries



nChipHit_F6_Fch9_Chip14	
Entries	1130217
Mean	4.122
Std Dev	4.884

nChipHit_F6_Fch9_Chip15

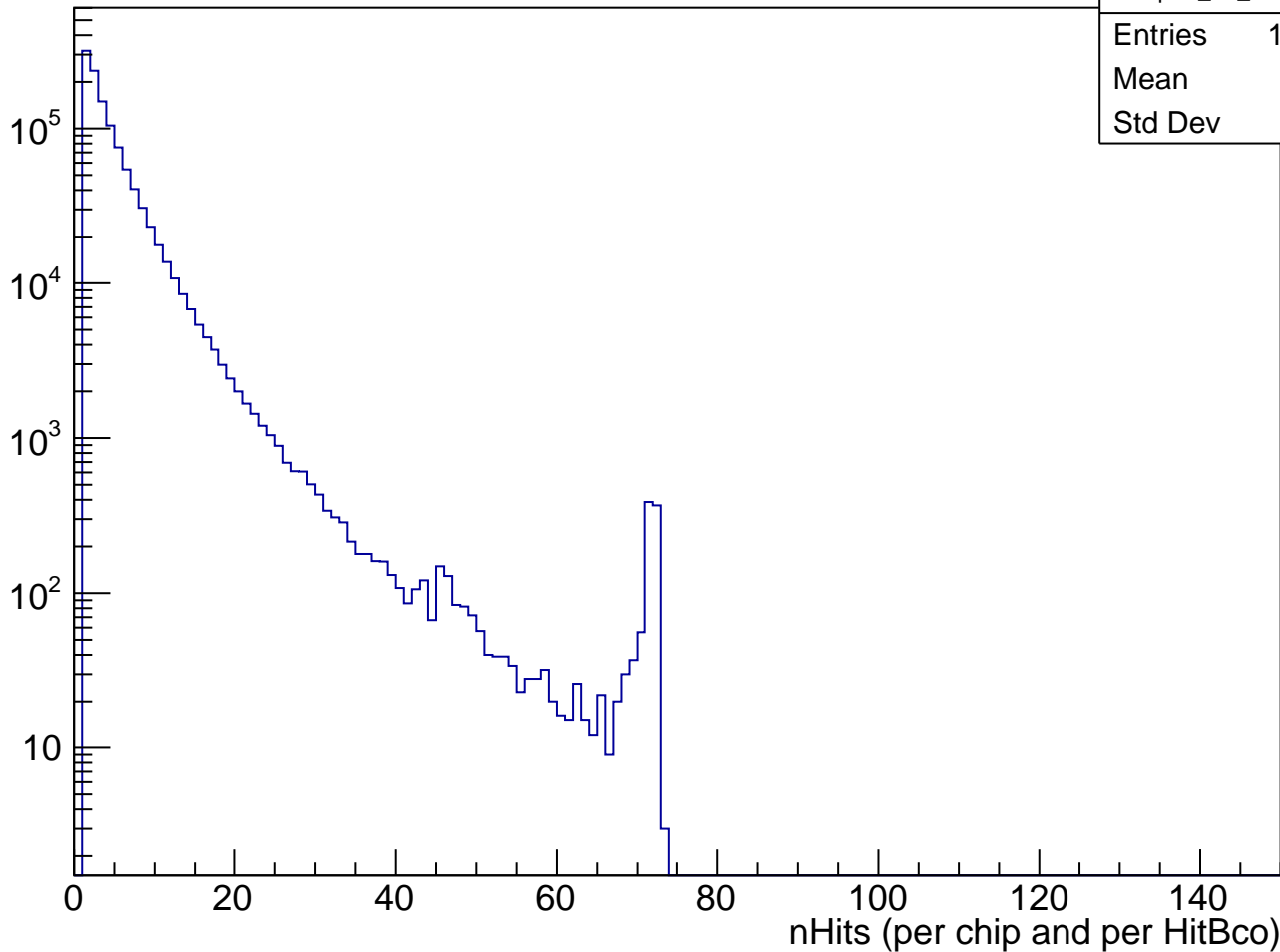
Entries



nChipHit_F6_Fch9_Chip15	
Entries	1127083
Mean	4.113
Std Dev	4.797

nChipHit_F6_Fch9_Chip16

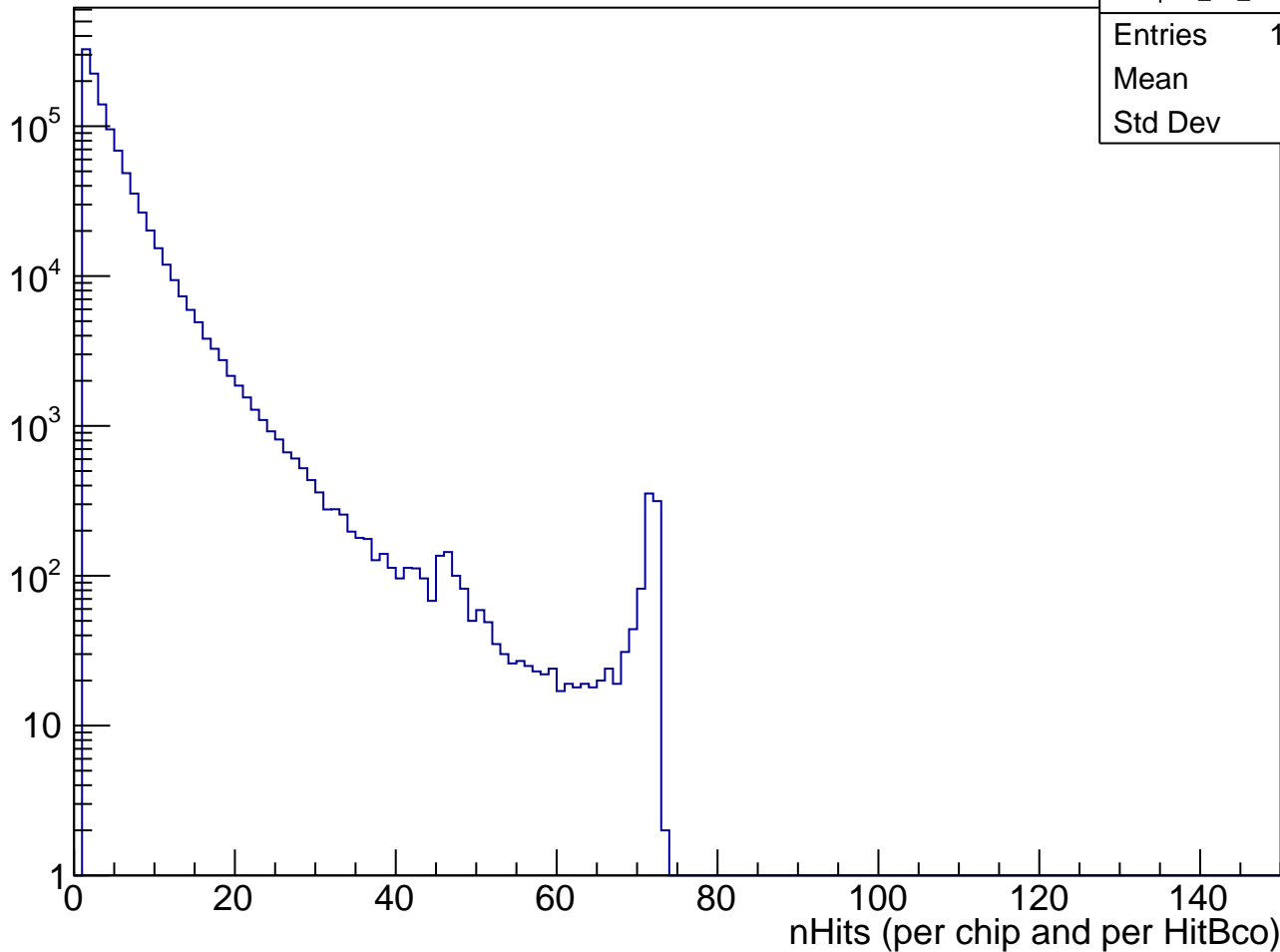
Entries



nChipHit_F6_Fch9_Chip16	
Entries	1124108
Mean	4.036
Std Dev	4.729

nChipHit_F6_Fch9_Chip17

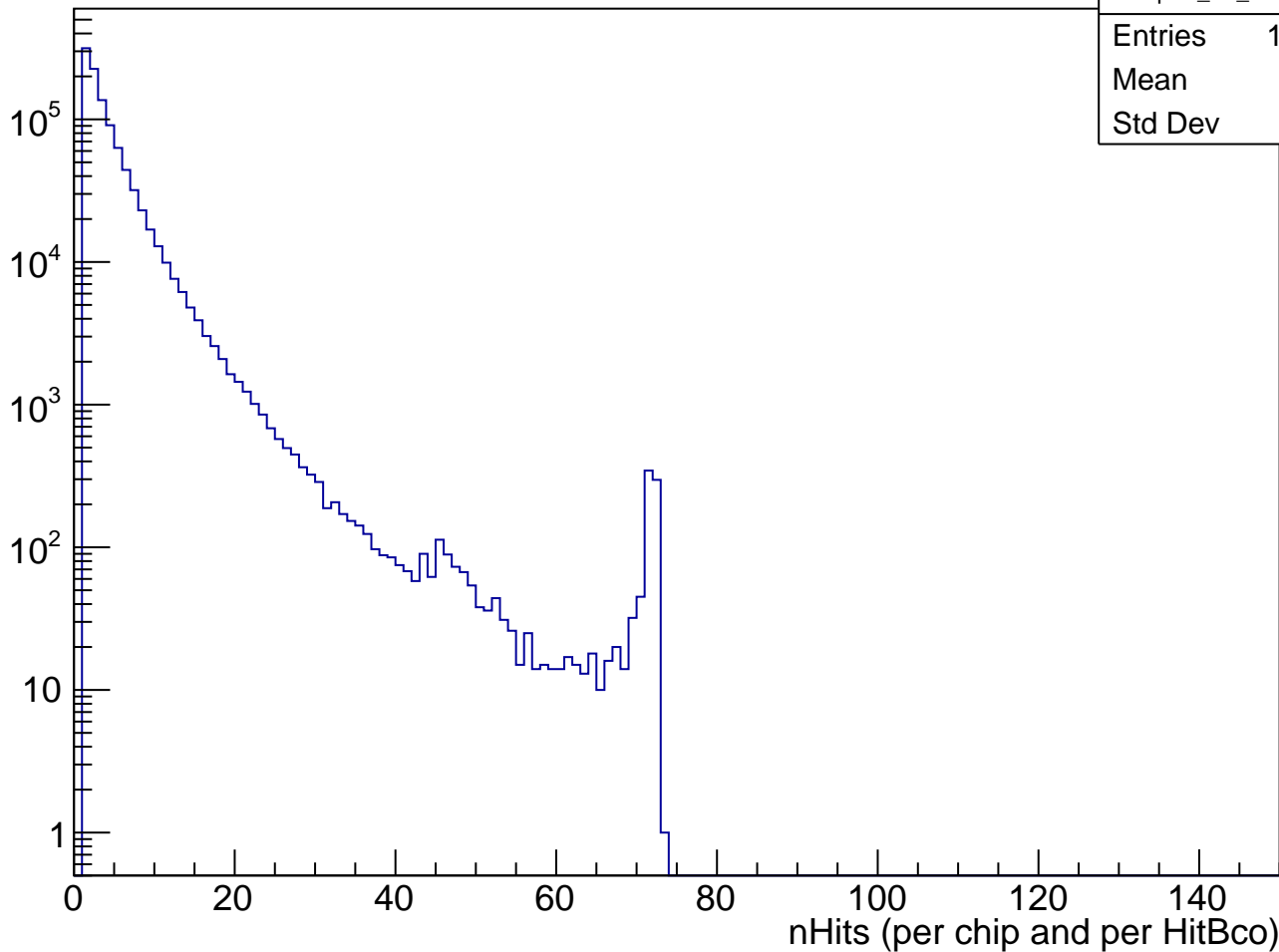
Entries



nChipHit_F6_Fch9_Chip17	
Entries	1066280
Mean	3.879
Std Dev	4.677

nChipHit_F6_Fch9_Chip18

Entries



nChipHit_F6_Fch9_Chip18

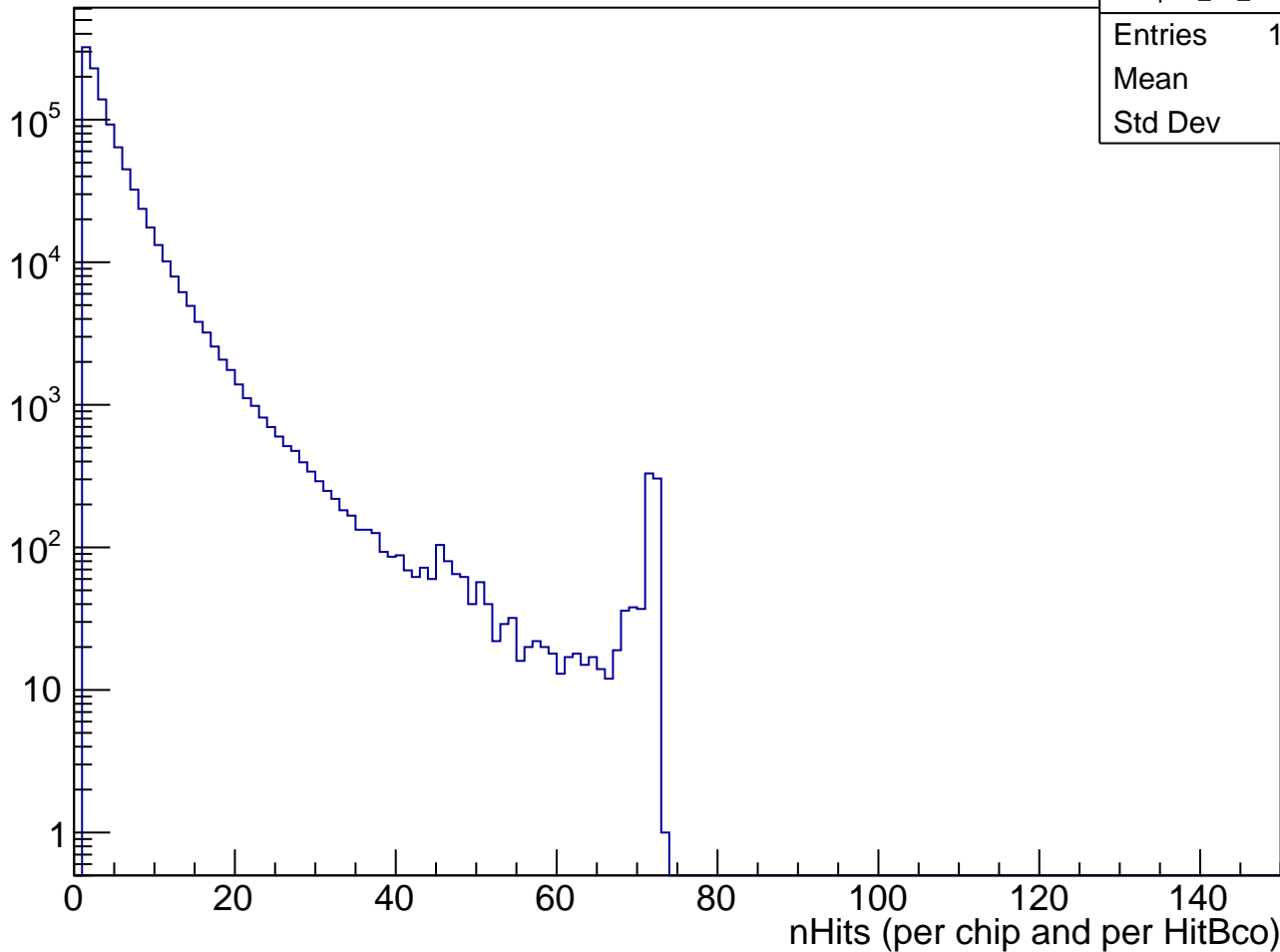
Entries 1012874

Mean 3.659

Std Dev 4.371

nChipHit_F6_Fch9_Chip19

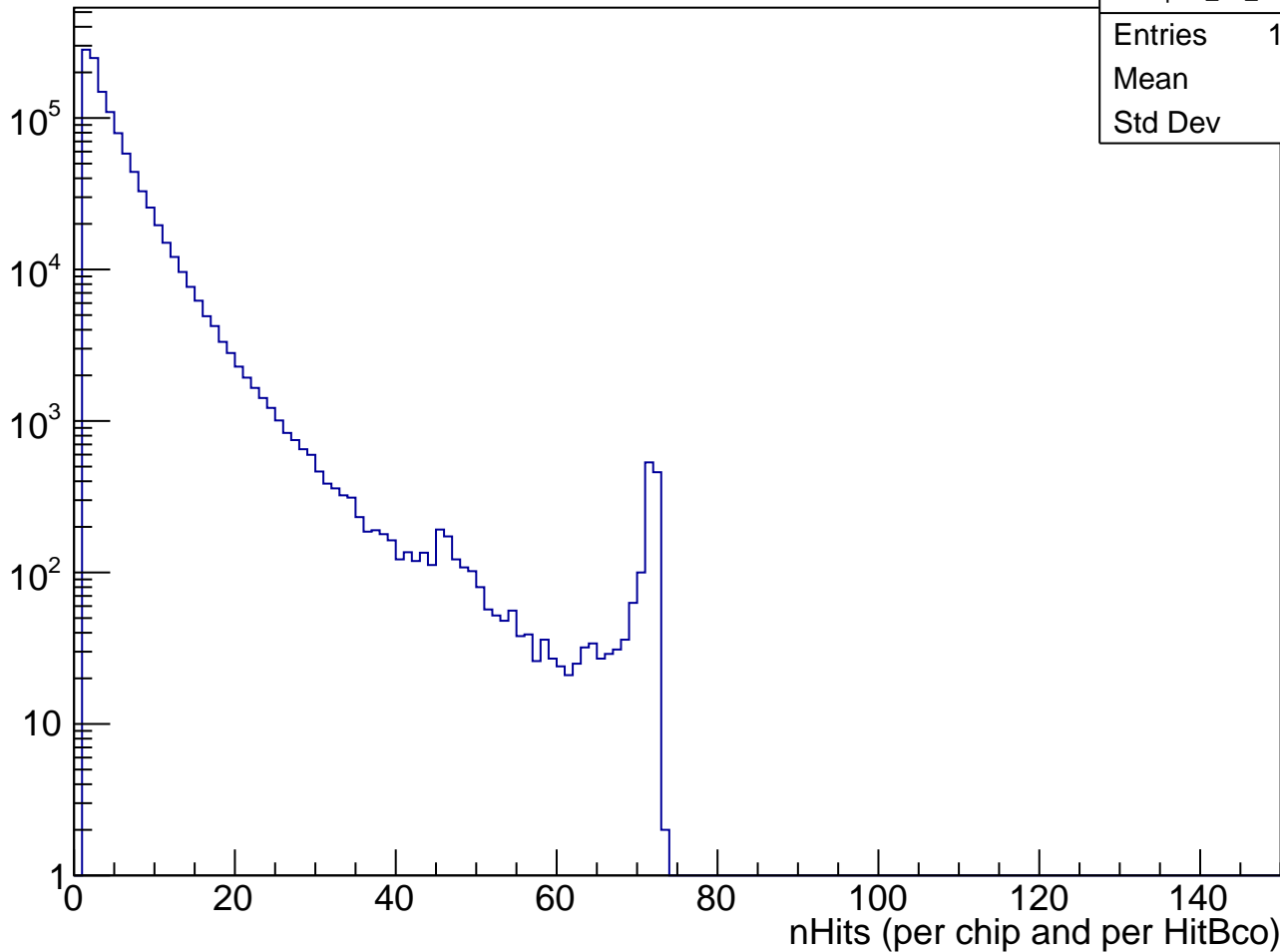
Entries



nChipHit_F6_Fch9_Chip19	
Entries	1031379
Mean	3.658
Std Dev	4.368

nChipHit_F6_Fch9_Chip2

Entries



nChipHit_F6_Fch9_Chip2

Entries 1133467

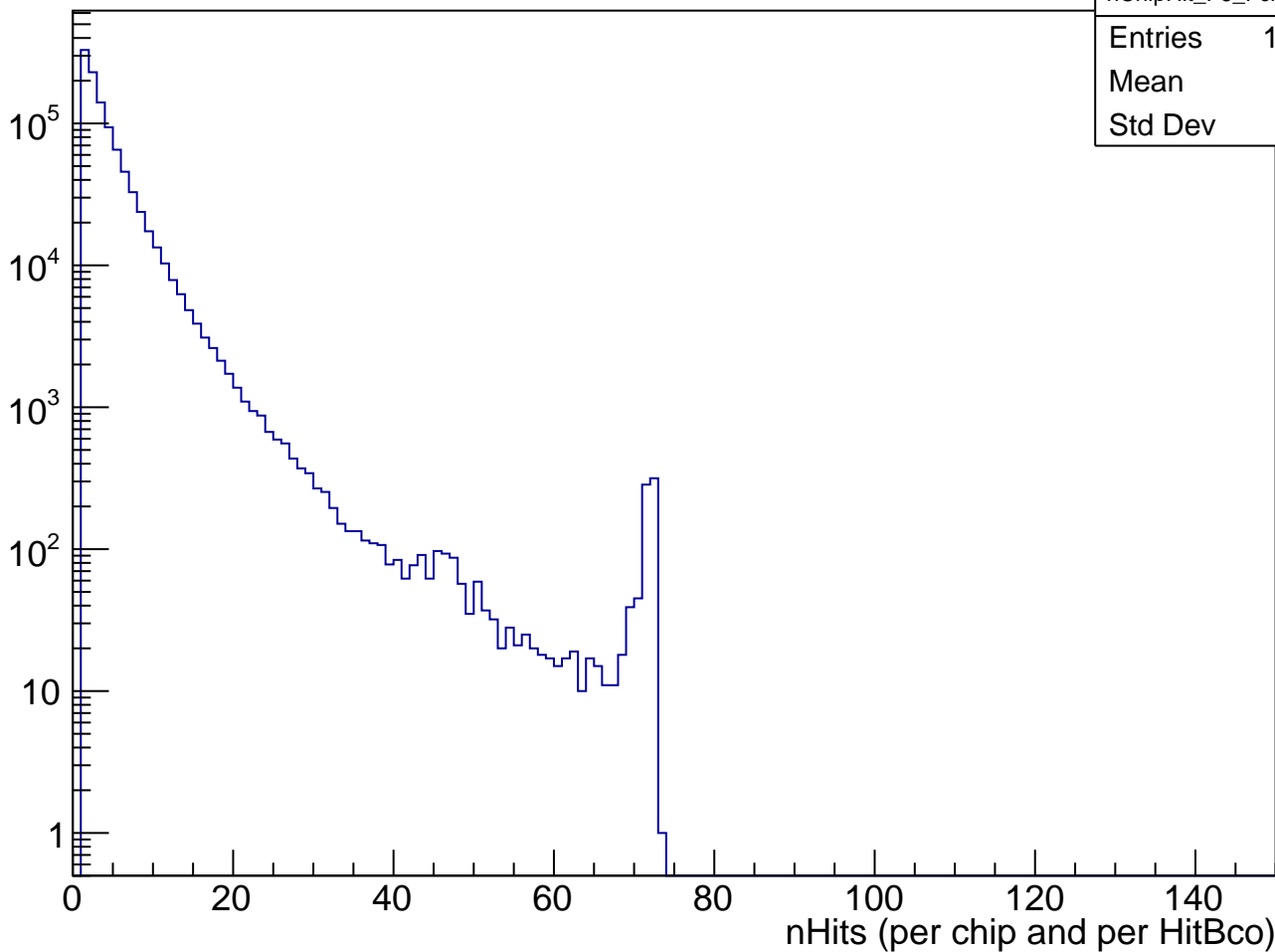
Mean 4.298

Std Dev 5.083

nHits (per chip and per HitBco)

nChipHit_F6_Fch9_Chip20

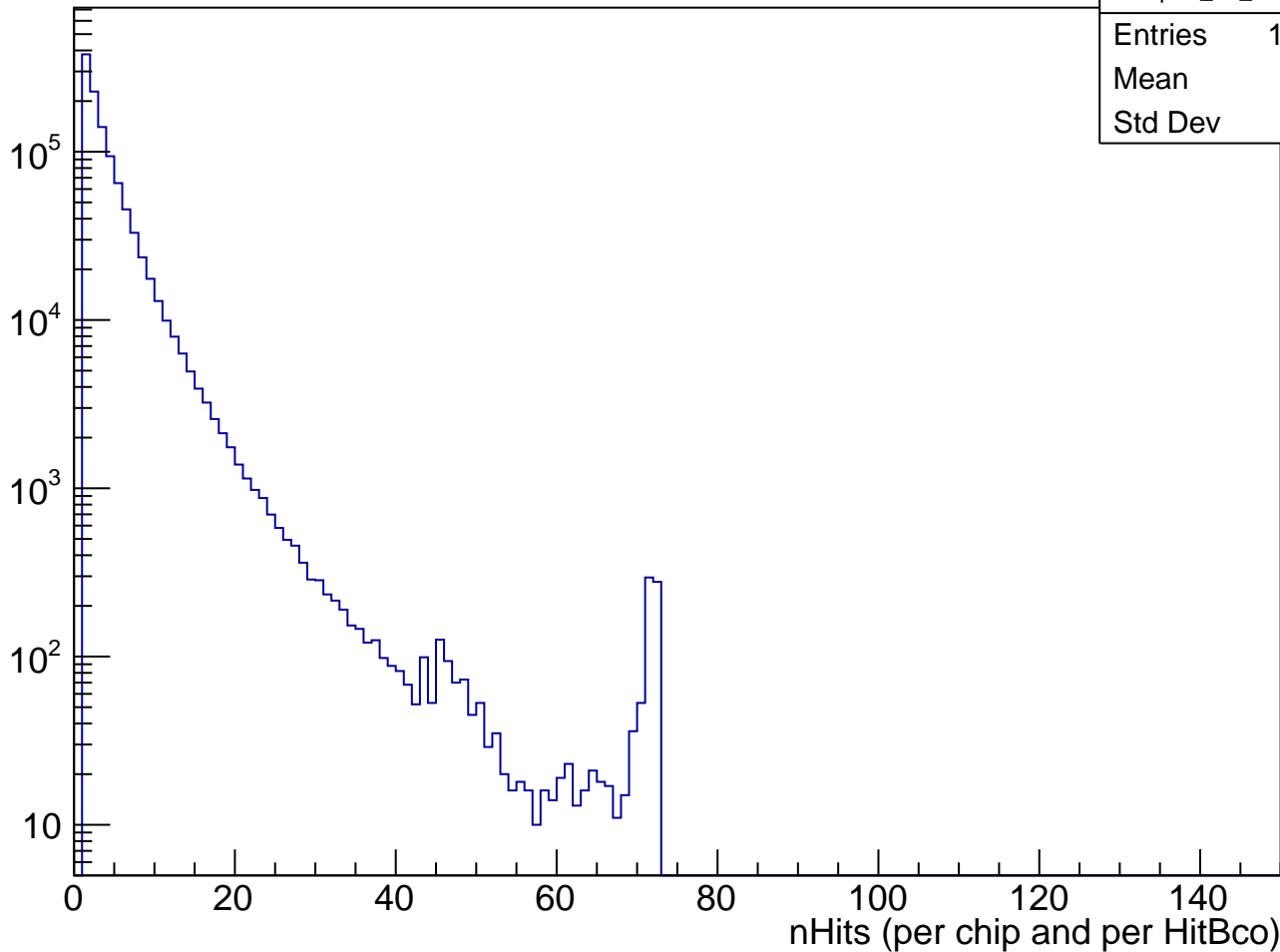
Entries



nChipHit_F6_Fch9_Chip20	
Entries	1044924
Mean	3.638
Std Dev	4.315

nChipHit_F6_Fch9_Chip21

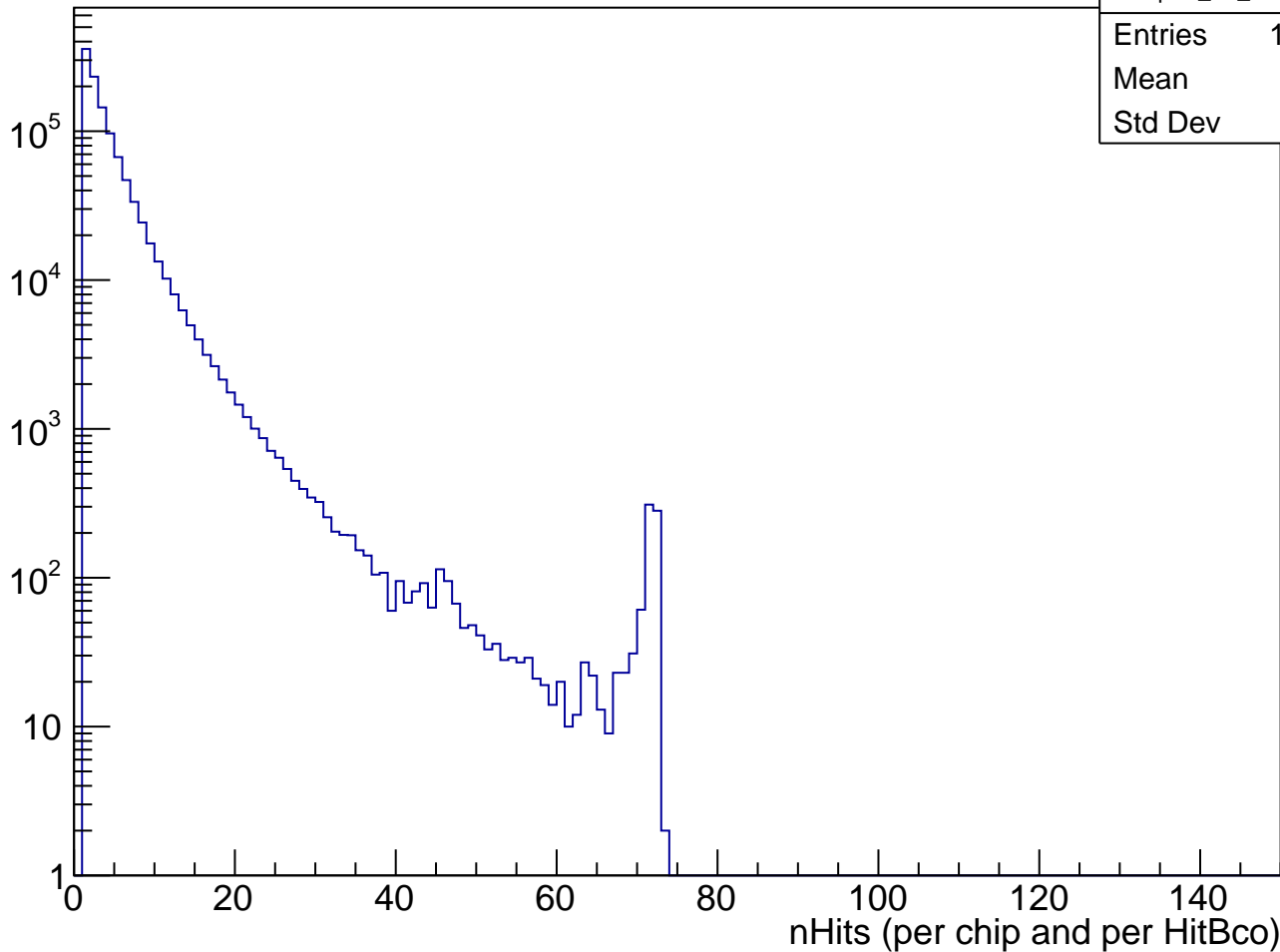
Entries



nChipHit_F6_Fch9_Chip21	
Entries	1092419
Mean	3.52
Std Dev	4.25

nChipHit_F6_Fch9_Chip22

Entries

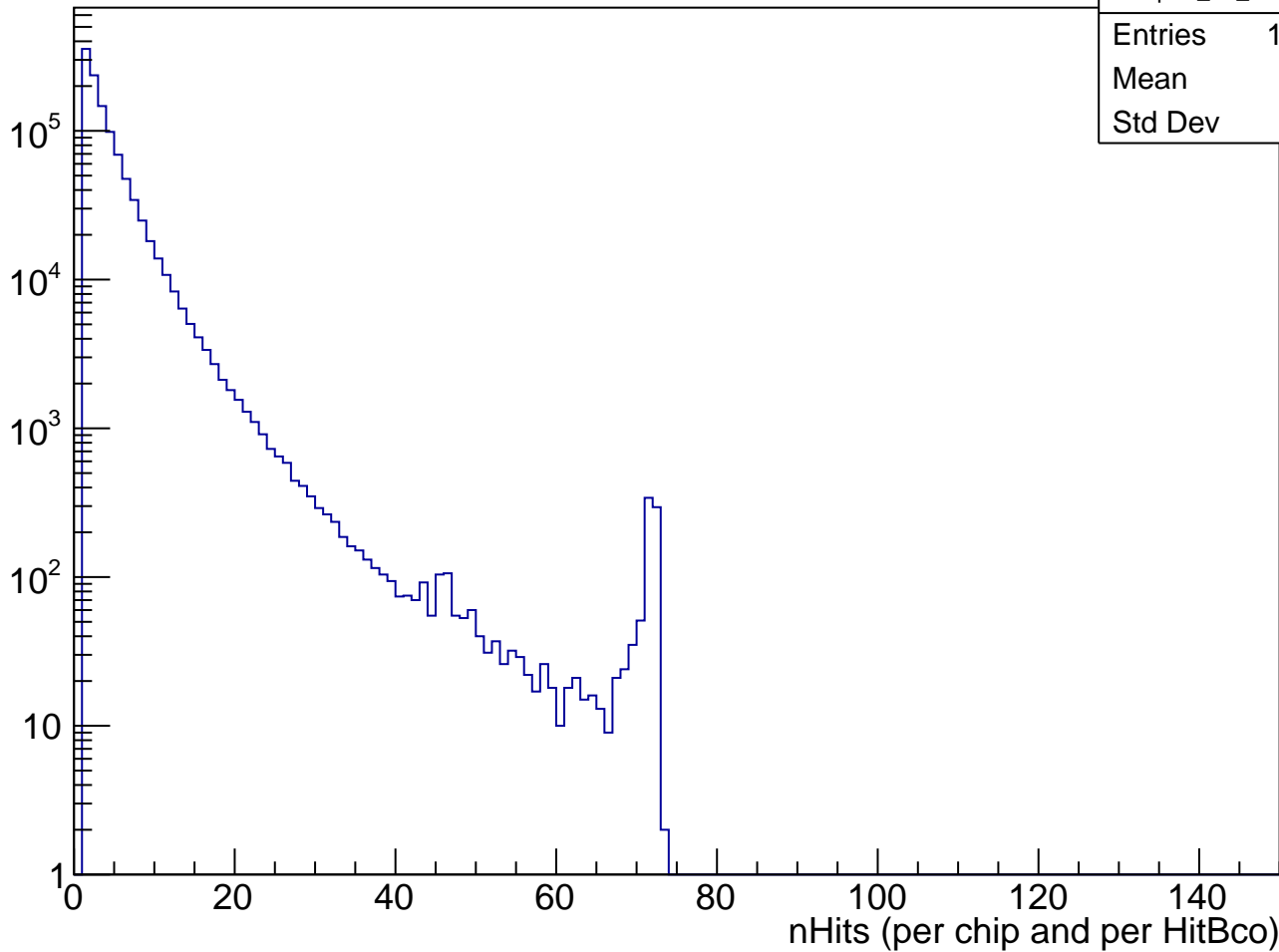


nChipHit_F6_Fch9_Chip22	
Entries	1087573
Mean	3.594
Std Dev	4.306

nHits (per chip and per HitBco)

nChipHit_F6_Fch9_Chip23

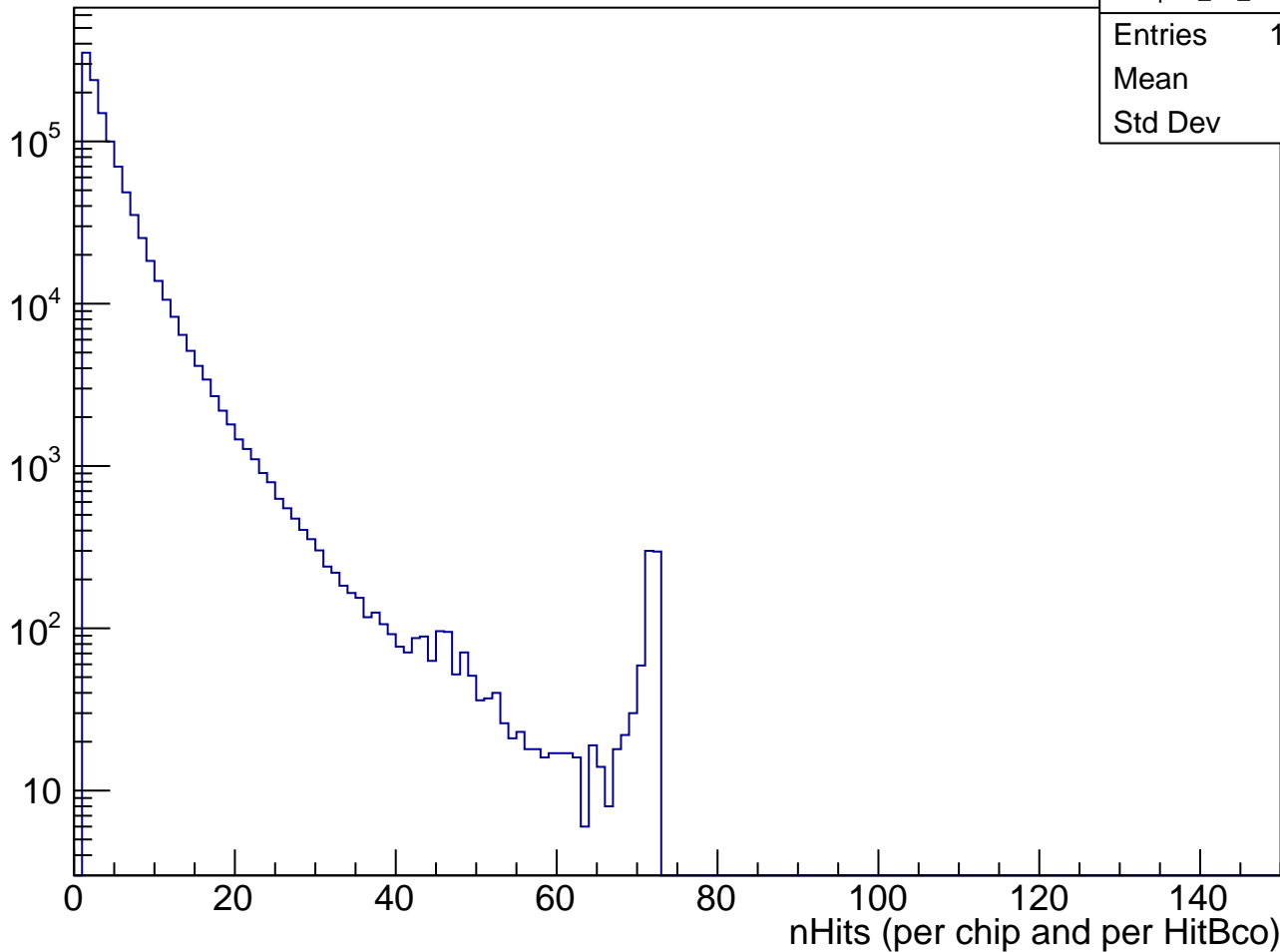
Entries



nChipHit_F6_Fch9_Chip23	
Entries	1101068
Mean	3.626
Std Dev	4.329

nChipHit_F6_Fch9_Chip24

Entries

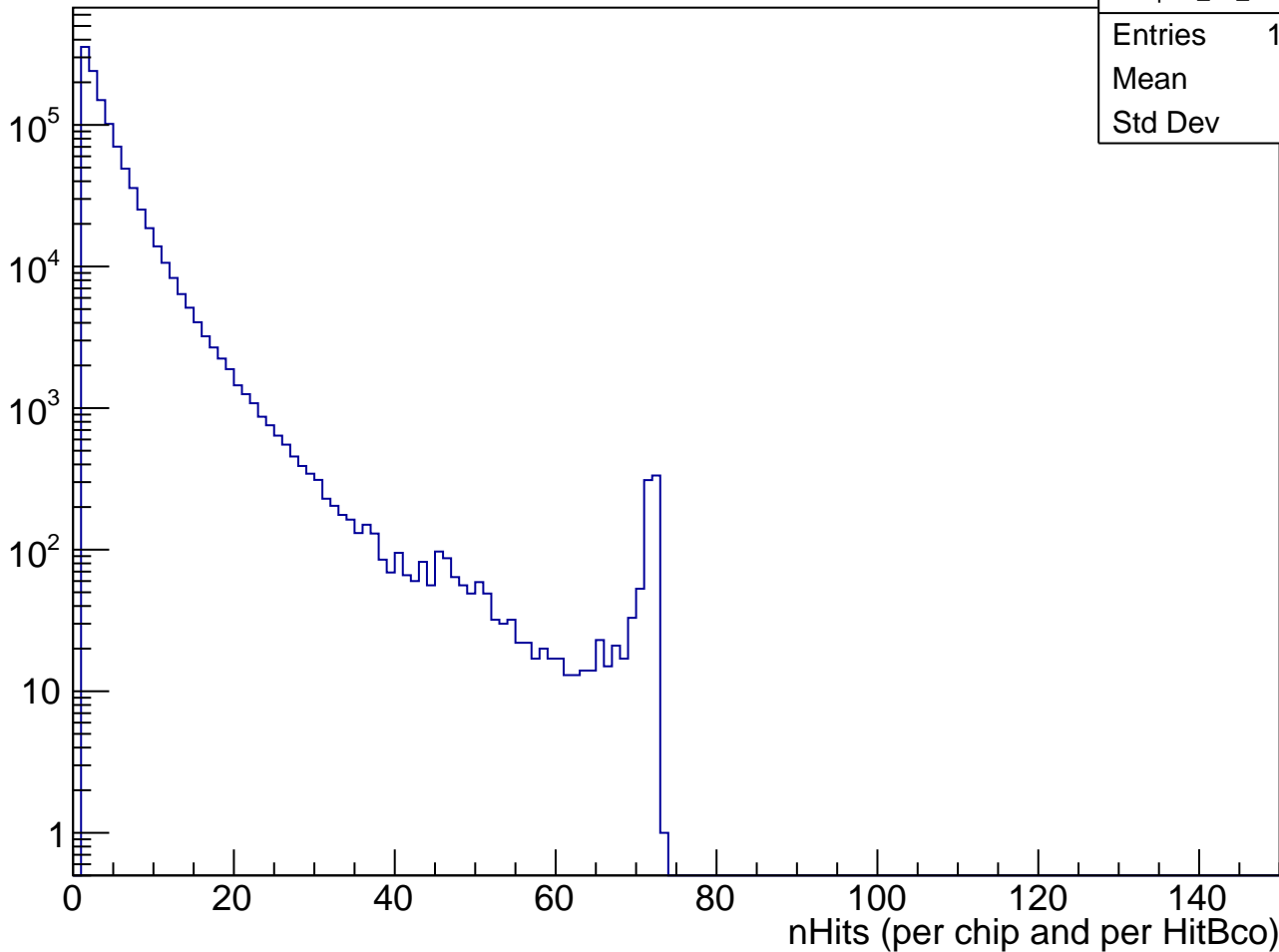


nChipHit_F6_Fch9_Chip24	
Entries	1107133
Mean	3.634
Std Dev	4.288

nHits (per chip and per HitBco)

nChipHit_F6_Fch9_Chip25

Entries



nChipHit_F6_Fch9_Chip25

Entries 1116057

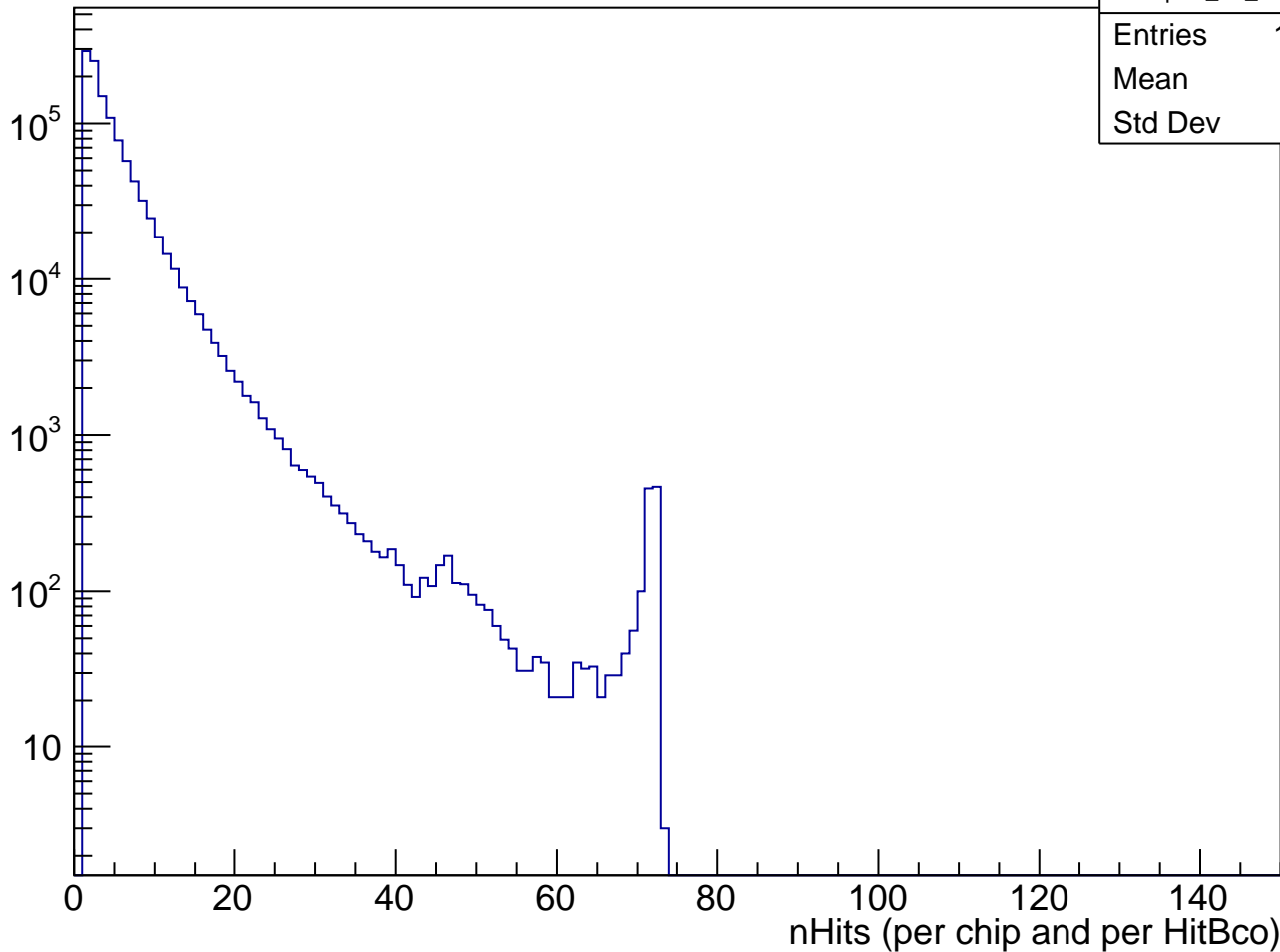
Mean 3.626

Std Dev 4.292

nHits (per chip and per HitBco)

nChipHit_F6_Fch9_Chip3

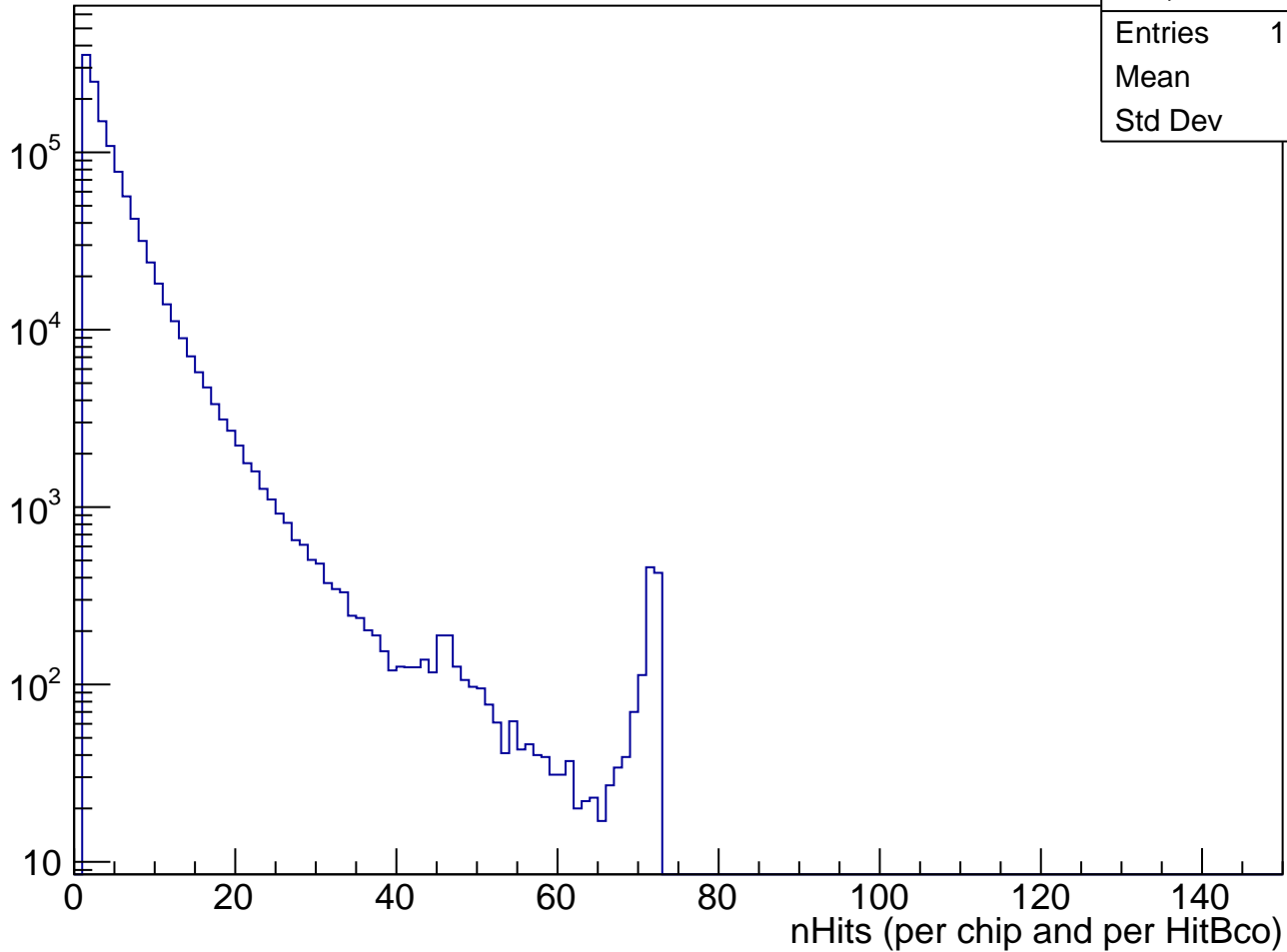
Entries



nChipHit_F6_Fch9_Chip3	
Entries	1133981
Mean	4.198
Std Dev	4.981

nChipHit_F6_Fch9_Chip4

Entries



nChipHit_F6_Fch9_Chip4

Entries 1191719

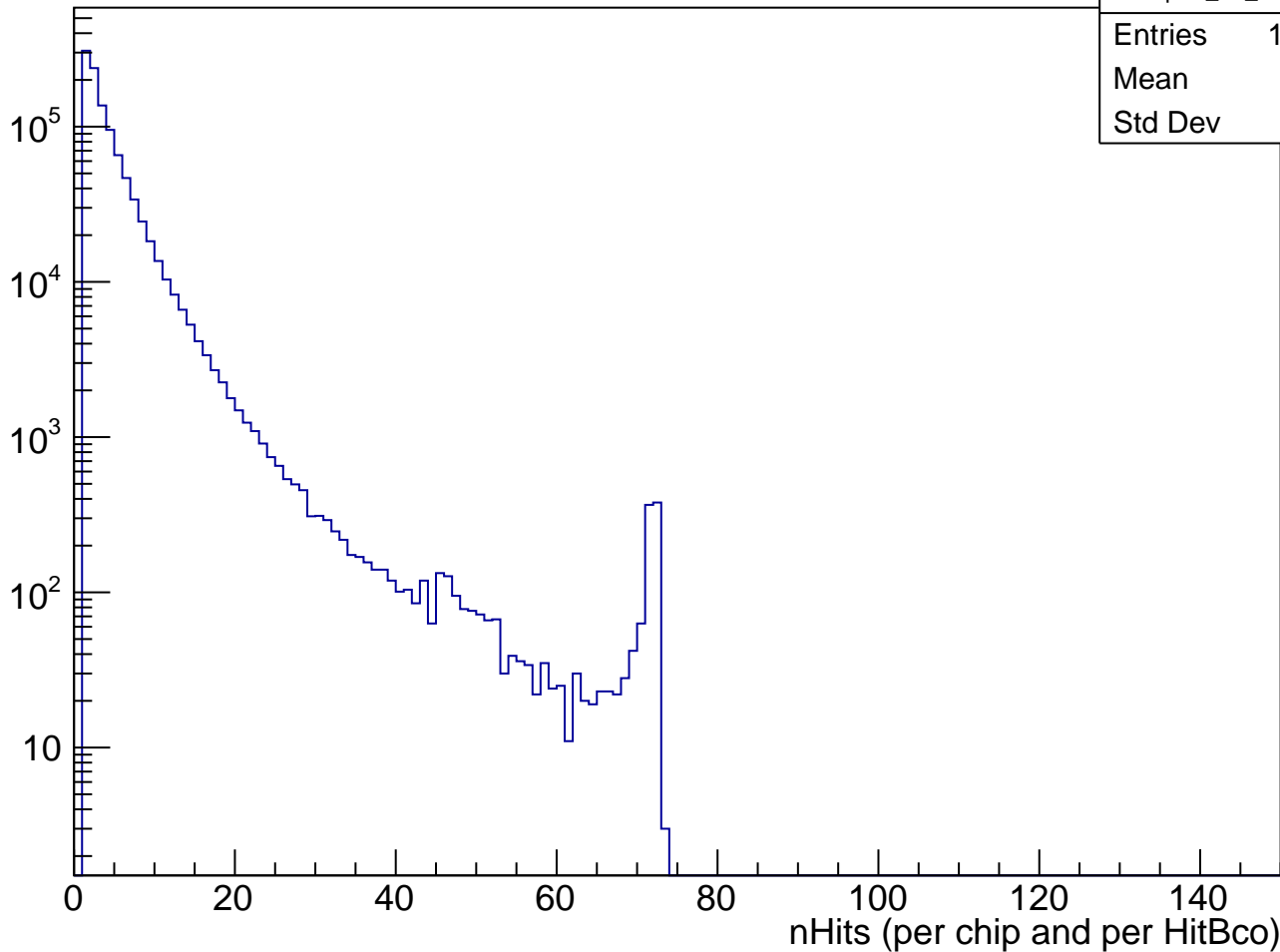
Mean 4.015

Std Dev 4.91

nHits (per chip and per HitBco)

nChipHit_F6_Fch9_Chip5

Entries



nChipHit_F6_Fch9_Chip5

Entries 1038848

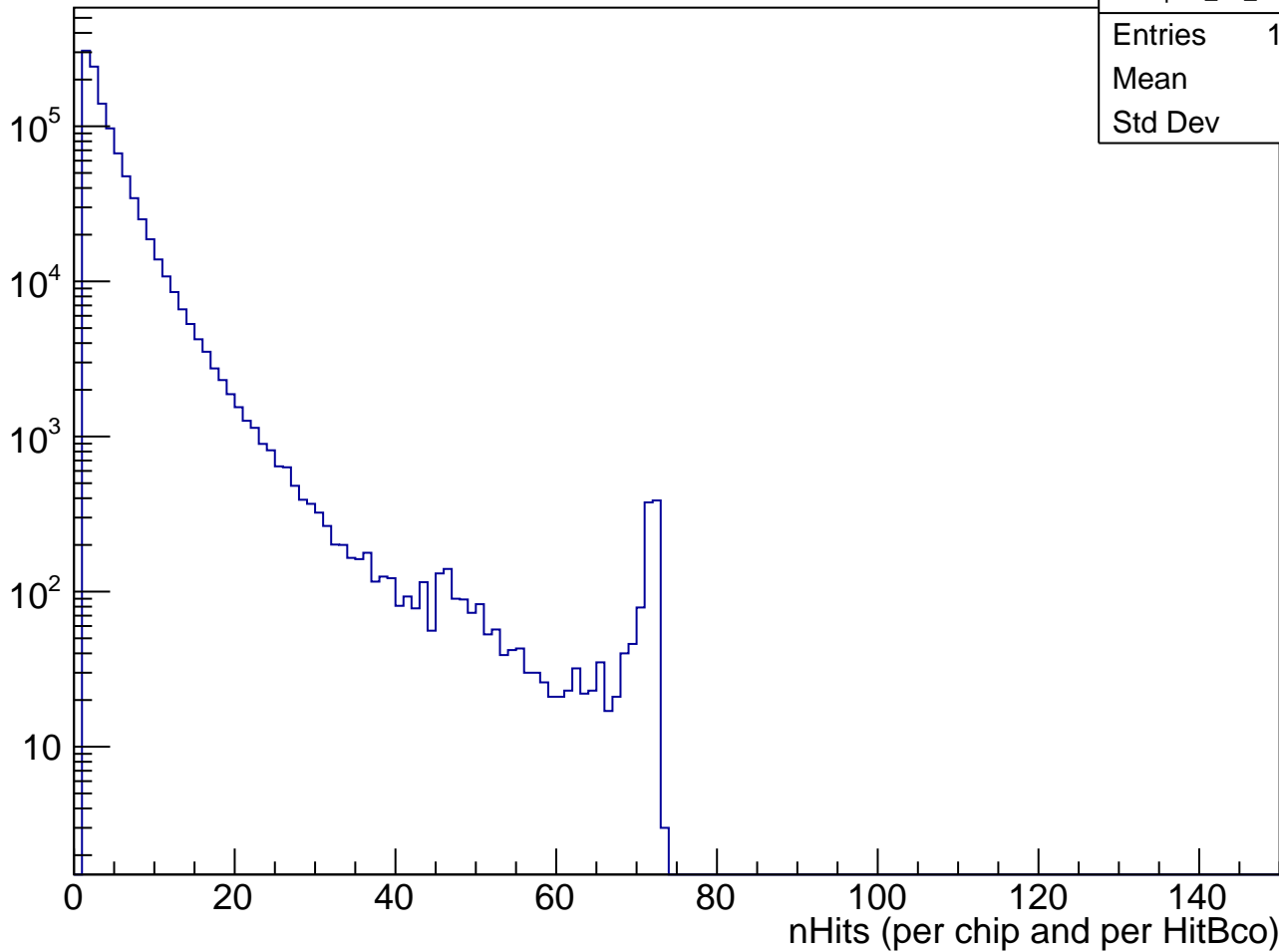
Mean 3.773

Std Dev 4.608

nHits (per chip and per HitBco)

nChipHit_F6_Fch9_Chip6

Entries



nChipHit_F6_Fch9_Chip6

Entries 1051184

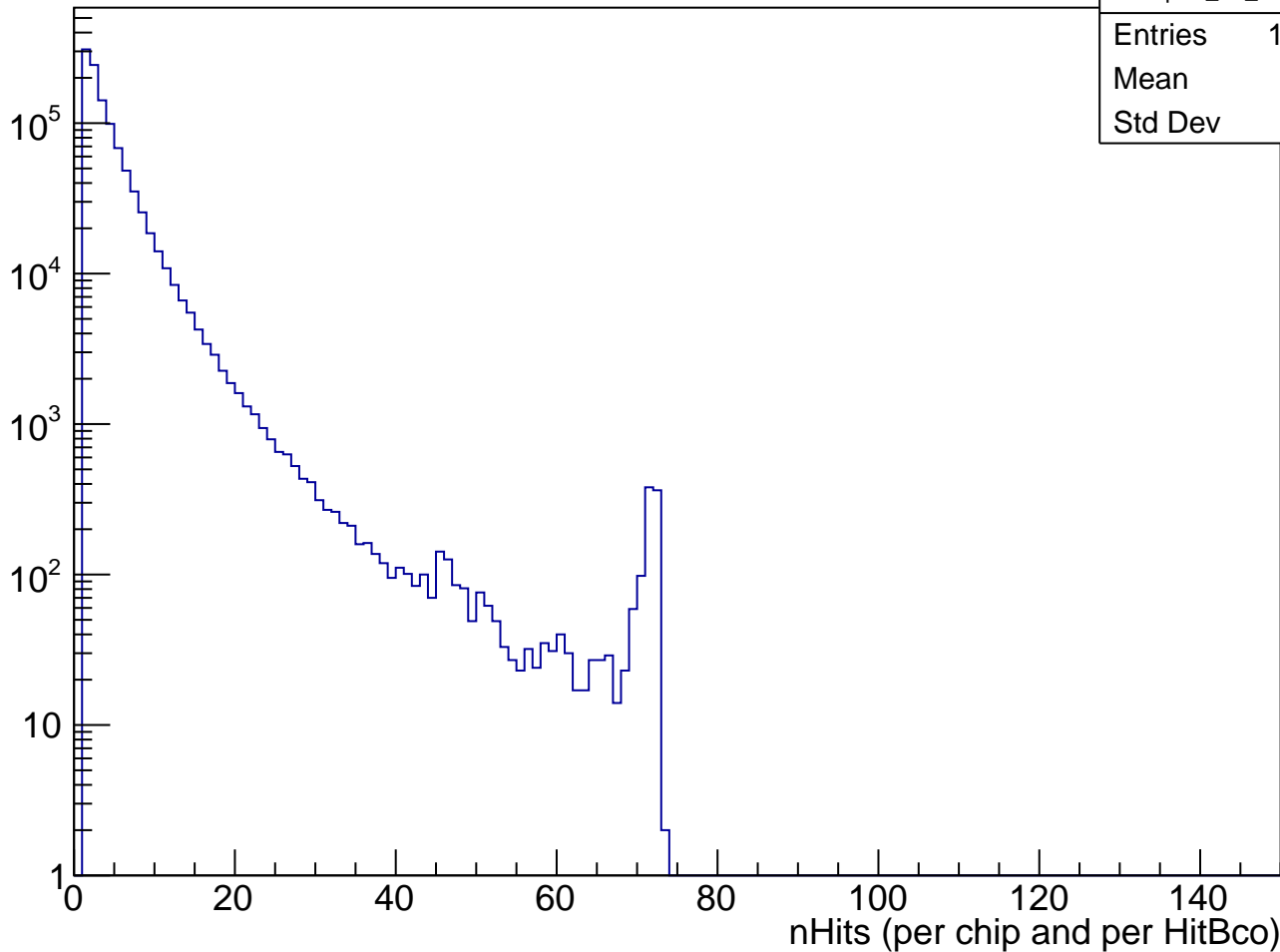
Mean 3.793

Std Dev 4.623

nHits (per chip and per HitBco)

nChipHit_F6_Fch9_Chip7

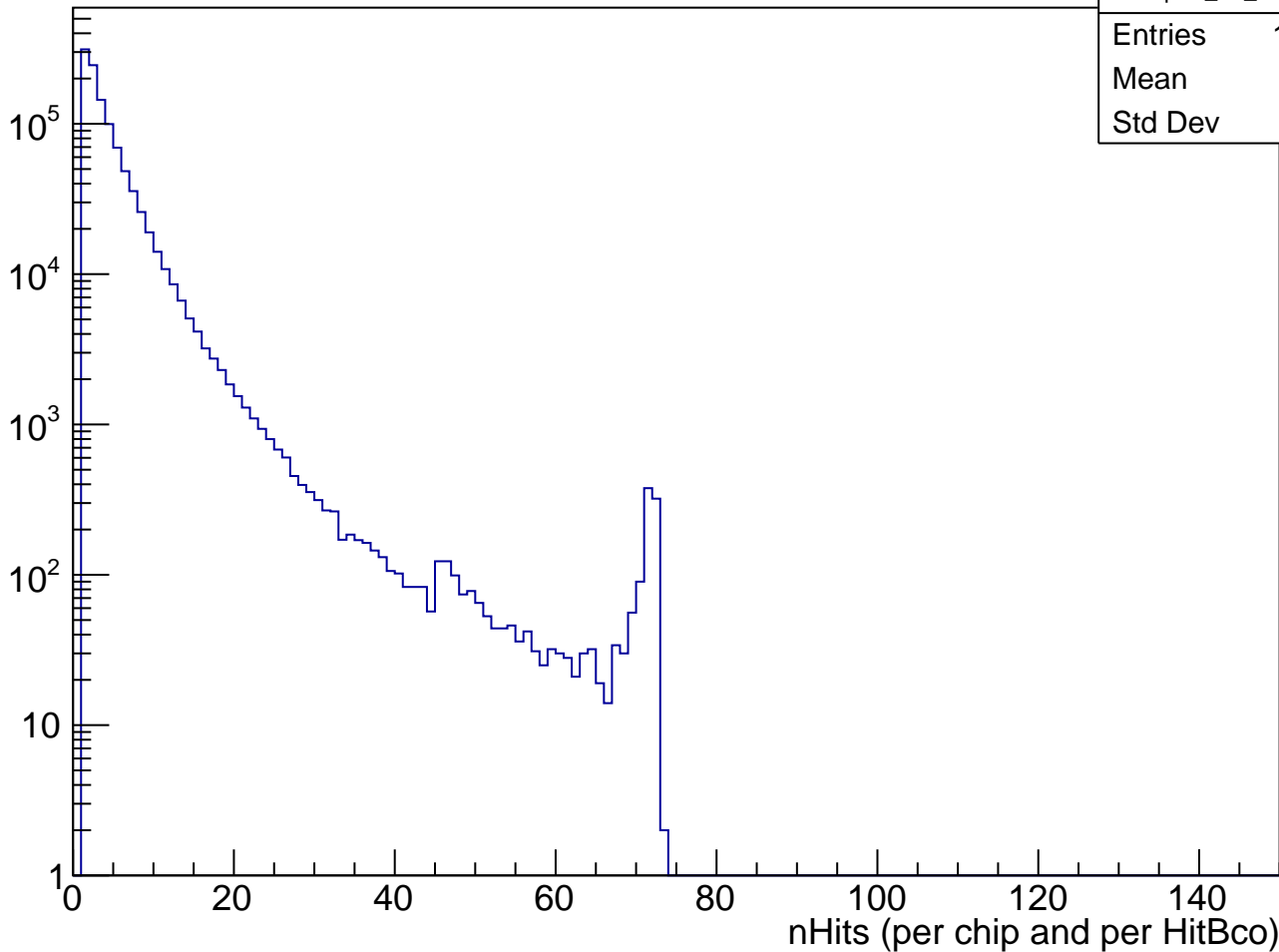
Entries



nChipHit_F6_Fch9_Chip7	
Entries	1062167
Mean	3.8
Std Dev	4.614

nChipHit_F6_Fch9_Chip8

Entries



nChipHit_F6_Fch9_Chip8

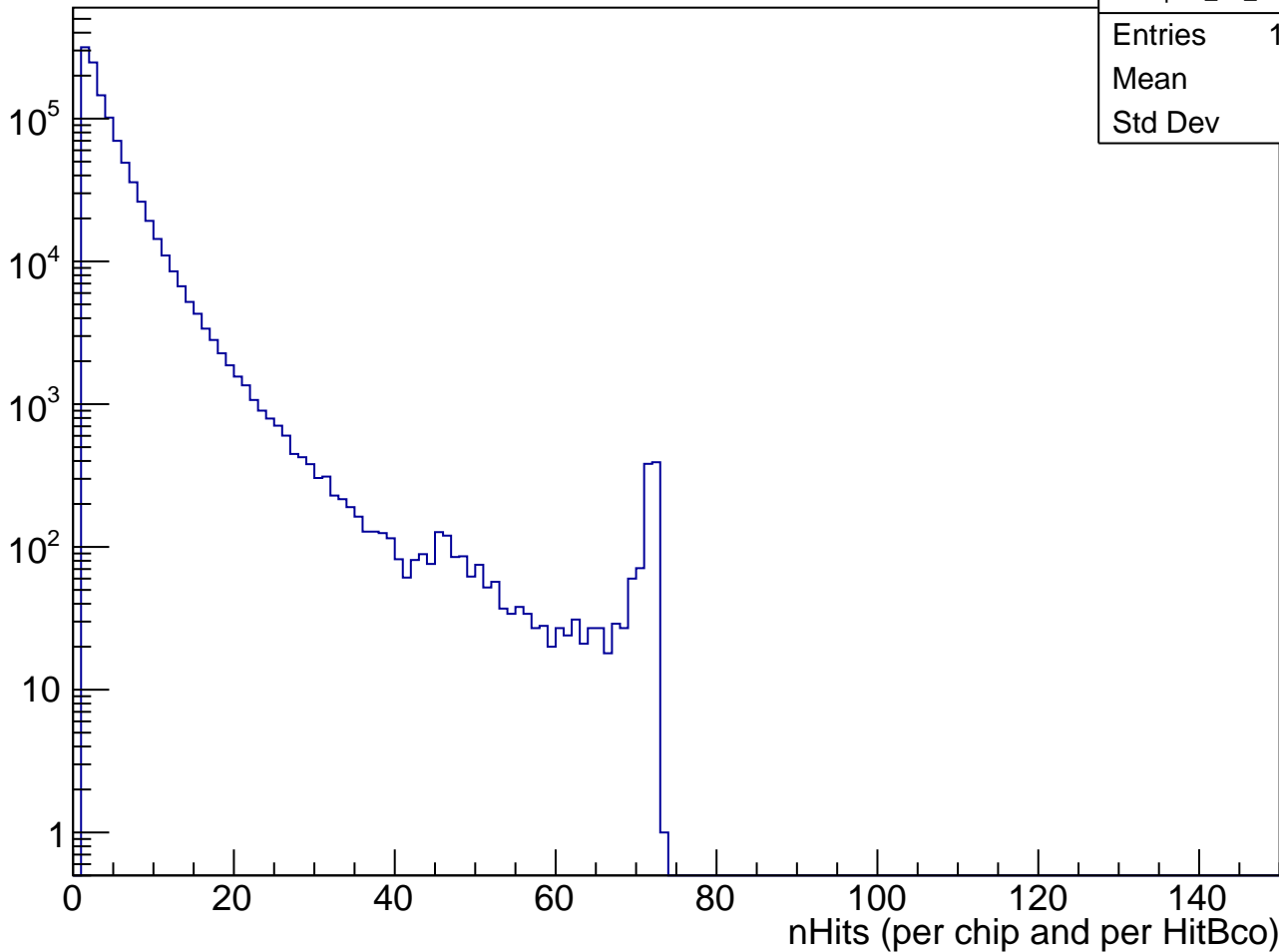
Entries 1072191

Mean 3.775

Std Dev 4.555

nChipHit_F6_Fch9_Chip9

Entries

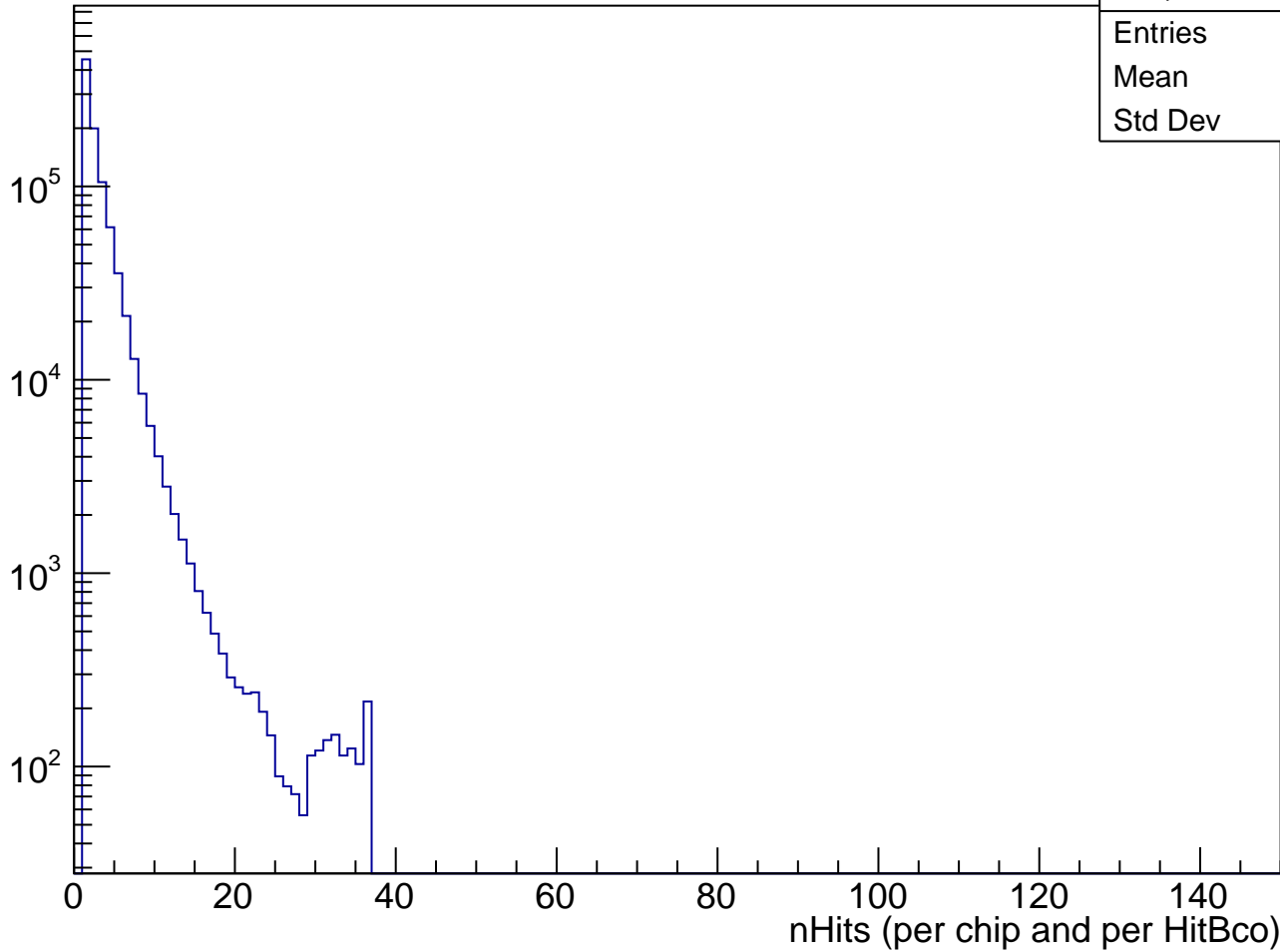


nChipHit_F6_Fch9_Chip9

Entries	1084273
Mean	3.779
Std Dev	4.562

nChipHit_F7_Fch0_Chip0

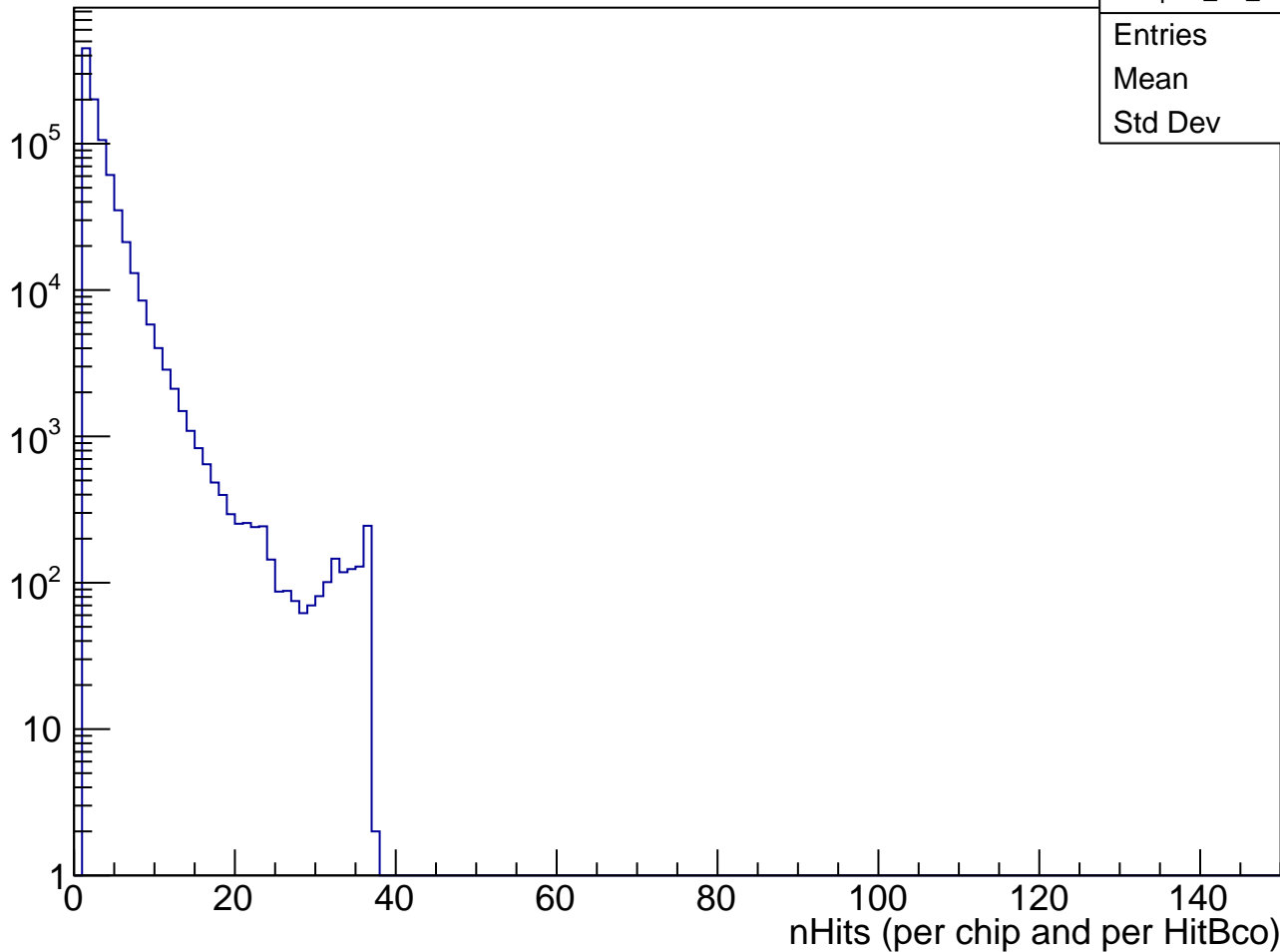
Entries



nChipHit_F7_Fch0_Chip0	
Entries	921053
Mean	2.356
Std Dev	2.434

nChipHit_F7_Fch0_Chip1

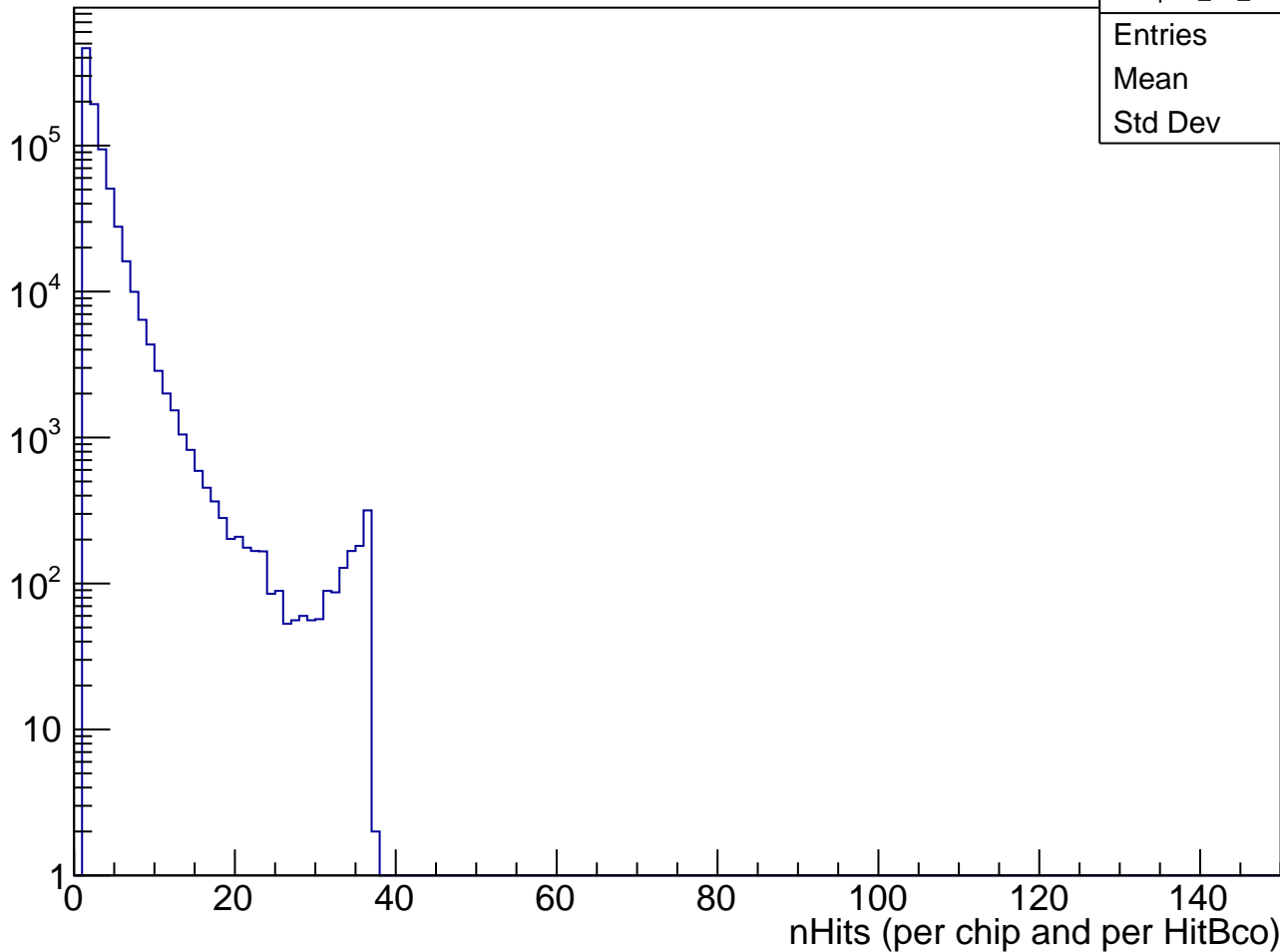
Entries



nChipHit_F7_Fch0_Chip1	
Entries	916932
Mean	2.366
Std Dev	2.444

nChipHit_F7_Fch0_Chip10

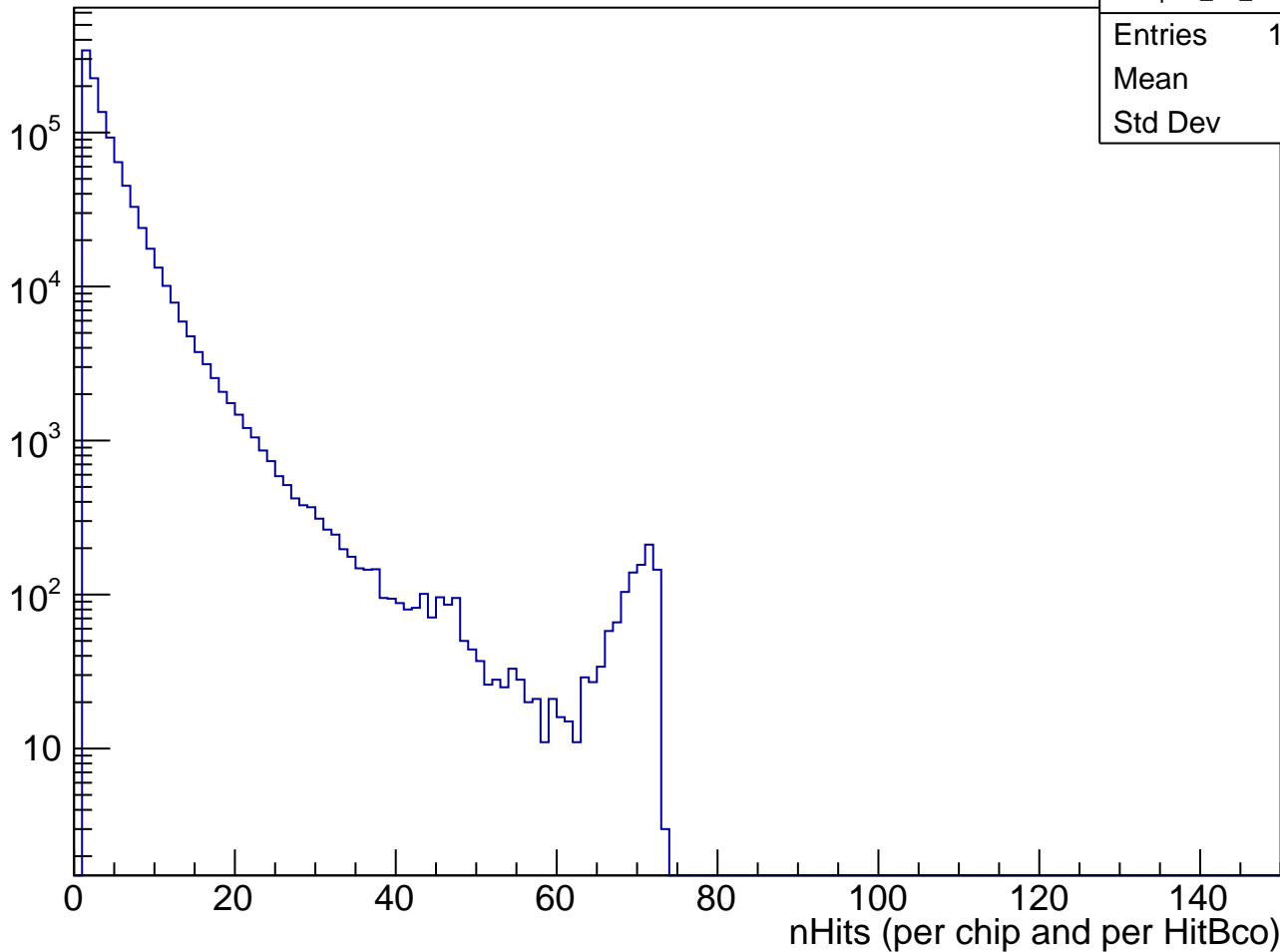
Entries



nChipHit_F7_Fch0_Chip10	
Entries	879147
Mean	2.181
Std Dev	2.298

nChipHit_F7_Fch0_Chip11

Entries



nChipHit_F7_Fch0_Chip11

Entries 1045795

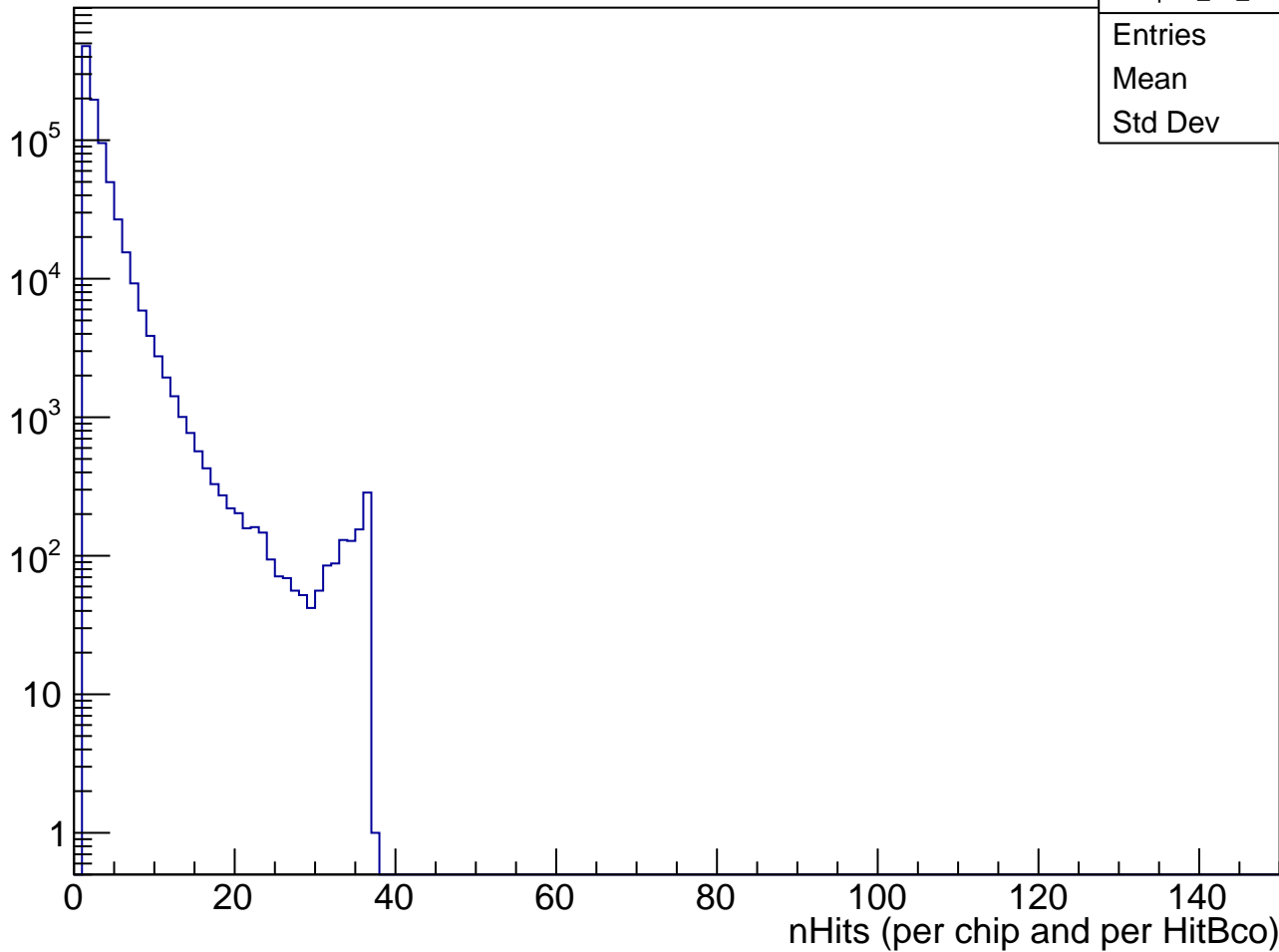
Mean 3.634

Std Dev 4.43

nHits (per chip and per HitBco)

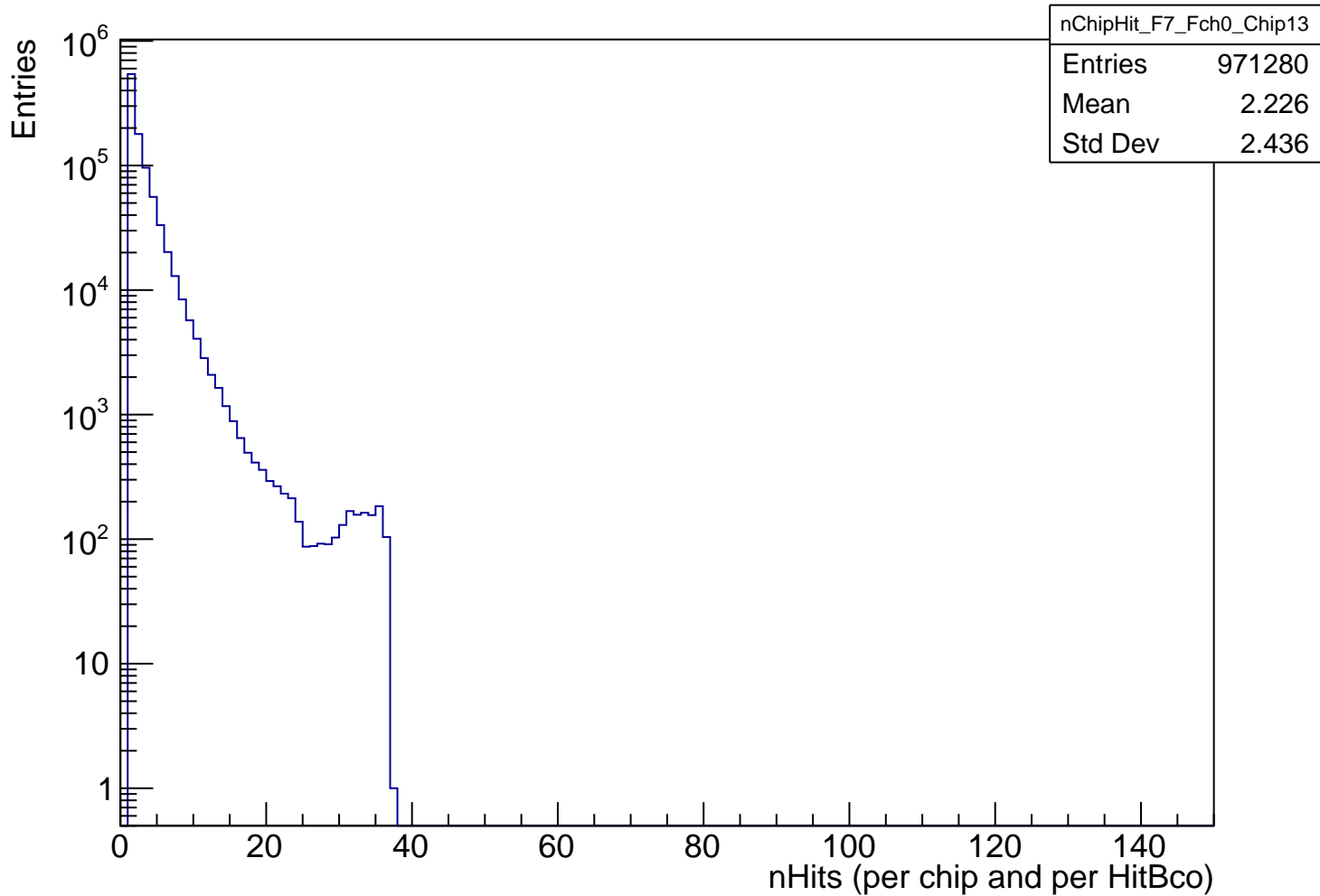
nChipHit_F7_Fch0_Chip12

Entries



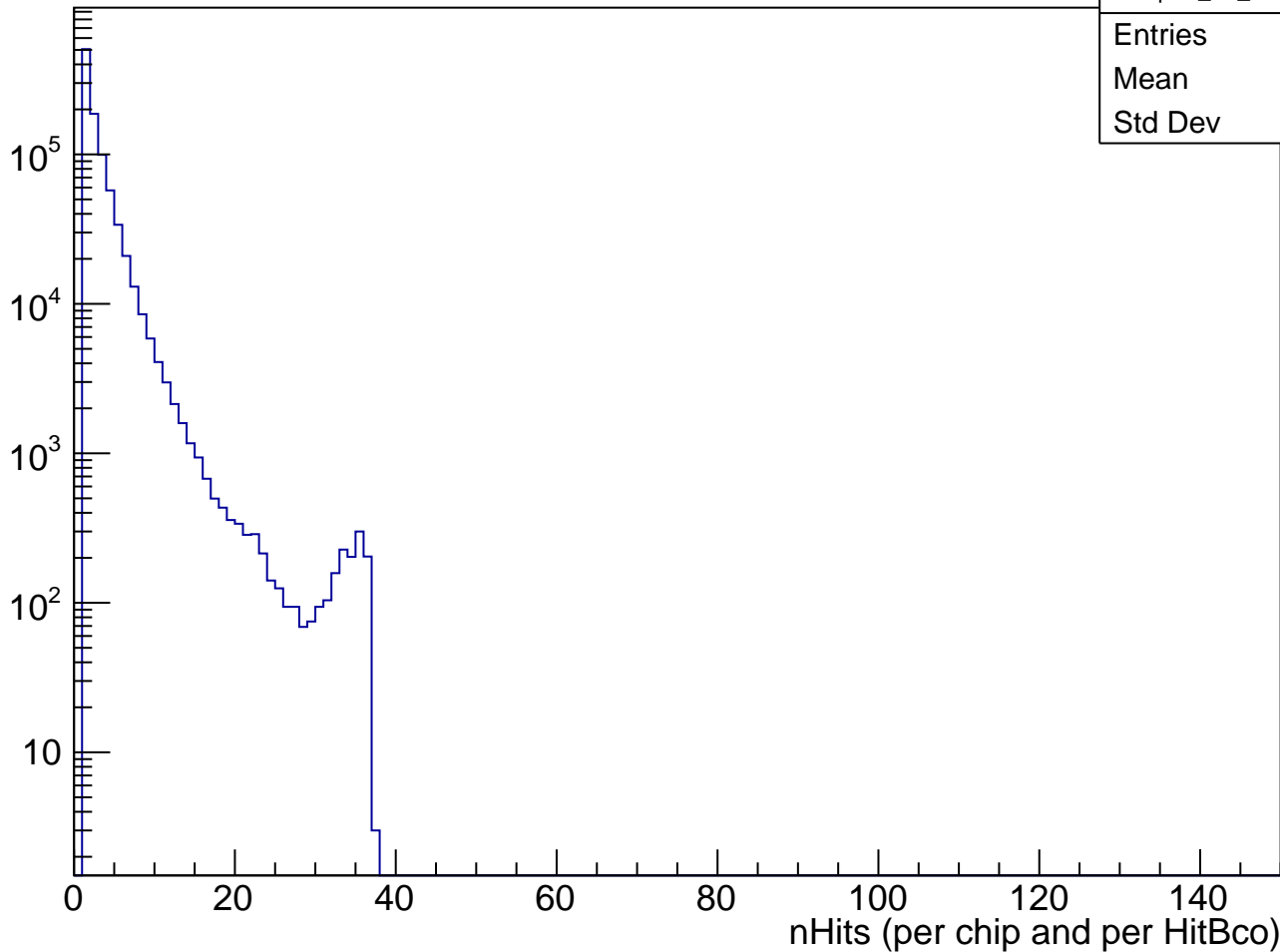
nChipHit_F7_Fch0_Chip12	
Entries	892164
Mean	2.135
Std Dev	2.221

nChipHit_F7_Fch0_Chip13



nChipHit_F7_Fch0_Chip14

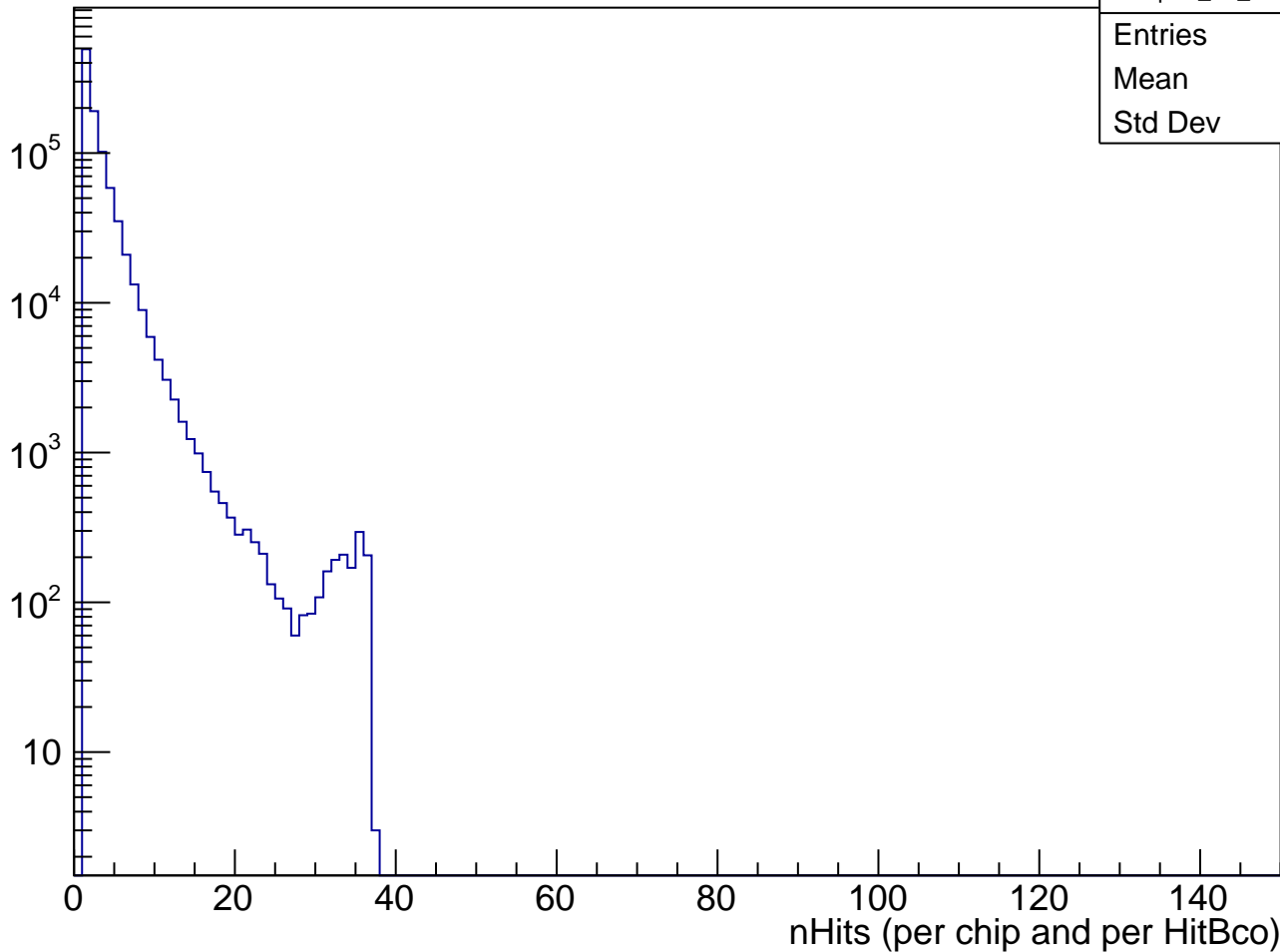
Entries



nChipHit_F7_Fch0_Chip14	
Entries	949589
Mean	2.297
Std Dev	2.528

nChipHit_F7_Fch0_Chip15

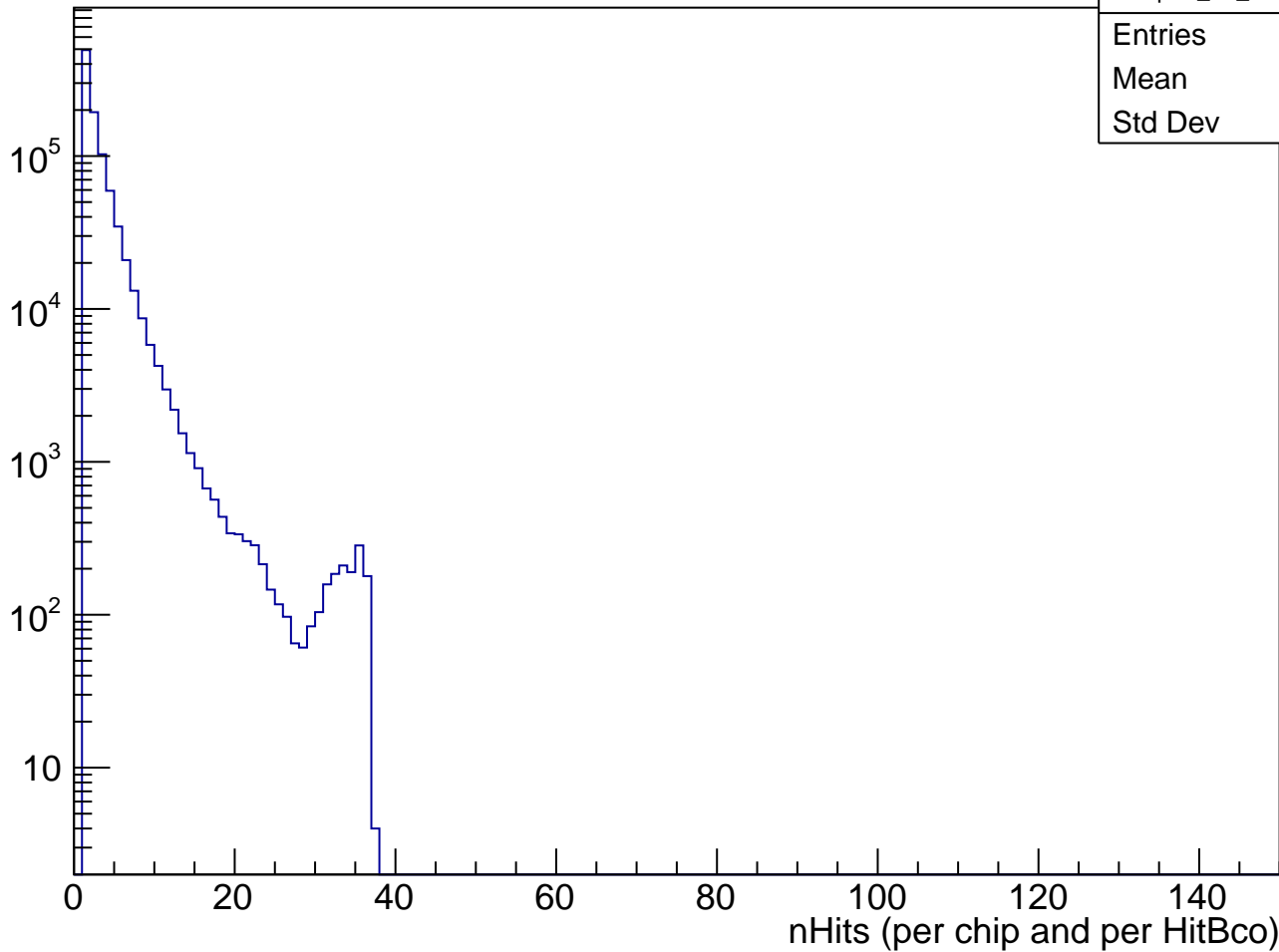
Entries



nChipHit_F7_Fch0_Chip15	
Entries	947319
Mean	2.33
Std Dev	2.547

nChipHit_F7_Fch0_Chip16

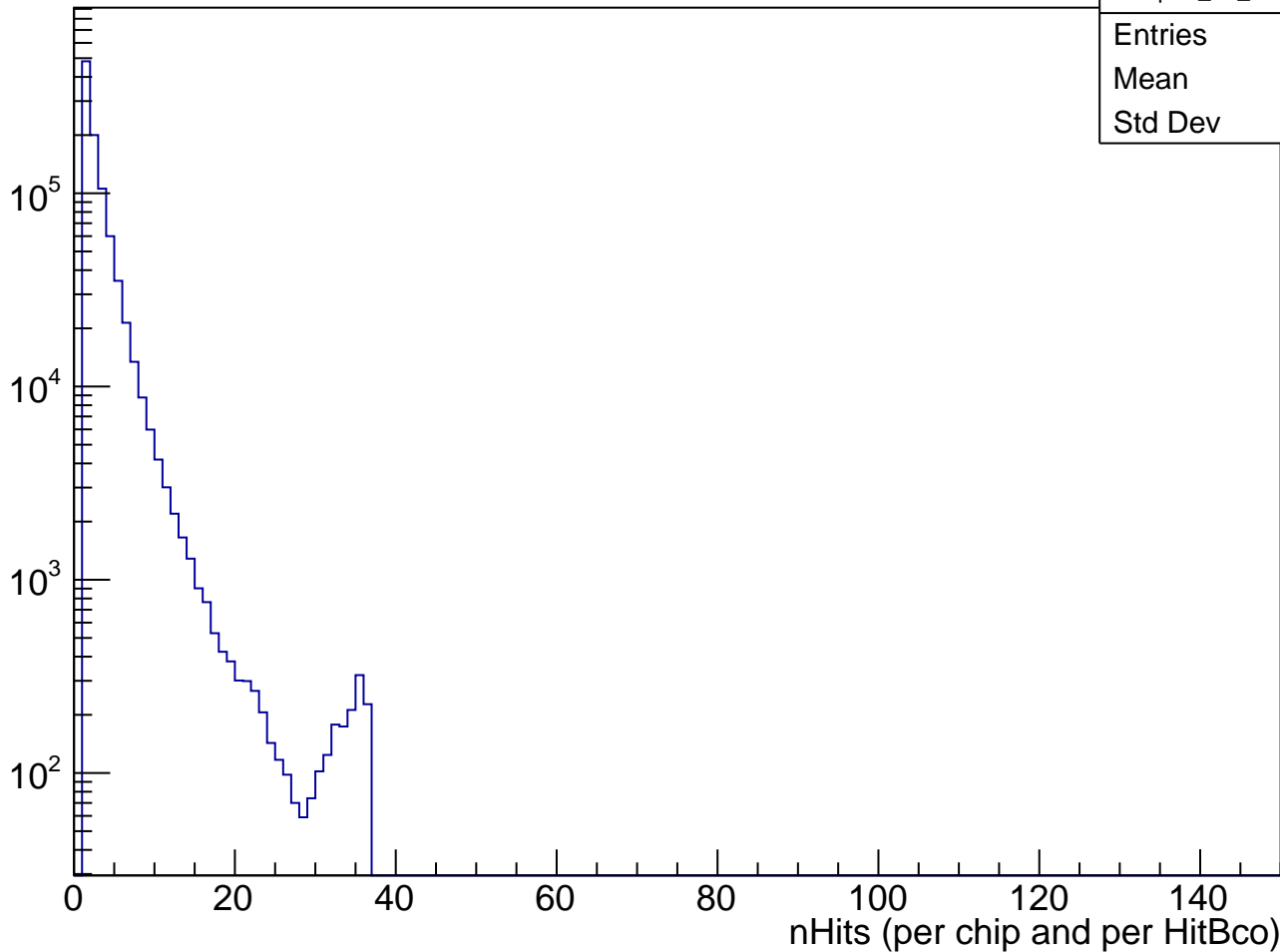
Entries



nChipHit_F7_Fch0_Chip16	
Entries	950080
Mean	2.323
Std Dev	2.527

nChipHit_F7_Fch0_Chip17

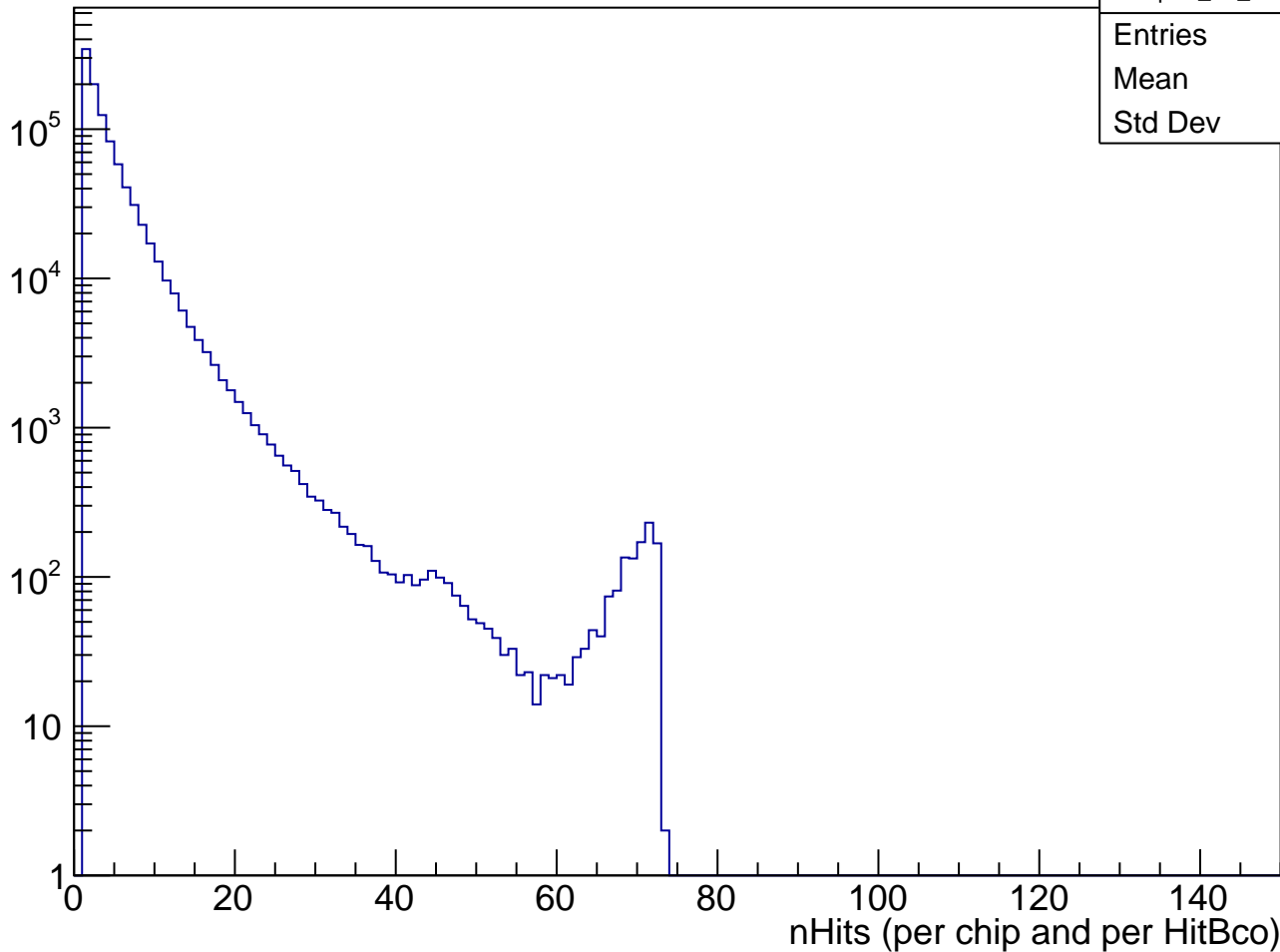
Entries



nChipHit_F7_Fch0_Chip17	
Entries	950816
Mean	2.35
Std Dev	2.541

nChipHit_F7_Fch0_Chip18

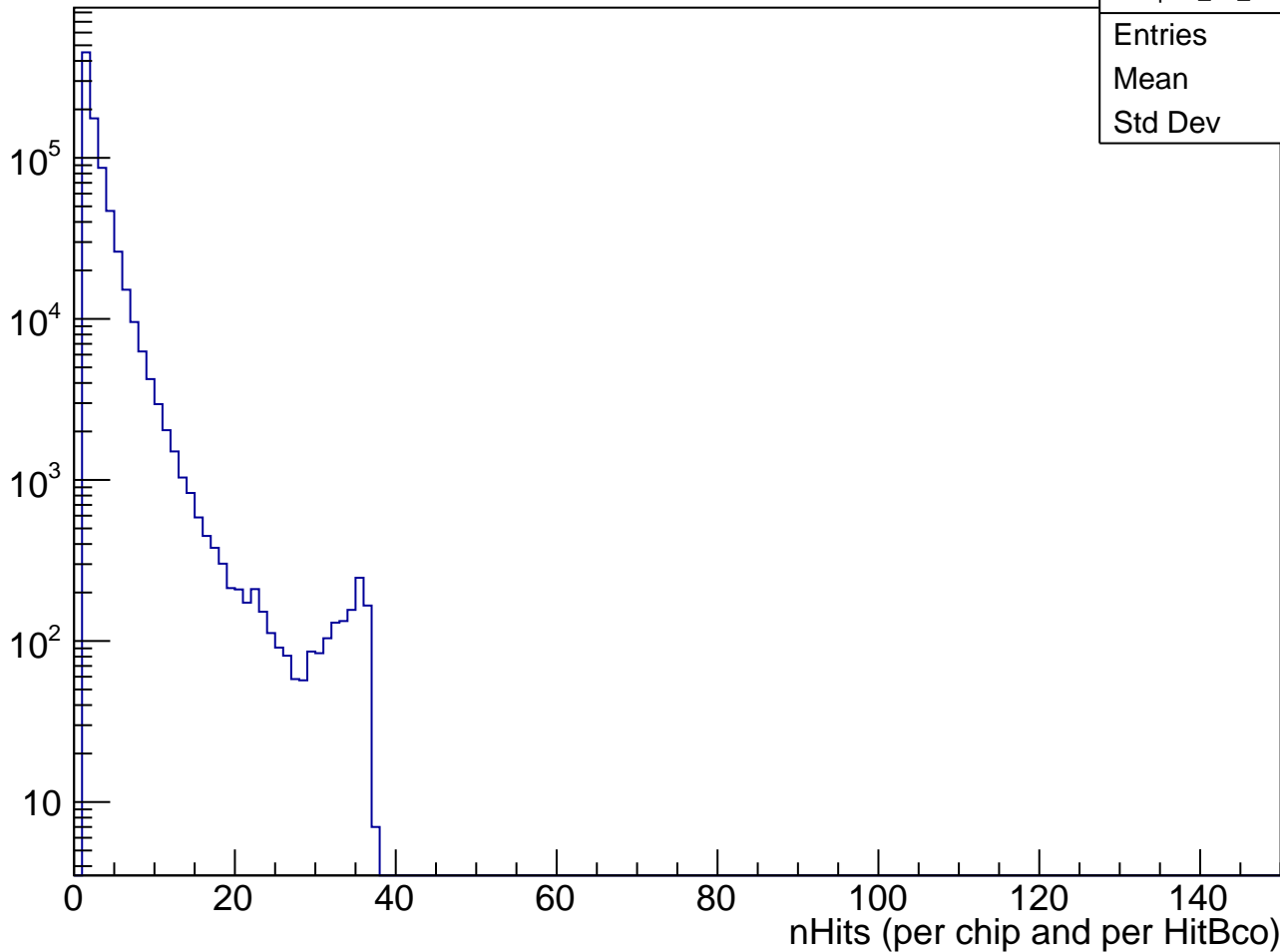
Entries



nChipHit_F7_Fch0_Chip18	
Entries	988659
Mean	3.668
Std Dev	4.673

nChipHit_F7_Fch0_Chip19

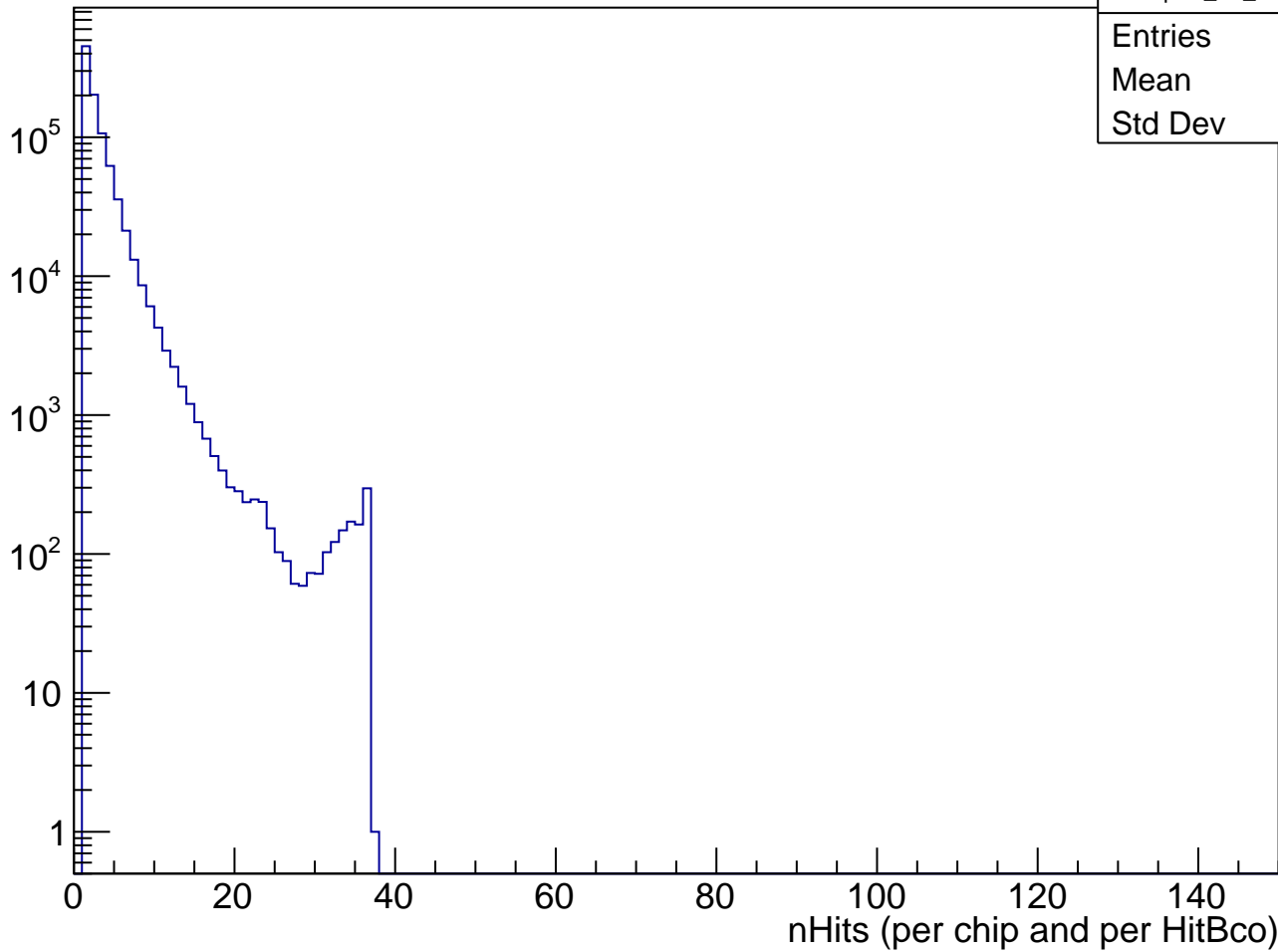
Entries



nChipHit_F7_Fch0_Chip19	
Entries	835479
Mean	2.177
Std Dev	2.348

nChipHit_F7_Fch0_Chip2

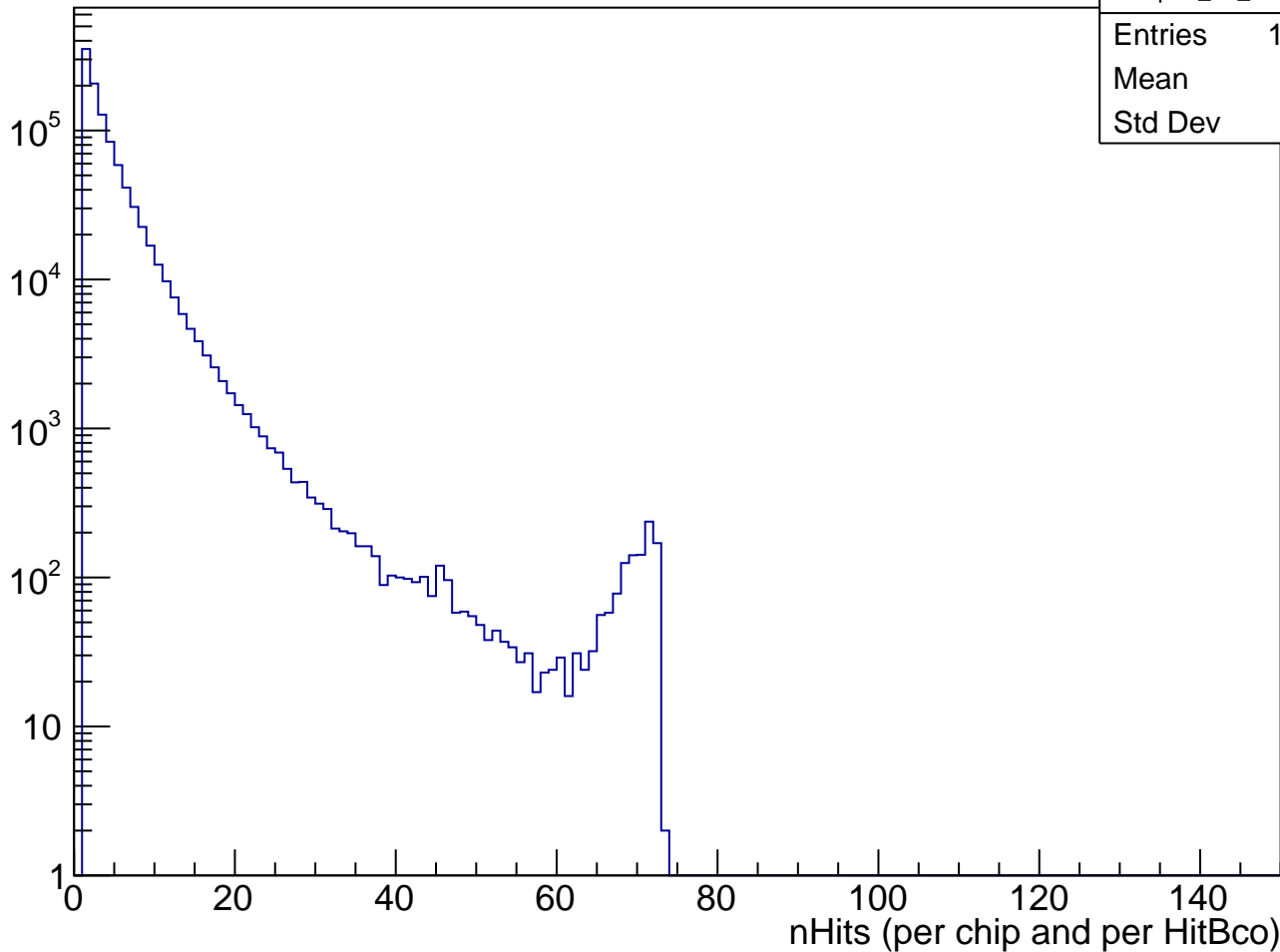
Entries



nChipHit_F7_Fch0_Chip2	
Entries	925951
Mean	2.38
Std Dev	2.489

nChipHit_F7_Fch0_Chip20

Entries

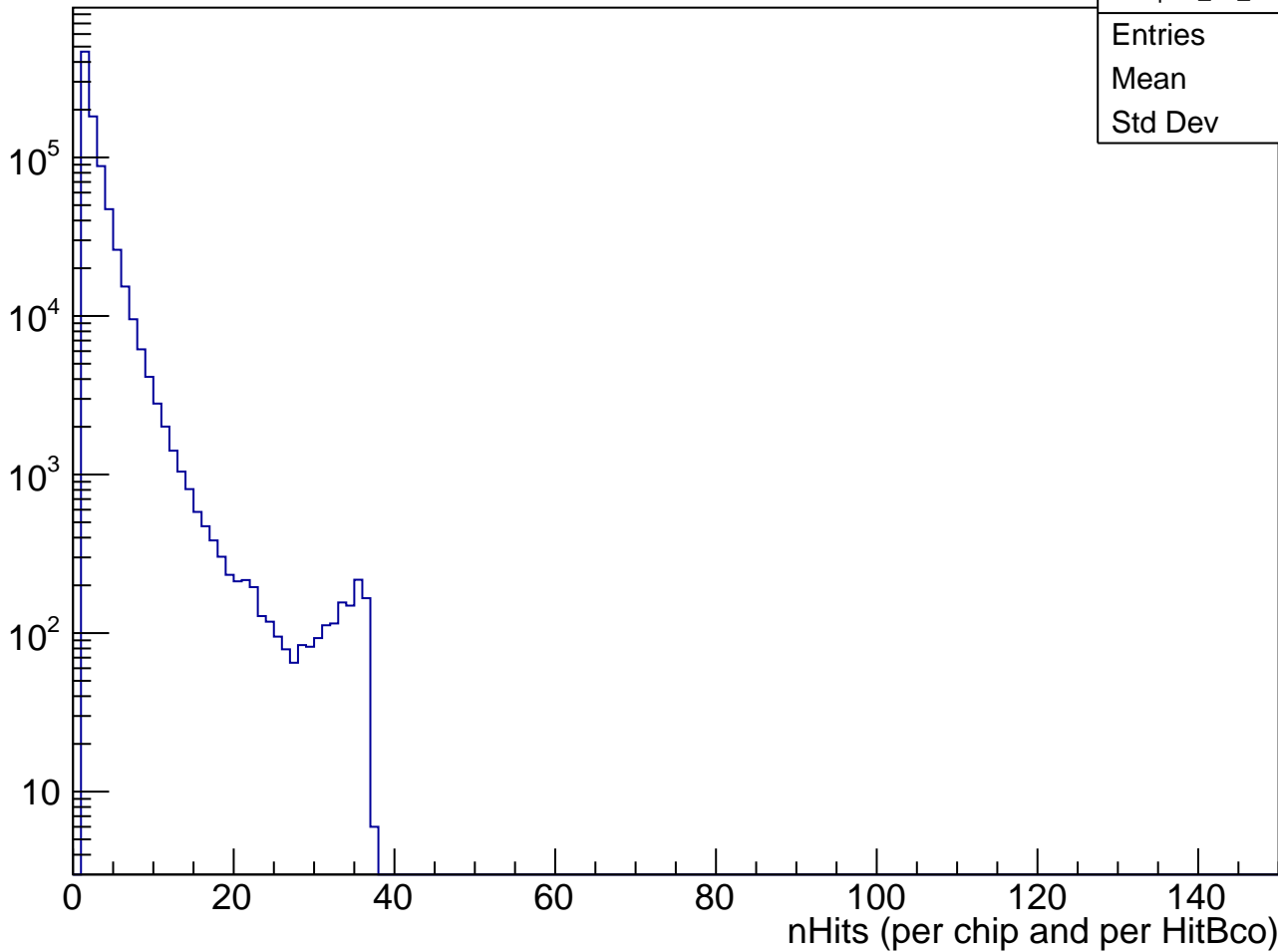


nChipHit_F7_Fch0_Chip20	
Entries	1006456
Mean	3.612
Std Dev	4.598

nHits (per chip and per HitBco)

nChipHit_F7_Fch0_Chip21

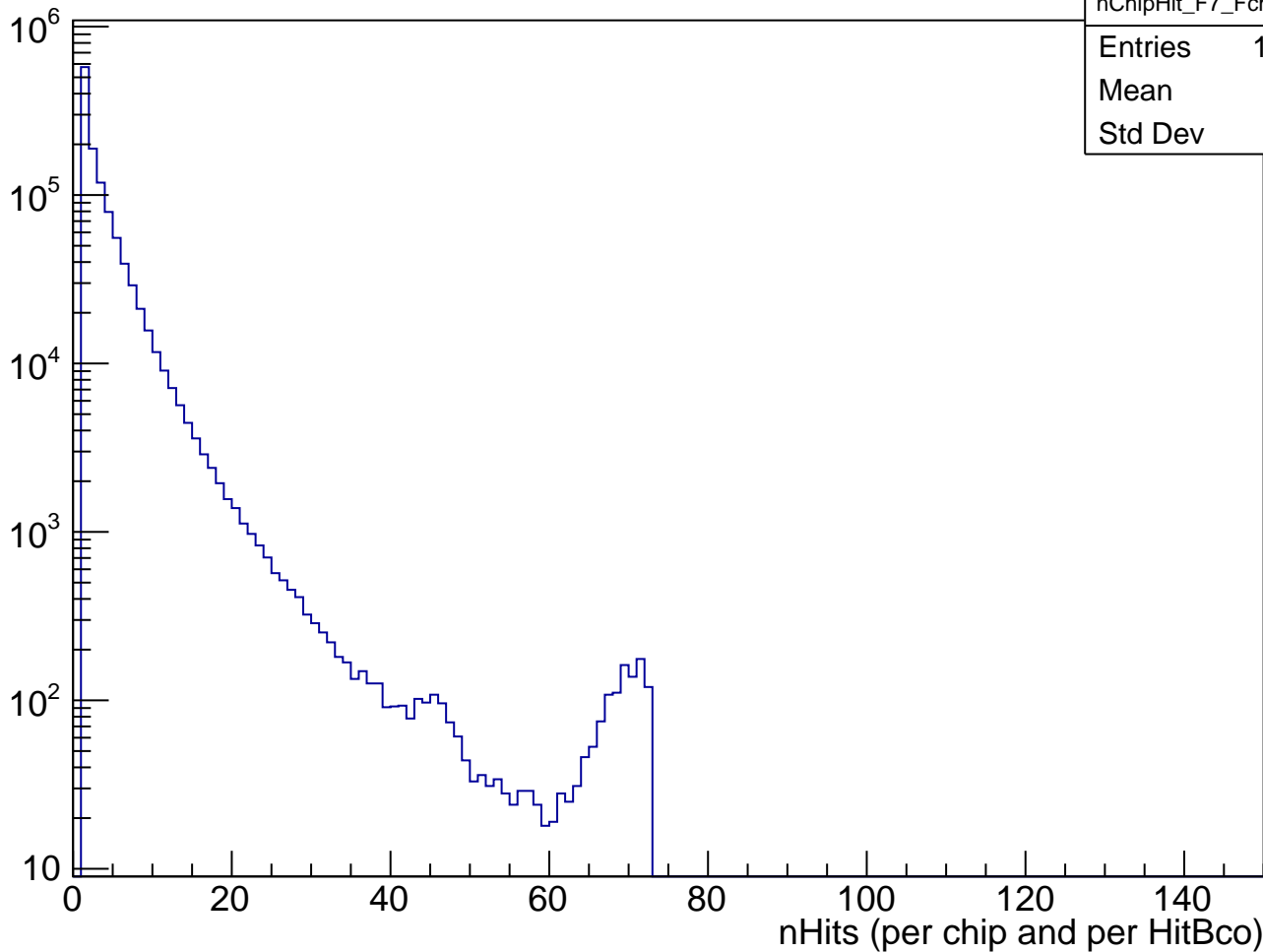
Entries



nChipHit_F7_Fch0_Chip21	
Entries	855063
Mean	2.158
Std Dev	2.322

nChipHit_F7_Fch0_Chip22

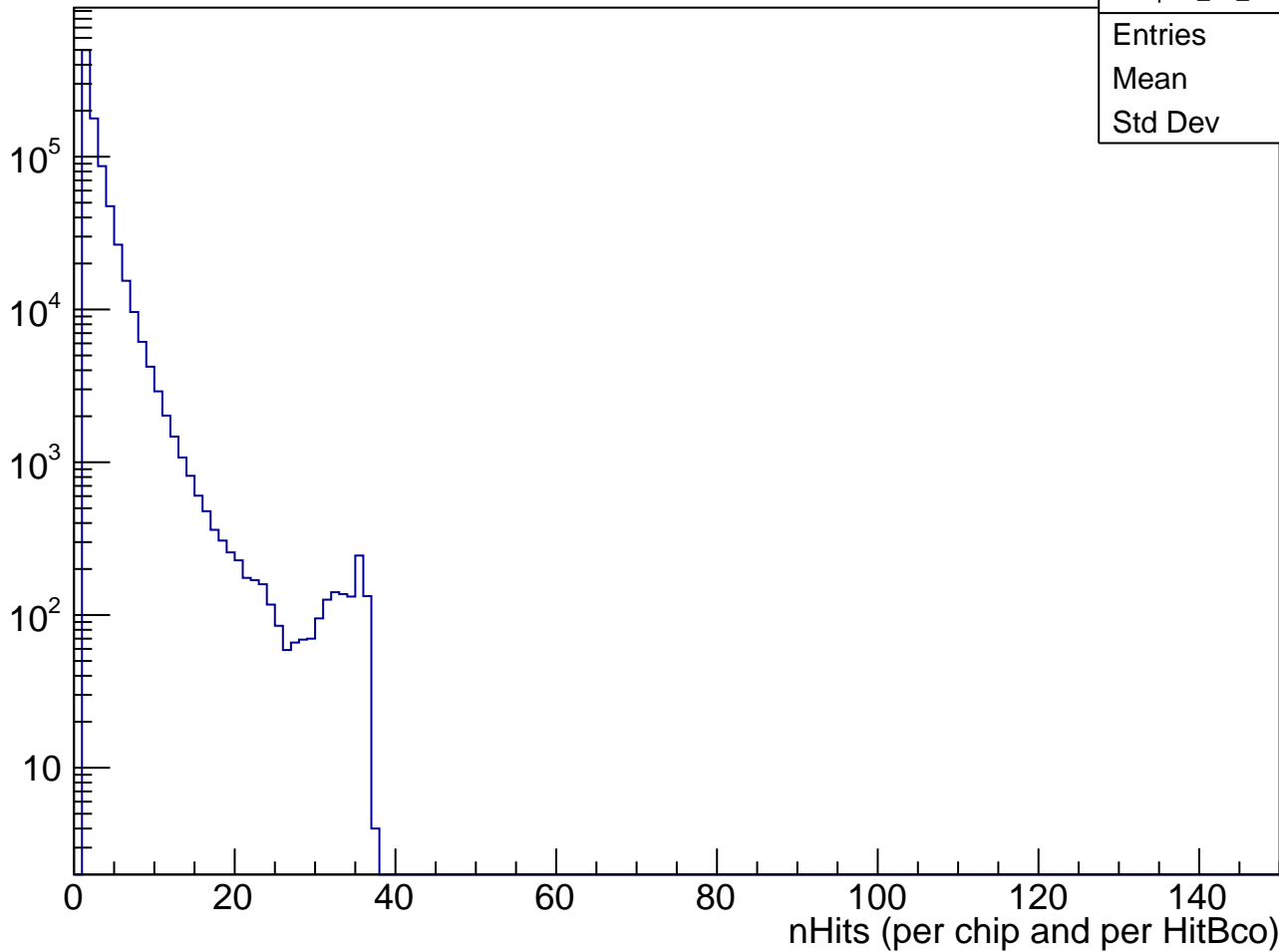
Entries



nChipHit_F7_Fch0_Chip22	
Entries	1183713
Mean	3.084
Std Dev	4.247

nChipHit_F7_Fch0_Chip23

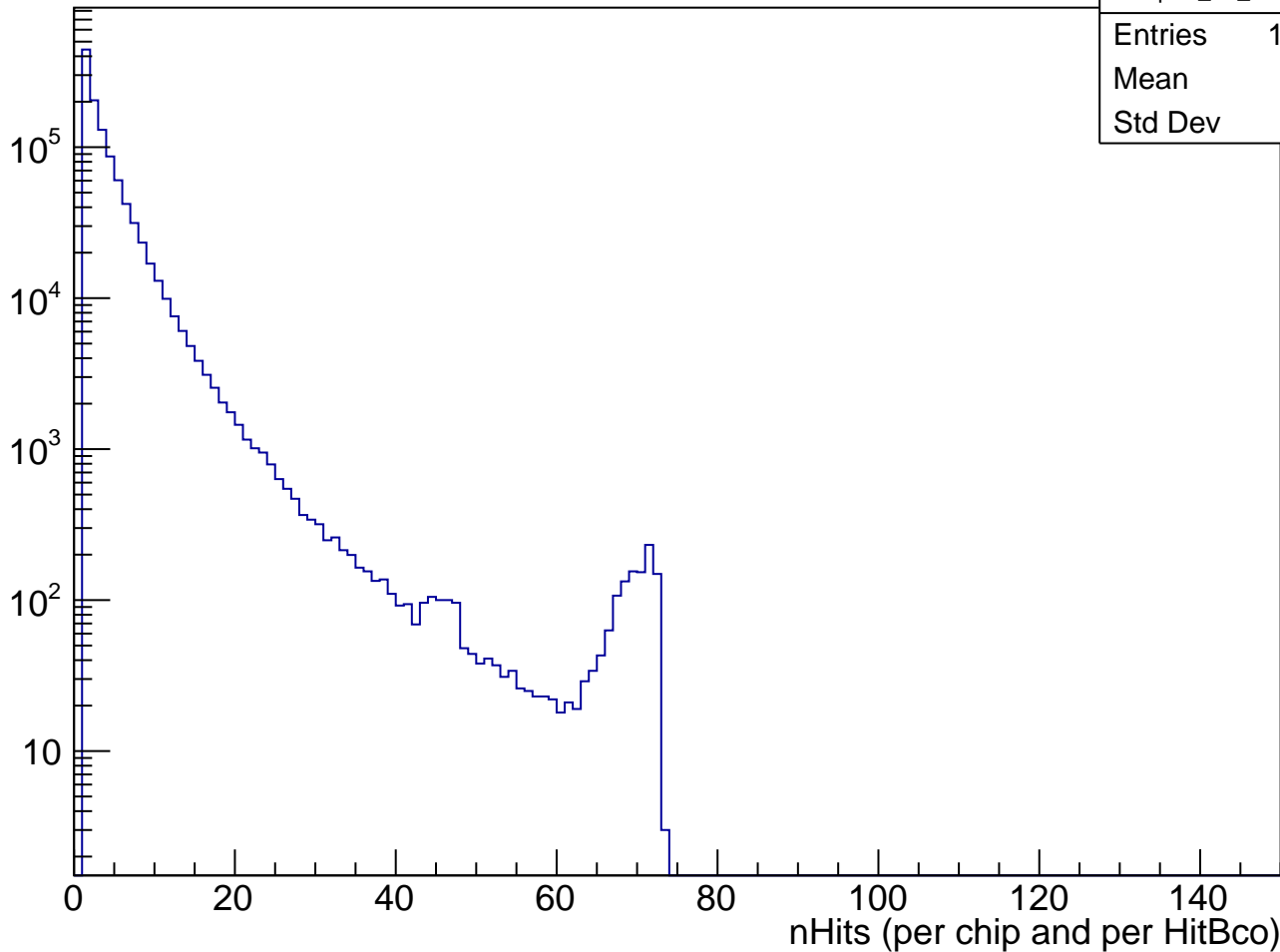
Entries



nChipHit_F7_Fch0_Chip23	
Entries	886143
Mean	2.116
Std Dev	2.289

nChipHit_F7_Fch0_Chip24

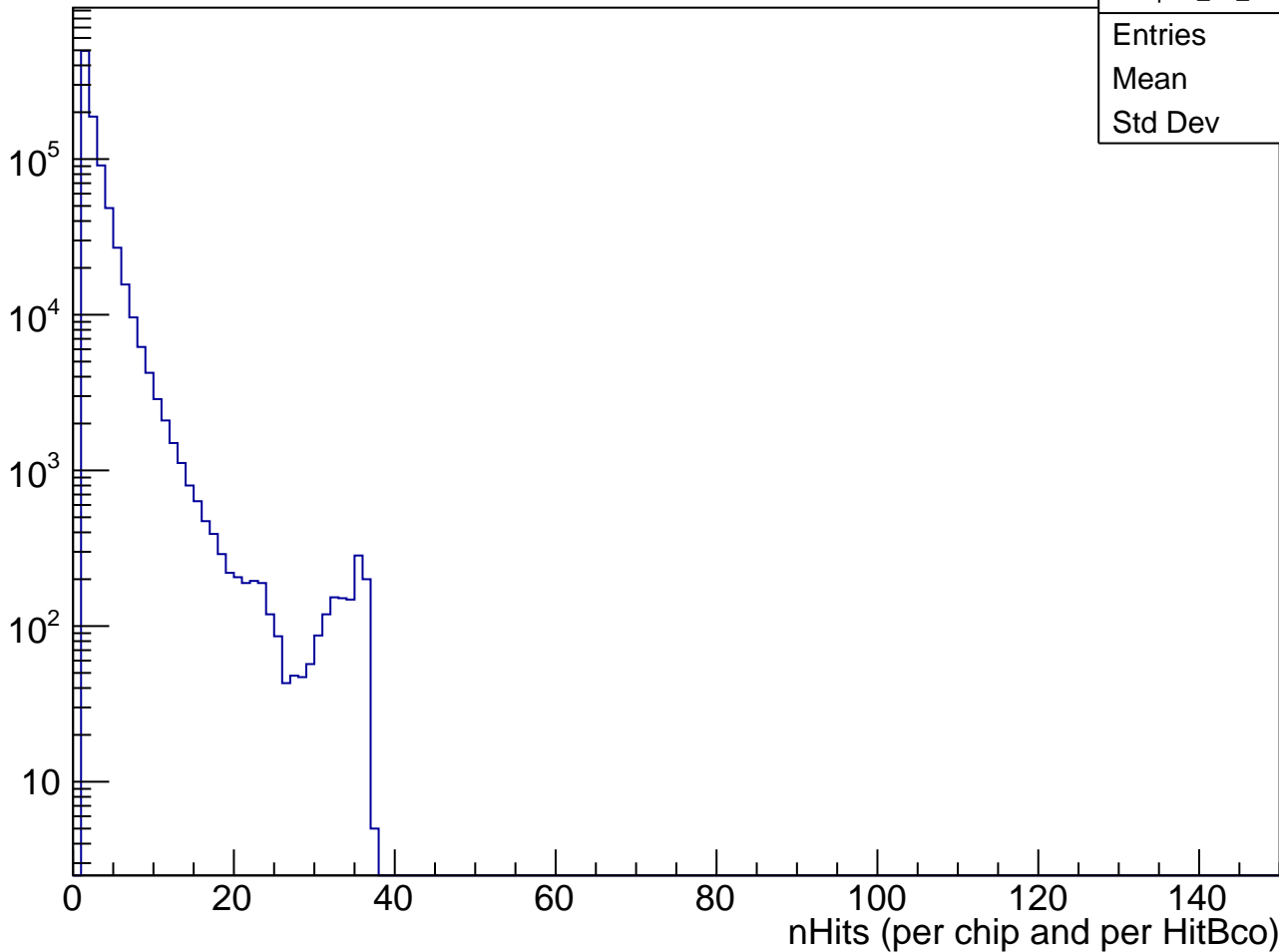
Entries



nChipHit_F7_Fch0_Chip24	
Entries	1105434
Mean	3.417
Std Dev	4.462

nChipHit_F7_Fch0_Chip25

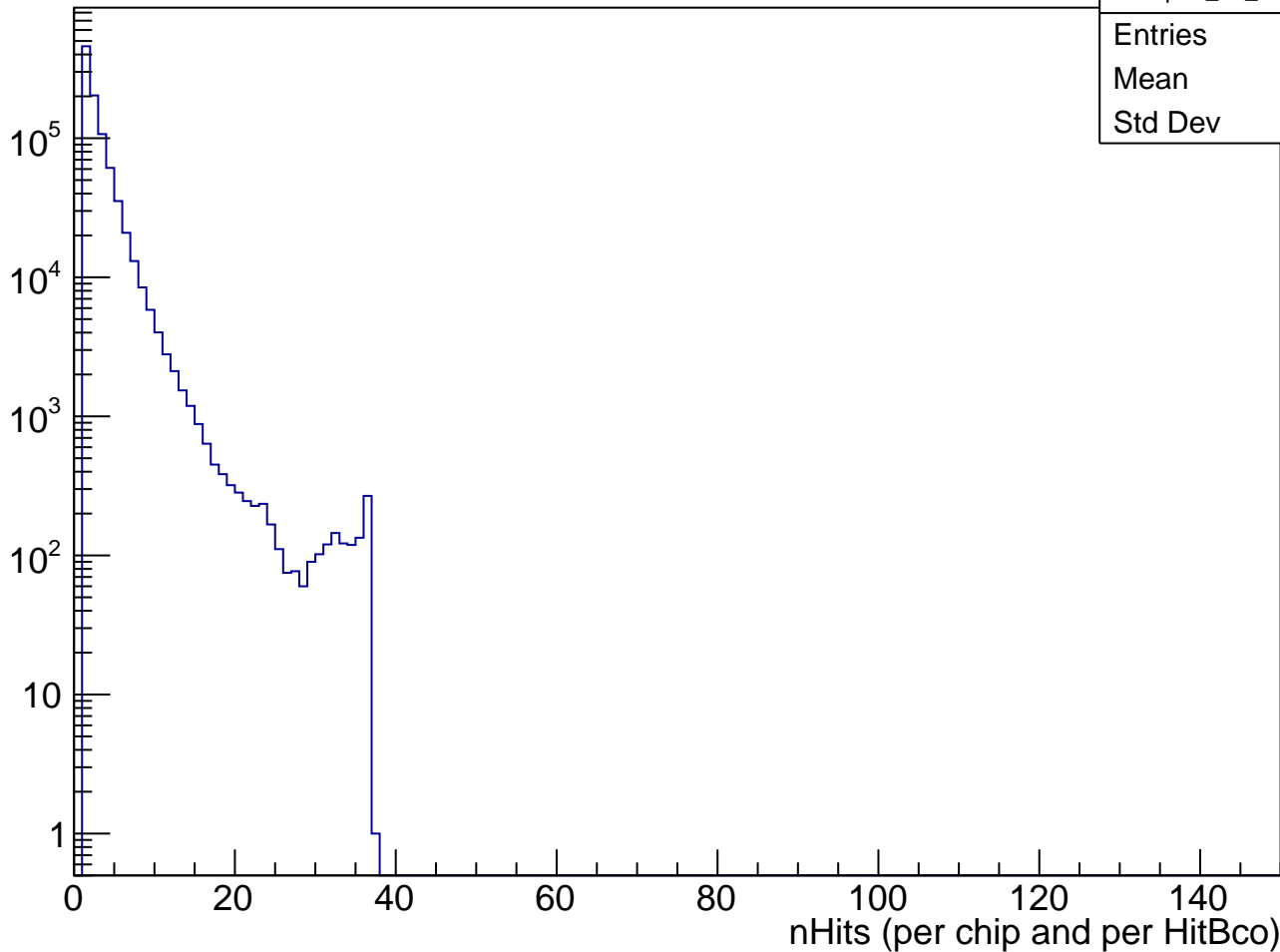
Entries



nChipHit_F7_Fch0_Chip25	
Entries	898088
Mean	2.133
Std Dev	2.308

nChipHit_F7_Fch0_Chip3

Entries



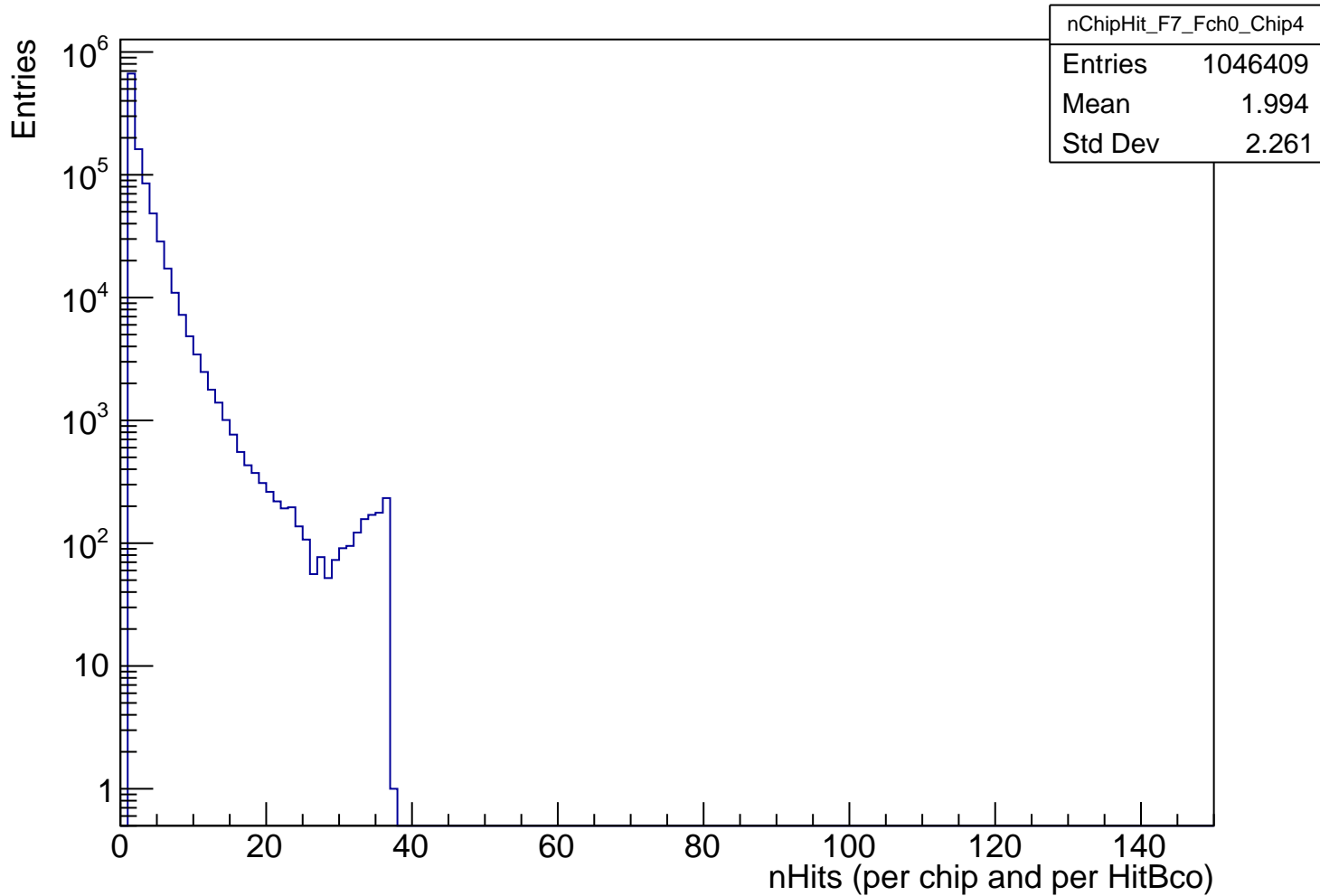
nChipHit_F7_Fch0_Chip3

Entries 930020

Mean 2.356

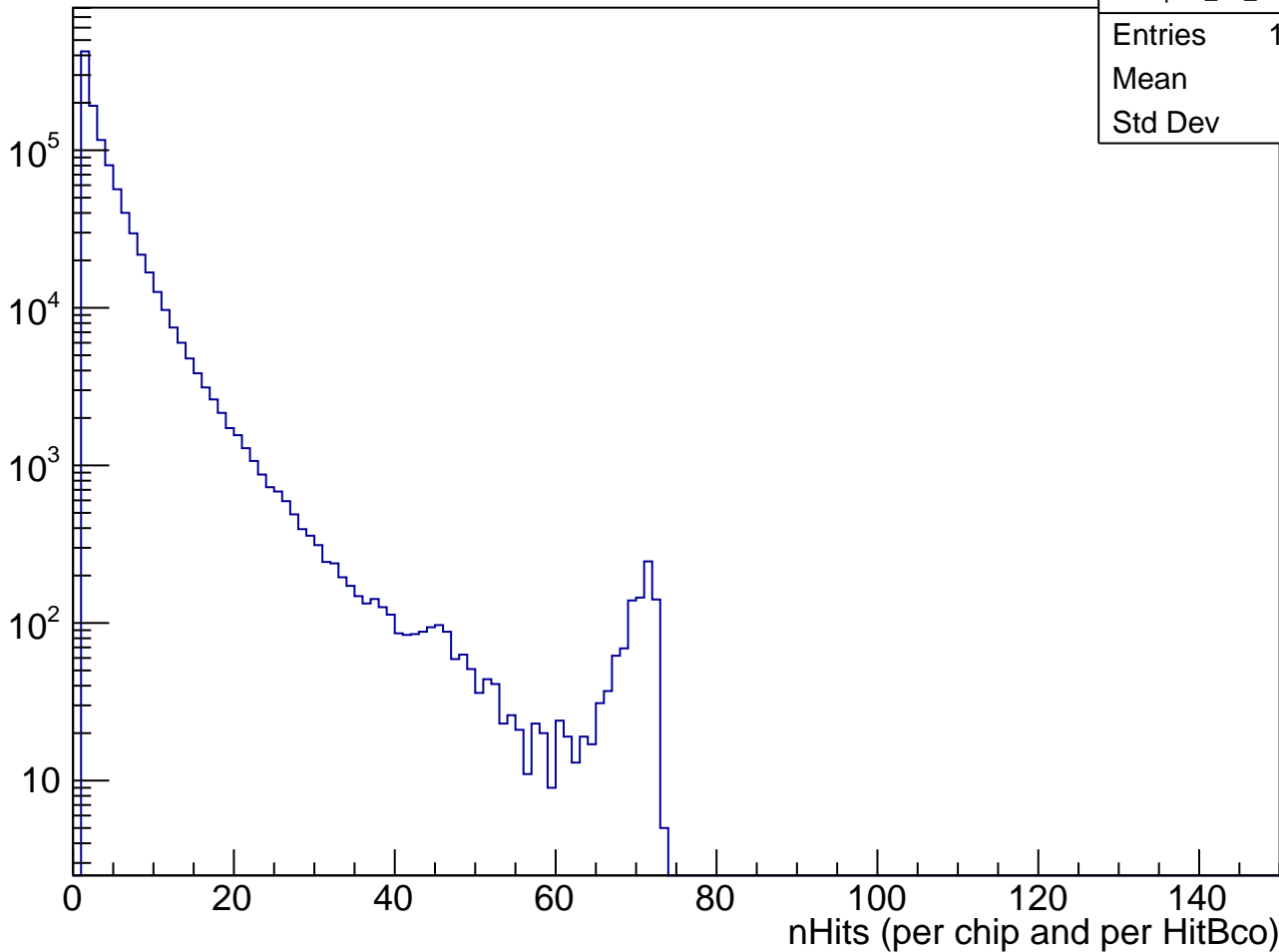
Std Dev 2.454

nChipHit_F7_Fch0_Chip4



nChipHit_F7_Fch0_Chip5

Entries

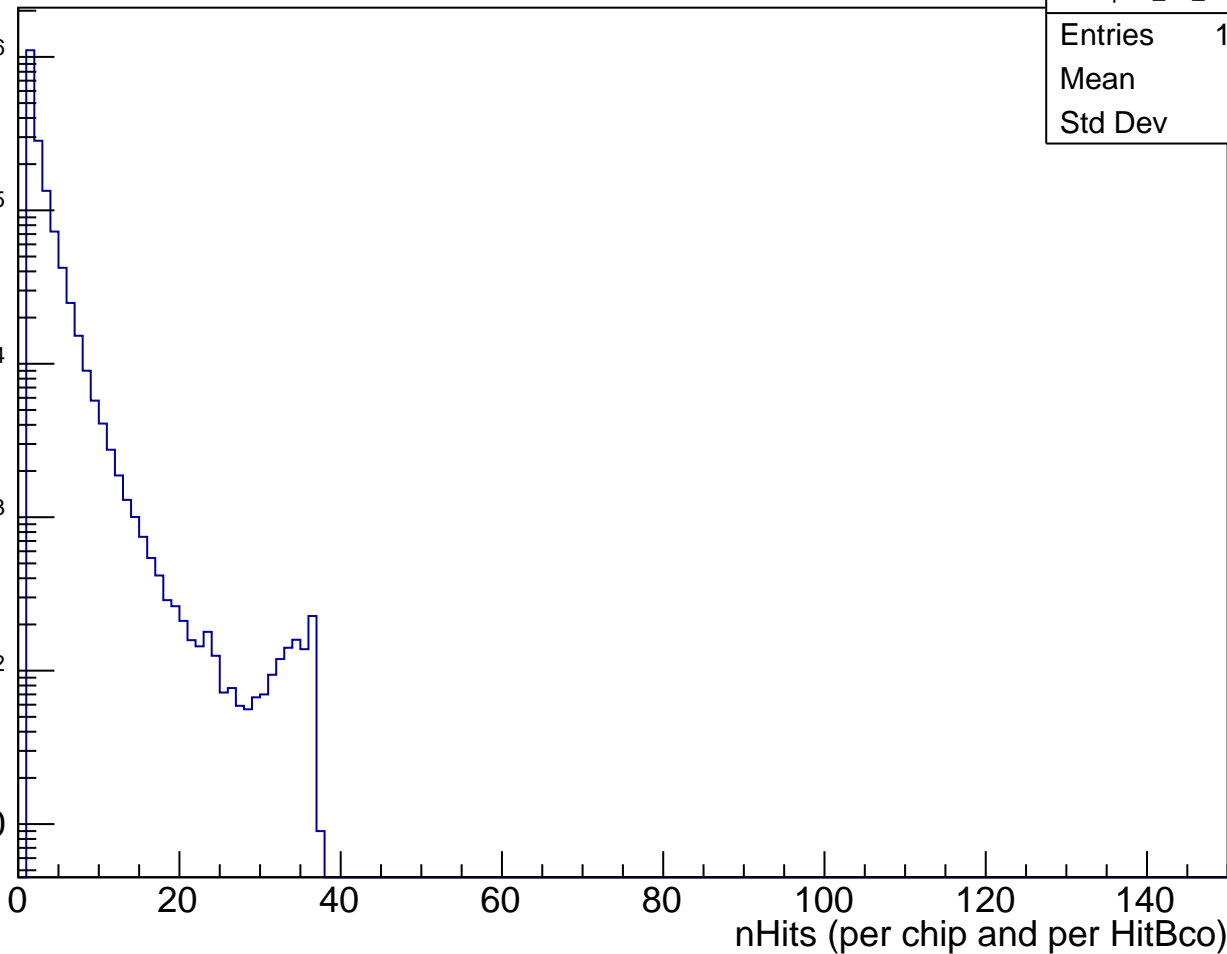


nChipHit_F7_Fch0_Chip5	
Entries	1042620
Mean	3.44
Std Dev	4.466

nChipHit_F7_Fch0_Chip6

Entries

10^6
 10^5
 10^4
 10^3
 10^2
10



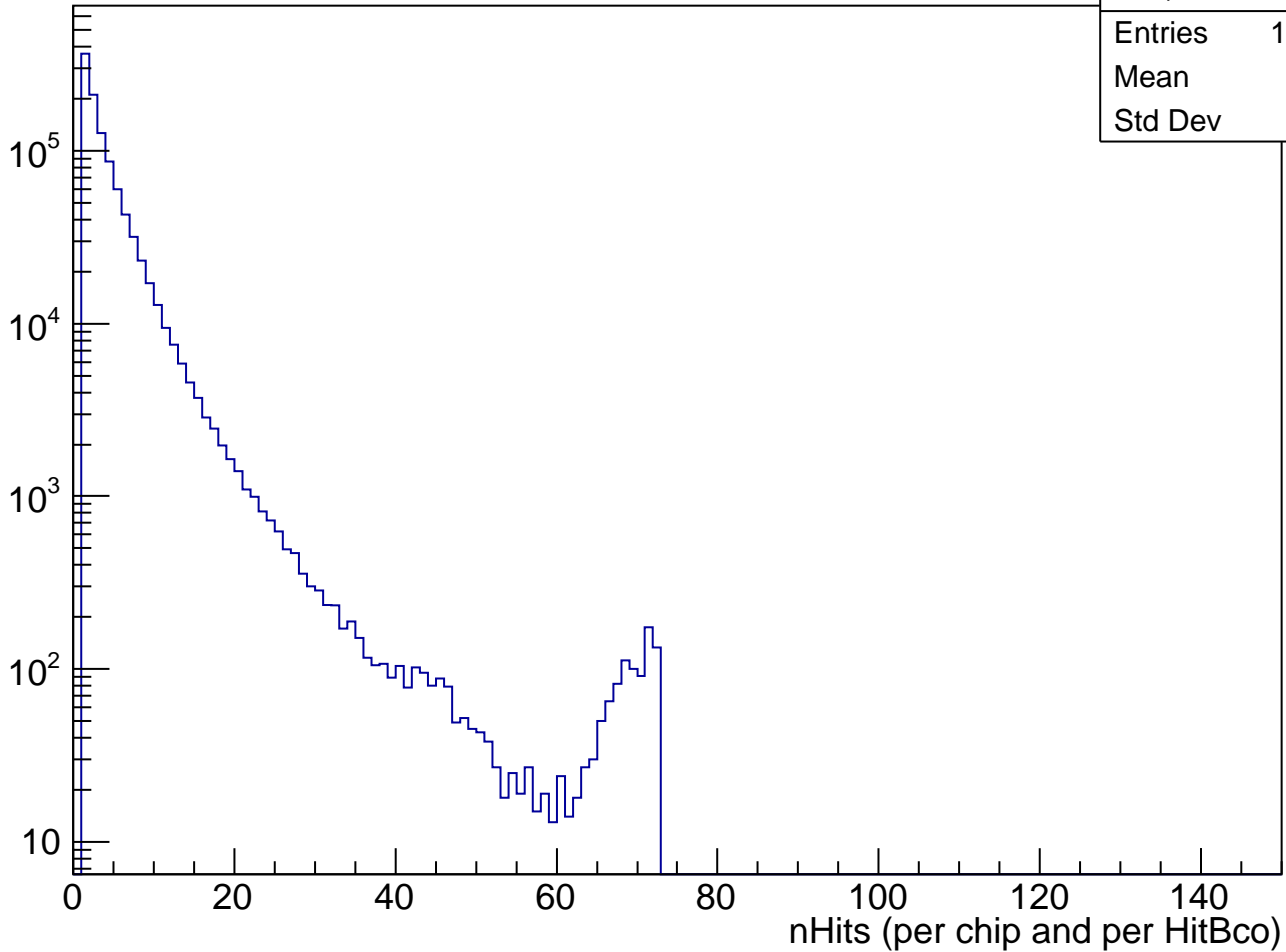
nChipHit_F7_Fch0_Chip6

Entries	1708475
Mean	1.86
Std Dev	1.905

nHits (per chip and per HitBco)

nChipHit_F7_Fch0_Chip7

Entries



nChipHit_F7_Fch0_Chip7

Entries 1027500

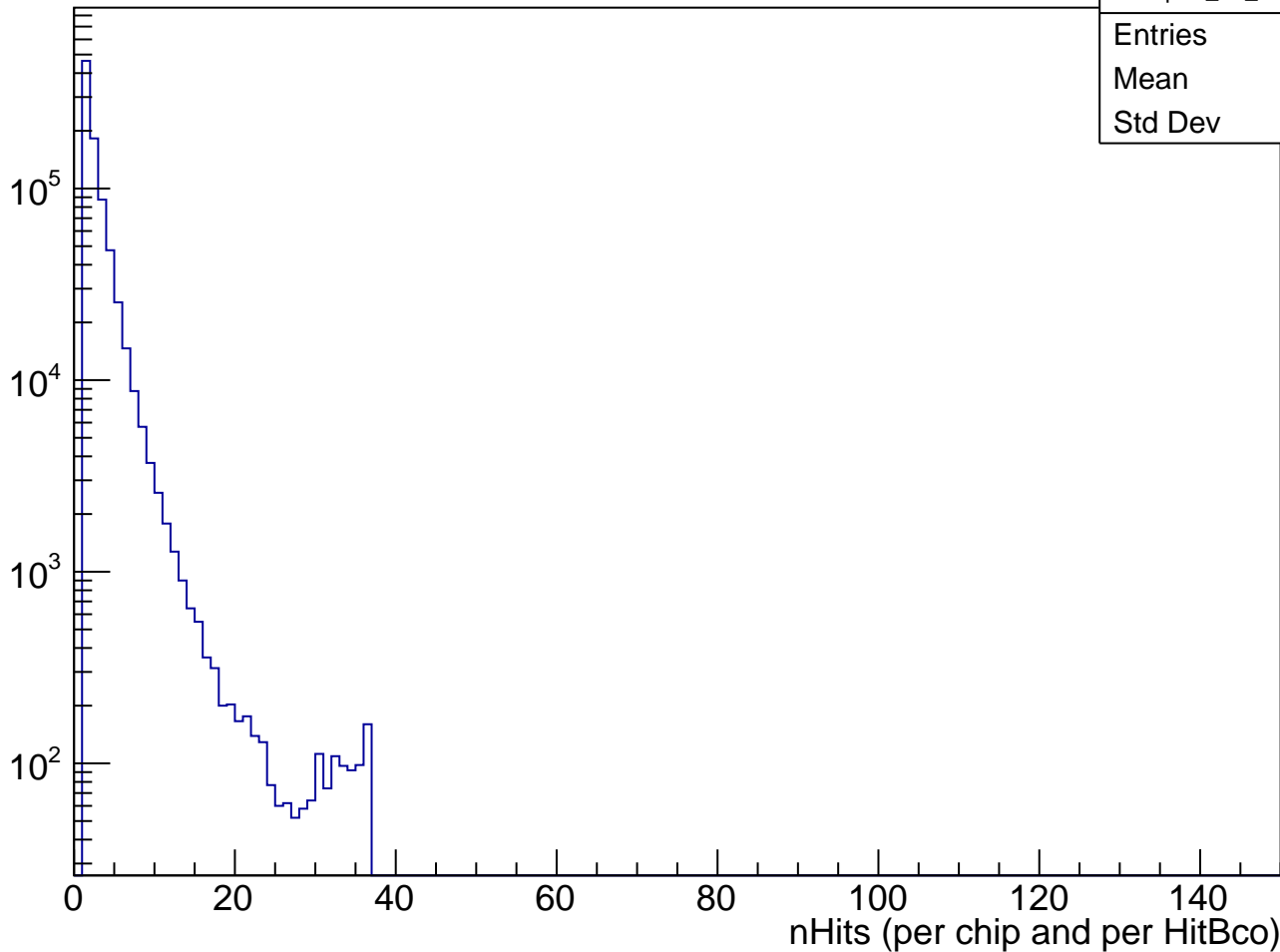
Mean 3.545

Std Dev 4.367

nHits (per chip and per HitBco)

nChipHit_F7_Fch0_Chip8

Entries

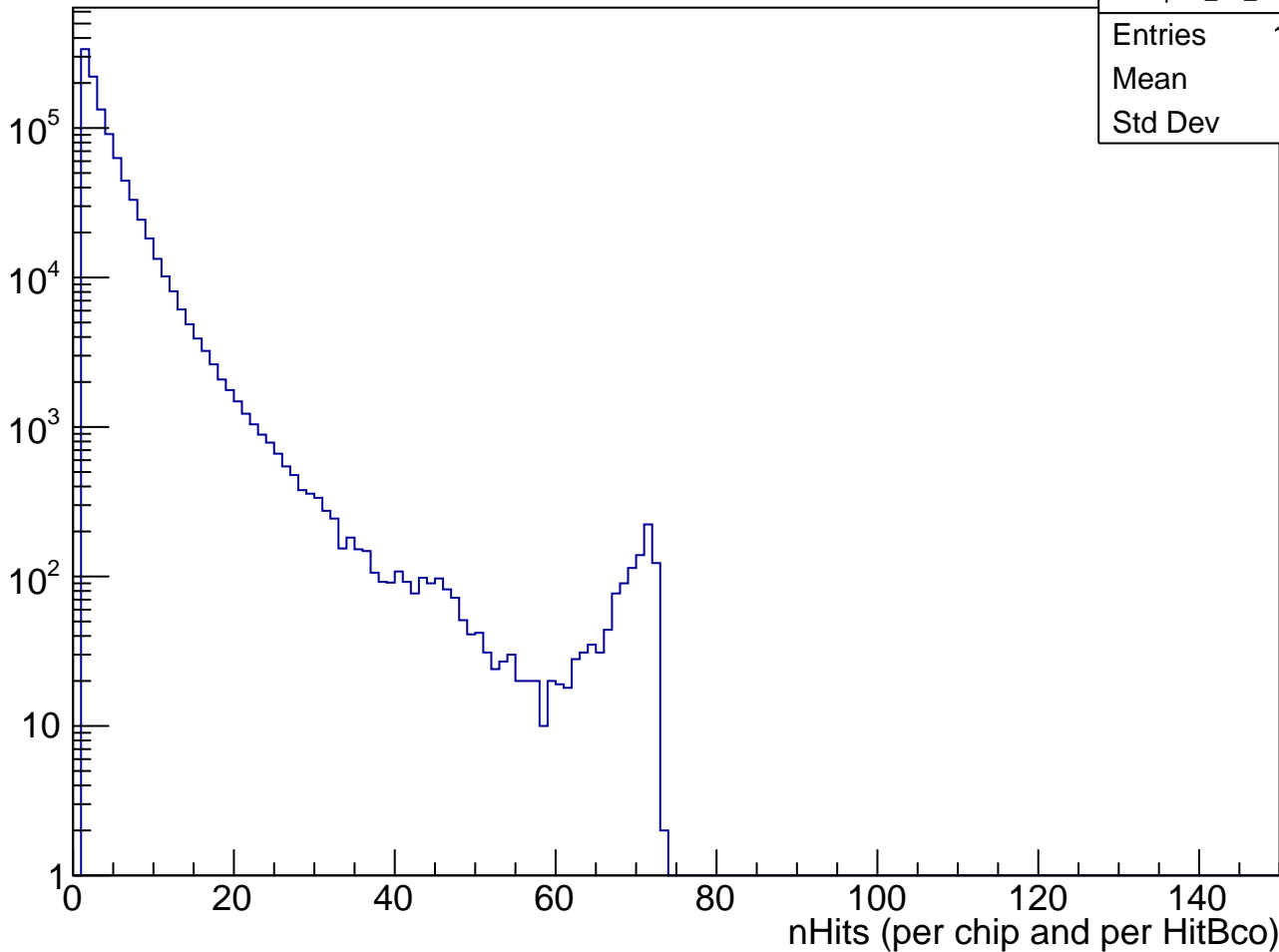


nChipHit_F7_Fch0_Chip8

Entries	850250
Mean	2.109
Std Dev	2.159

nChipHit_F7_Fch0_Chip9

Entries

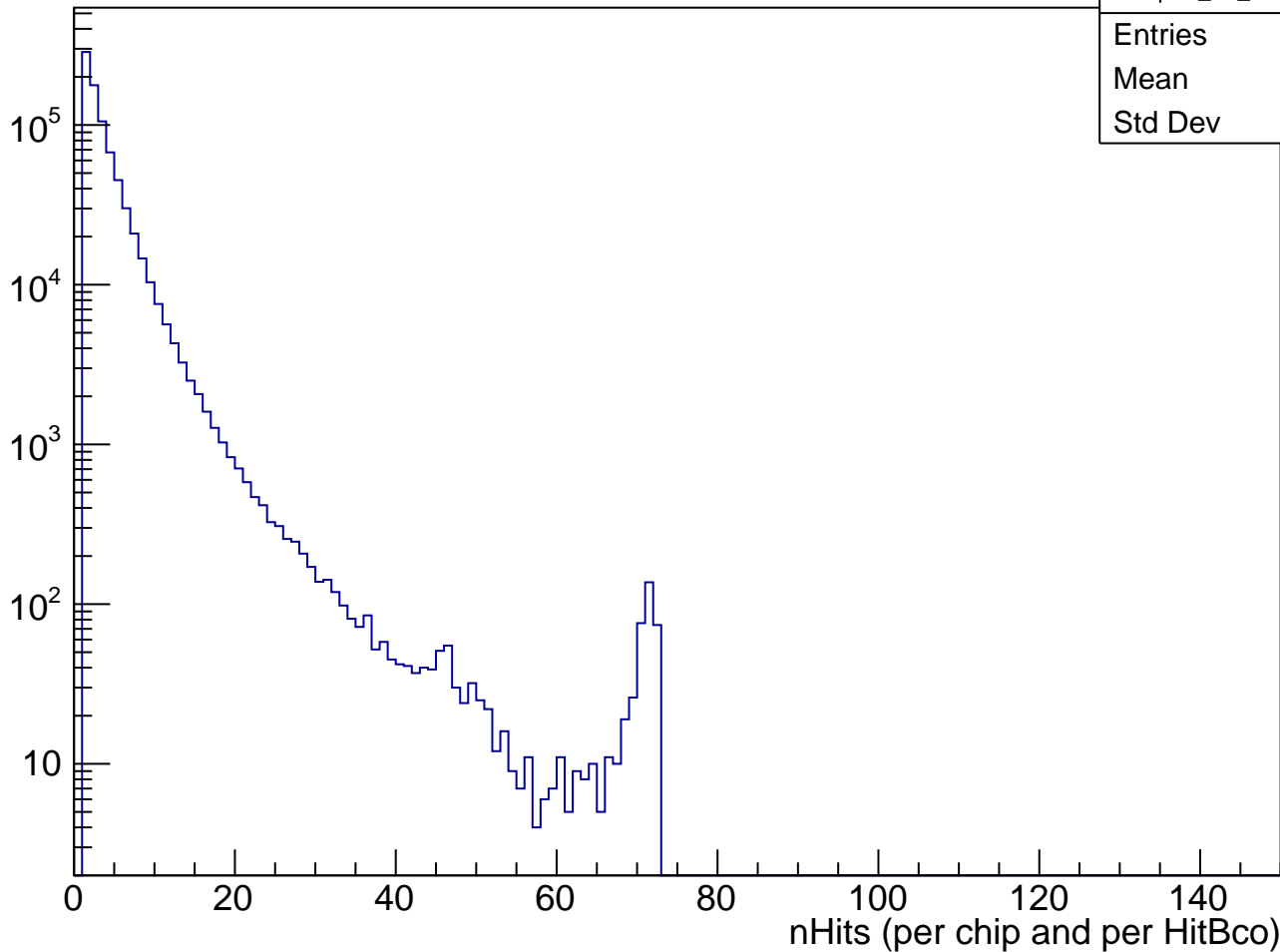


nChipHit_F7_Fch0_Chip9	
Entries	1032901
Mean	3.666
Std Dev	4.45

nHits (per chip and per HitBco)

nChipHit_F7_Fch10_Chip0

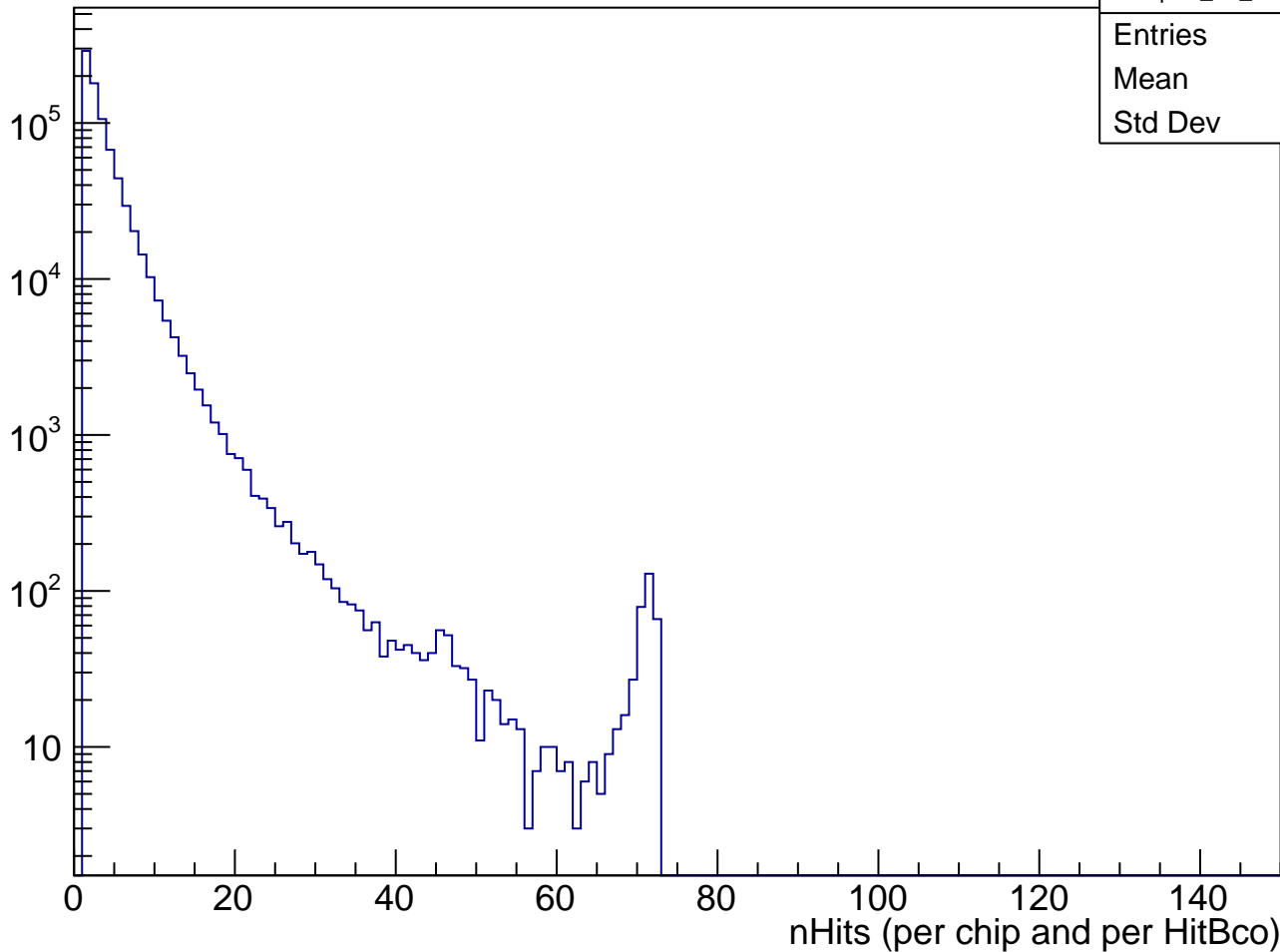
Entries



nChipHit_F7_Fch10_Chip0	
Entries	793479
Mean	3.197
Std Dev	3.719

nChipHit_F7_Fch10_Chip1

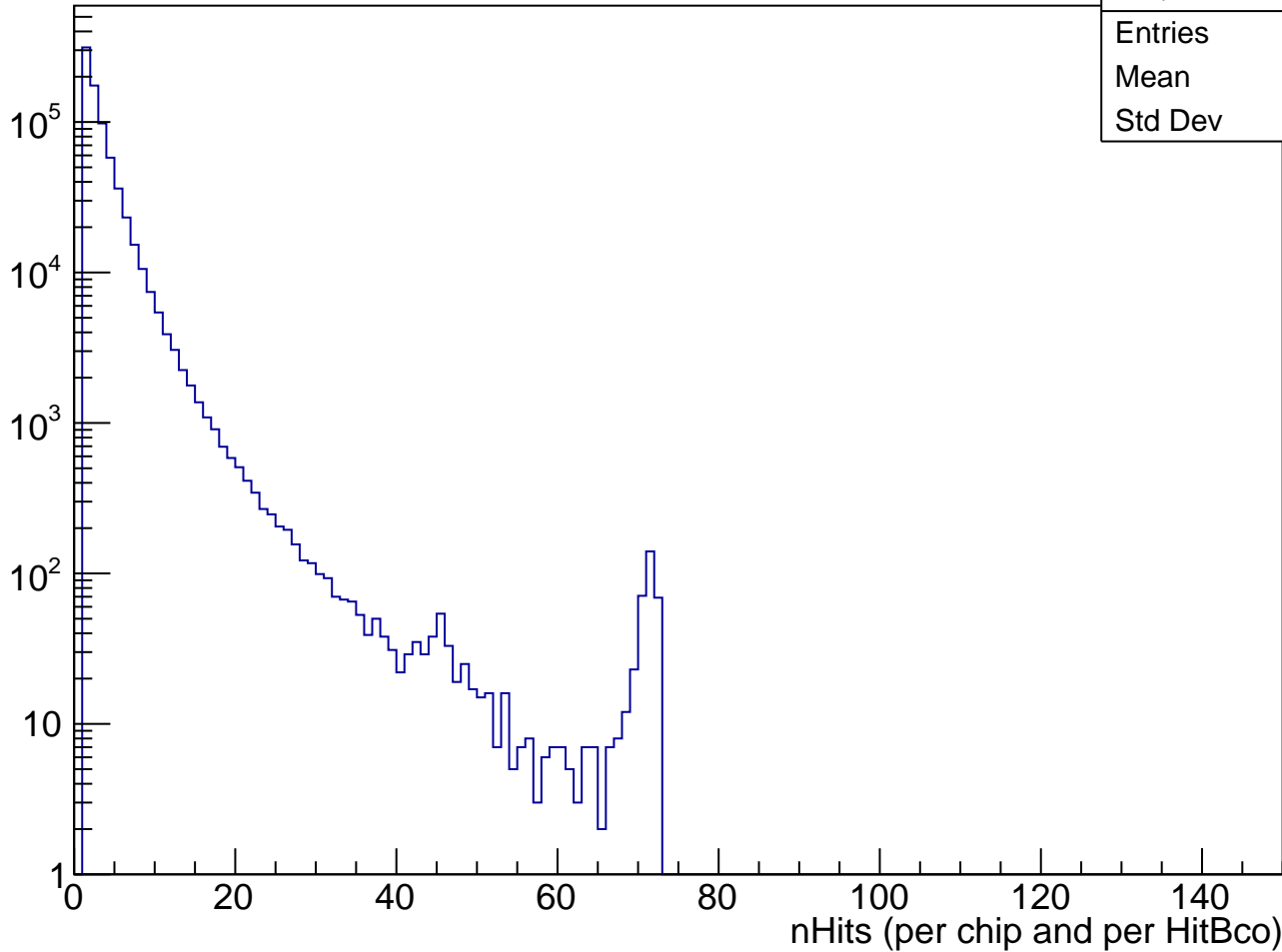
Entries



nChipHit_F7_Fch10_Chip1	
Entries	795743
Mean	3.155
Std Dev	3.661

nChipHit_F7_Fch10_Chip10

Entries



nChipHit_F7_Fch10_Chip10

Entries 761309

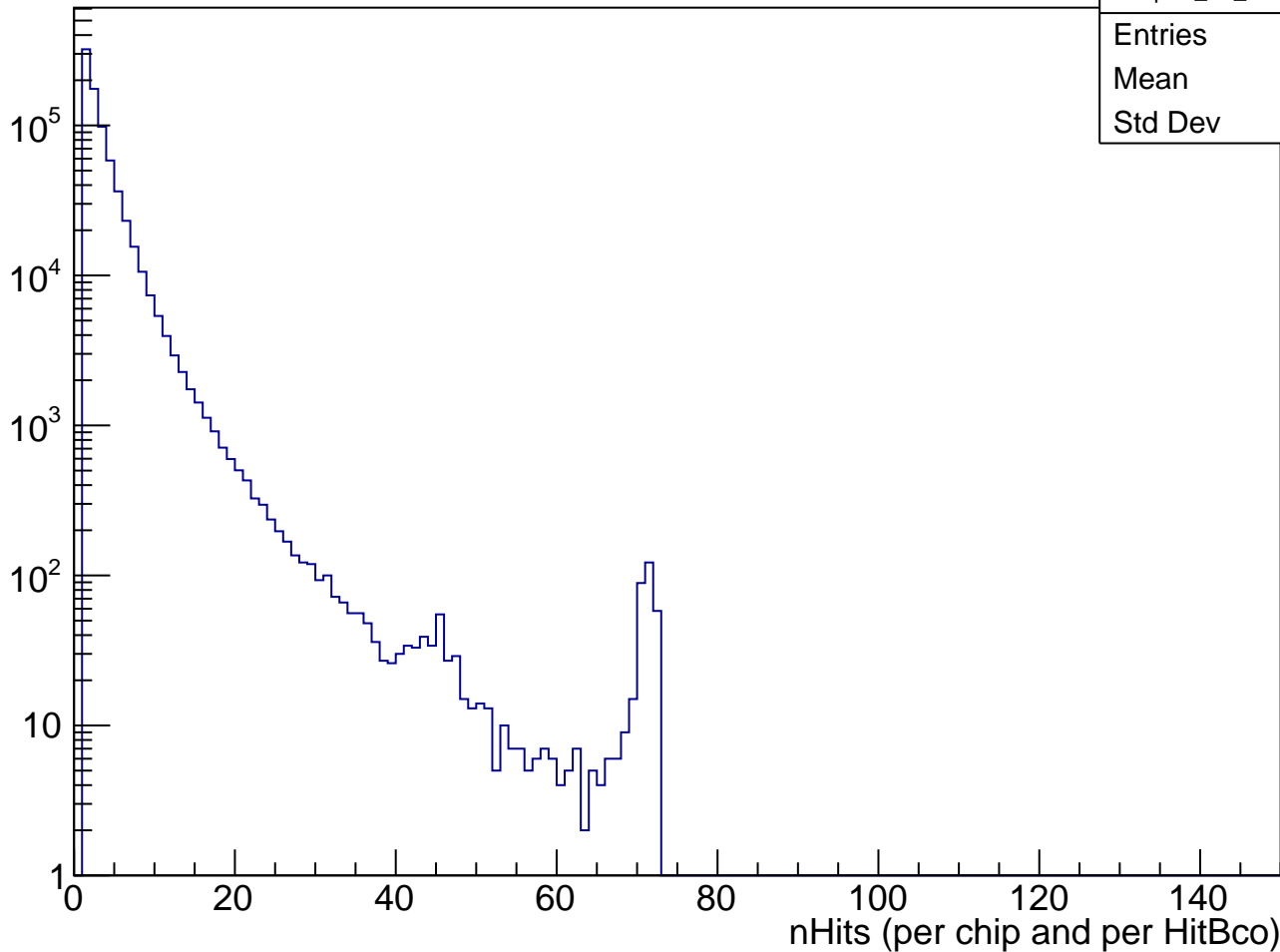
Mean 2.83

Std Dev 3.372

nHits (per chip and per HitBco)

nChipHit_F7_Fch10_Chip11

Entries

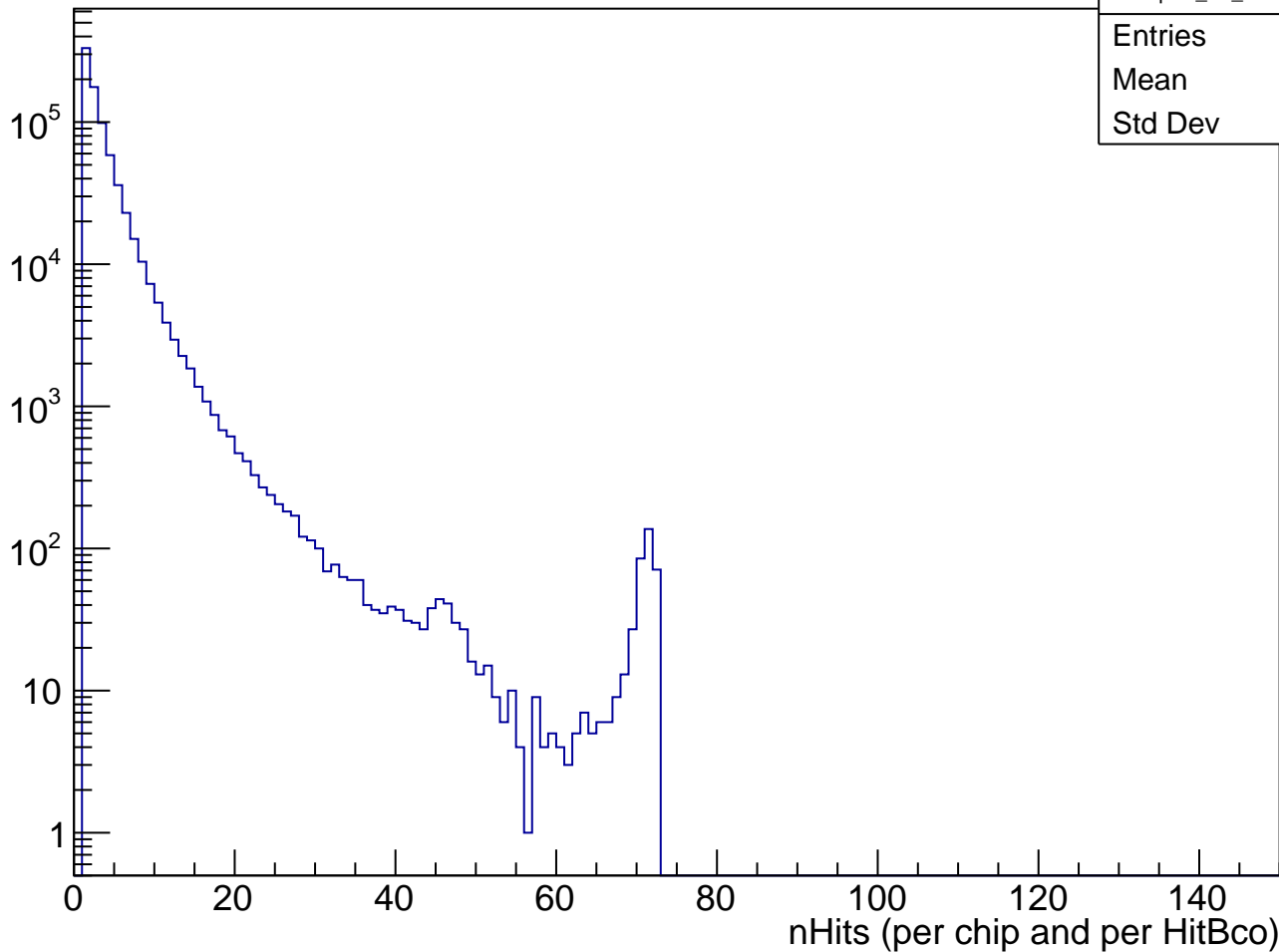


nChipHit_F7_Fch10_Chip11	
Entries	771082
Mean	2.806
Std Dev	3.319

nHits (per chip and per HitBco)

nChipHit_F7_Fch10_Chip12

Entries



nChipHit_F7_Fch10_Chip12

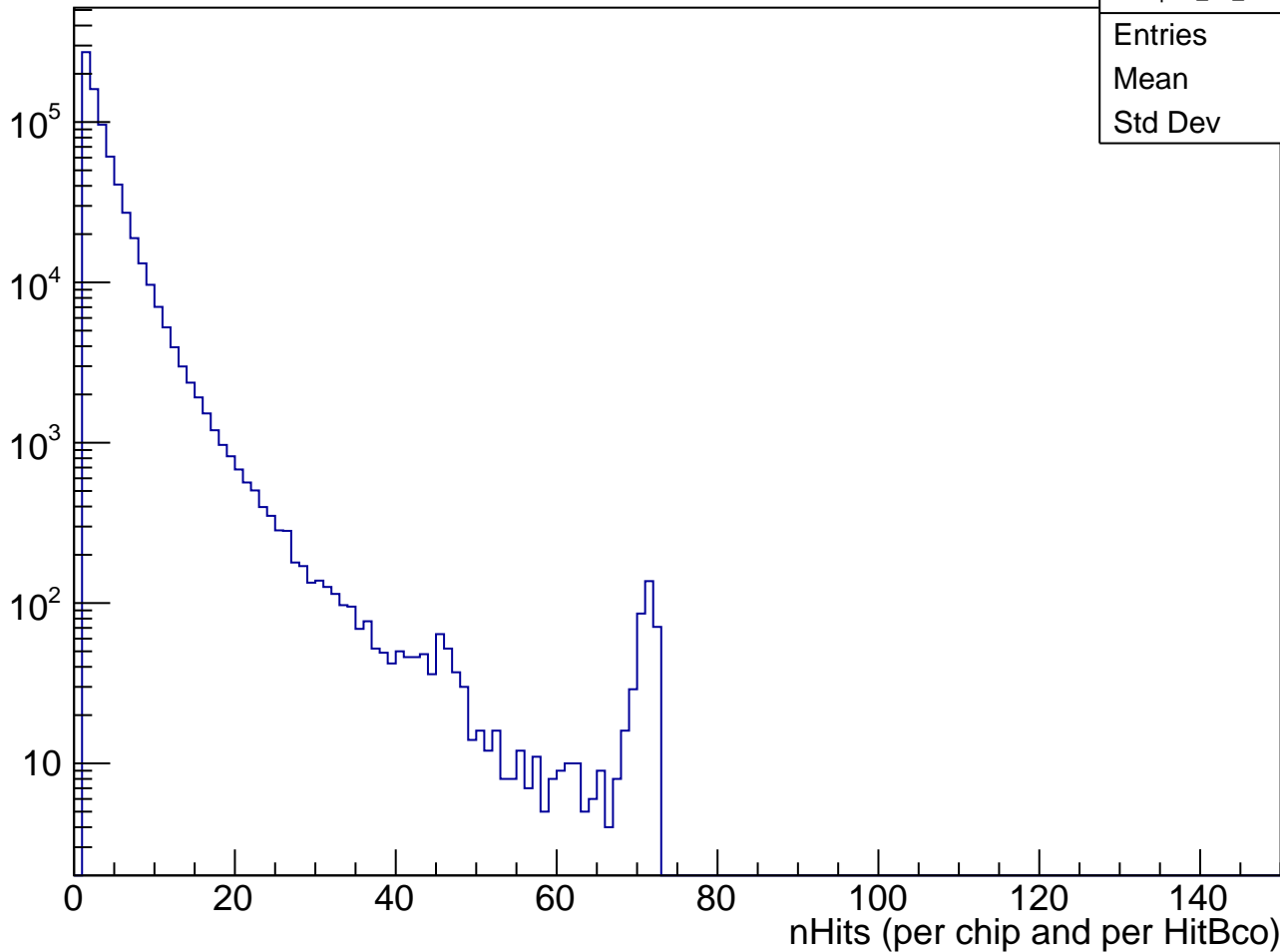
Entries 781224

Mean 2.779

Std Dev 3.342

nChipHit_F7_Fch10_Chip13

Entries



nChipHit_F7_Fch10_Chip13

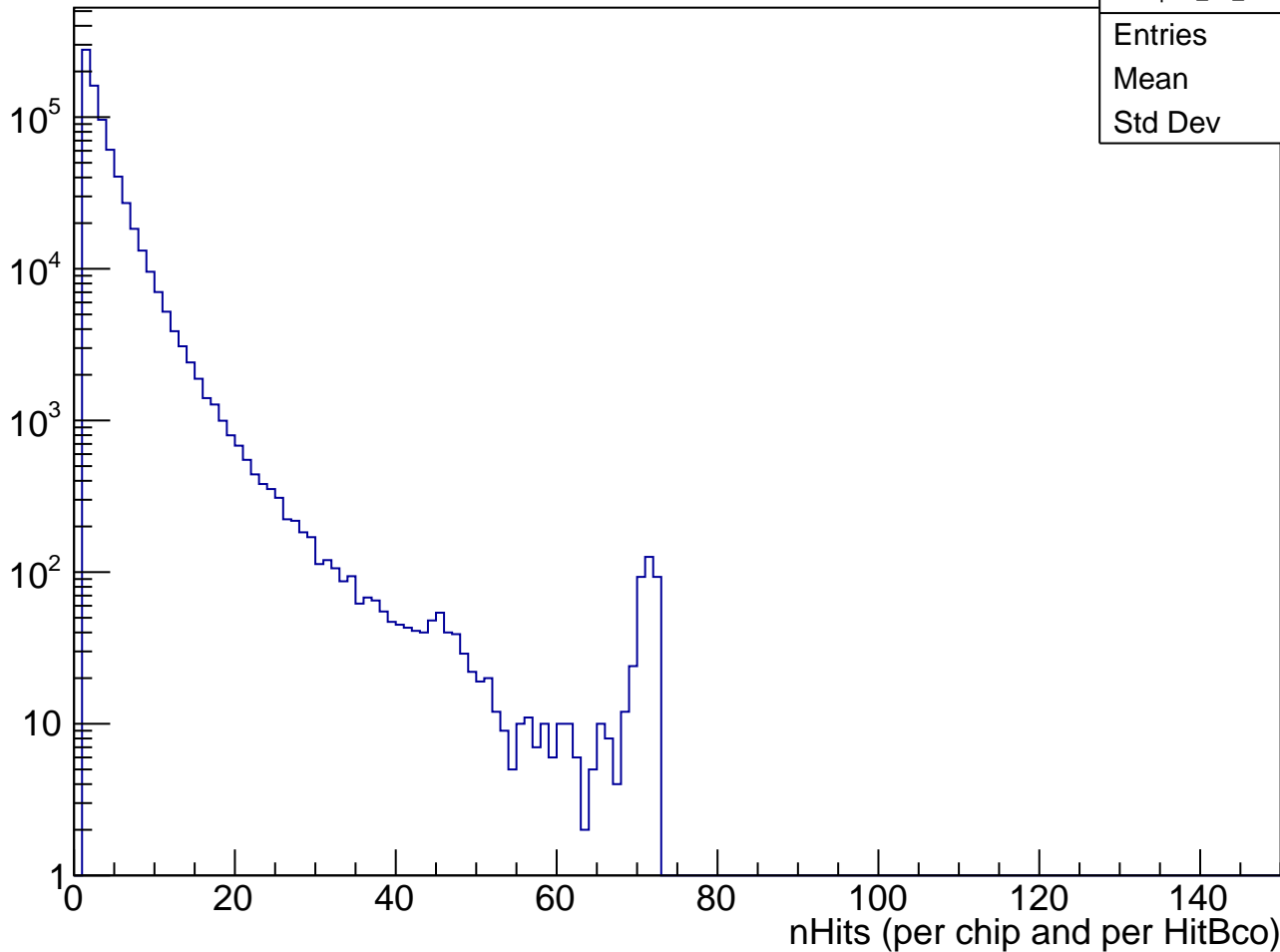
Entries 733513

Mean 3.182

Std Dev 3.779

nChipHit_F7_Fch10_Chip14

Entries



nChipHit_F7_Fch10_Chip14

Entries 738330

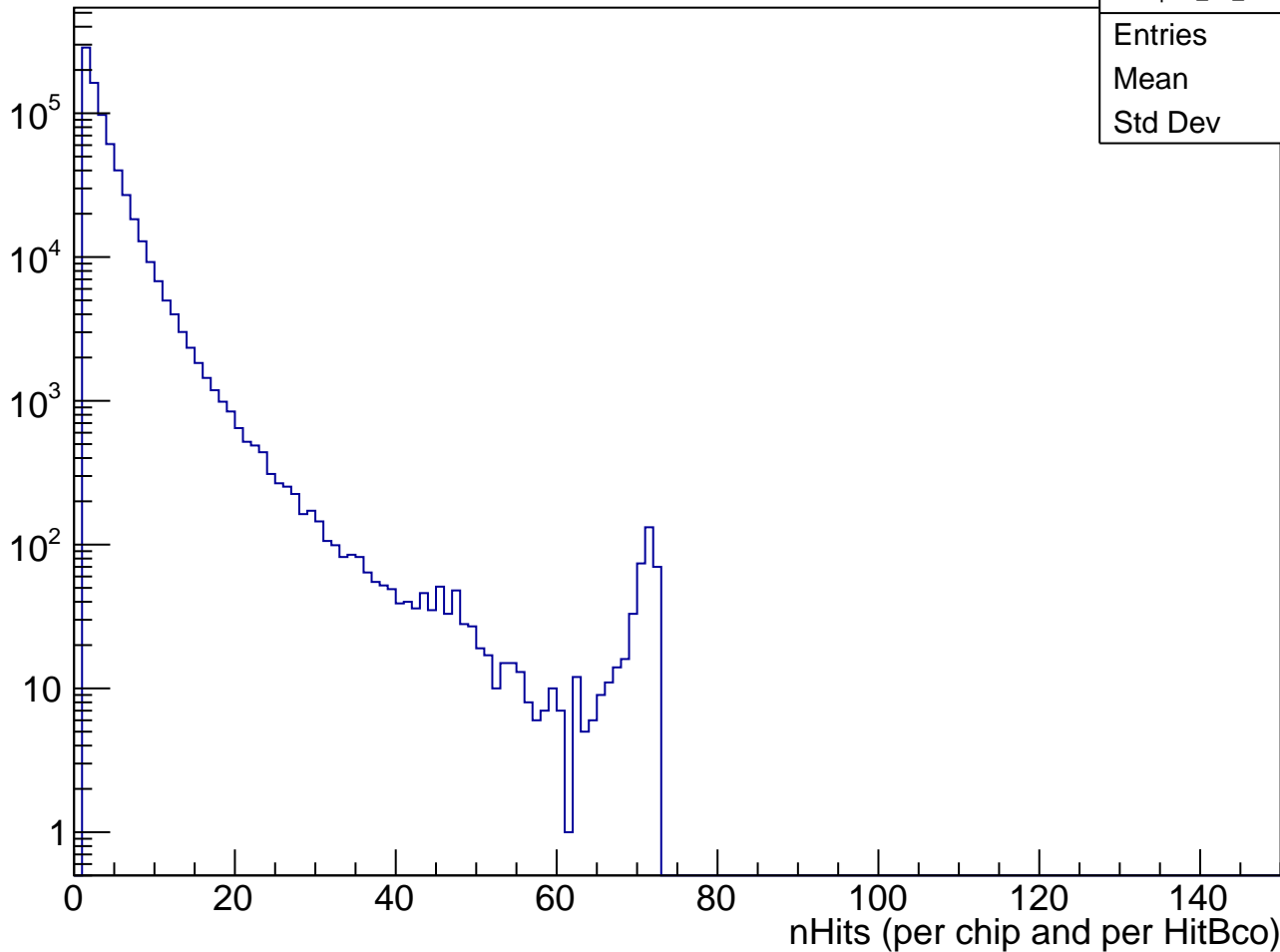
Mean 3.158

Std Dev 3.765

nHits (per chip and per HitBco)

nChipHit_F7_Fch10_Chip15

Entries



nChipHit_F7_Fch10_Chip15

Entries 747569

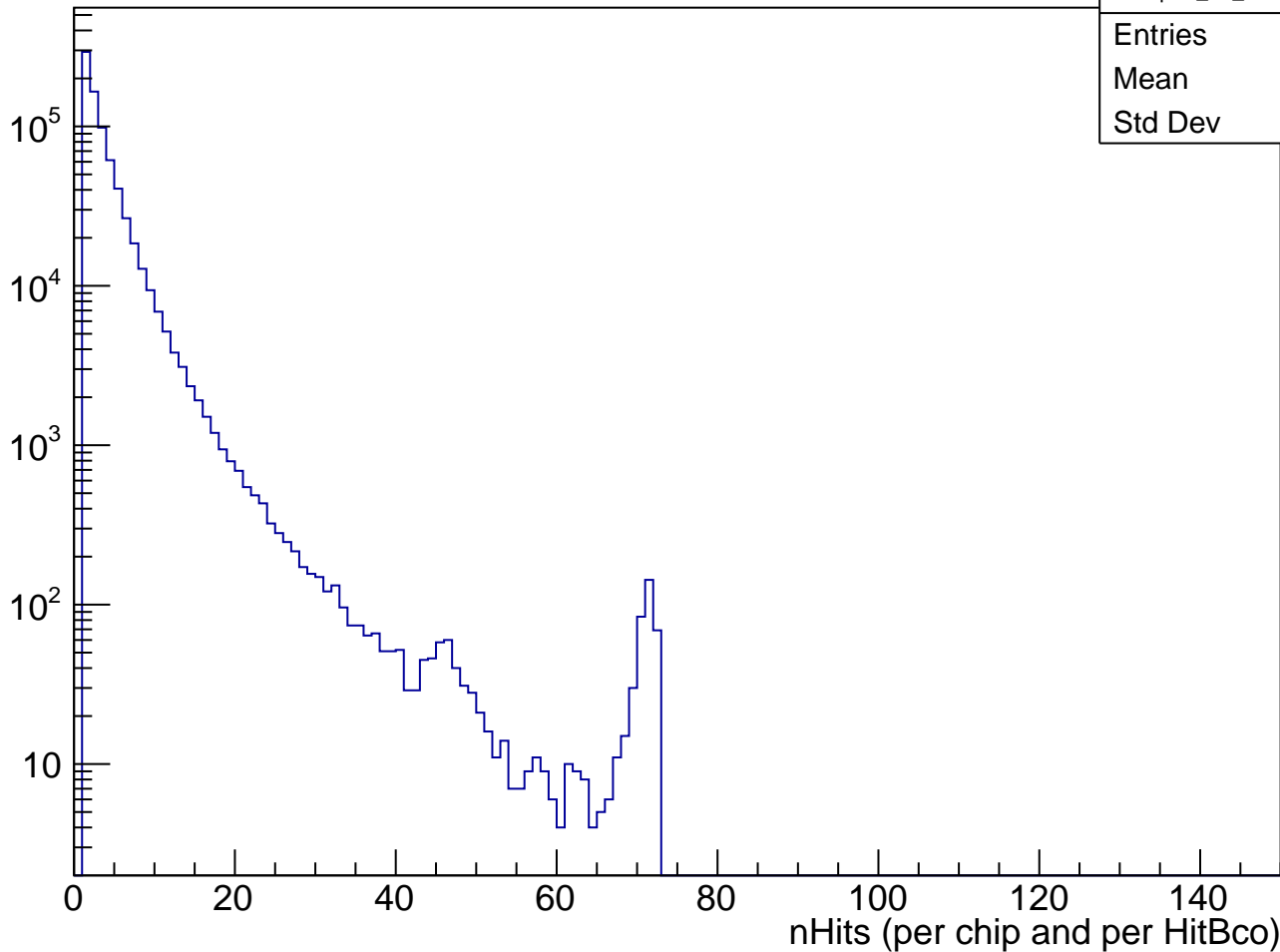
Mean 3.118

Std Dev 3.724

nHits (per chip and per HitBco)

nChipHit_F7_Fch10_Chip16

Entries



nChipHit_F7_Fch10_Chip16

Entries 759300

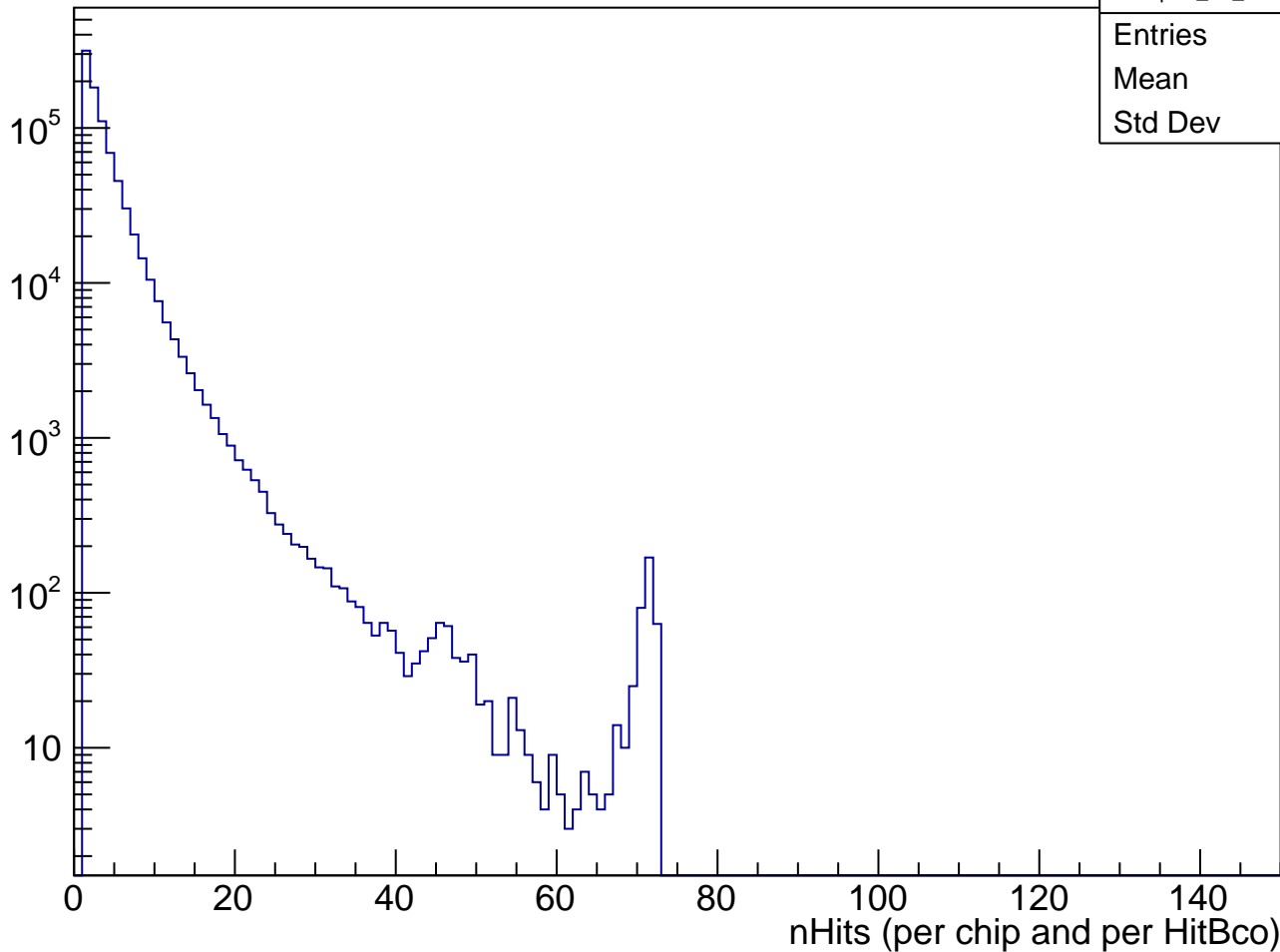
Mean 3.104

Std Dev 3.73

nHits (per chip and per HitBco)

nChipHit_F7_Fch10_Chip17

Entries



nChipHit_F7_Fch10_Chip17

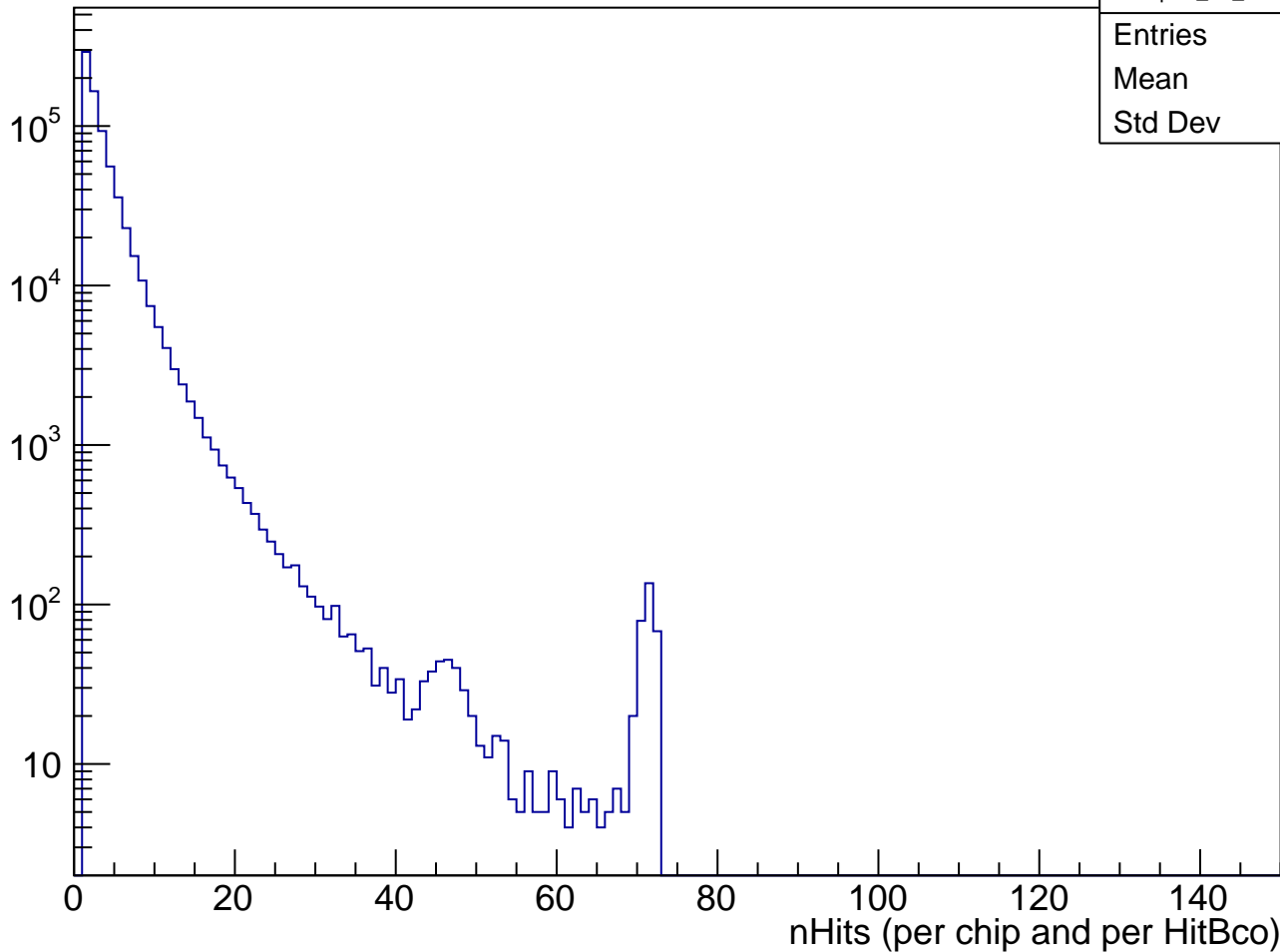
Entries 834786

Mean 3.122

Std Dev 3.677

nChipHit_F7_Fch10_Chip18

Entries



nChipHit_F7_Fch10_Chip18

Entries 723685

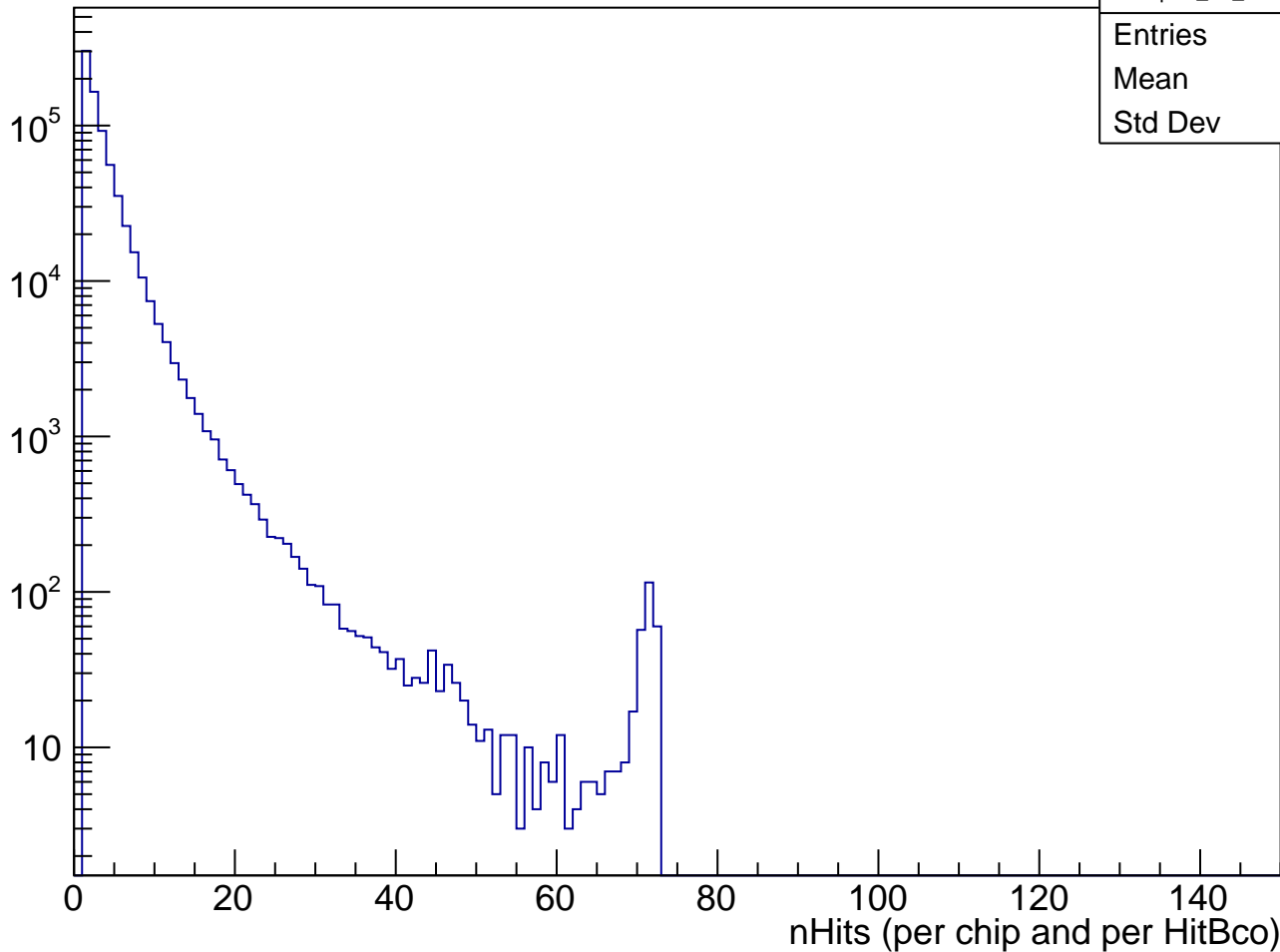
Mean 2.903

Std Dev 3.469

nHits (per chip and per HitBco)

nChipHit_F7_Fch10_Chip19

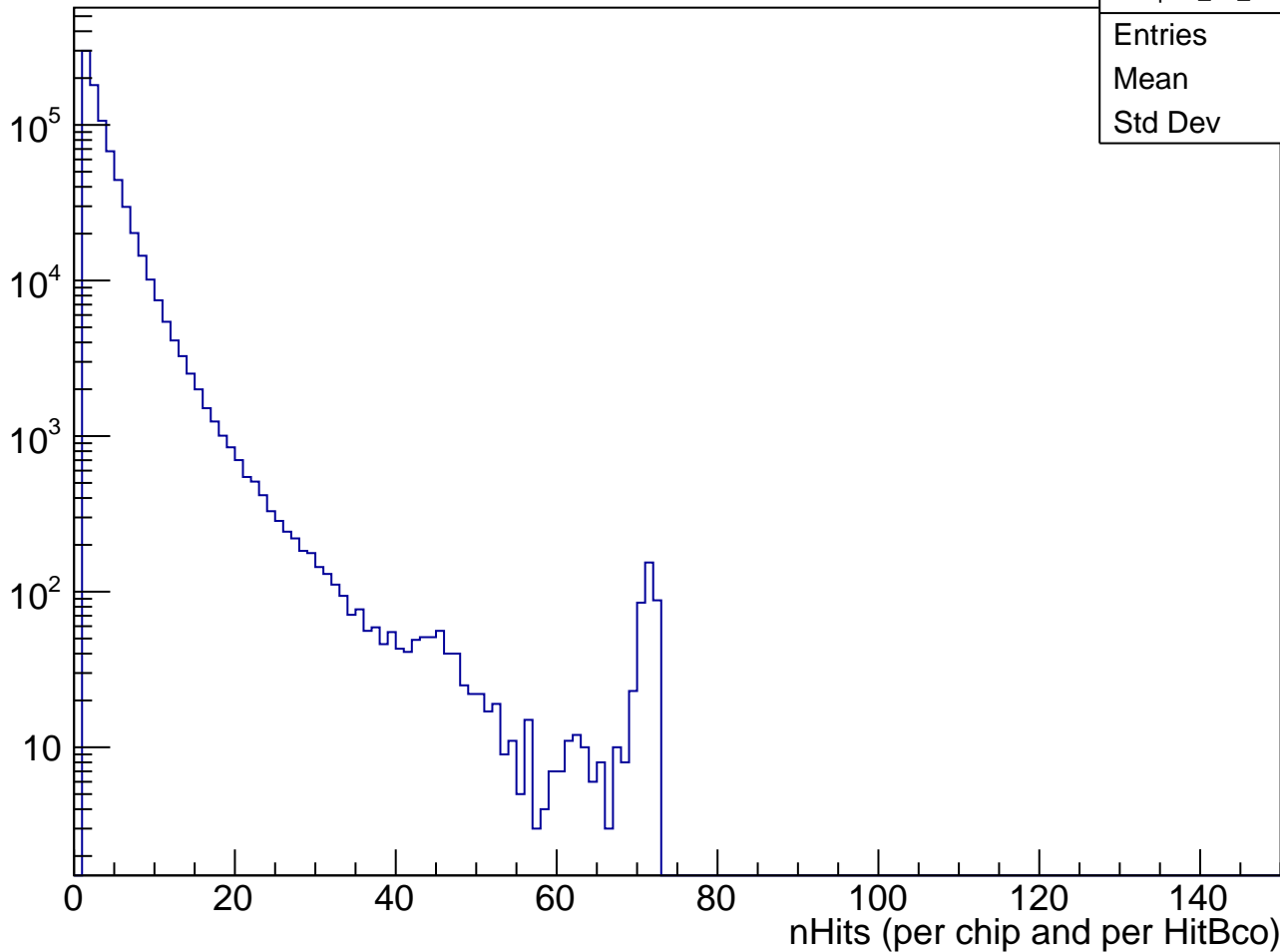
Entries



nChipHit_F7_Fch10_Chip19	
Entries	731988
Mean	2.855
Std Dev	3.377

nChipHit_F7_Fch10_Chip2

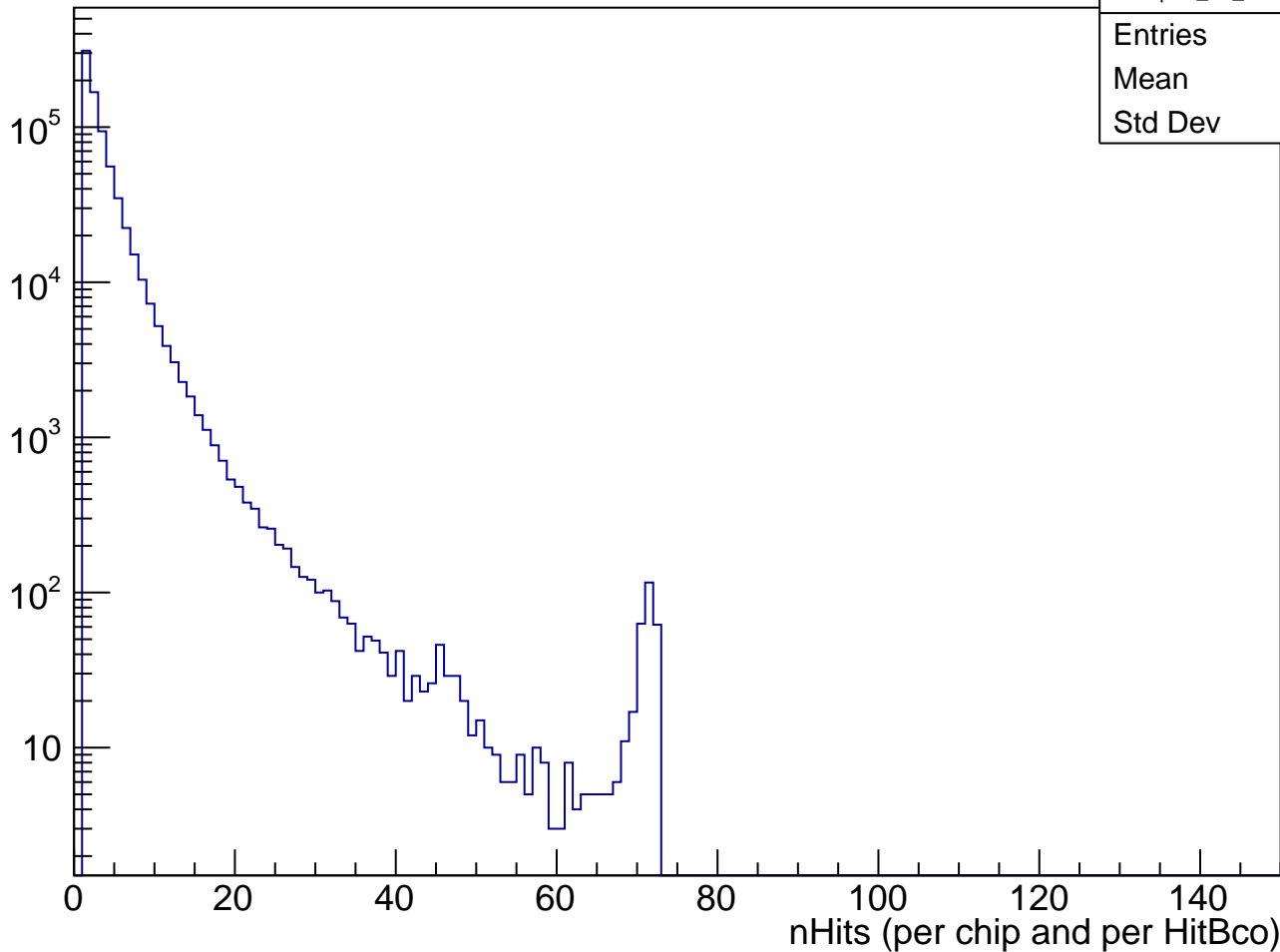
Entries



nChipHit_F7_Fch10_Chip2	
Entries	807347
Mean	3.139
Std Dev	3.692

nChipHit_F7_Fch10_Chip20

Entries



nChipHit_F7_Fch10_Chip20

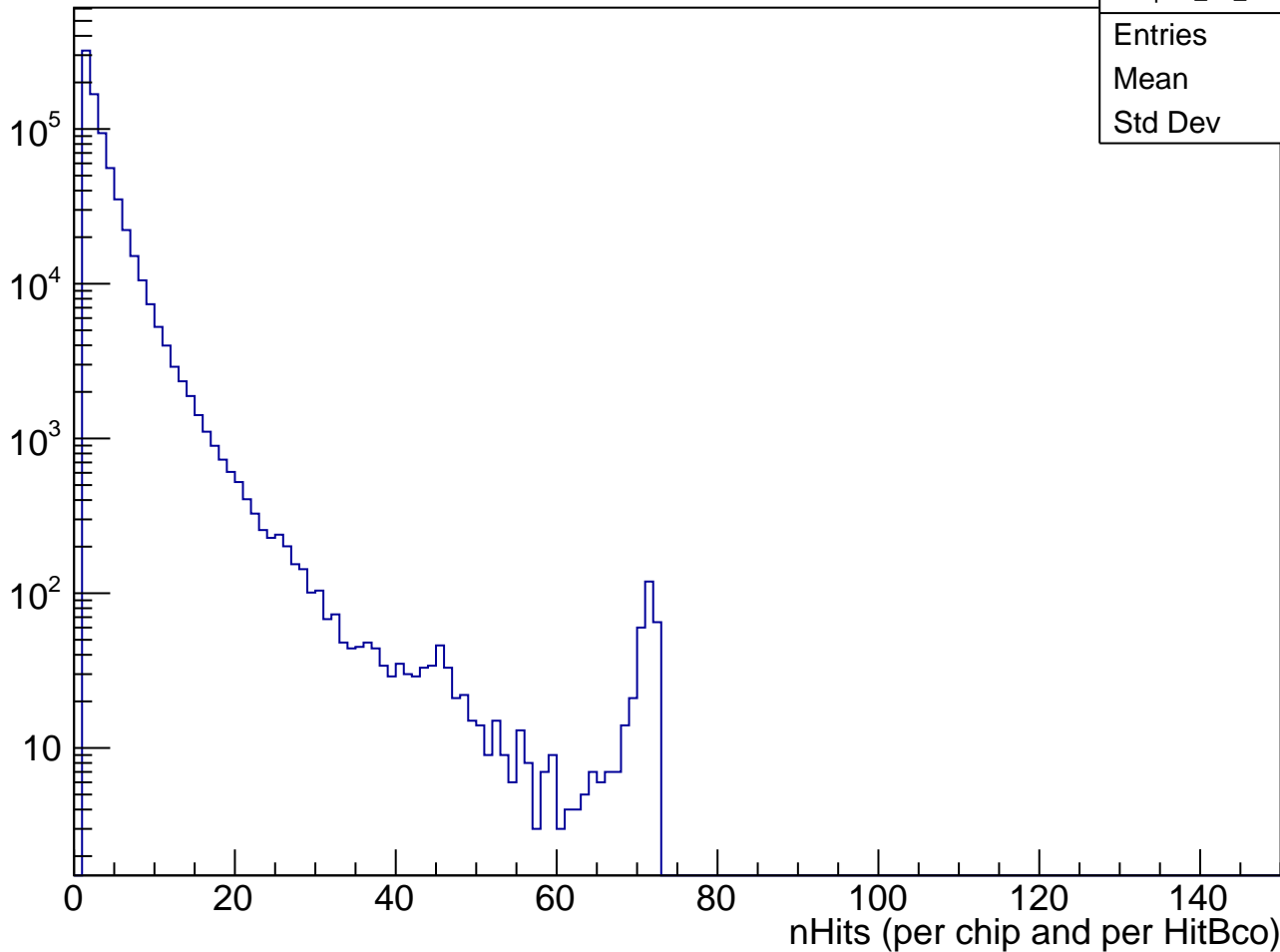
Entries 743390

Mean 2.82

Std Dev 3.344

nChipHit_F7_Fch10_Chip21

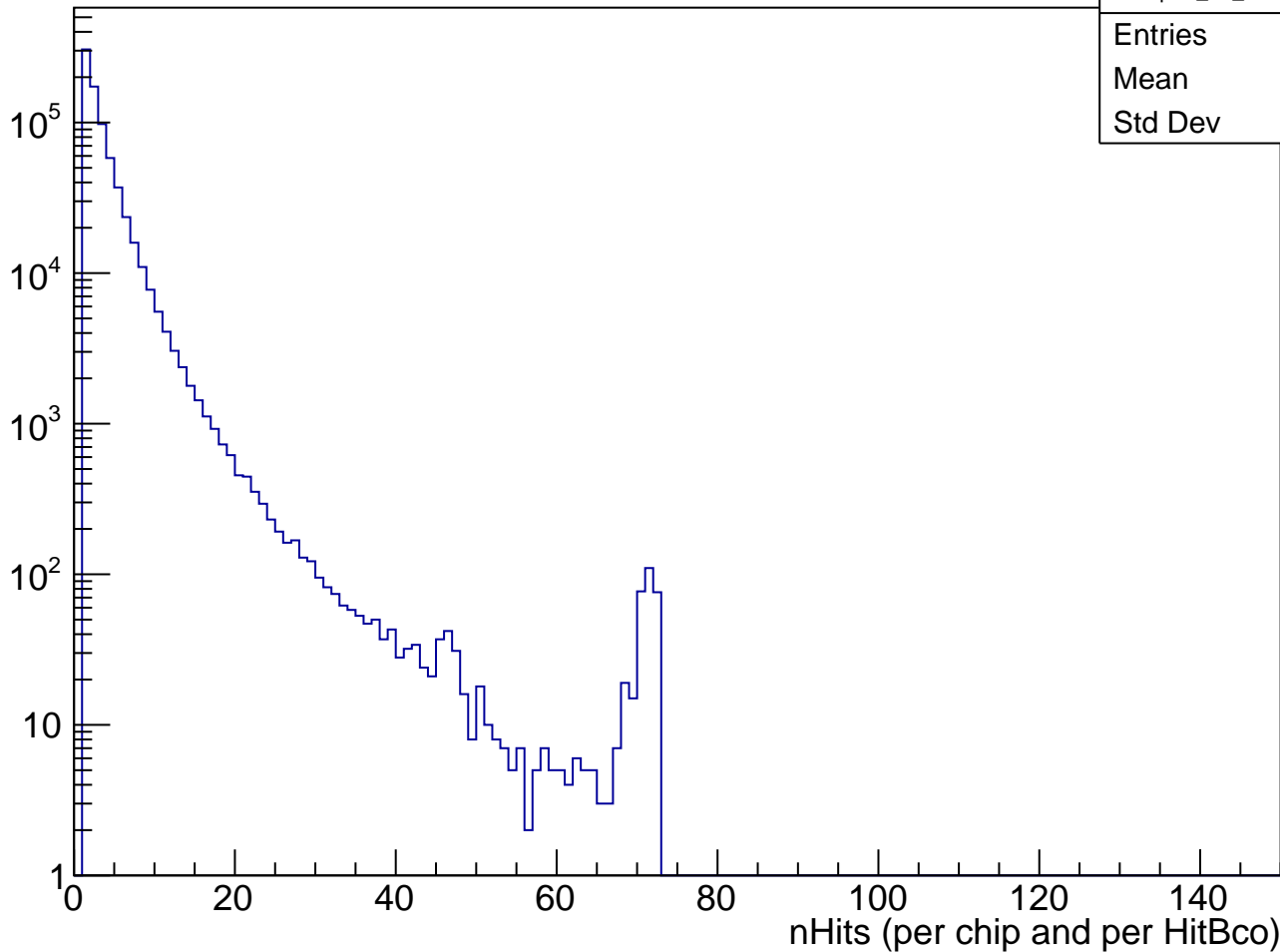
Entries



nChipHit_F7_Fch10_Chip21	
Entries	754358
Mean	2.802
Std Dev	3.347

nChipHit_F7_Fch10_Chip22

Entries



nChipHit_F7_Fch10_Chip22

Entries 755404

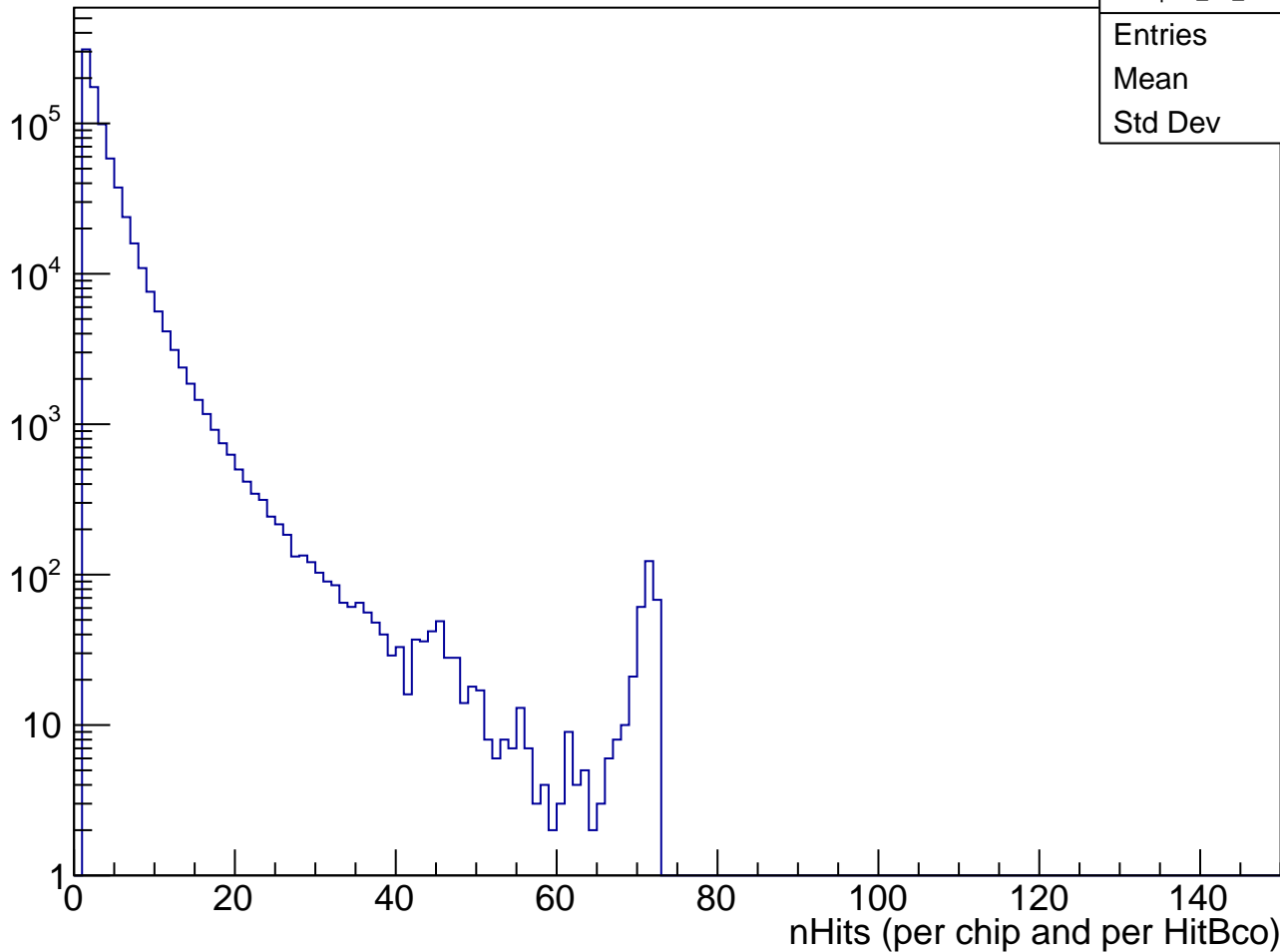
Mean 2.865

Std Dev 3.359

nHits (per chip and per HitBco)

nChipHit_F7_Fch10_Chip23

Entries



nChipHit_F7_Fch10_Chip23

Entries 763626

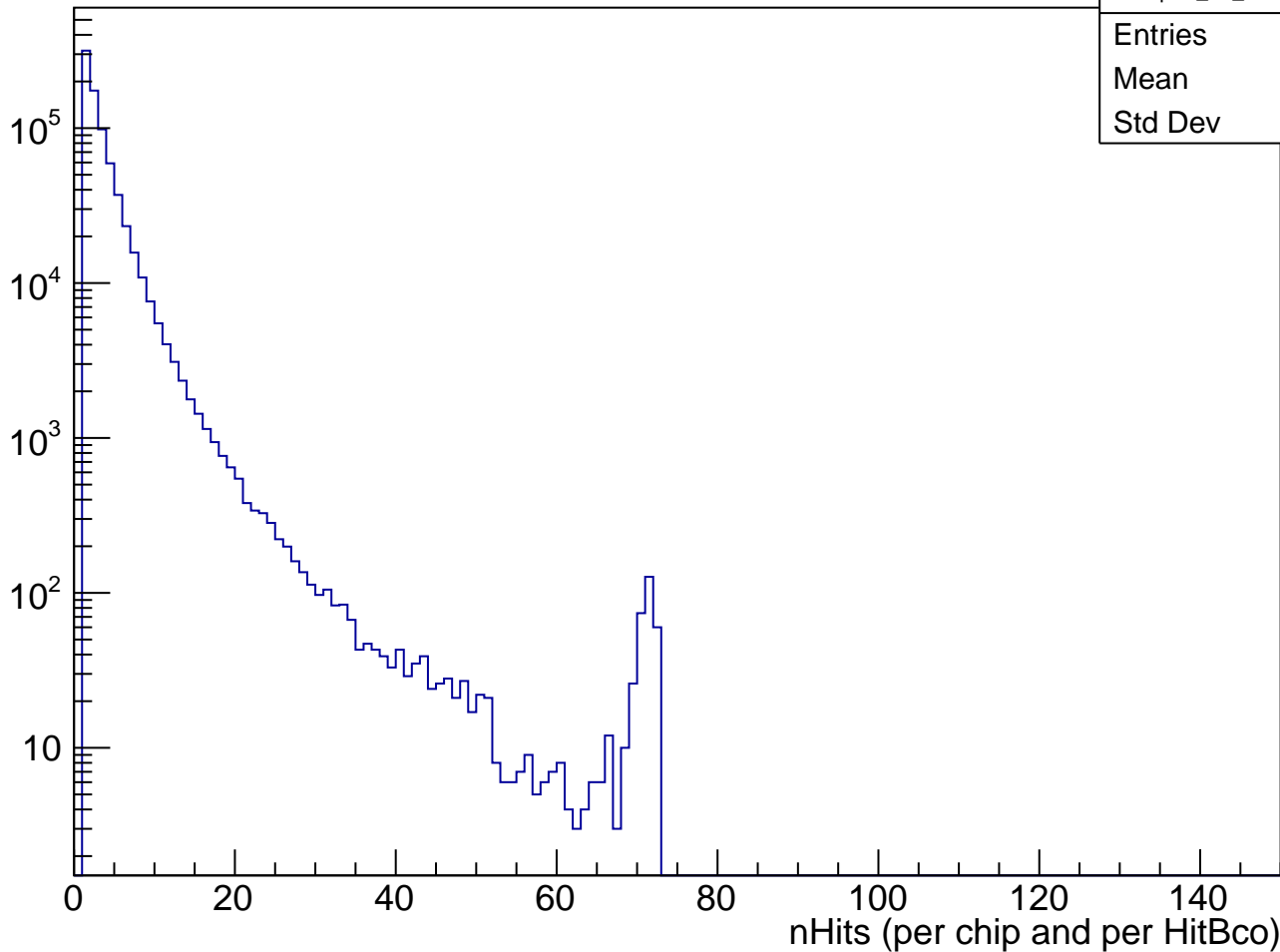
Mean 2.862

Std Dev 3.362

nHits (per chip and per HitBco)

nChipHit_F7_Fch10_Chip24

Entries



nChipHit_F7_Fch10_Chip24

Entries 768783

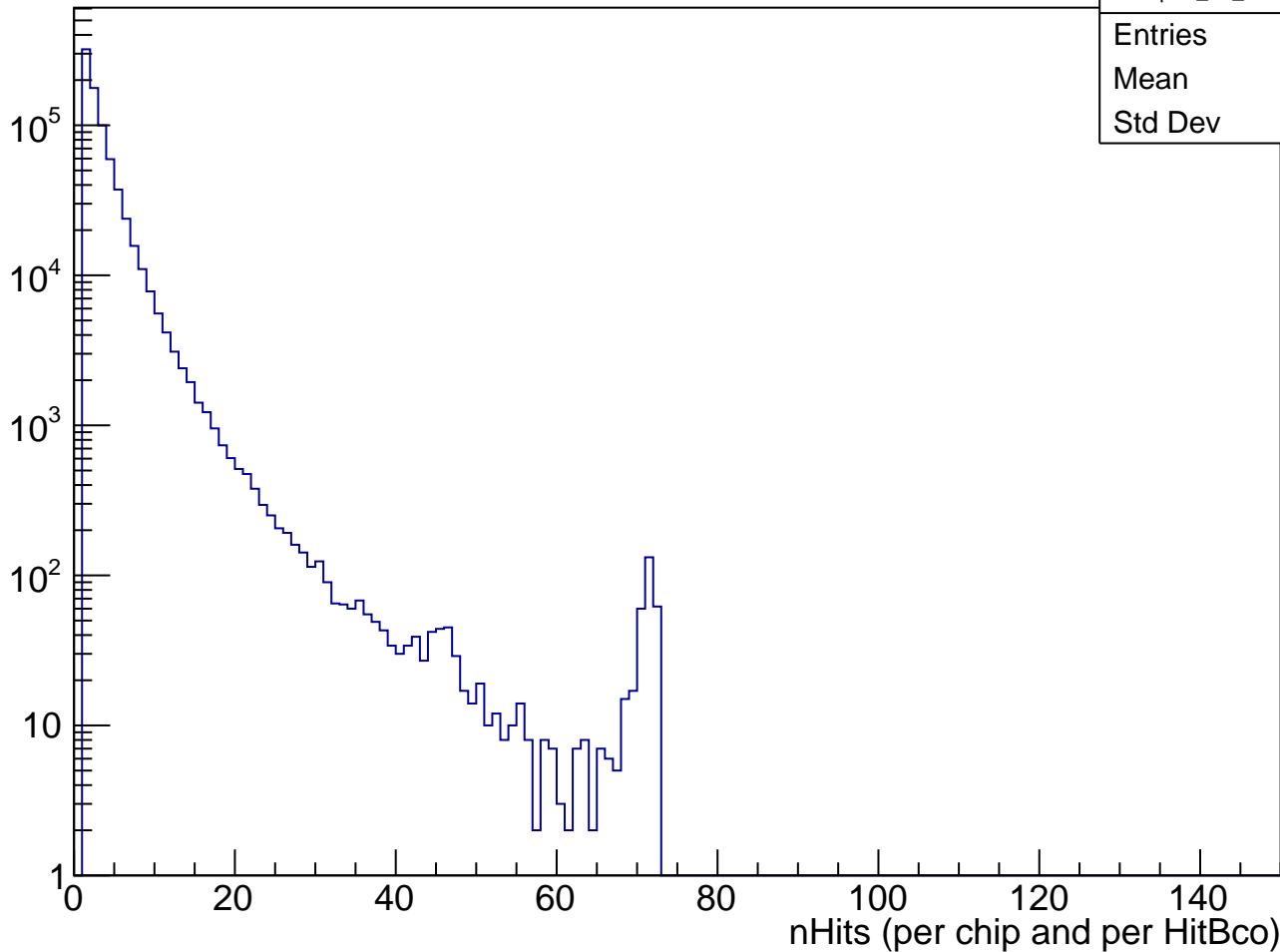
Mean 2.845

Std Dev 3.377

nHits (per chip and per HitBco)

nChipHit_F7_Fch10_Chip25

Entries



nChipHit_F7_Fch10_Chip25

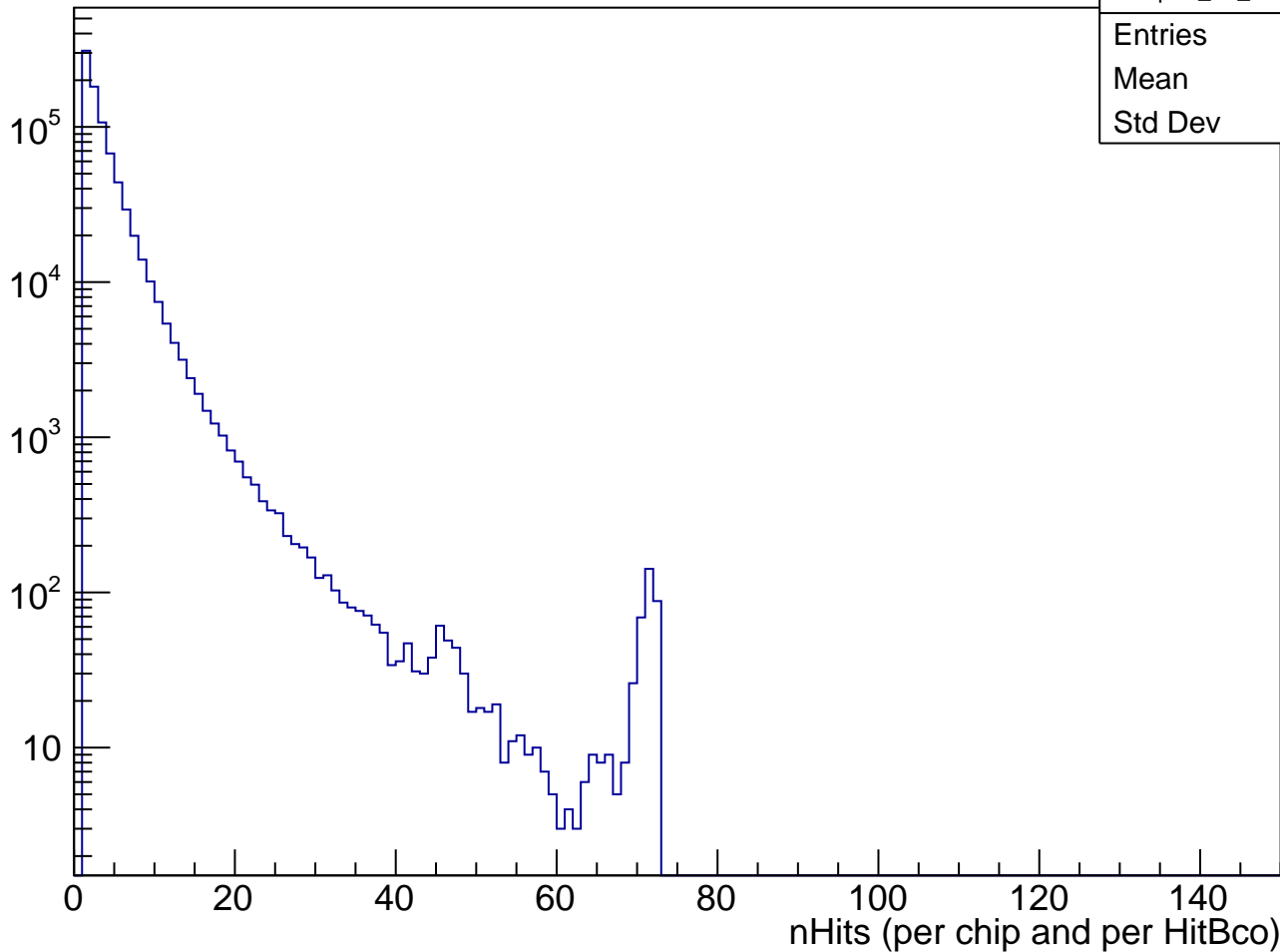
Entries 779346

Mean 2.844

Std Dev 3.37

nChipHit_F7_Fch10_Chip3

Entries

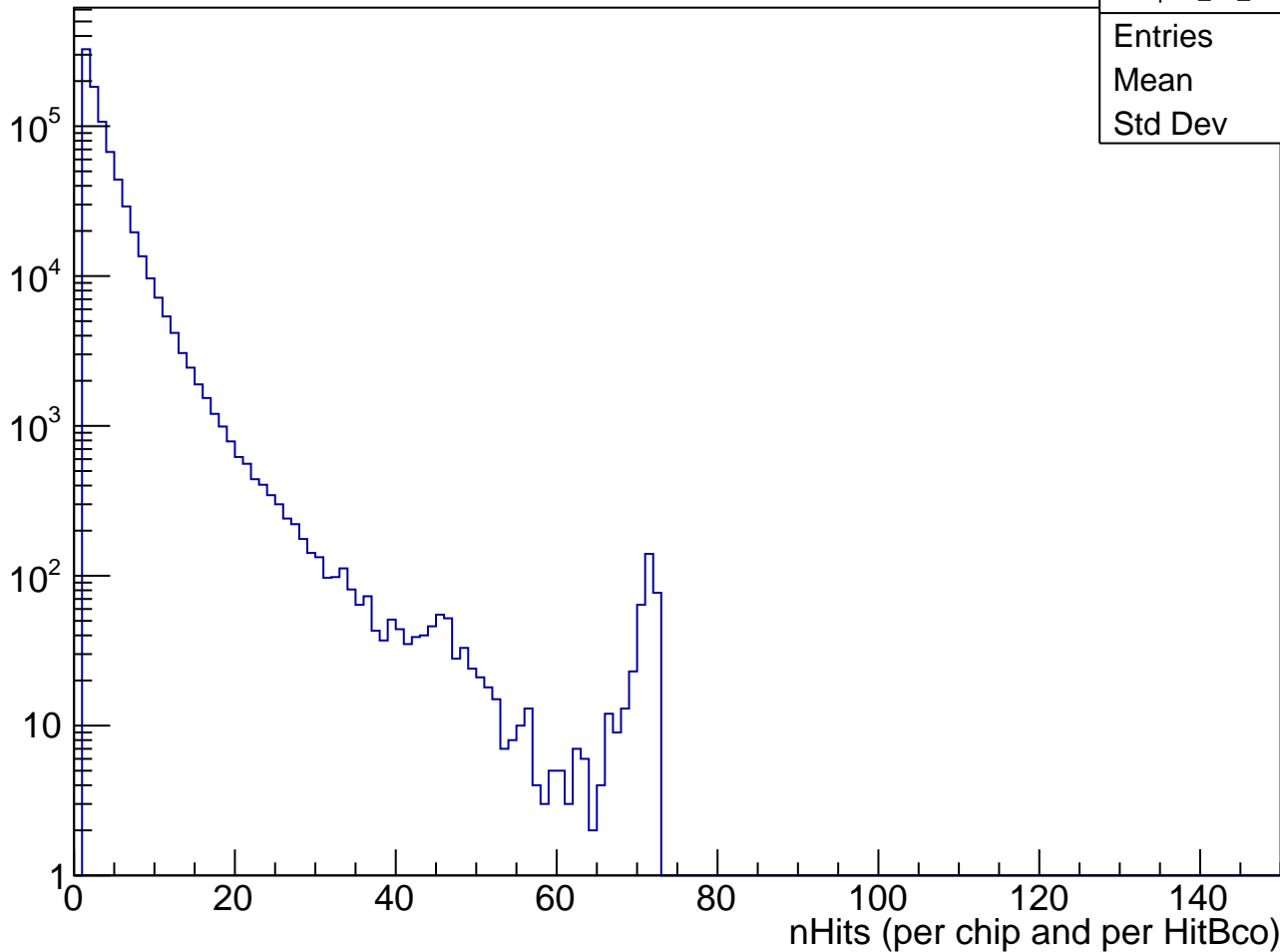


nChipHit_F7_Fch10_Chip3	
Entries	817500
Mean	3.091
Std Dev	3.623

nHits (per chip and per HitBco)

nChipHit_F7_Fch10_Chip4

Entries

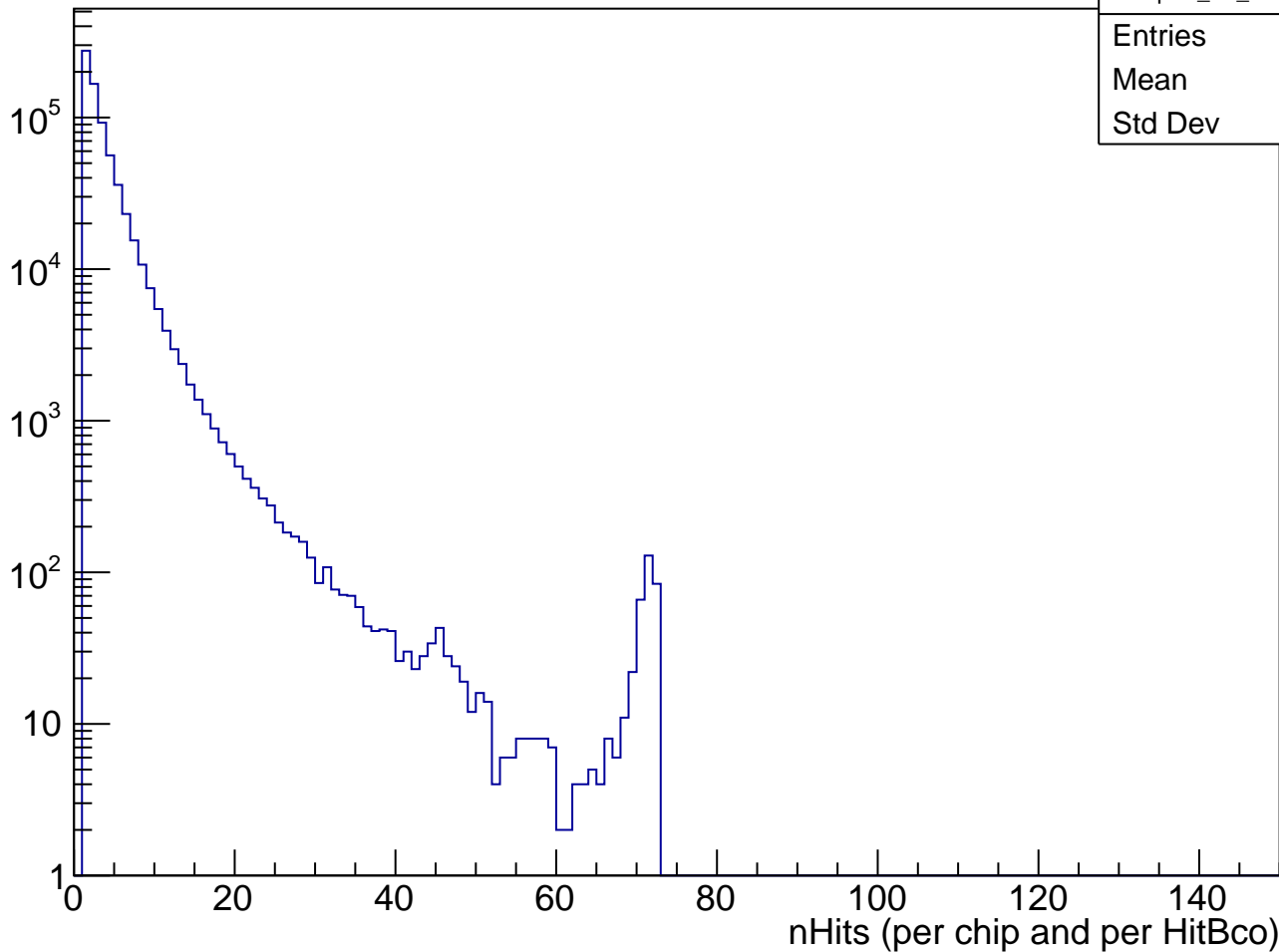


nChipHit_F7_Fch10_Chip4	
Entries	833731
Mean	3.031
Std Dev	3.562

nHits (per chip and per HitBco)

nChipHit_F7_Fch10_Chip5

Entries

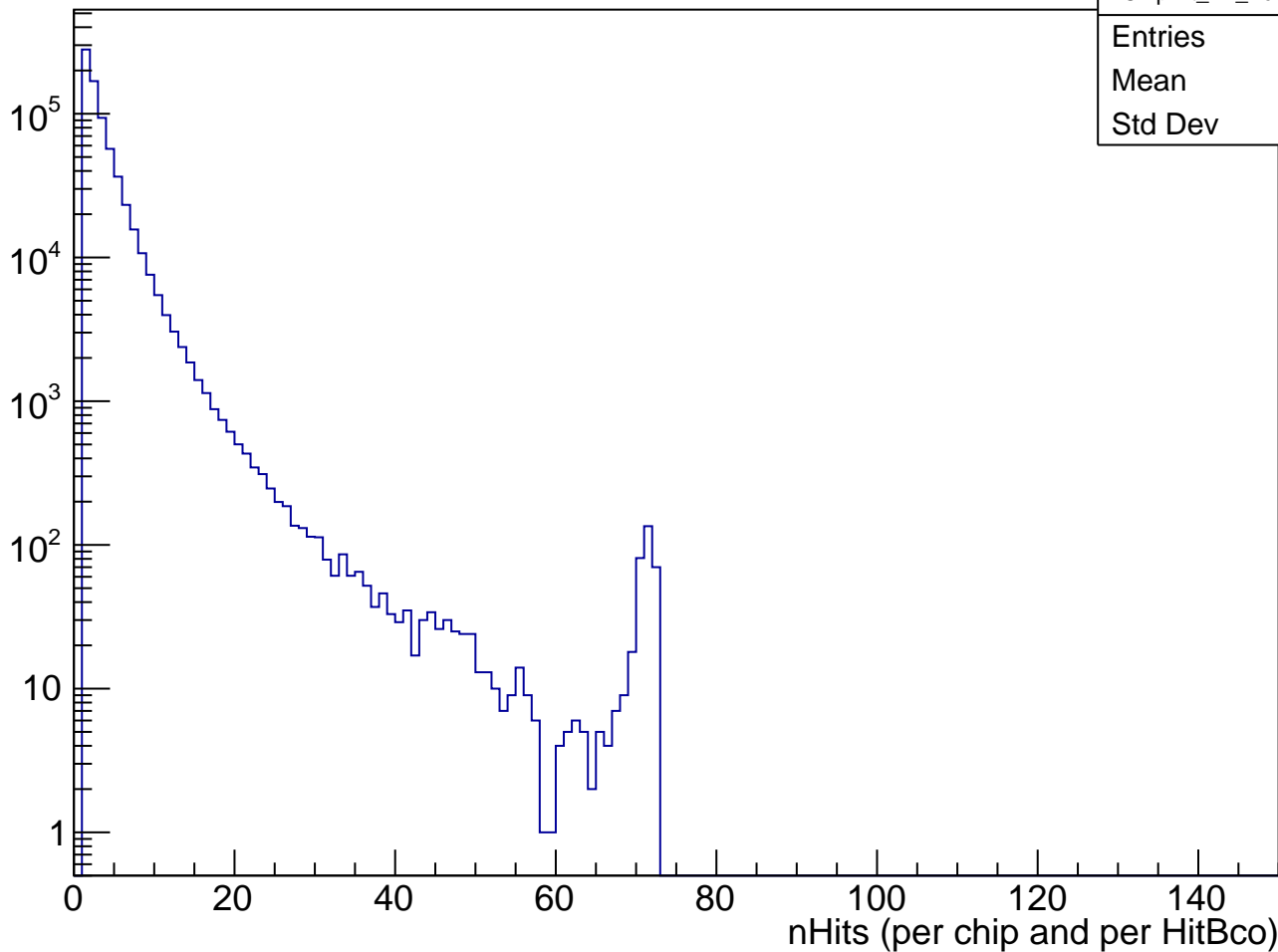


nChipHit_F7_Fch10_Chip5	
Entries	709262
Mean	2.937
Std Dev	3.466

nHits (per chip and per HitBco)

nChipHit_F7_Fch10_Chip6

Entries



nChipHit_F7_Fch10_Chip6

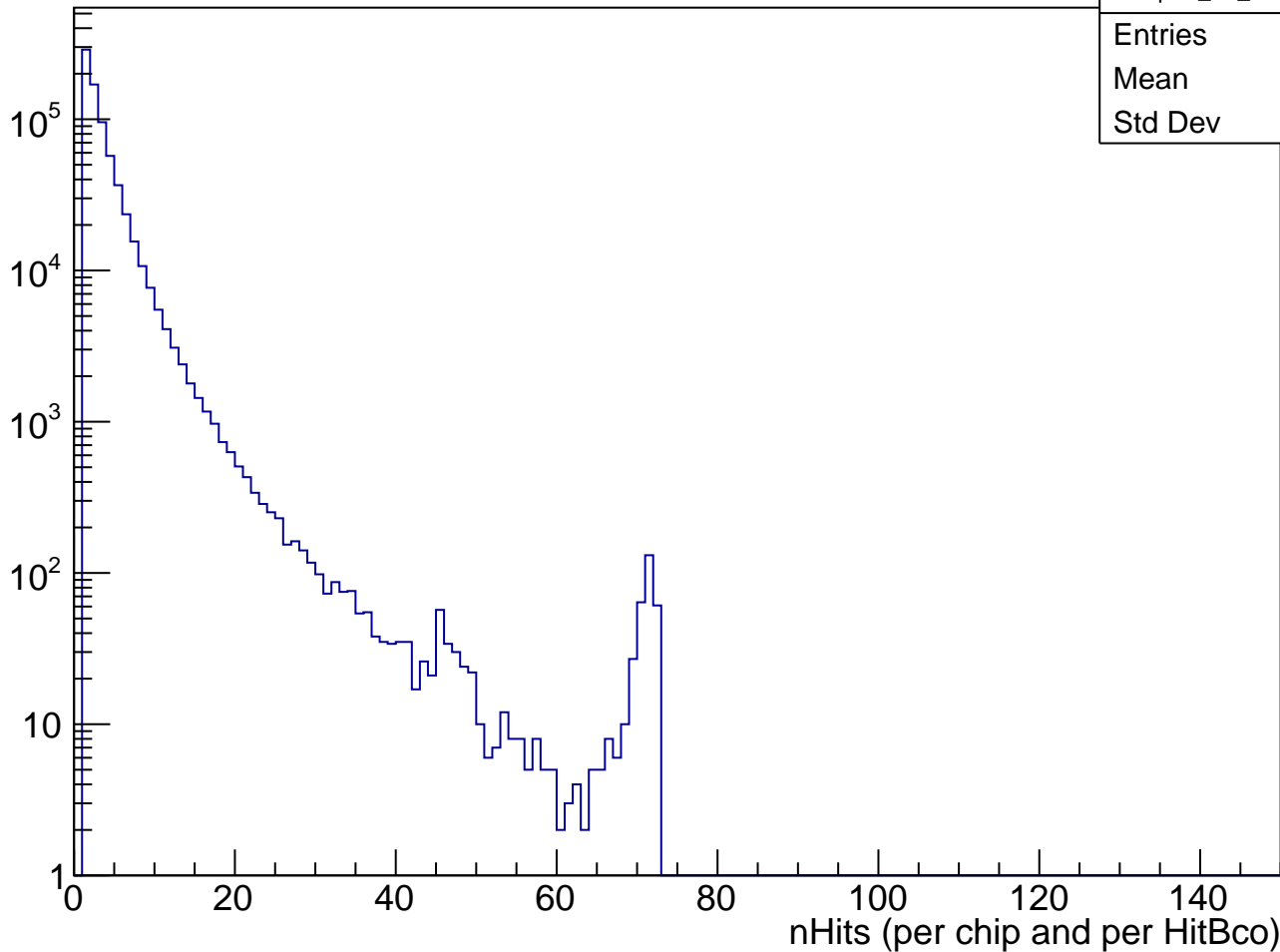
Entries 718360

Mean 2.931

Std Dev 3.448

nChipHit_F7_Fch10_Chip7

Entries

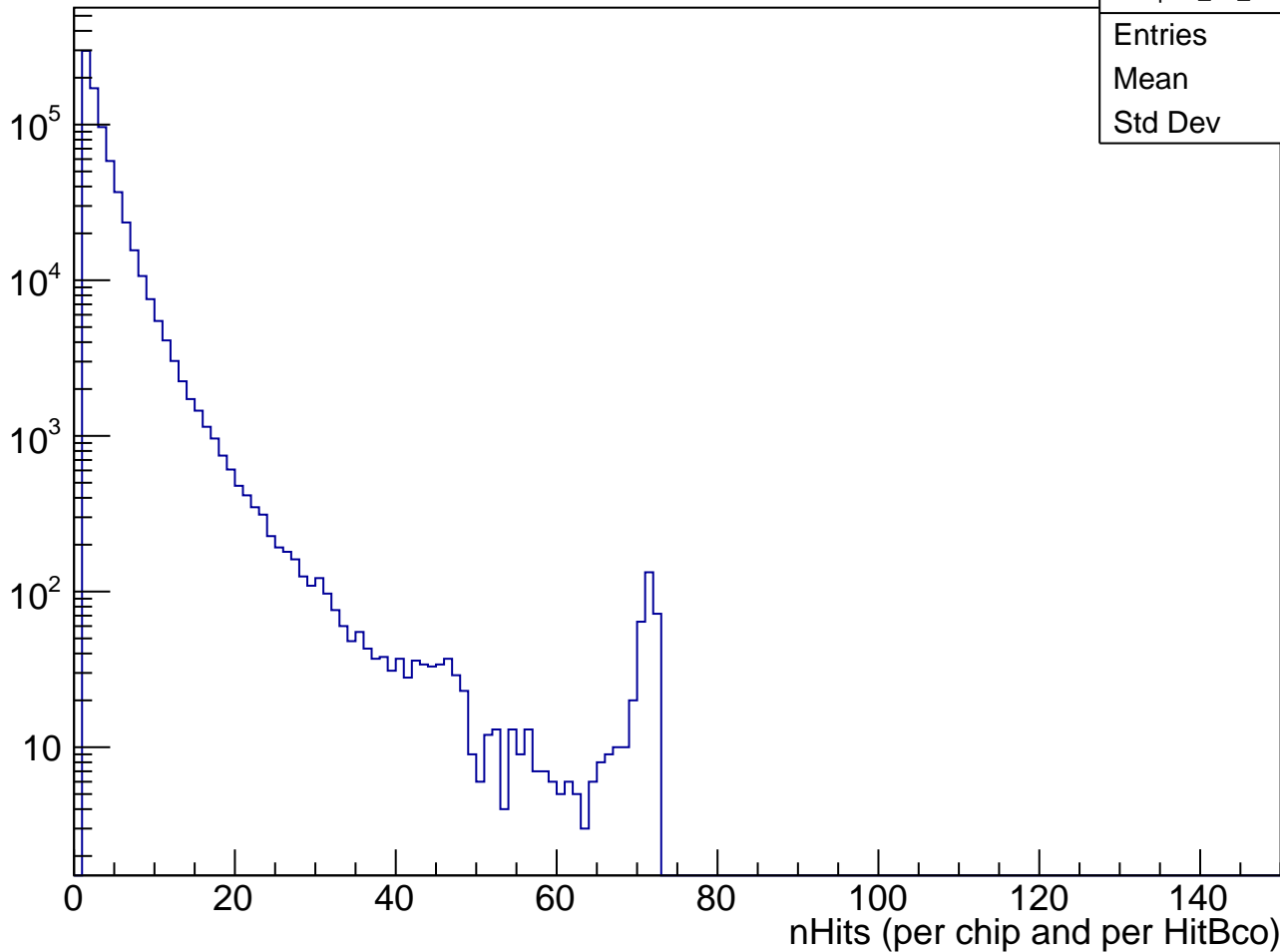


nChipHit_F7_Fch10_Chip7	
Entries	731043
Mean	2.912
Std Dev	3.419

nHits (per chip and per HitBco)

nChipHit_F7_Fch10_Chip8

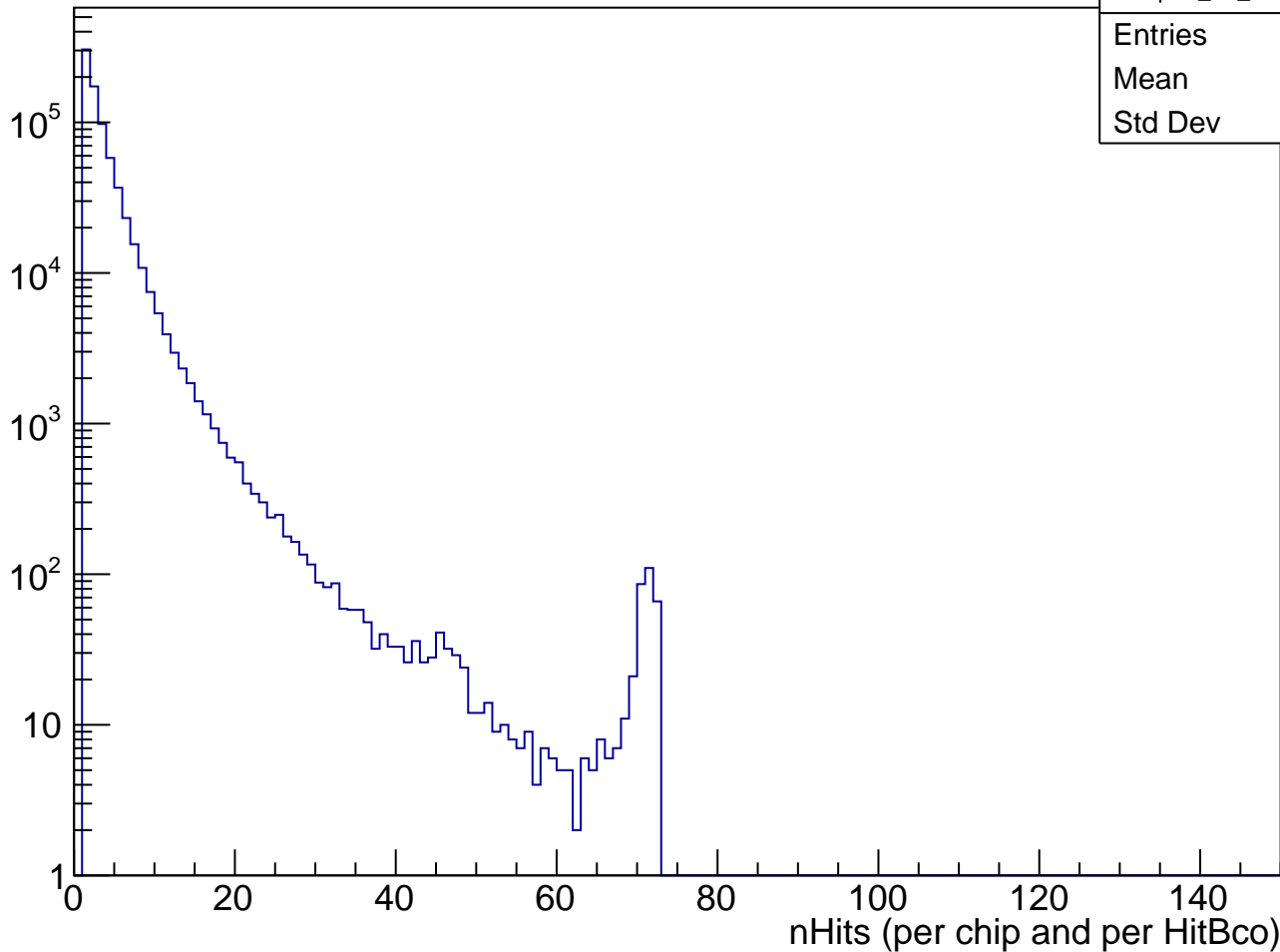
Entries



nChipHit_F7_Fch10_Chip8	
Entries	743013
Mean	2.884
Std Dev	3.404

nChipHit_F7_Fch10_Chip9

Entries

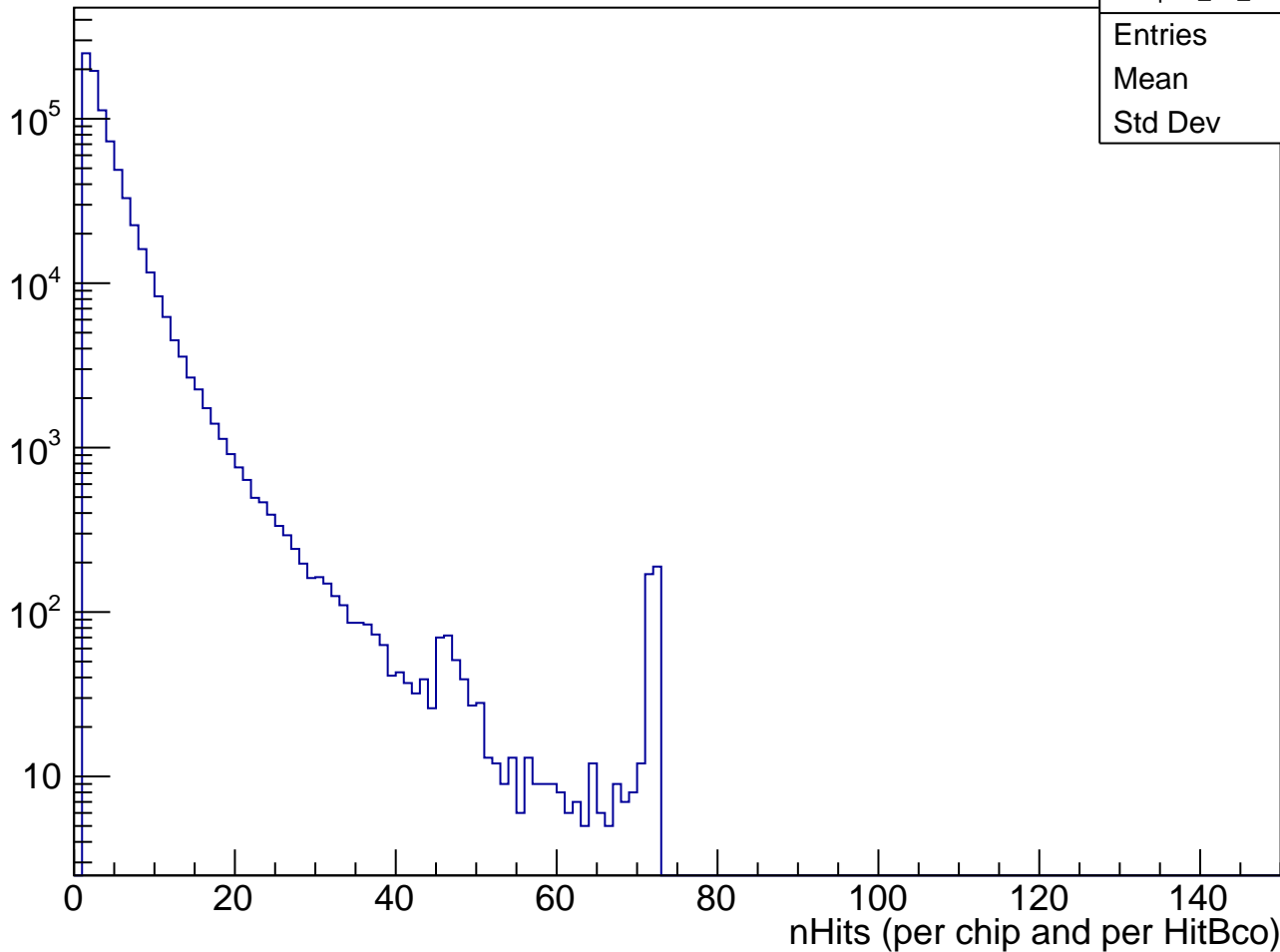


nChipHit_F7_Fch10_Chip9	
Entries	752797
Mean	2.86
Std Dev	3.374

nHits (per chip and per HitBco)

nChipHit_F7_Fch11_Chip0

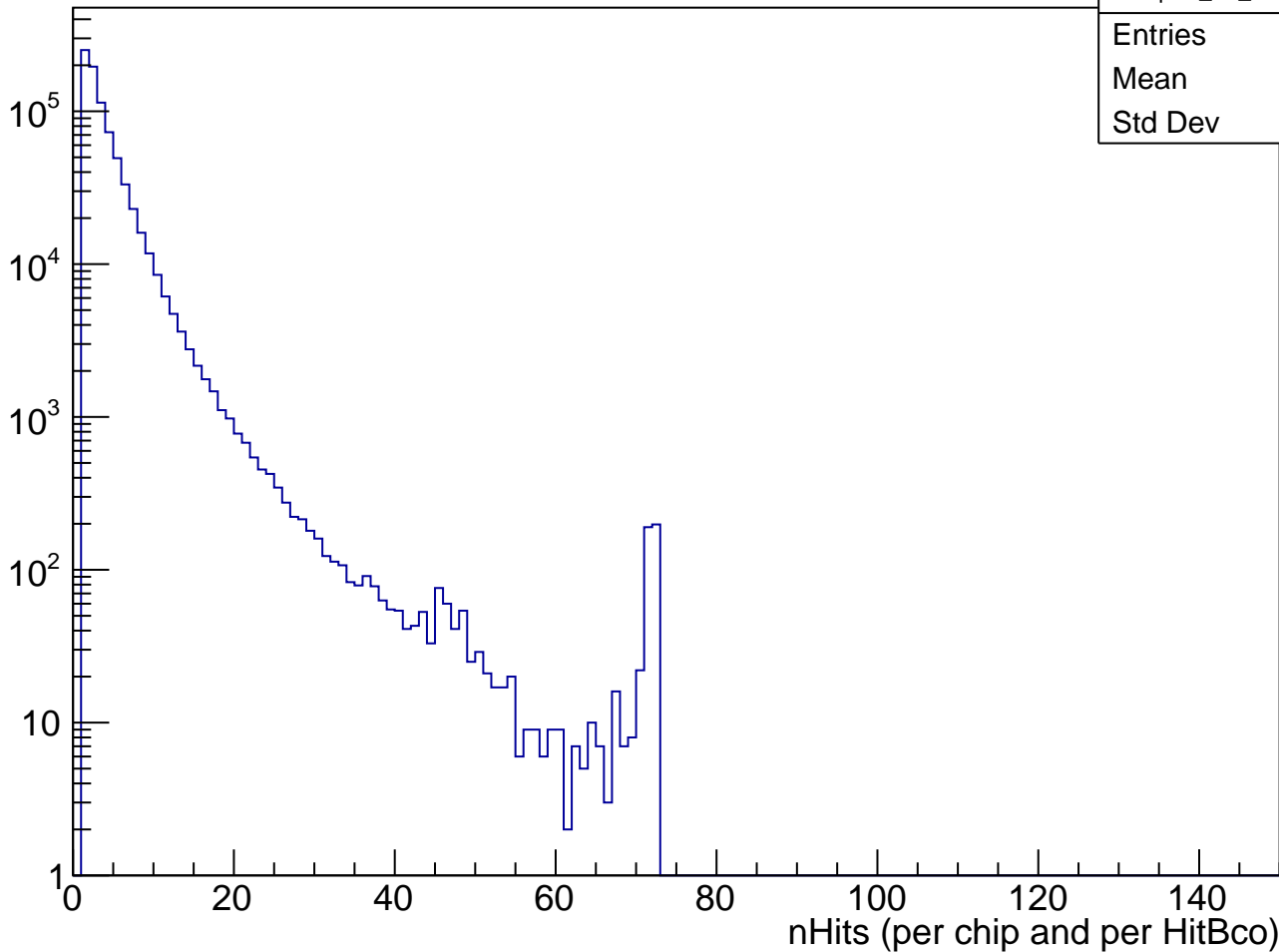
Entries



nChipHit_F7_Fch11_Chip0	
Entries	801999
Mean	3.365
Std Dev	3.823

nChipHit_F7_Fch11_Chip1

Entries

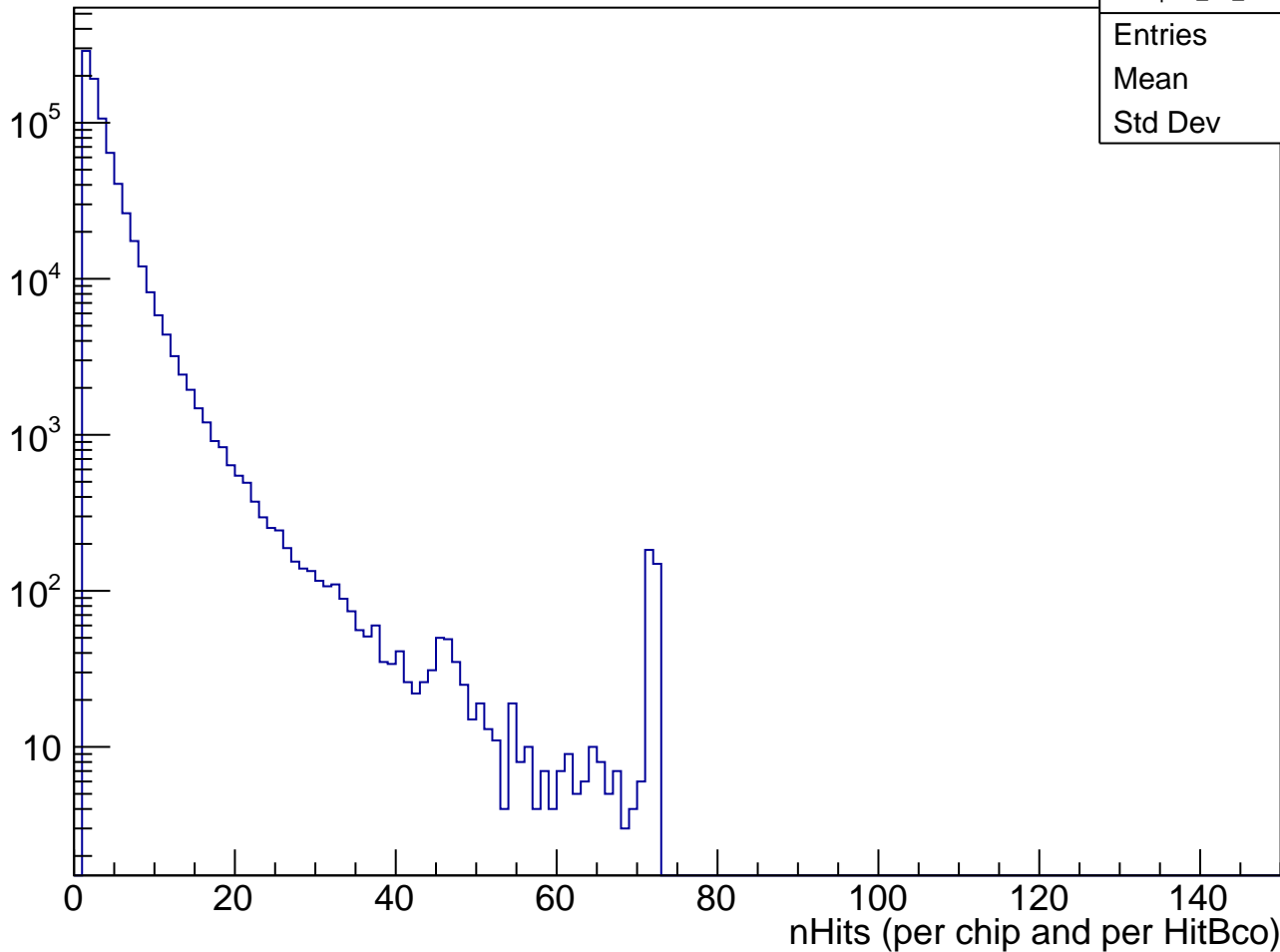


nChipHit_F7_Fch11_Chip1	
Entries	806980
Mean	3.38
Std Dev	3.877

nHits (per chip and per HitBco)

nChipHit_F7_Fch11_Chip10

Entries



nChipHit_F7_Fch11_Chip10

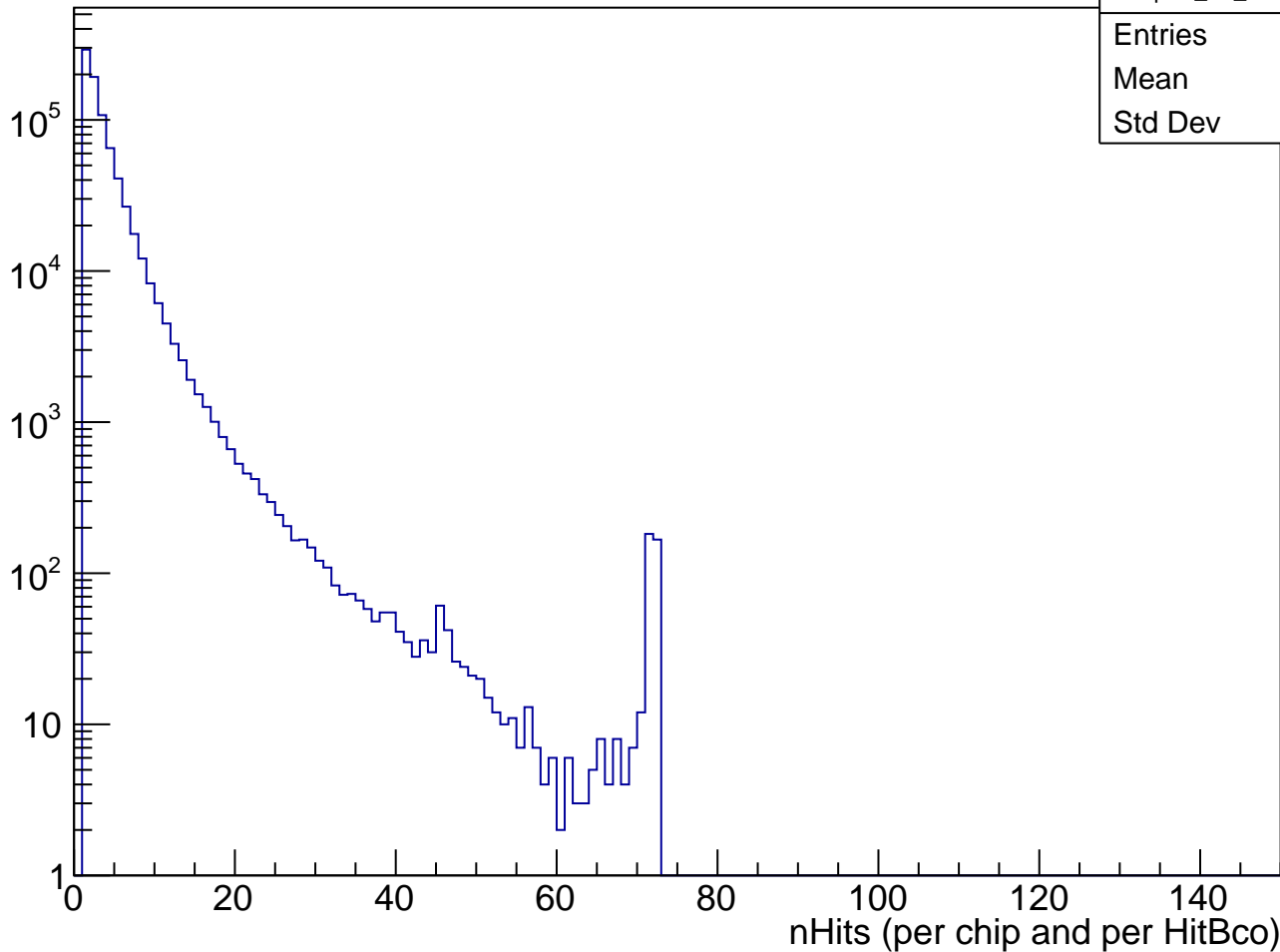
Entries 782349

Mean 2.972

Std Dev 3.467

nChipHit_F7_Fch11_Chip11

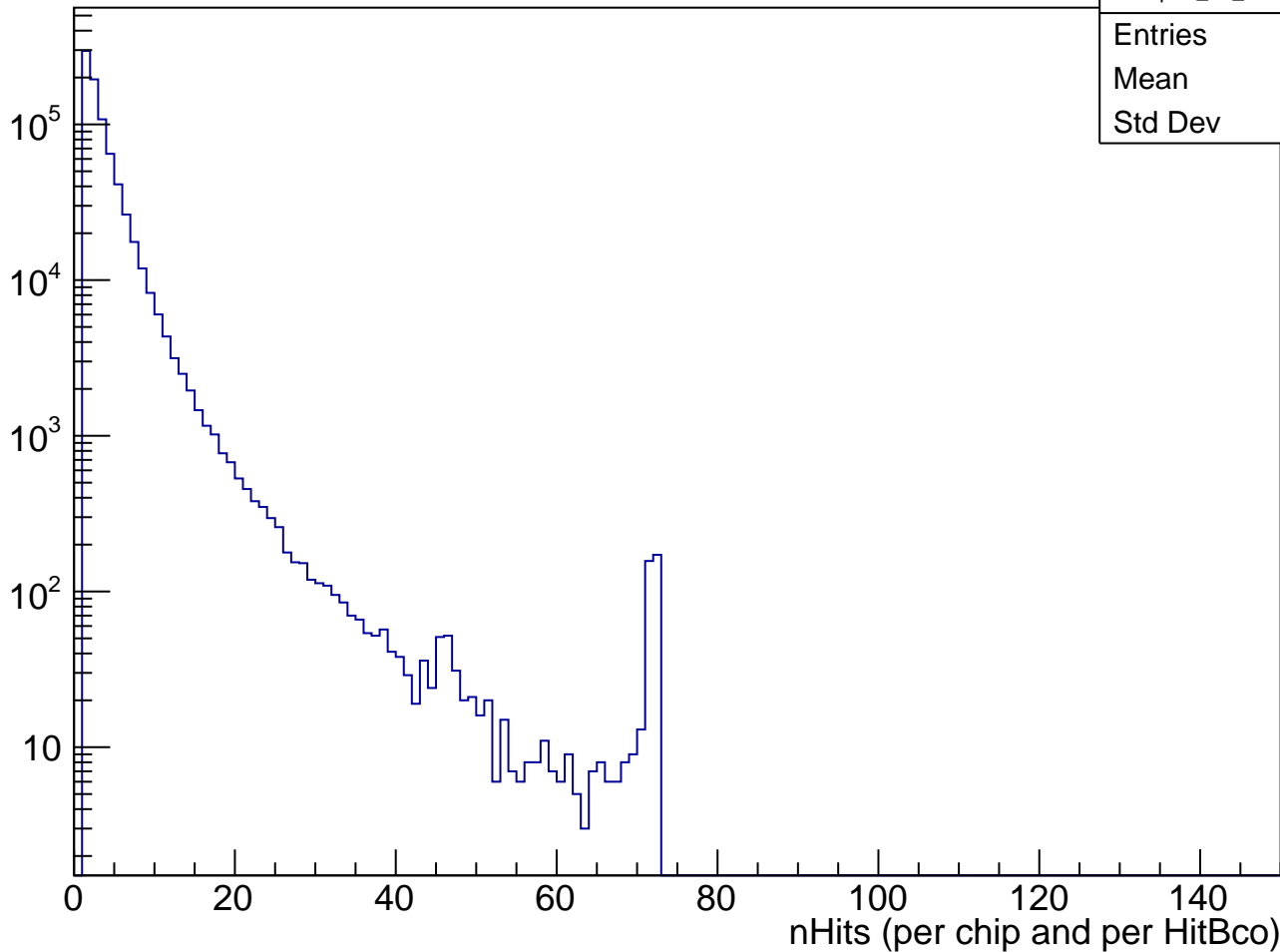
Entries



nChipHit_F7_Fch11_Chip11	
Entries	790736
Mean	2.984
Std Dev	3.505

nChipHit_F7_Fch11_Chip12

Entries



nChipHit_F7_Fch11_Chip12

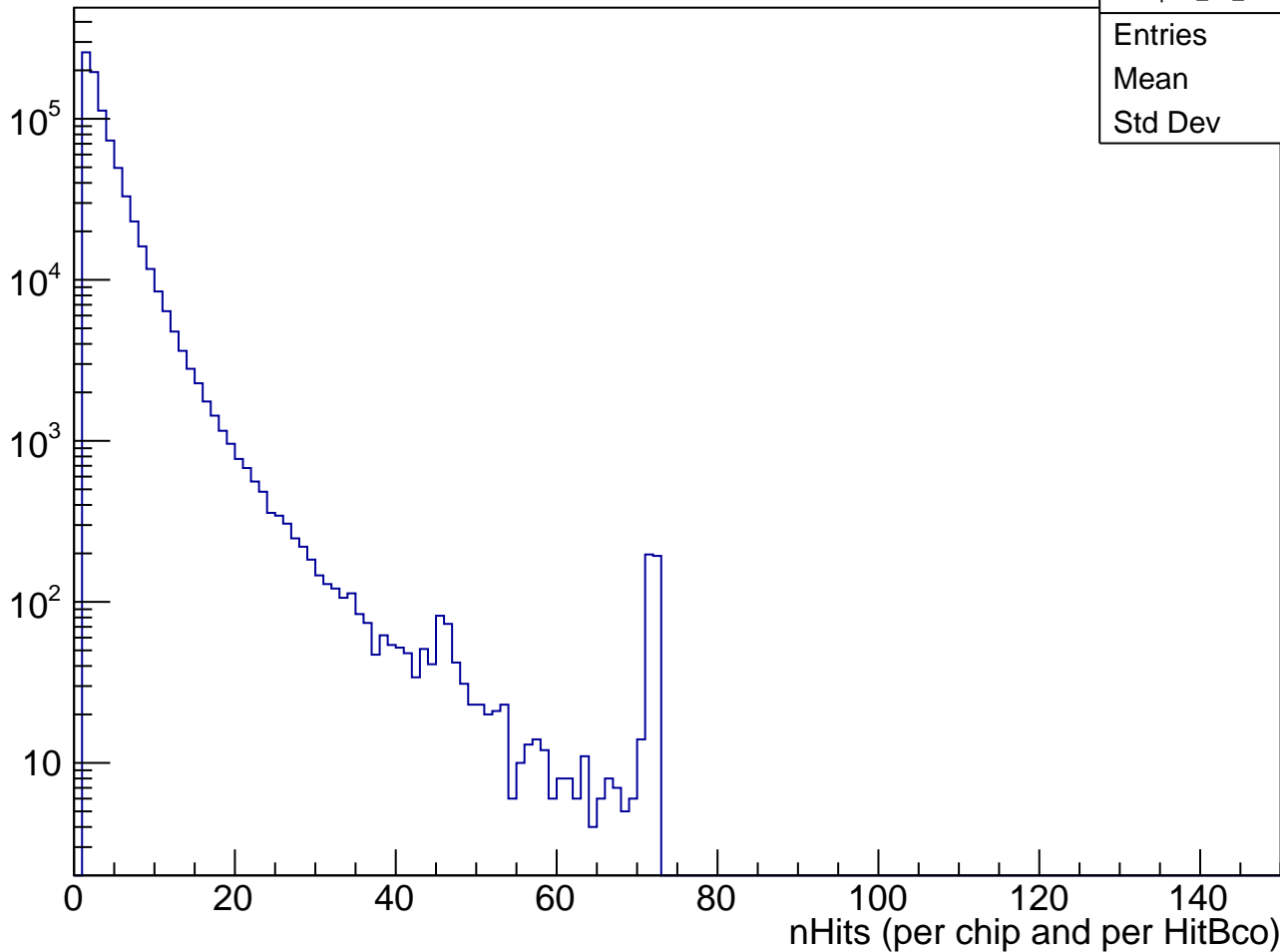
Entries 796633

Mean 2.957

Std Dev 3.465

nChipHit_F7_Fch11_Chip13

Entries



nChipHit_F7_Fch11_Chip13

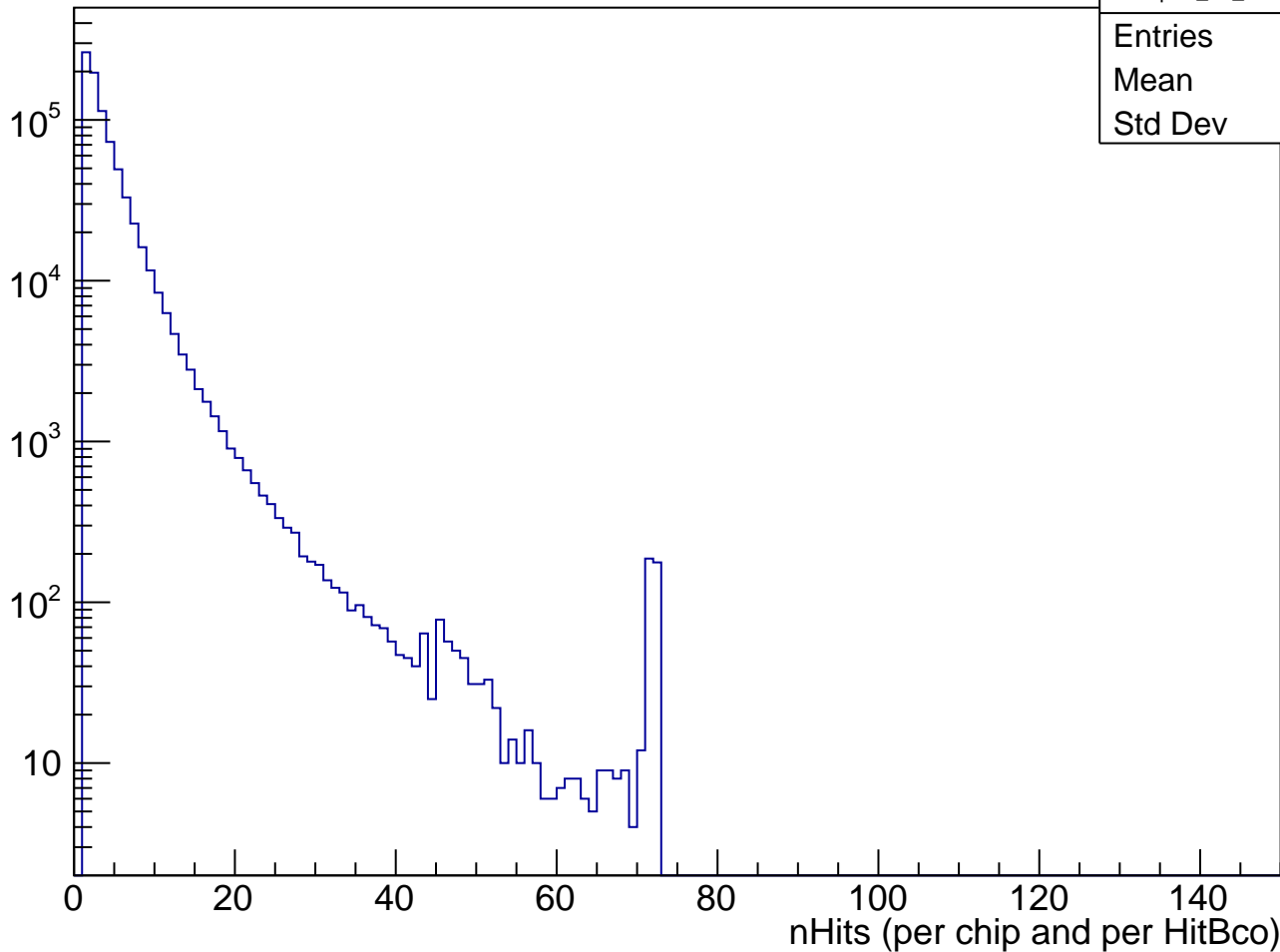
Entries 812719

Mean 3.365

Std Dev 3.868

nChipHit_F7_Fch11_Chip14

Entries



nChipHit_F7_Fch11_Chip14

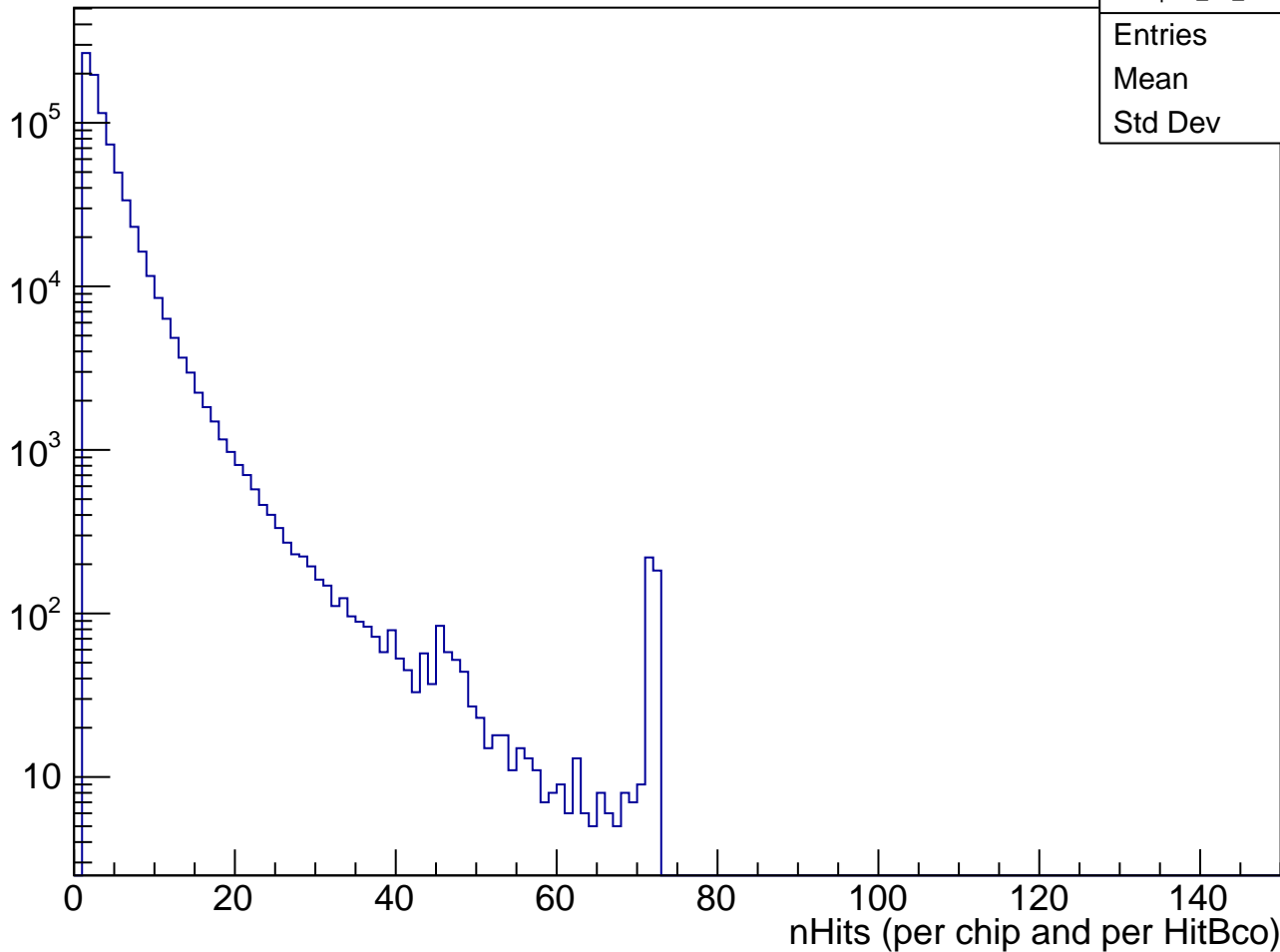
Entries 818661

Mean 3.339

Std Dev 3.844

nChipHit_F7_Fch11_Chip15

Entries



nChipHit_F7_Fch11_Chip15

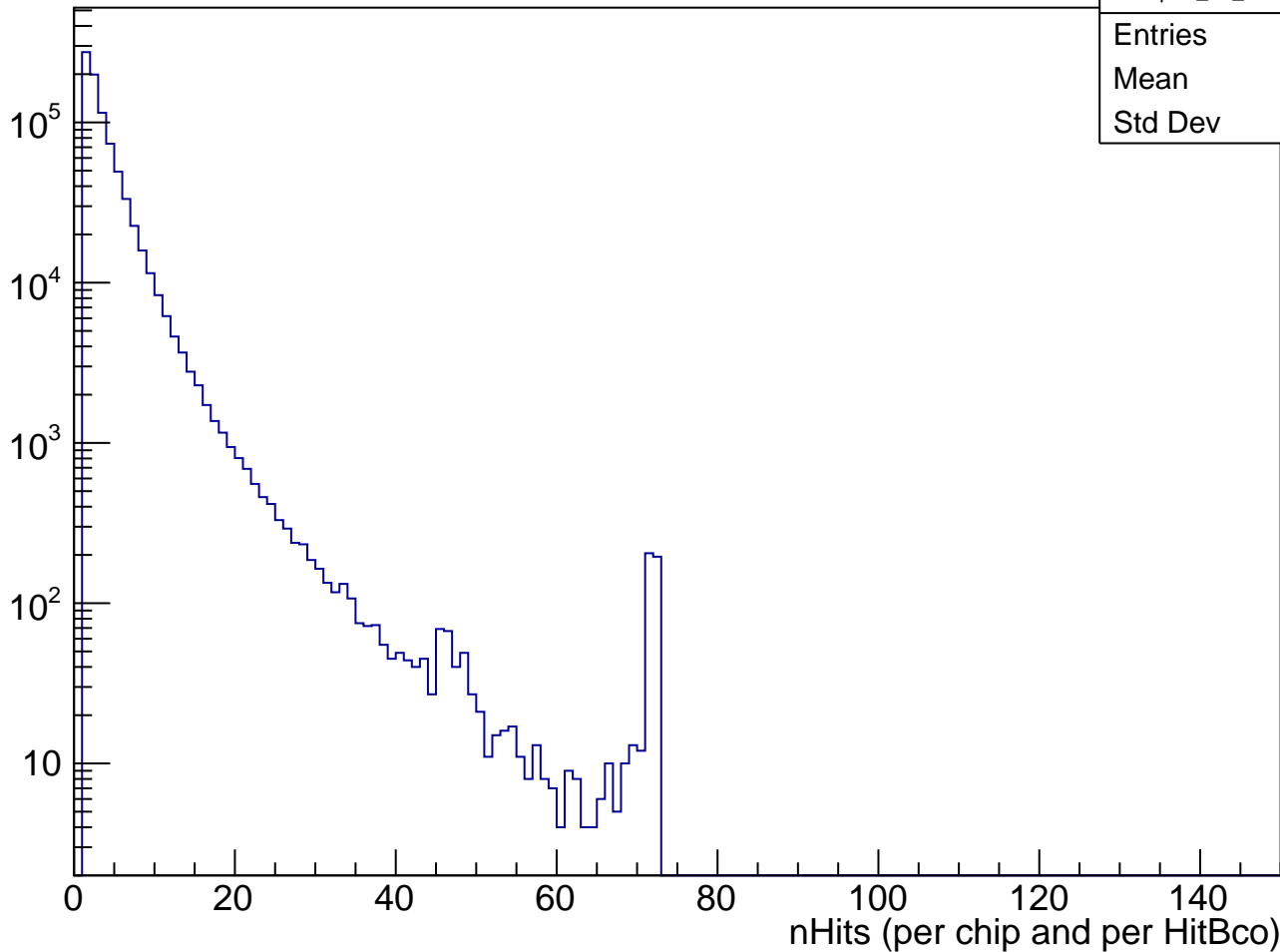
Entries 826903

Mean 3.35

Std Dev 3.875

nChipHit_F7_Fch11_Chip16

Entries



nChipHit_F7_Fch11_Chip16

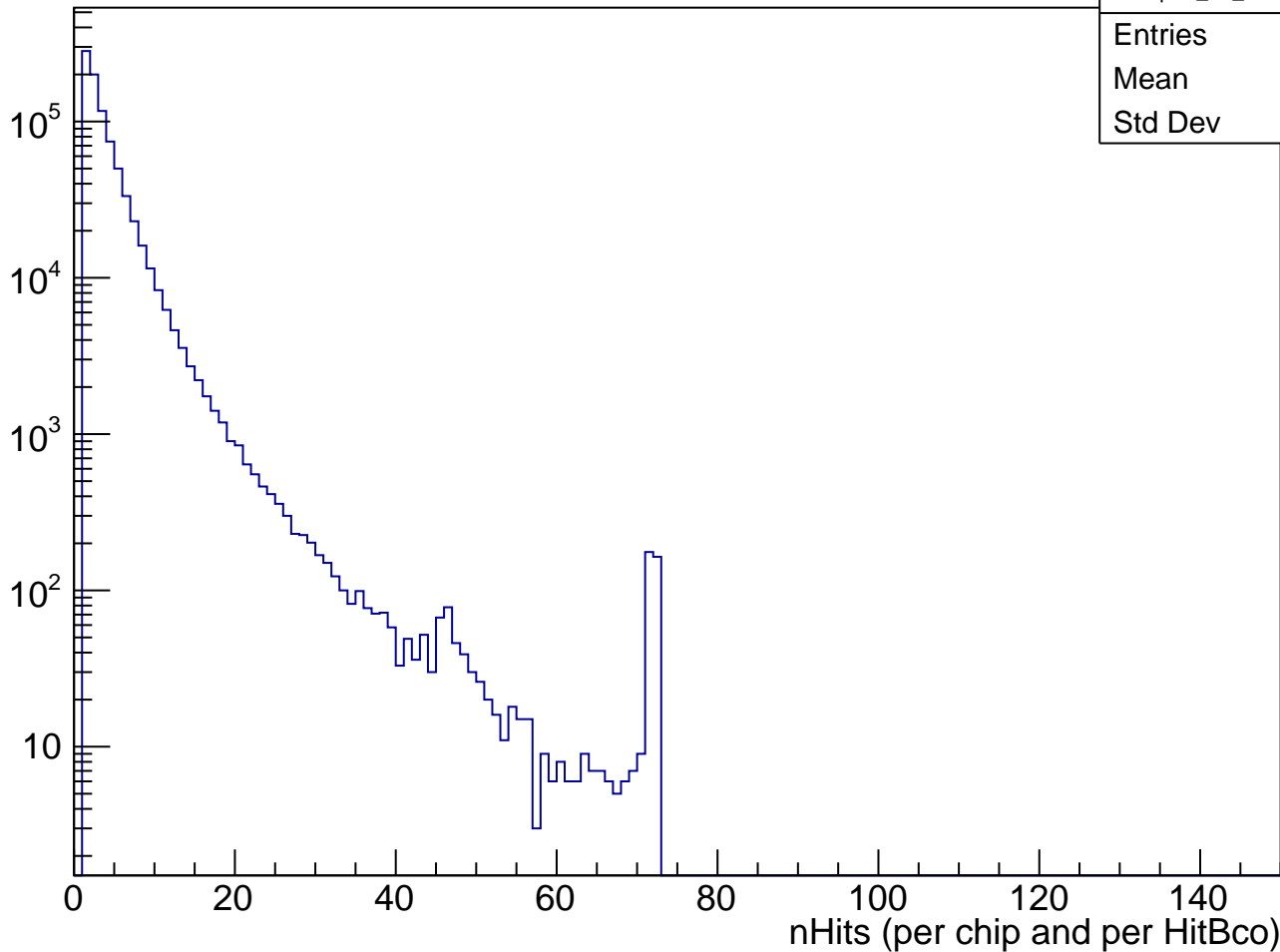
Entries 833886

Mean 3.304

Std Dev 3.828

nChipHit_F7_Fch11_Chip17

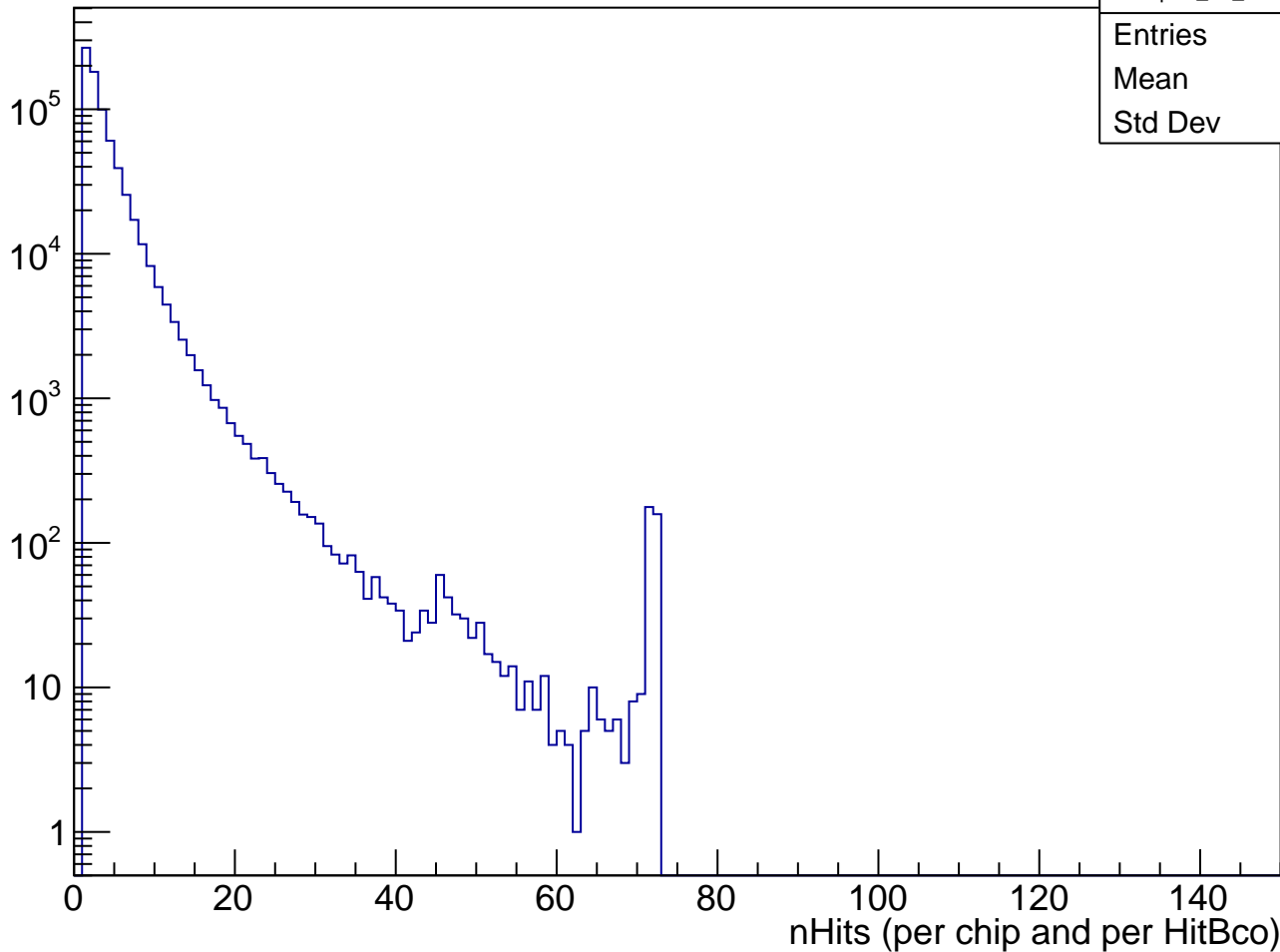
Entries



nChipHit_F7_Fch11_Chip17	
Entries	846689
Mean	3.281
Std Dev	3.768

nChipHit_F7_Fch11_Chip18

Entries



nChipHit_F7_Fch11_Chip18

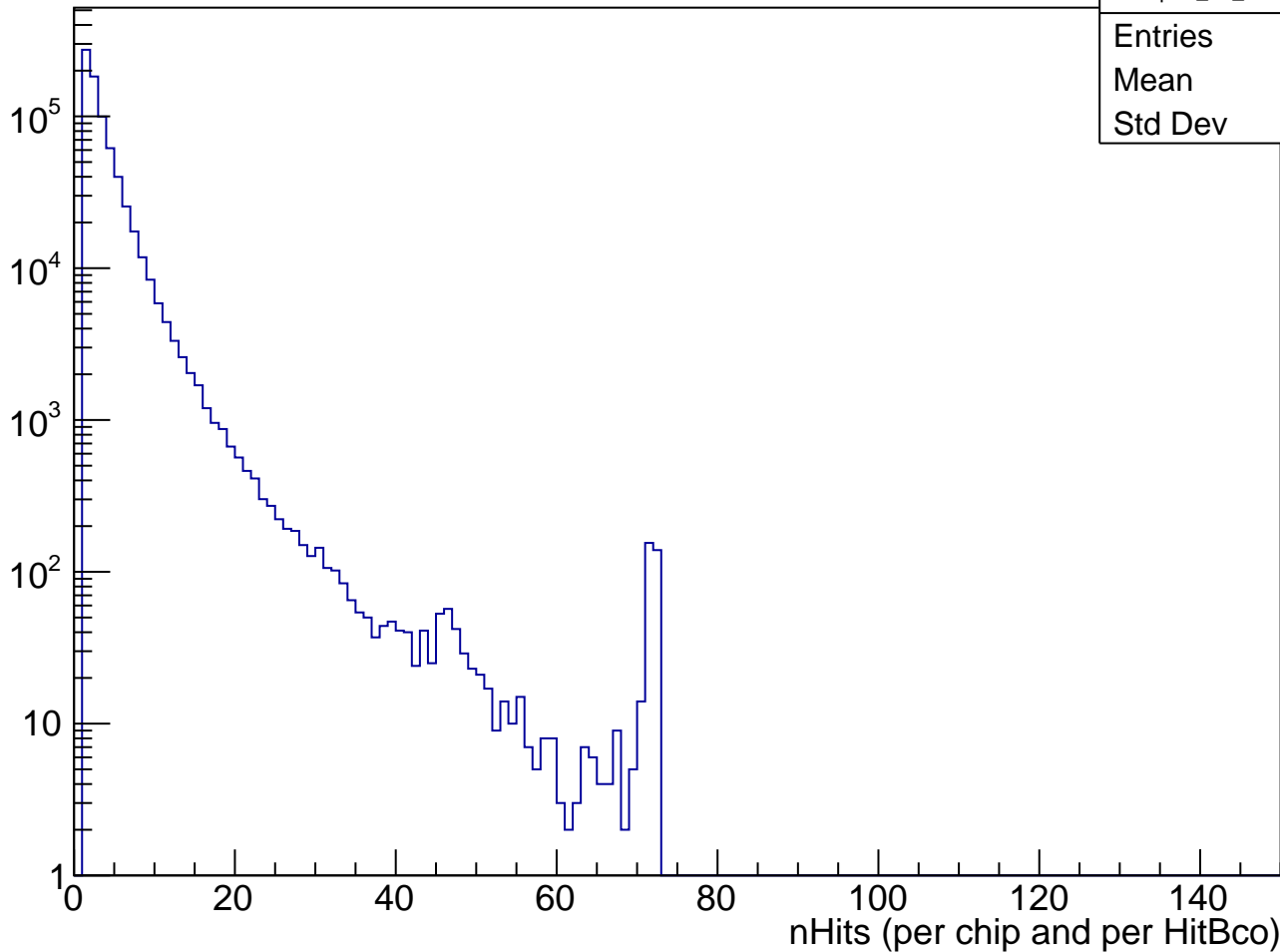
Entries 738057

Mean 3.048

Std Dev 3.598

nChipHit_F7_Fch11_Chip19

Entries



nChipHit_F7_Fch11_Chip19

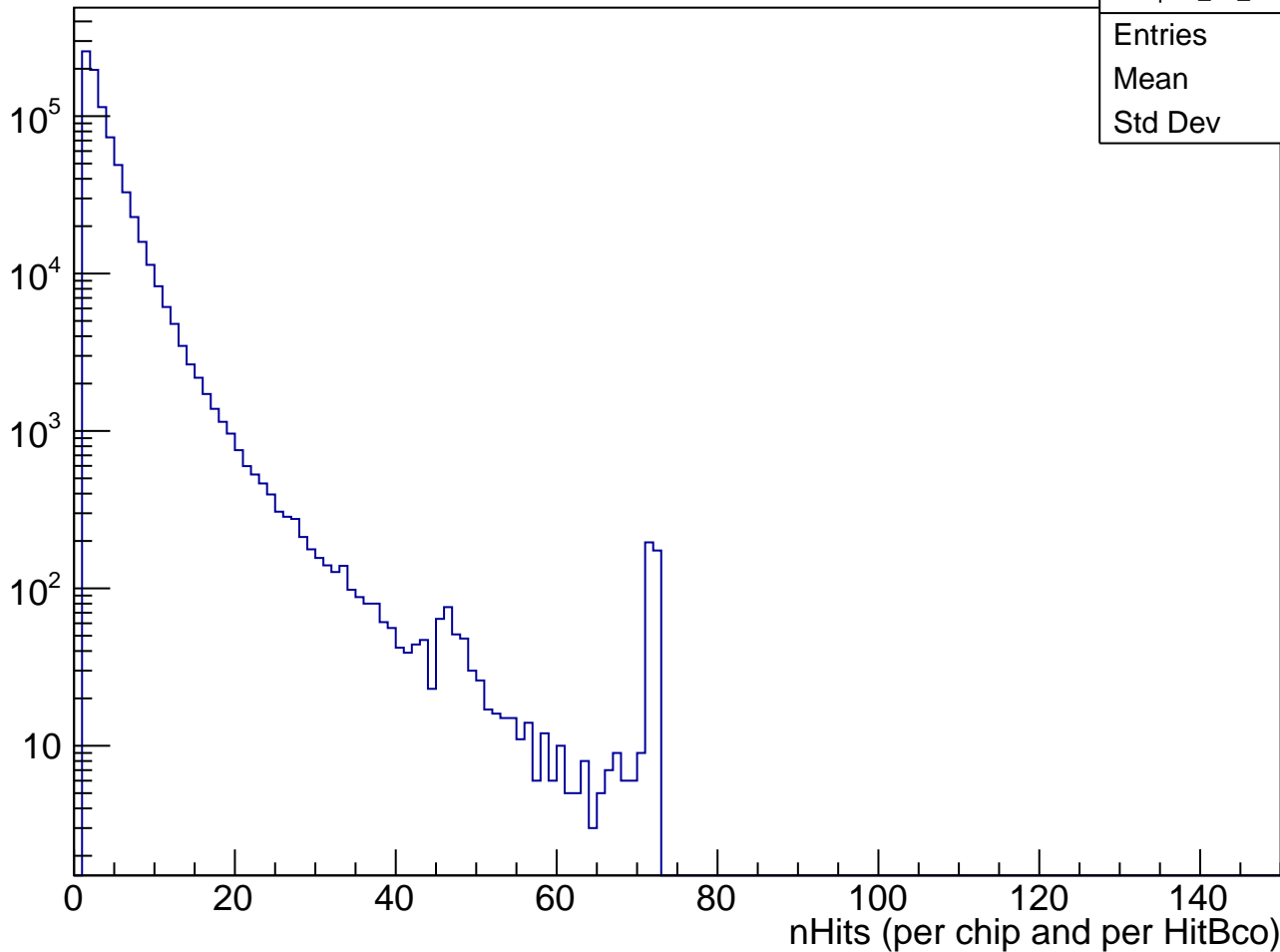
Entries 749981

Mean 3.025

Std Dev 3.537

nChipHit_F7_Fch11_Chip2

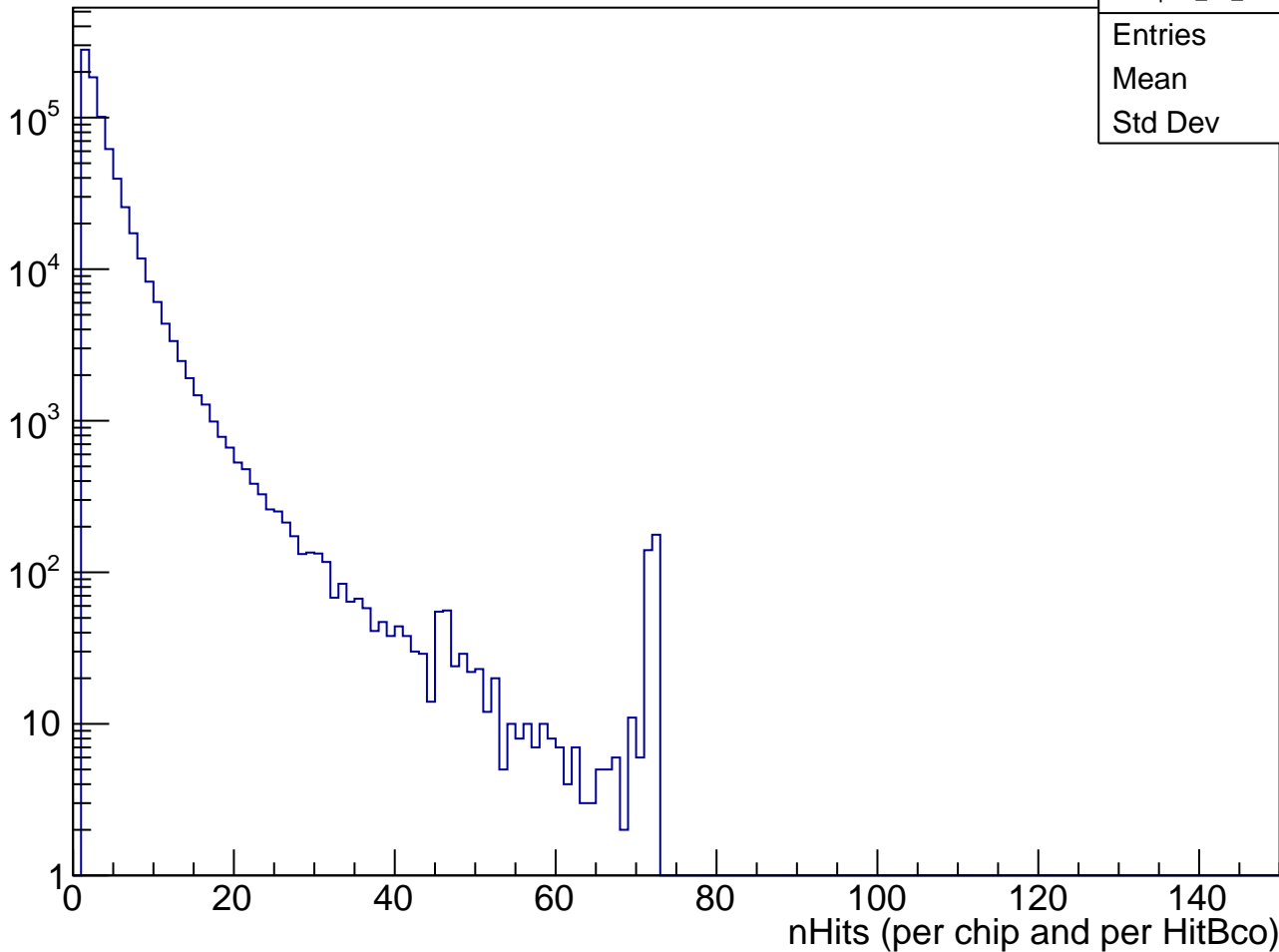
Entries



nChipHit_F7_Fch11_Chip2	
Entries	813147
Mean	3.343
Std Dev	3.829

nChipHit_F7_Fch11_Chip20

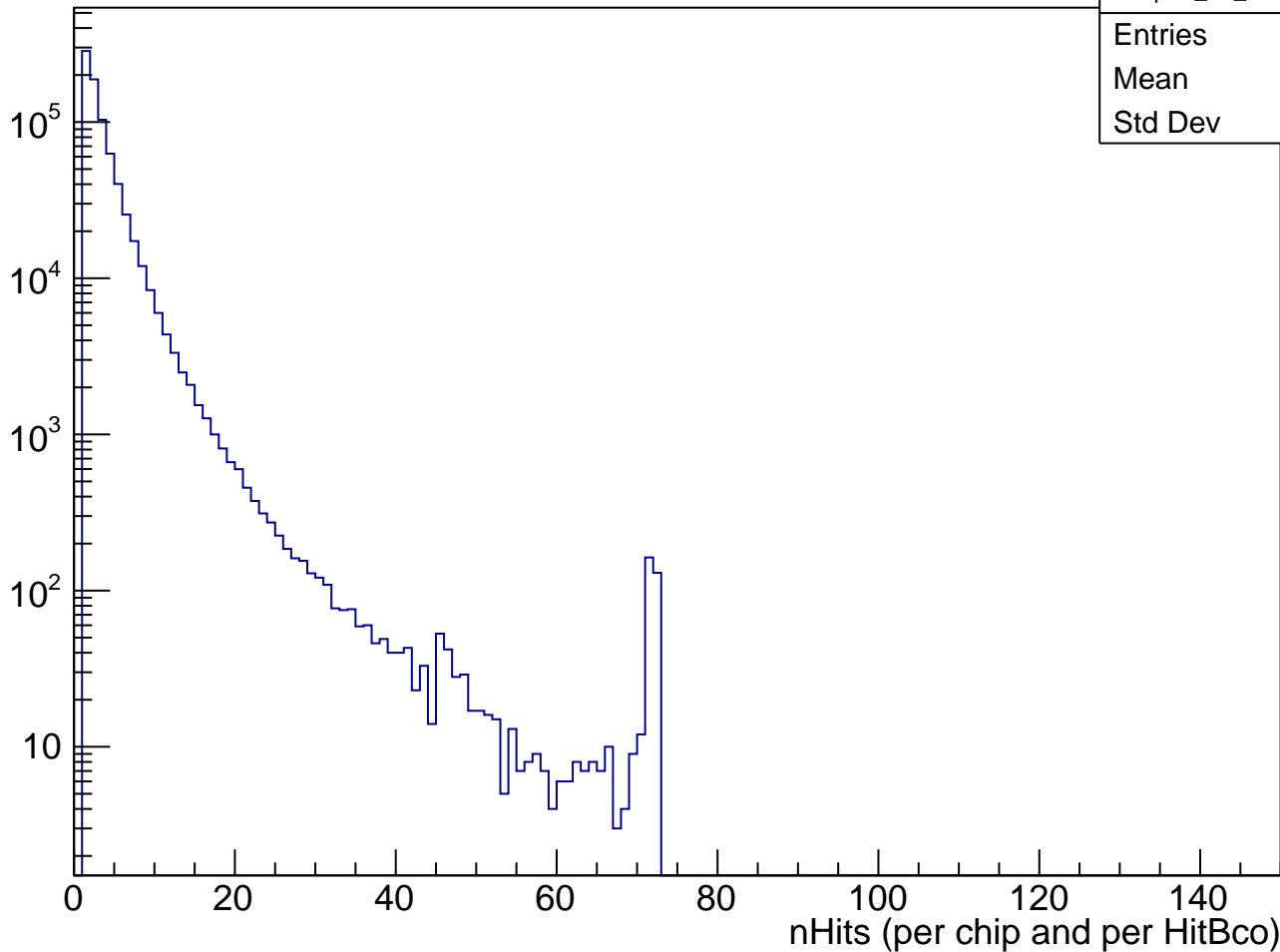
Entries



nChipHit_F7_Fch11_Chip20	
Entries	758237
Mean	2.999
Std Dev	3.518

nChipHit_F7_Fch11_Chip21

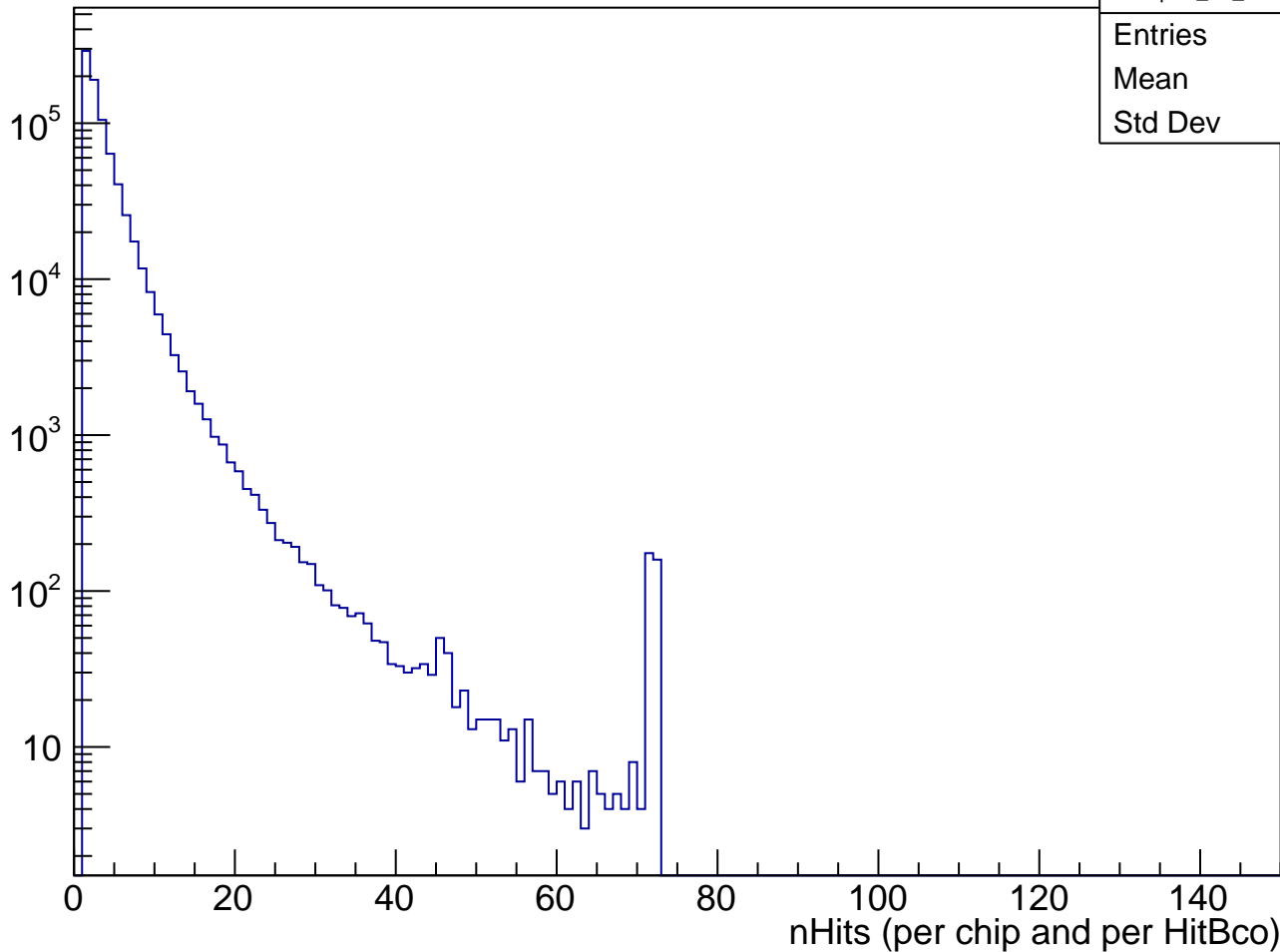
Entries



nChipHit_F7_Fch11_Chip21	
Entries	770224
Mean	2.988
Std Dev	3.481

nChipHit_F7_Fch11_Chip22

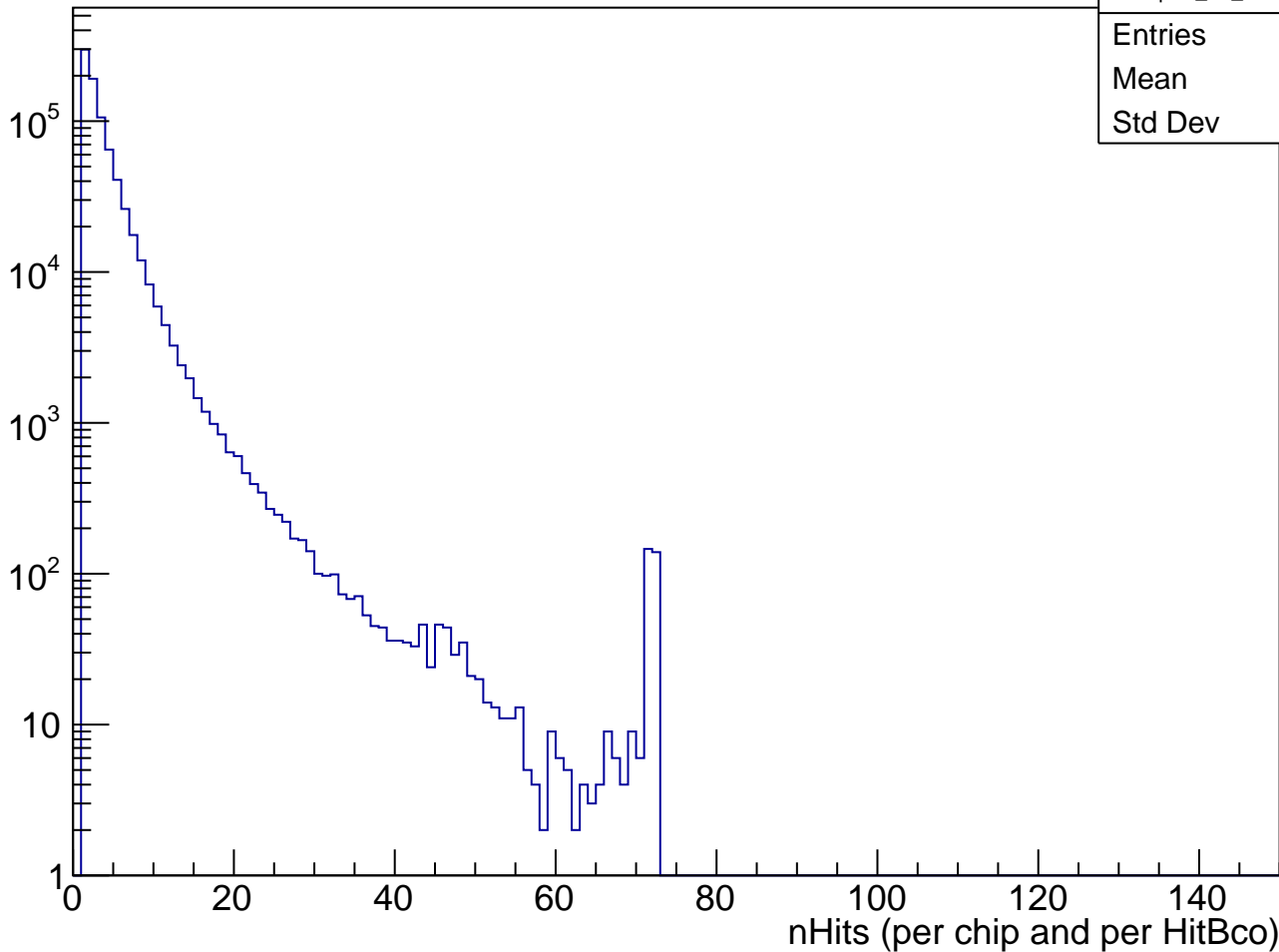
Entries



nChipHit_F7_Fch11_Chip22	
Entries	780936
Mean	2.974
Std Dev	3.483

nChipHit_F7_Fch11_Chip23

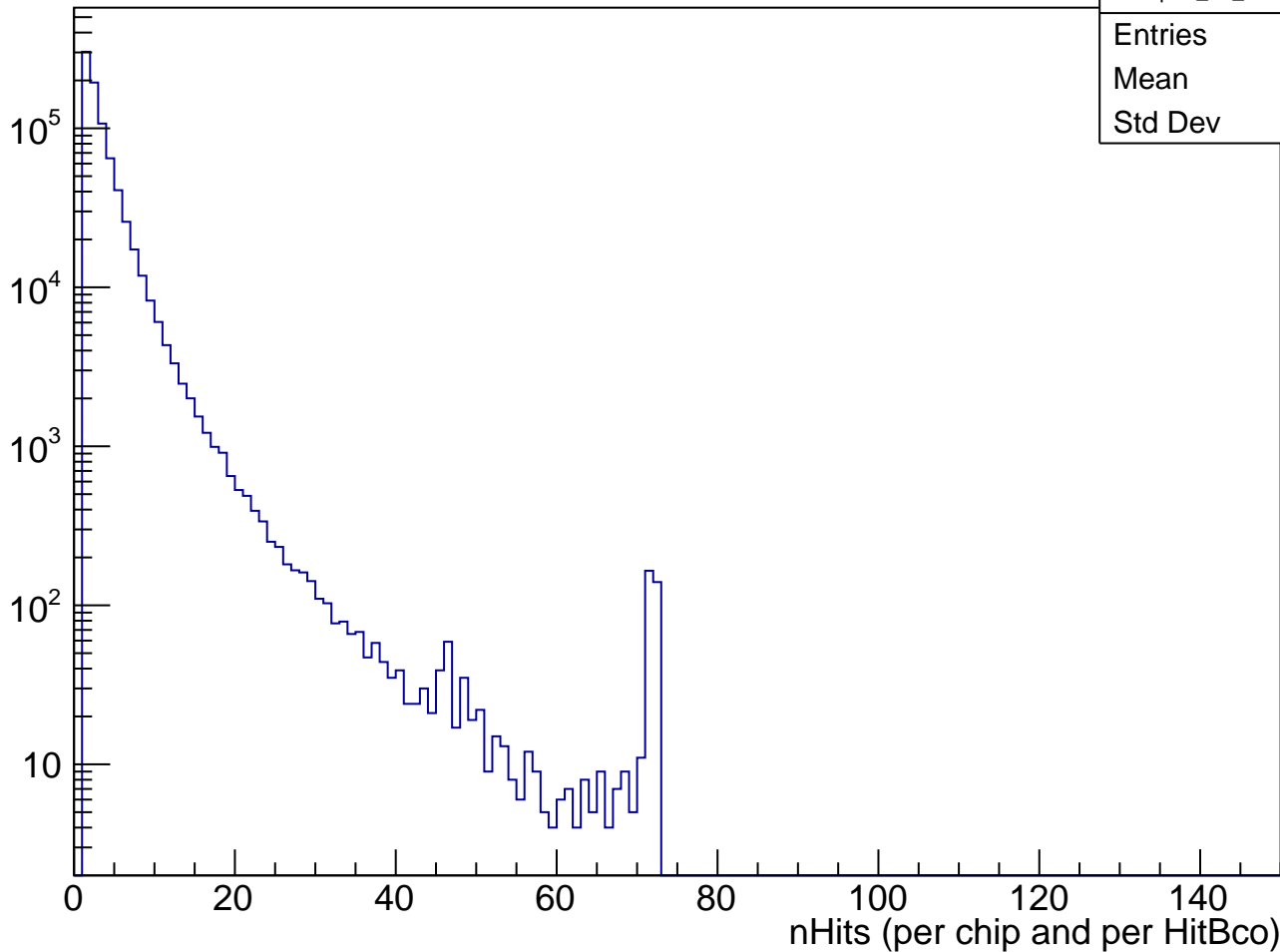
Entries



nChipHit_F7_Fch11_Chip23	
Entries	792320
Mean	2.953
Std Dev	3.43

nChipHit_F7_Fch11_Chip24

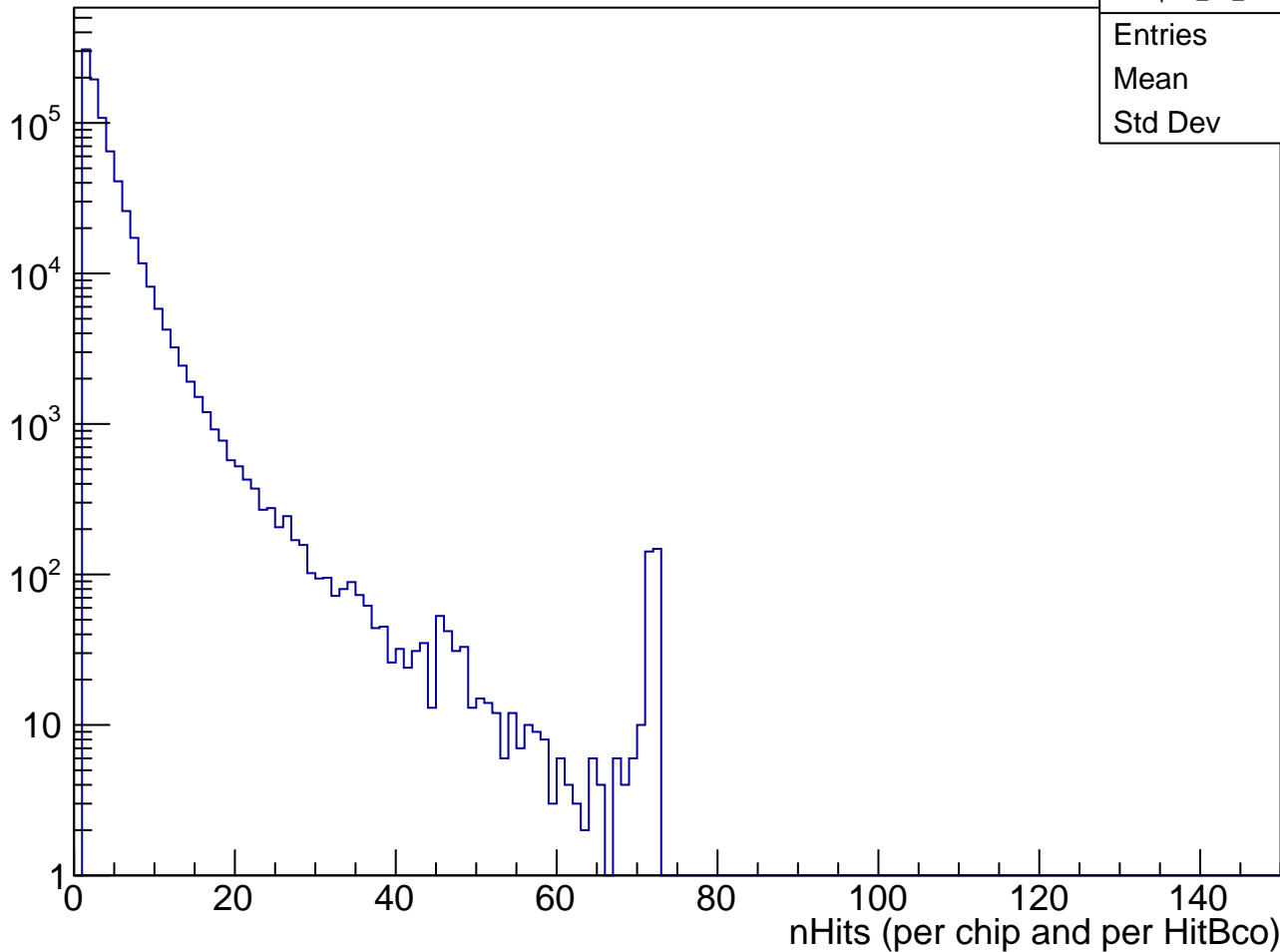
Entries



nChipHit_F7_Fch11_Chip24	
Entries	800587
Mean	2.938
Std Dev	3.43

nChipHit_F7_Fch11_Chip25

Entries



nChipHit_F7_Fch11_Chip25

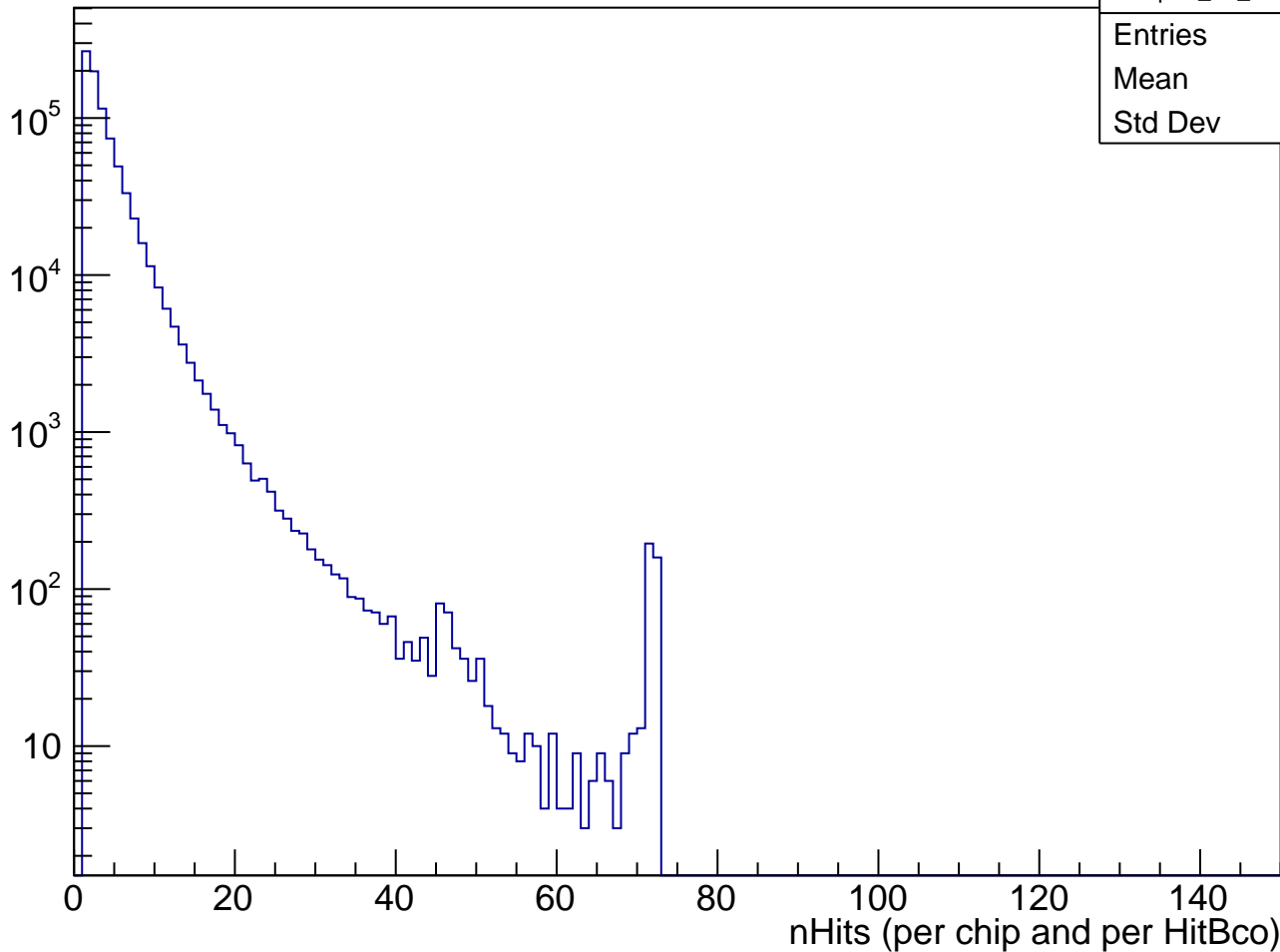
Entries 806300

Mean 2.905

Std Dev 3.357

nChipHit_F7_Fch11_Chip3

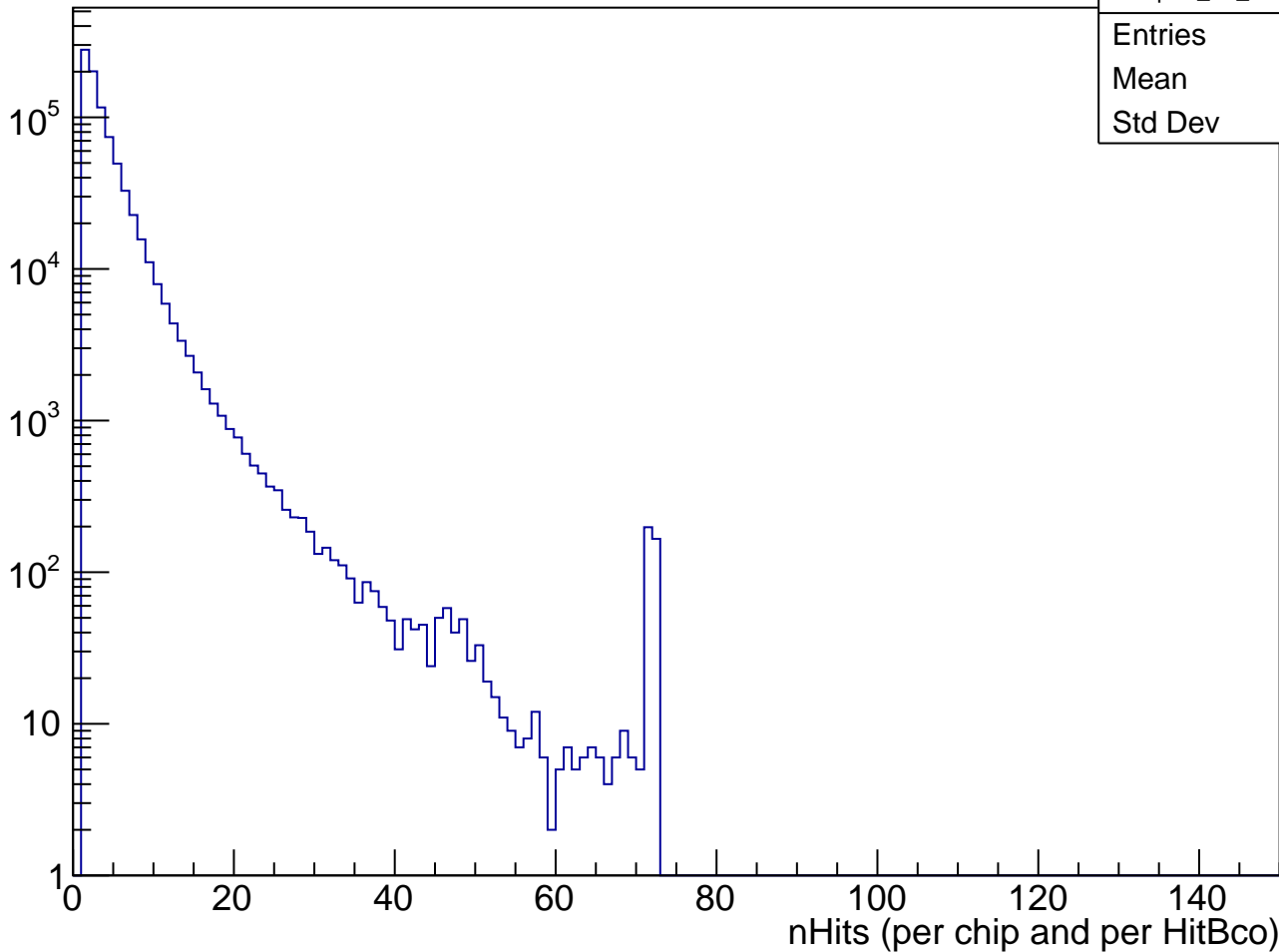
Entries



nChipHit_F7_Fch11_Chip3	
Entries	825535
Mean	3.32
Std Dev	3.796

nChipHit_F7_Fch11_Chip4

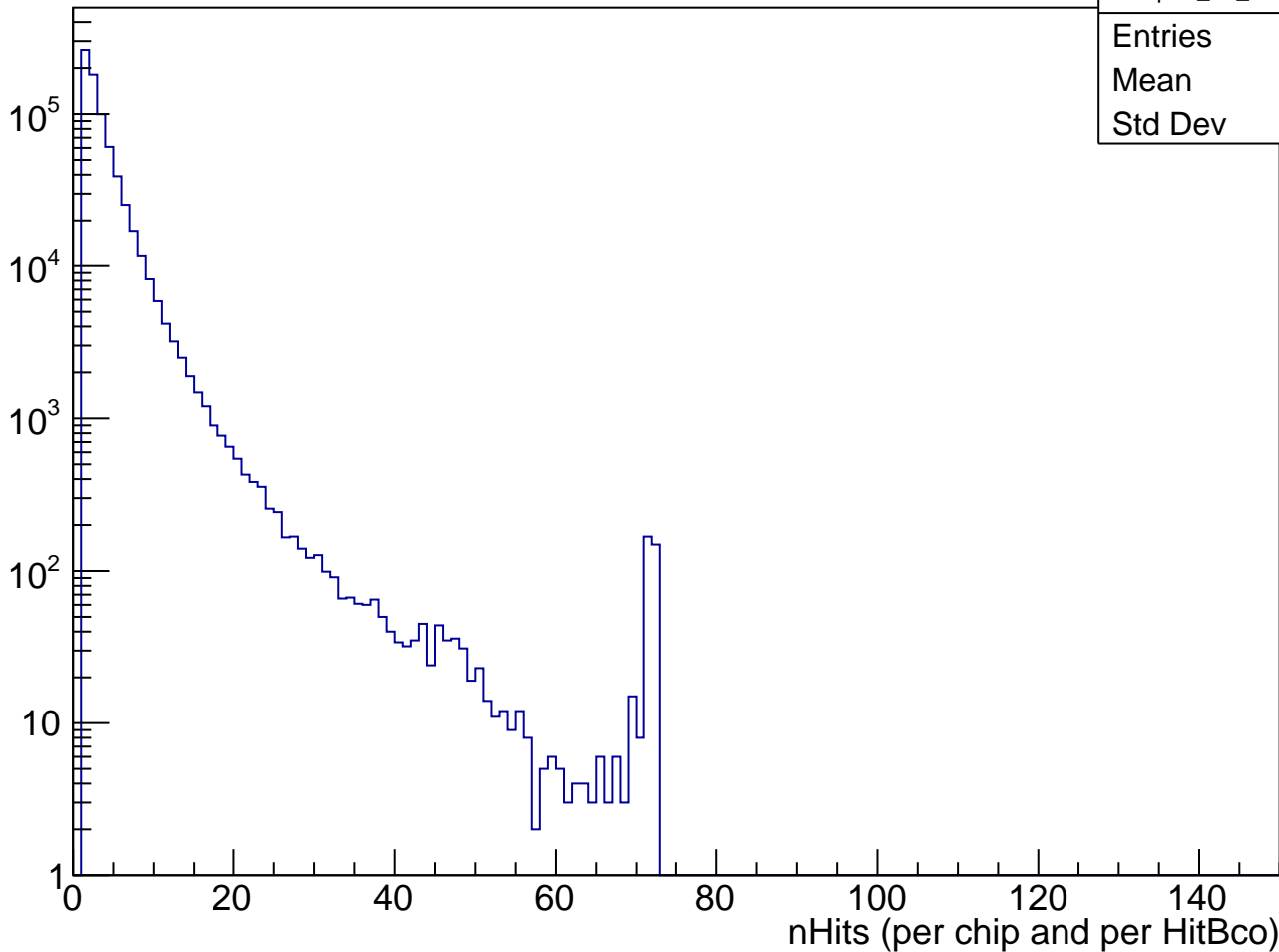
Entries



nChipHit_F7_Fch11_Chip4	
Entries	839408
Mean	3.247
Std Dev	3.712

nChipHit_F7_Fch11_Chip5

Entries

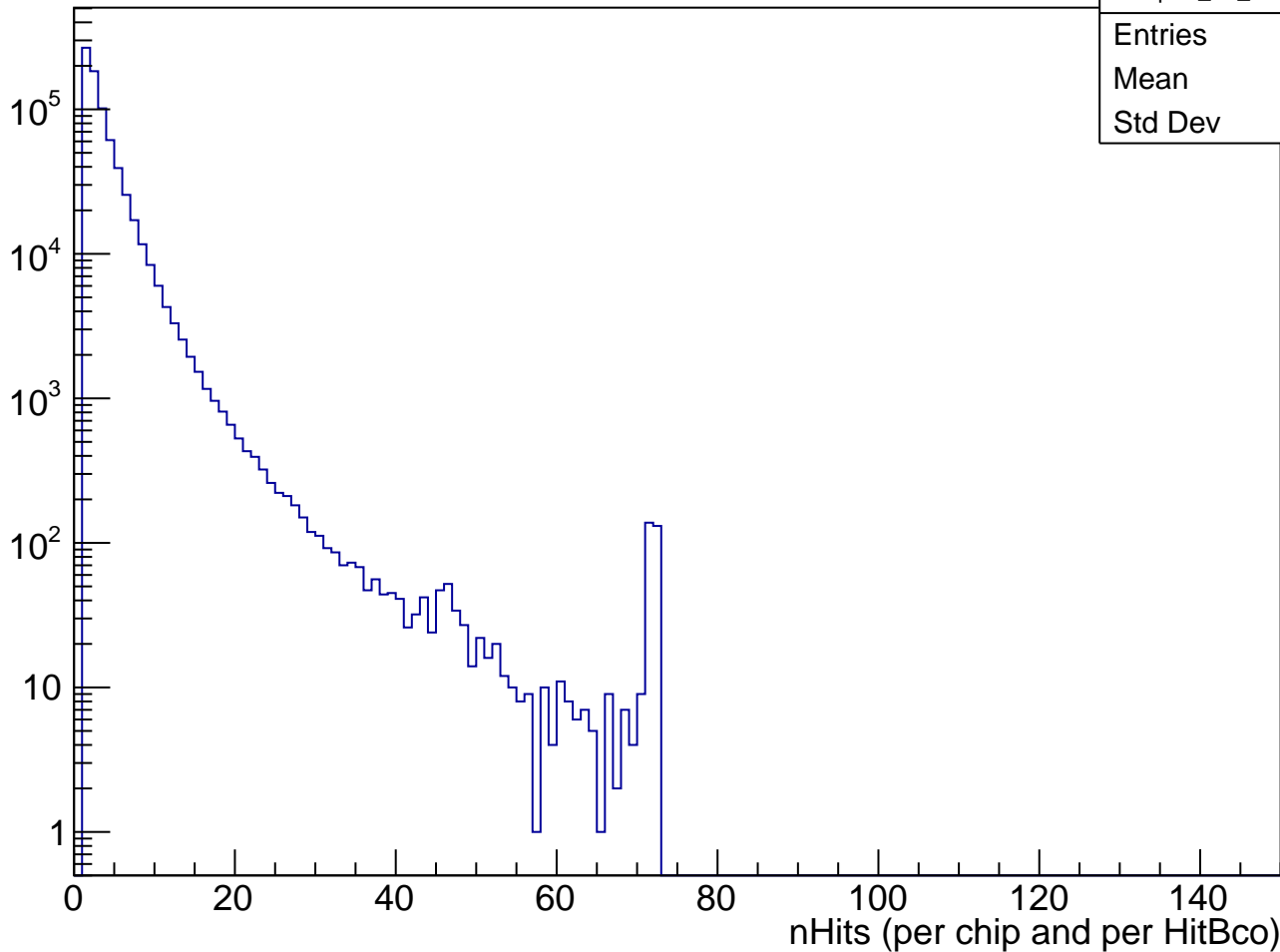


nChipHit_F7_Fch11_Chip5	
Entries	732681
Mean	3.033
Std Dev	3.536

nHits (per chip and per HitBco)

nChipHit_F7_Fch11_Chip6

Entries

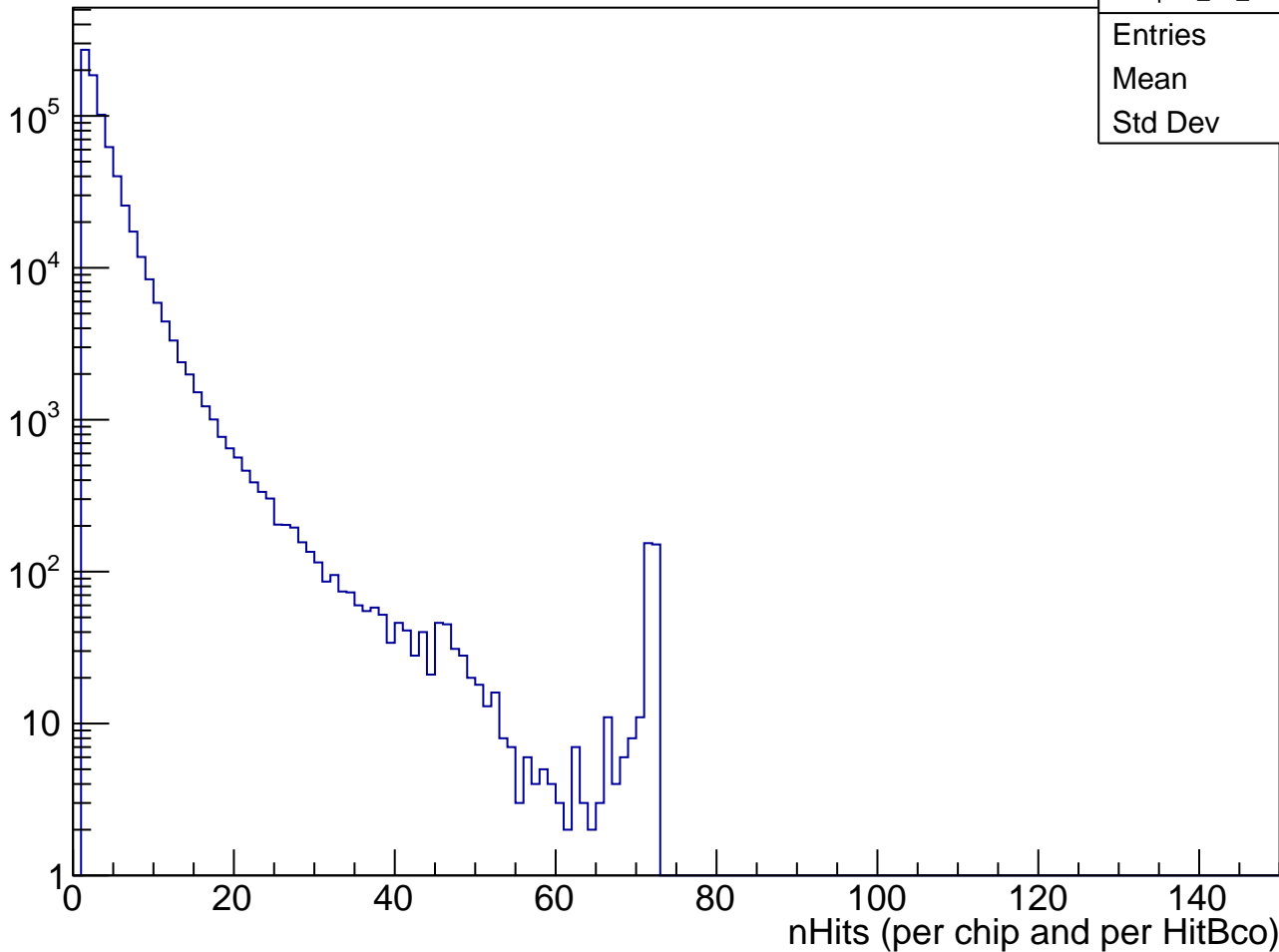


nChipHit_F7_Fch11_Chip6	
Entries	742645
Mean	3.026
Std Dev	3.494

nHits (per chip and per HitBco)

nChipHit_F7_Fch11_Chip7

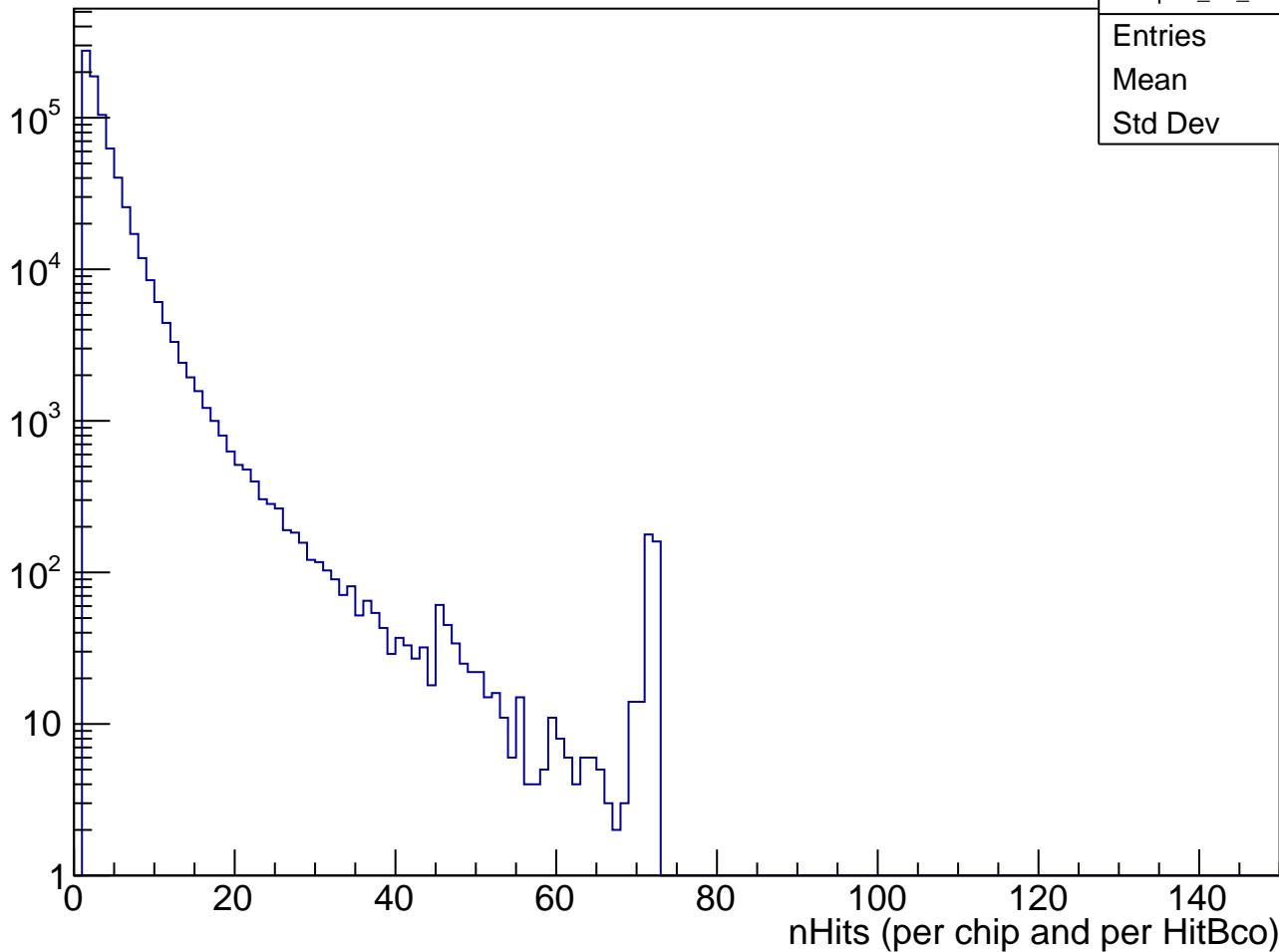
Entries



nChipHit_F7_Fch11_Chip7	
Entries	752687
Mean	3.019
Std Dev	3.505

nChipHit_F7_Fch11_Chip8

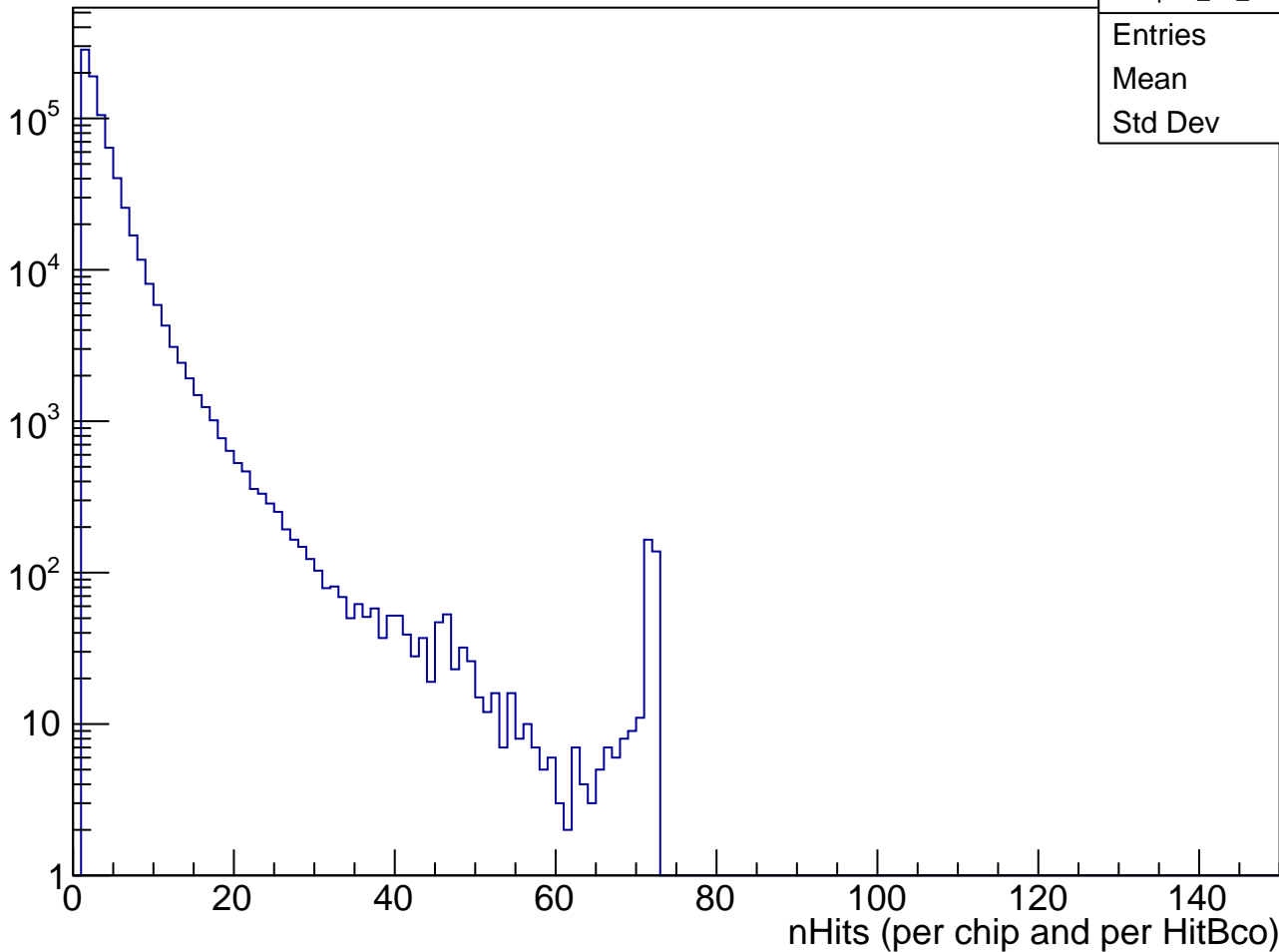
Entries



nChipHit_F7_Fch11_Chip8	
Entries	762838
Mean	3.011
Std Dev	3.53

nChipHit_F7_Fch11_Chip9

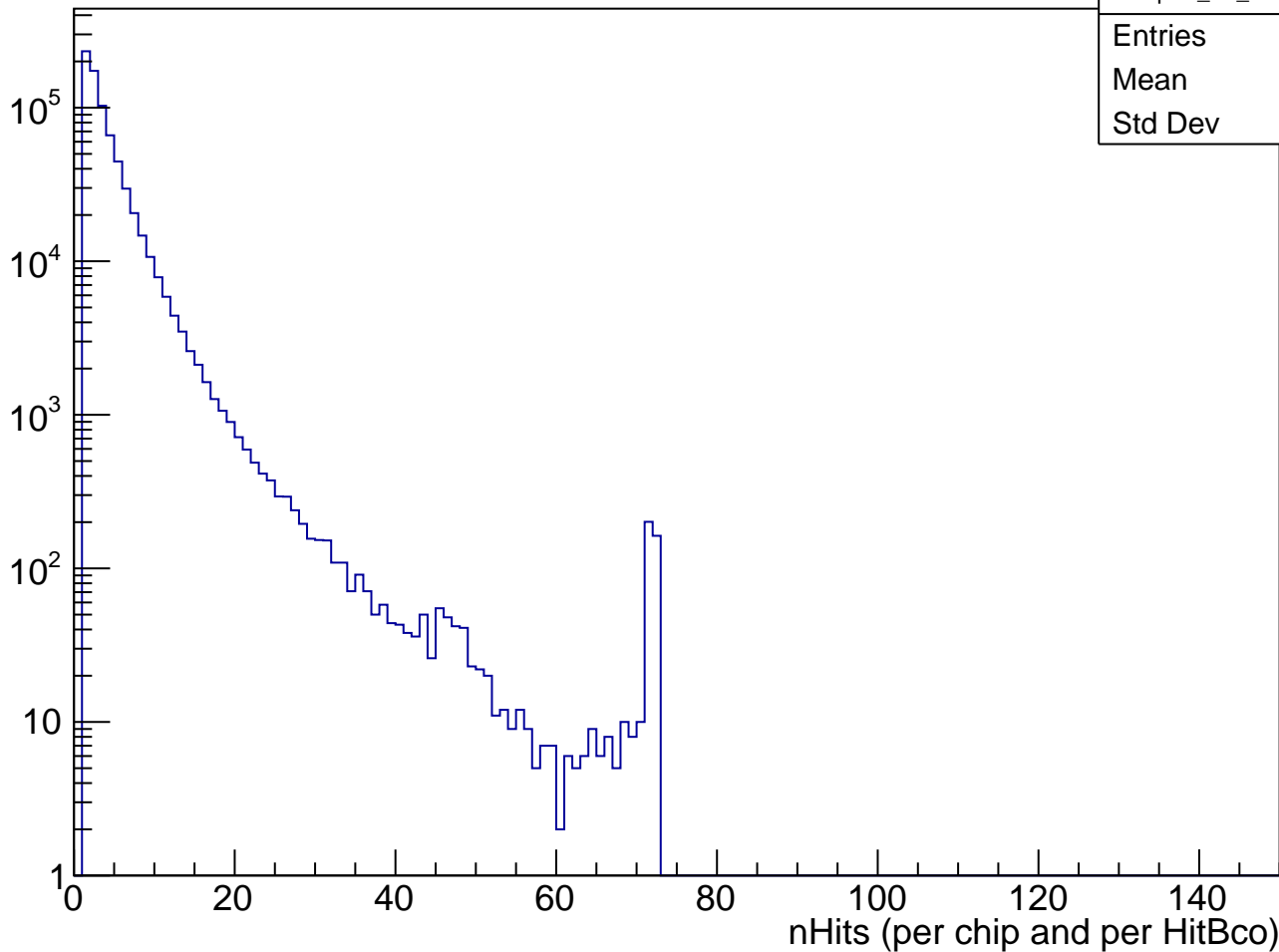
Entries



nChipHit_F7_Fch11_Chip9	
Entries	772647
Mean	2.972
Std Dev	3.456

nChipHit_F7_Fch12_Chip0

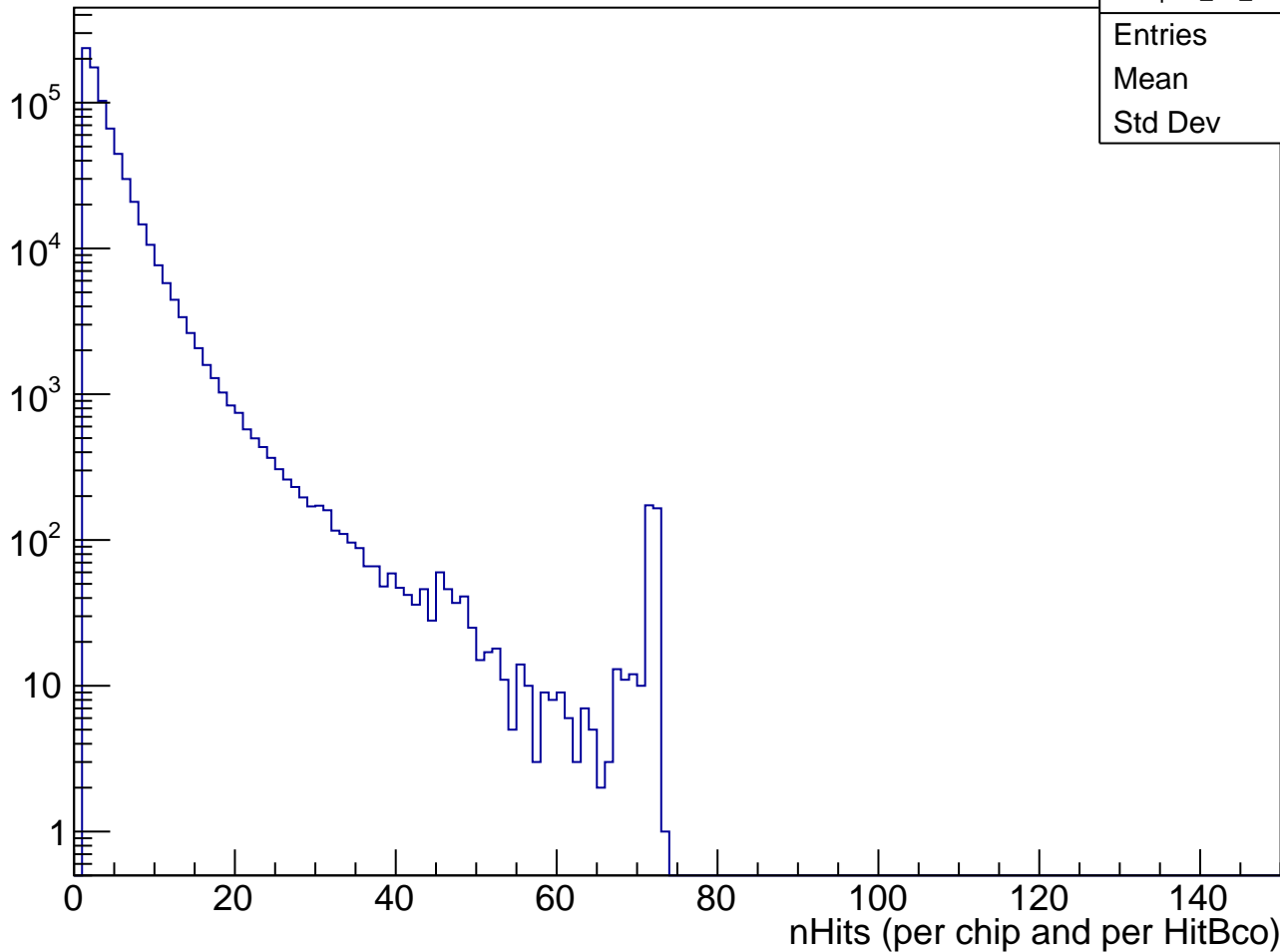
Entries



nChipHit_F7_Fch12_Chip0	
Entries	732710
Mean	3.382
Std Dev	3.886

nChipHit_F7_Fch12_Chip1

Entries



nChipHit_F7_Fch12_Chip1

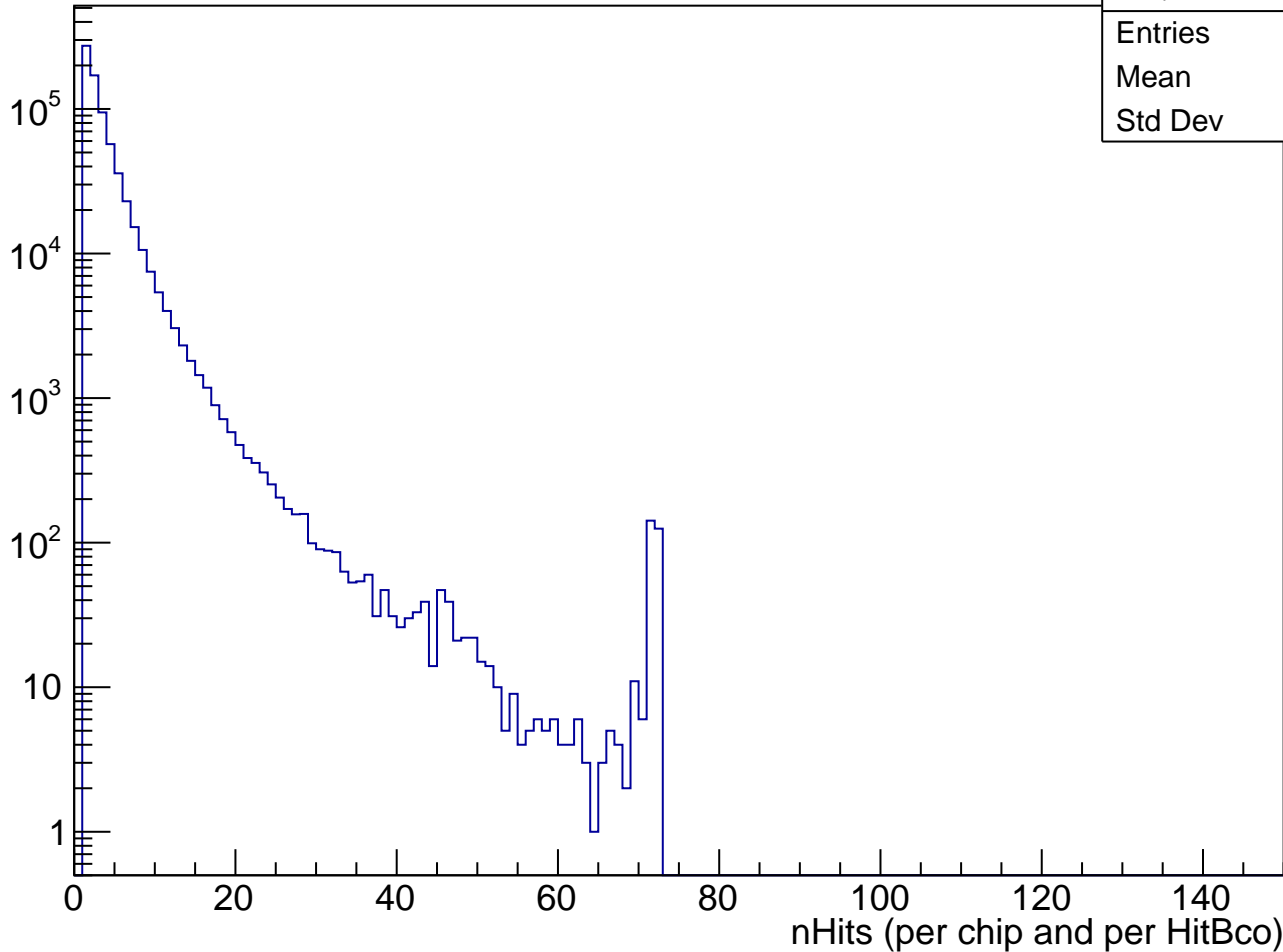
Entries 737443

Mean 3.365

Std Dev 3.866

nChipHit_F7_Fch12_Chip10

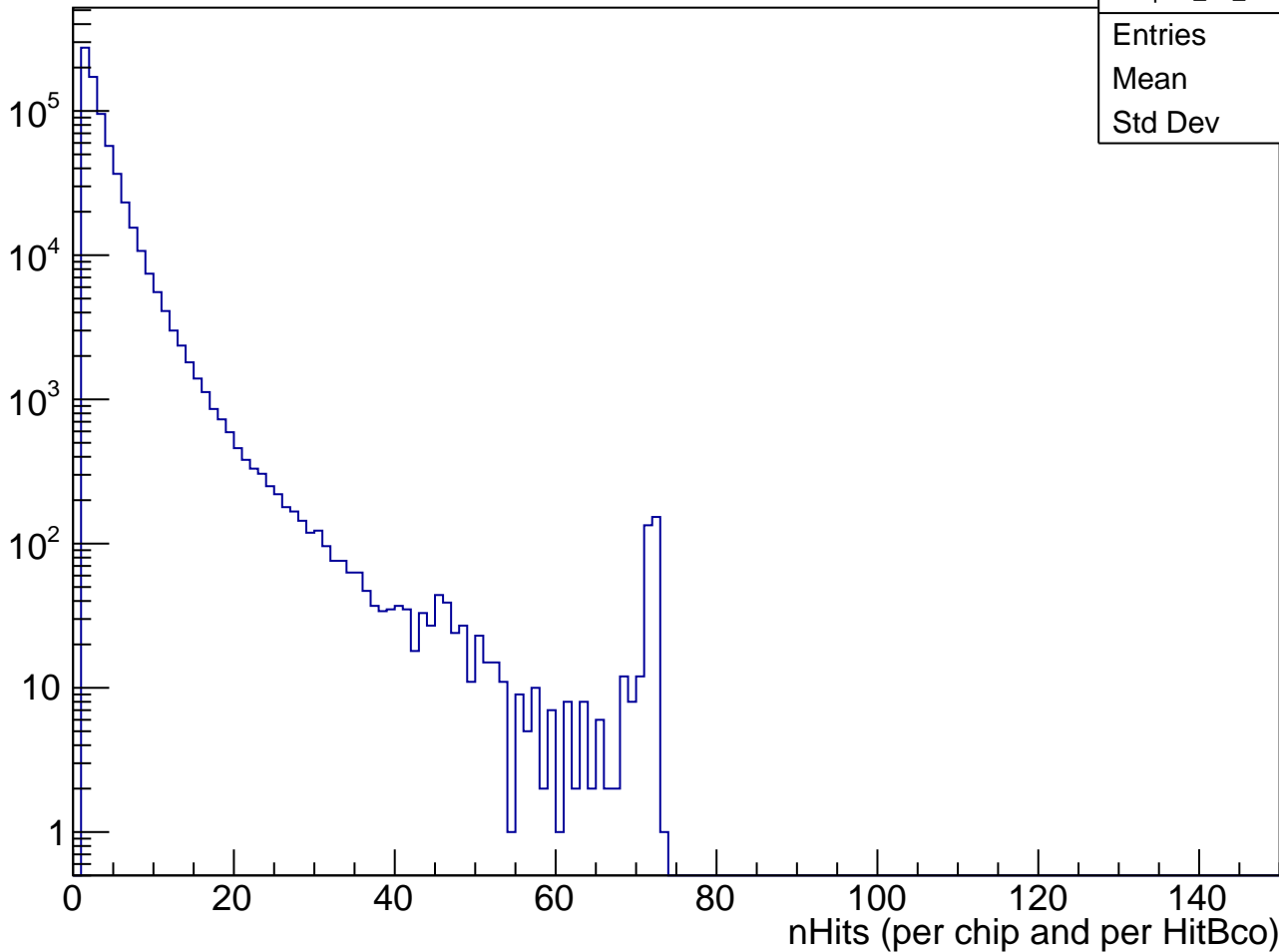
Entries



nChipHit_F7_Fch12_Chip10	
Entries	713575
Mean	2.933
Std Dev	3.42

nChipHit_F7_Fch12_Chip11

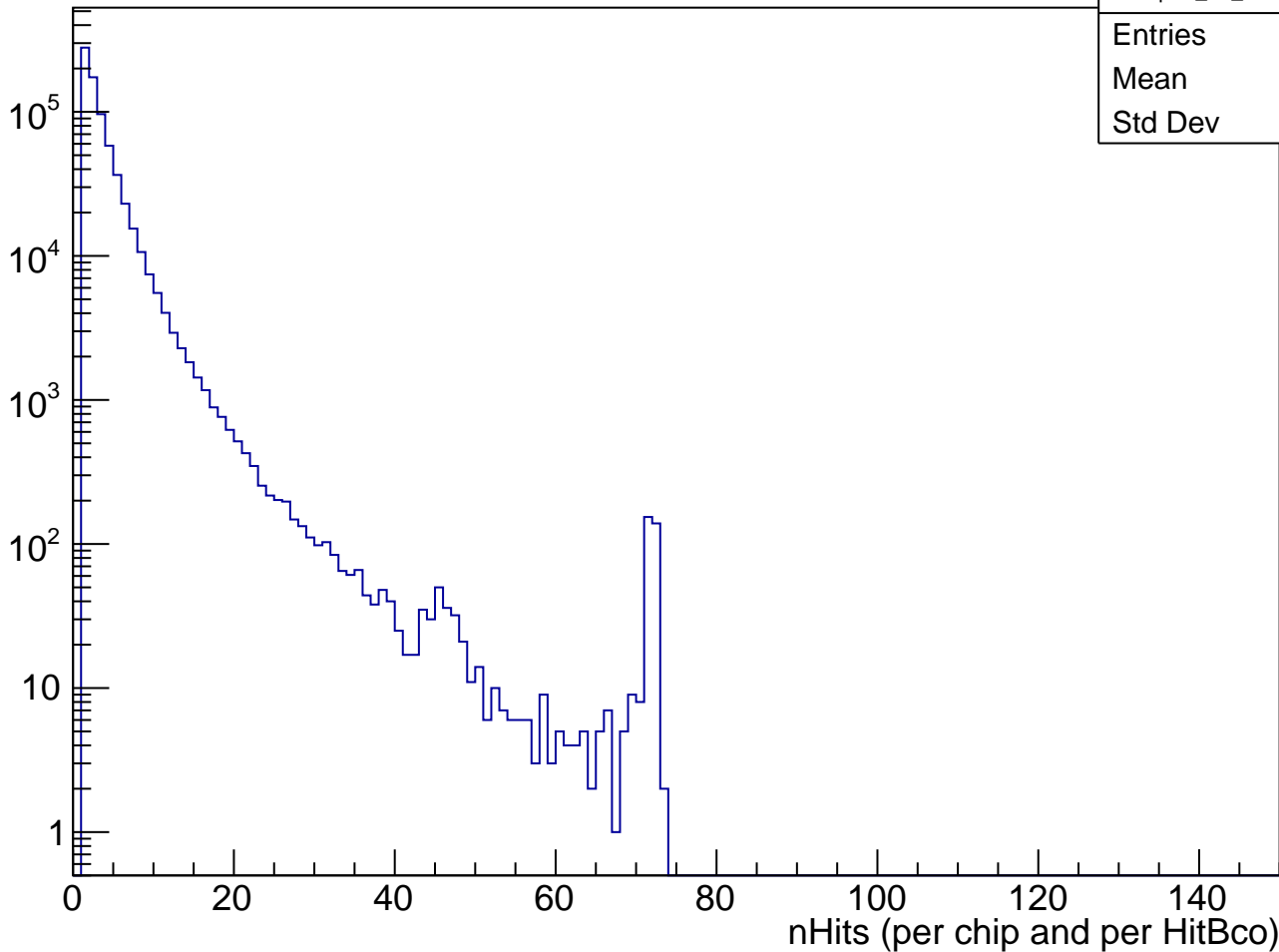
Entries



nChipHit_F7_Fch12_Chip11	
Entries	718453
Mean	2.94
Std Dev	3.46

nChipHit_F7_Fch12_Chip12

Entries



nChipHit_F7_Fch12_Chip12

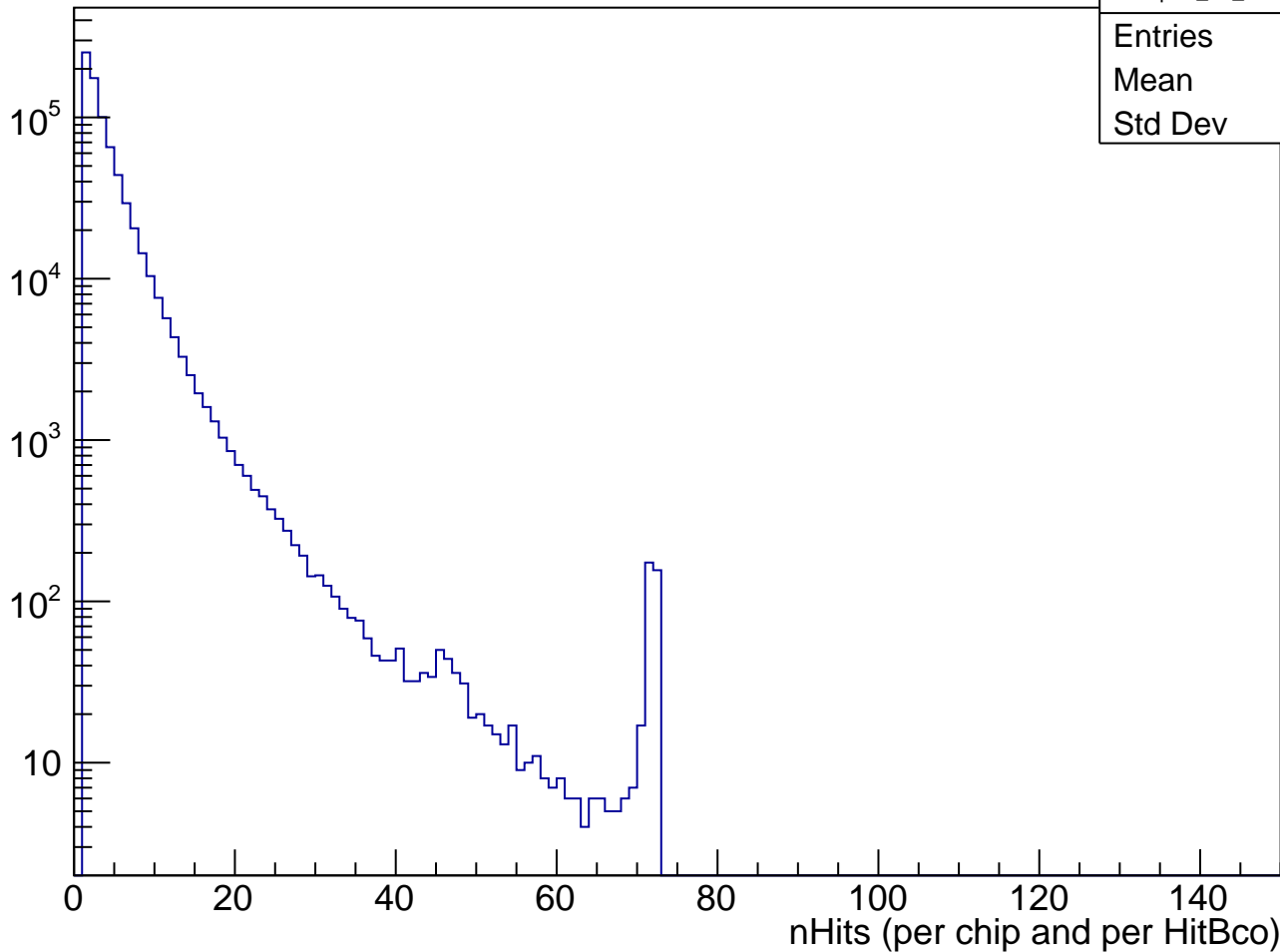
Entries 726164

Mean 2.923

Std Dev 3.426

nChipHit_F7_Fch12_Chip13

Entries



nChipHit_F7_Fch12_Chip13

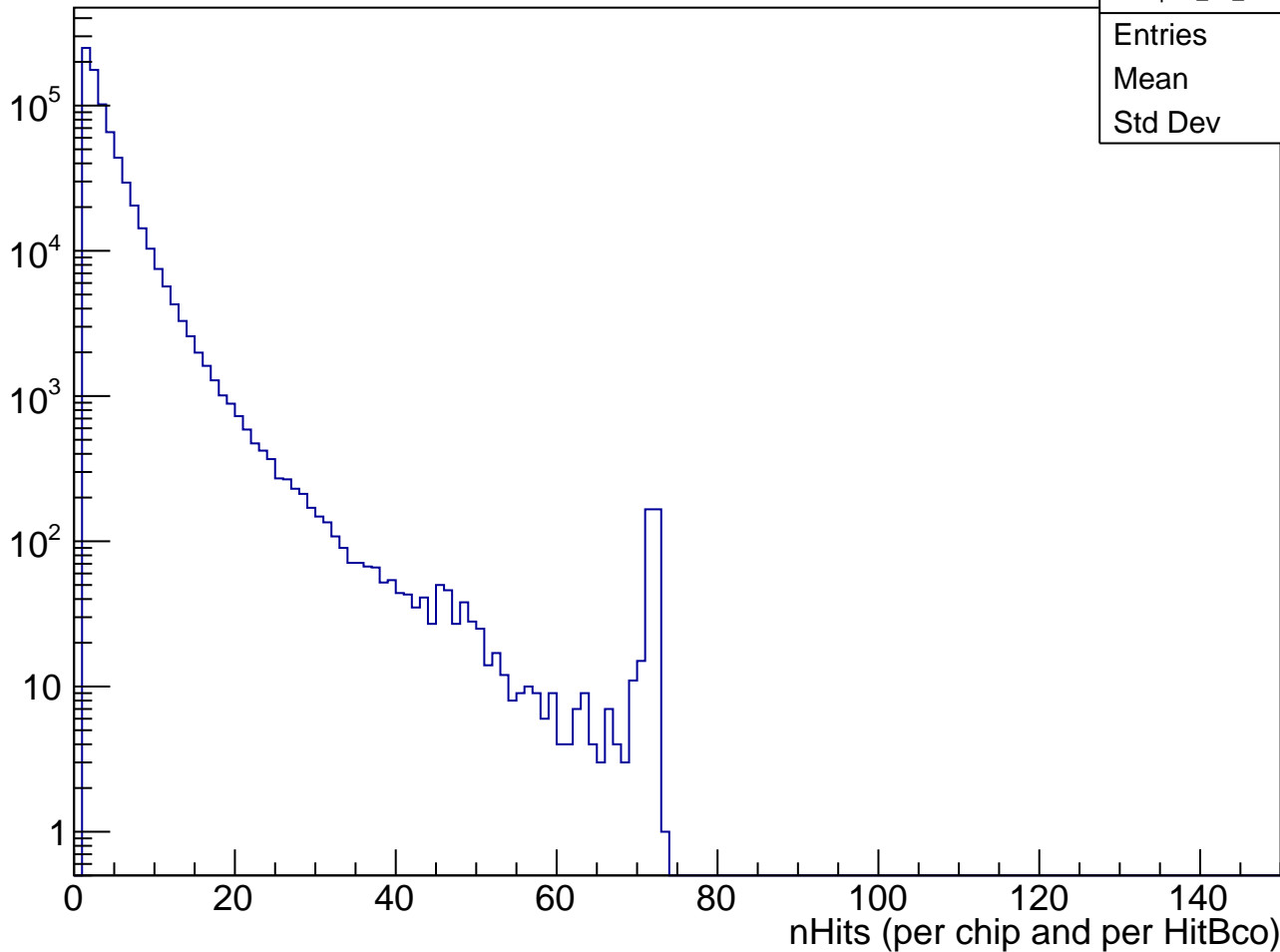
Entries 748031

Mean 3.29

Std Dev 3.797

nChipHit_F7_Fch12_Chip14

Entries



nChipHit_F7_Fch12_Chip14

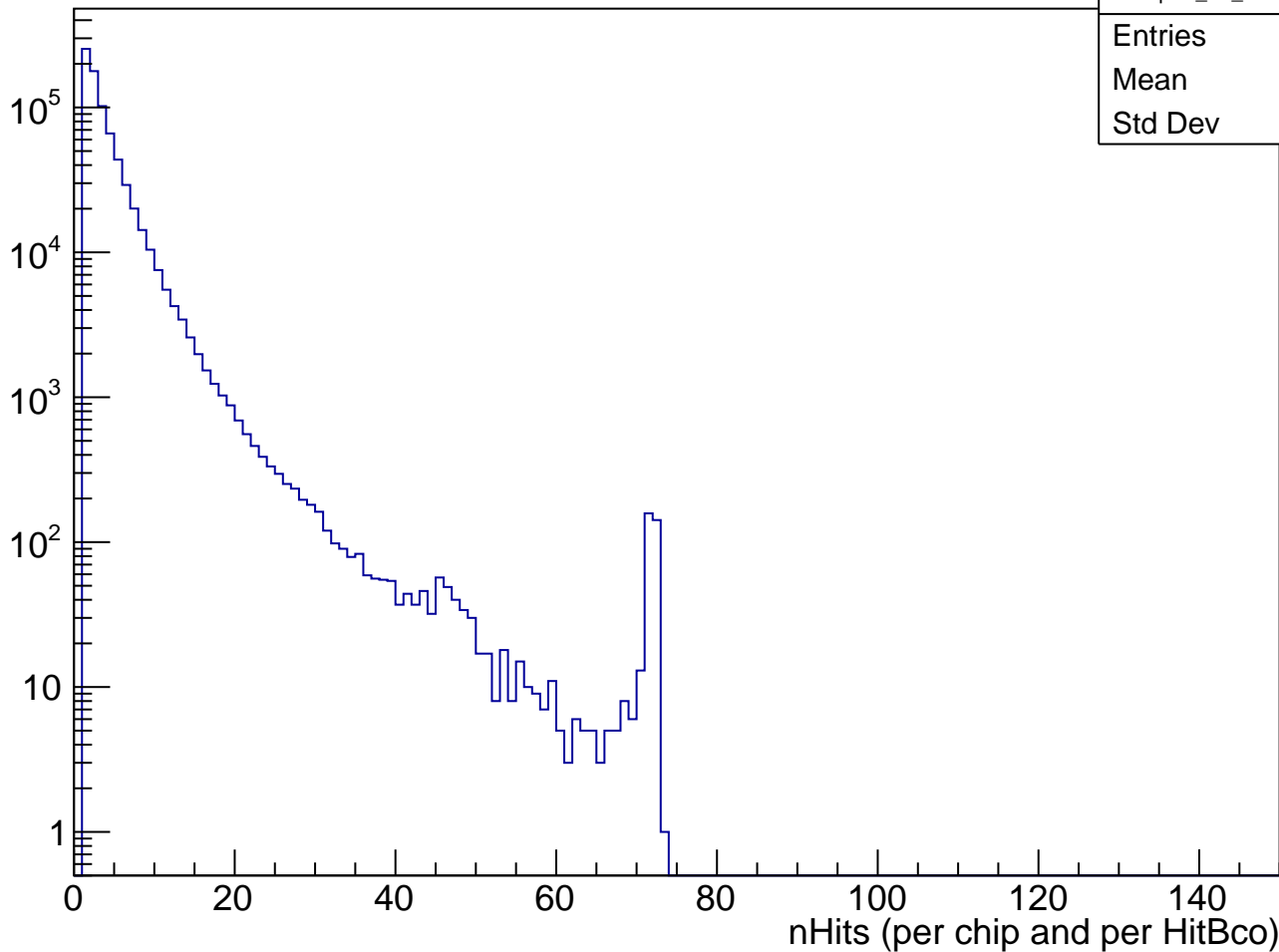
Entries 746868

Mean 3.298

Std Dev 3.805

nChipHit_F7_Fch12_Chip15

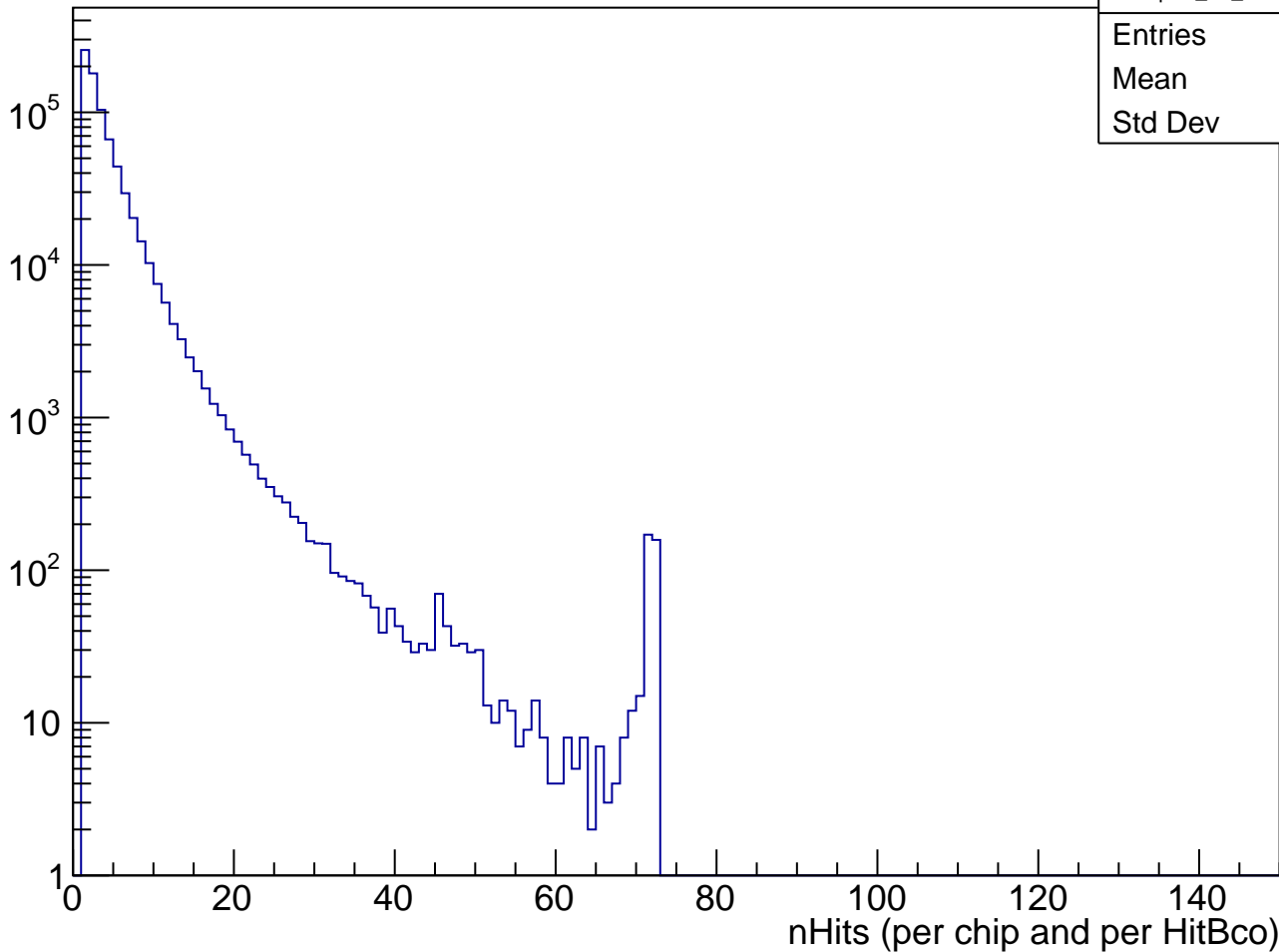
Entries



nChipHit_F7_Fch12_Chip15	
Entries	752898
Mean	3.272
Std Dev	3.76

nChipHit_F7_Fch12_Chip16

Entries



nChipHit_F7_Fch12_Chip16

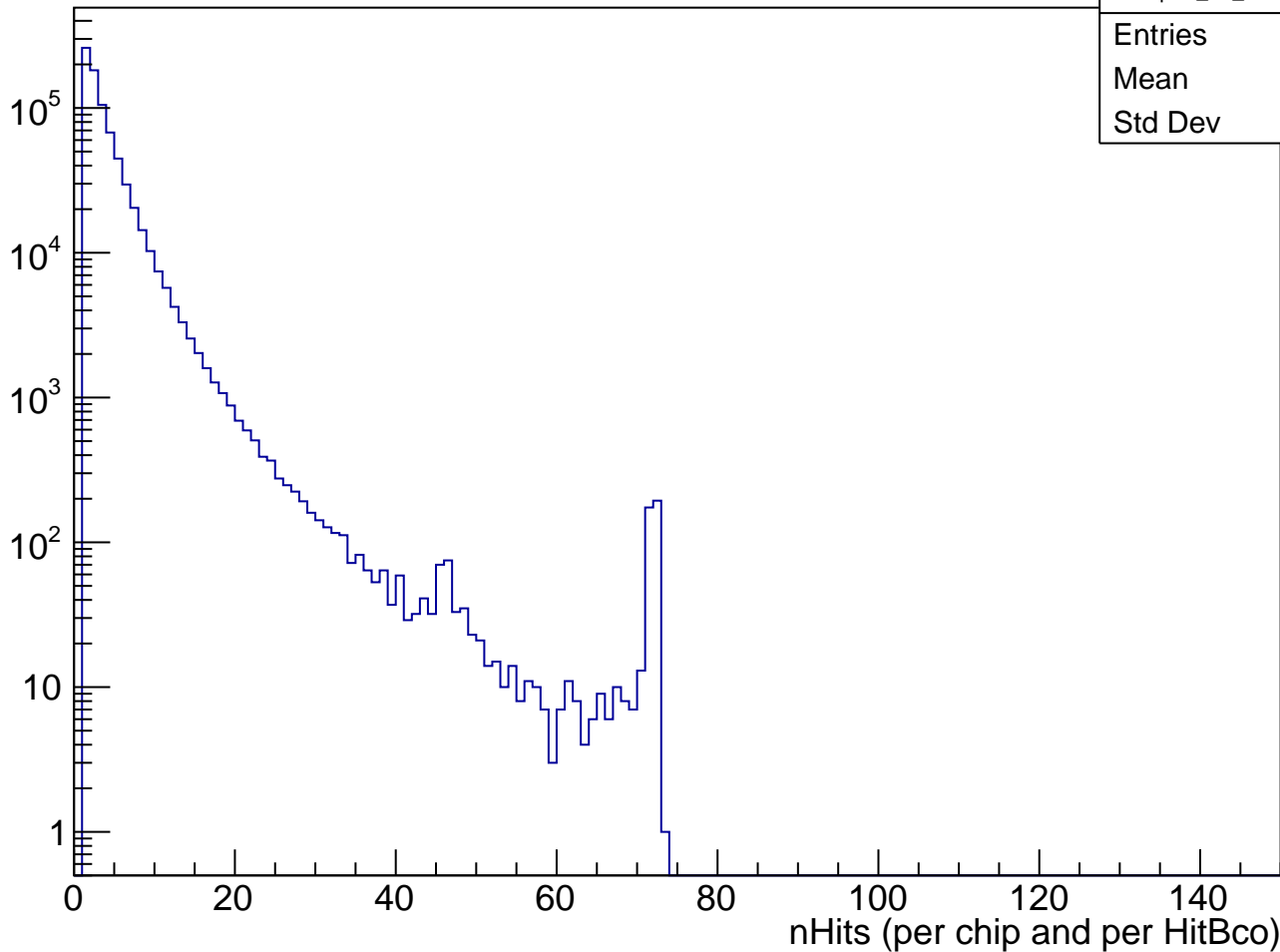
Entries 760064

Mean 3.264

Std Dev 3.768

nChipHit_F7_Fch12_Chip17

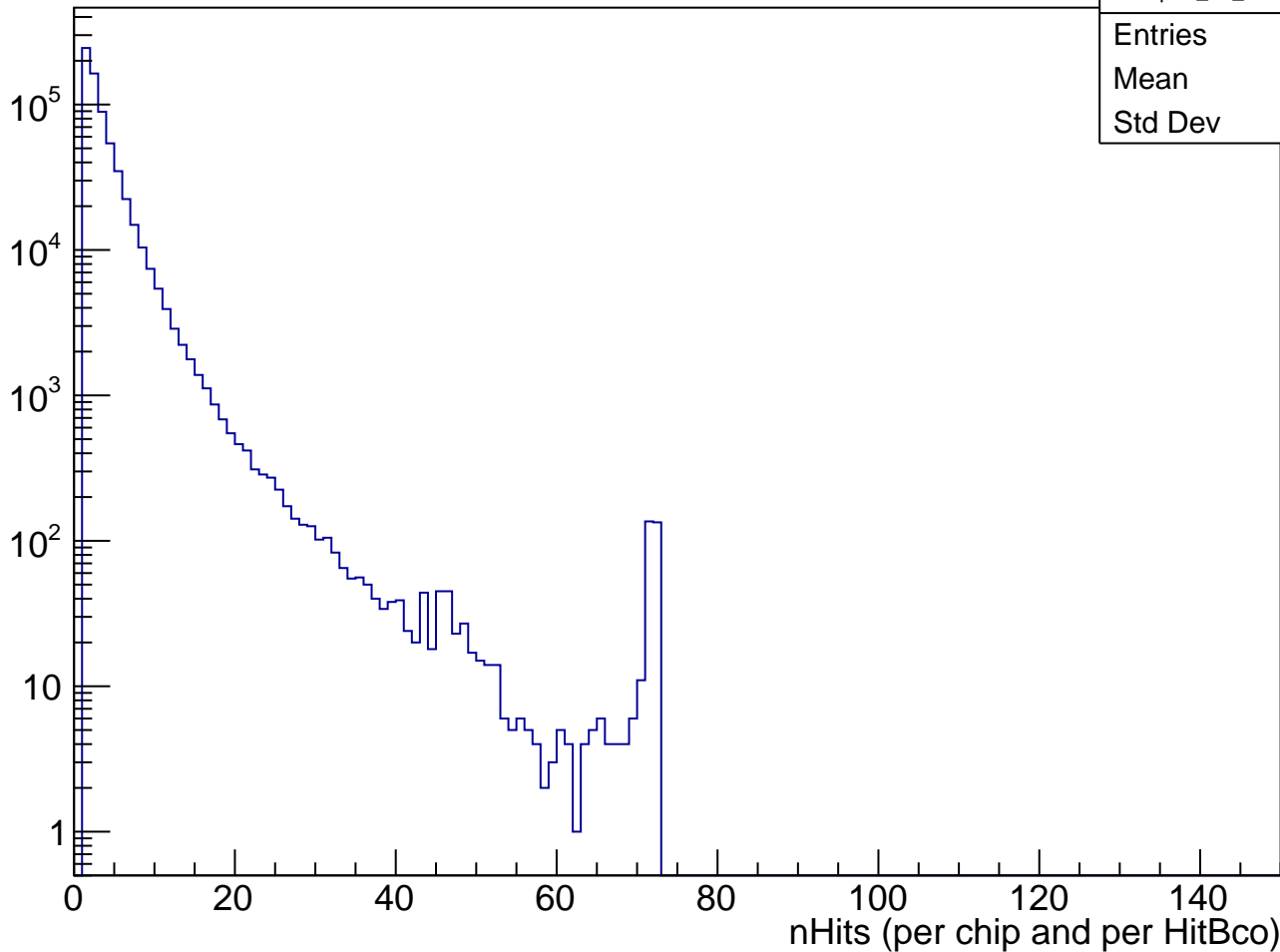
Entries



nChipHit_F7_Fch12_Chip17	
Entries	770175
Mean	3.263
Std Dev	3.807

nChipHit_F7_Fch12_Chip18

Entries



nChipHit_F7_Fch12_Chip18

Entries 666163

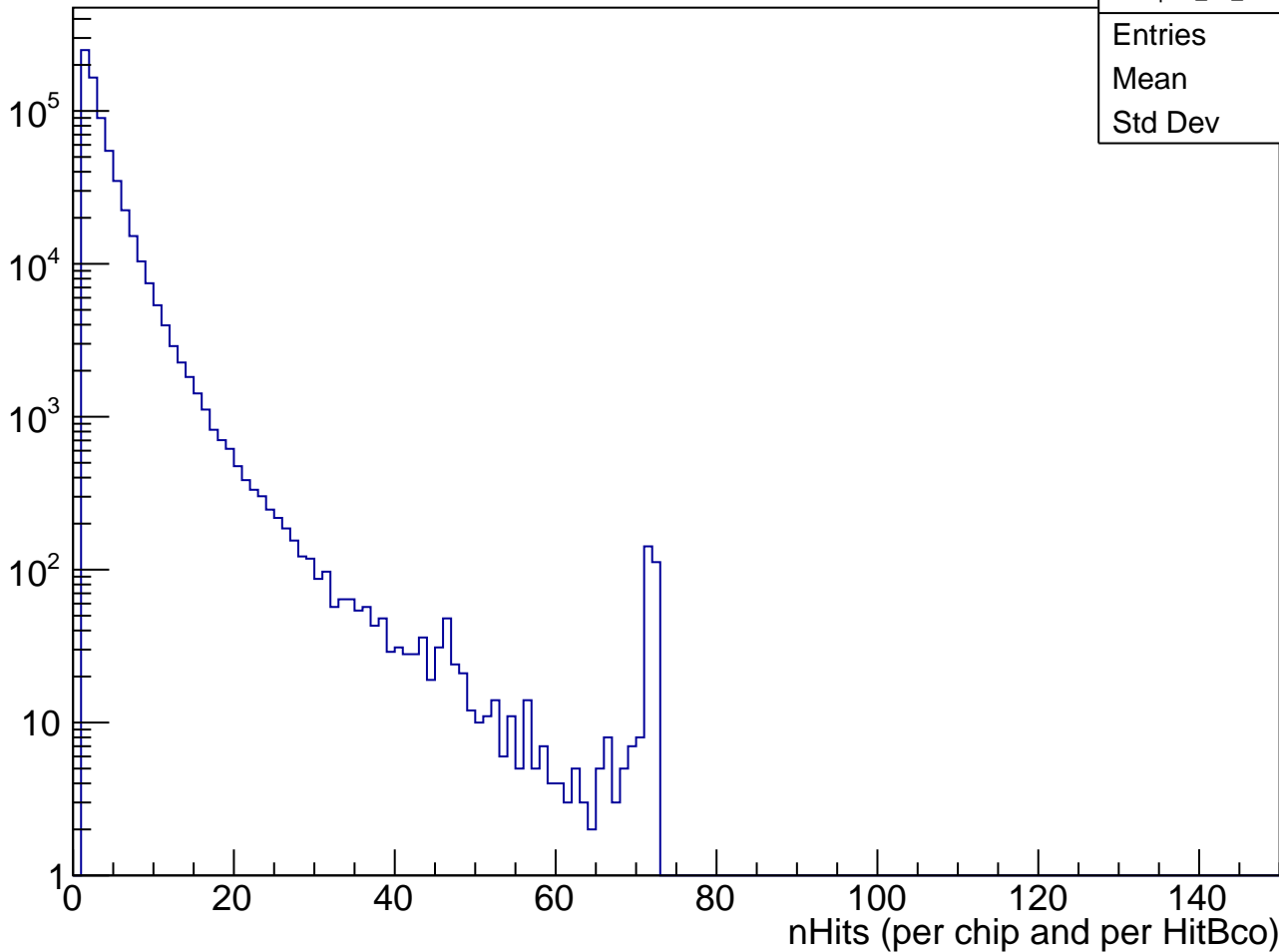
Mean 3.003

Std Dev 3.503

nHits (per chip and per HitBco)

nChipHit_F7_Fch12_Chip19

Entries



nChipHit_F7_Fch12_Chip19

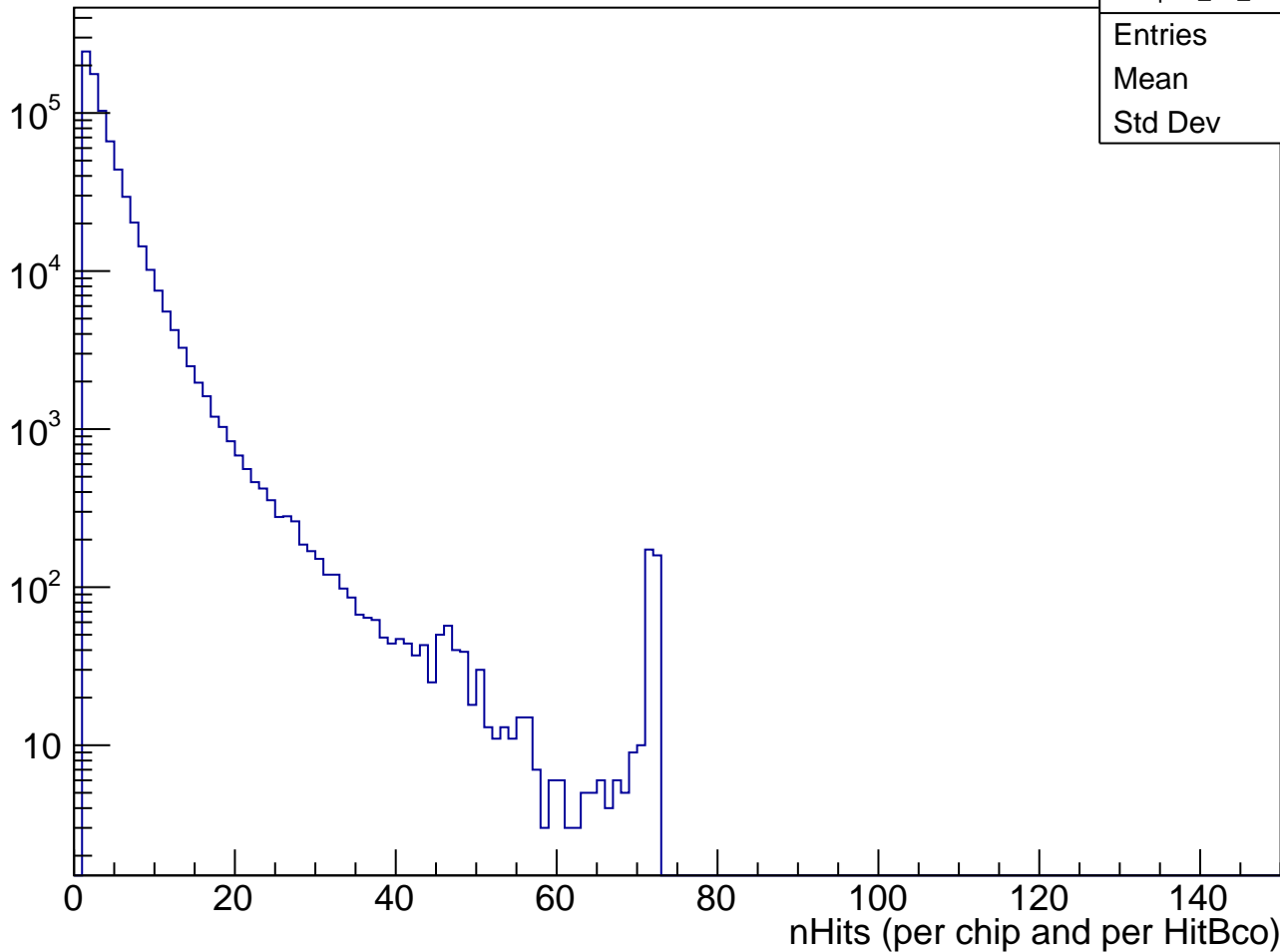
Entries 675192

Mean 2.988

Std Dev 3.47

nChipHit_F7_Fch12_Chip2

Entries

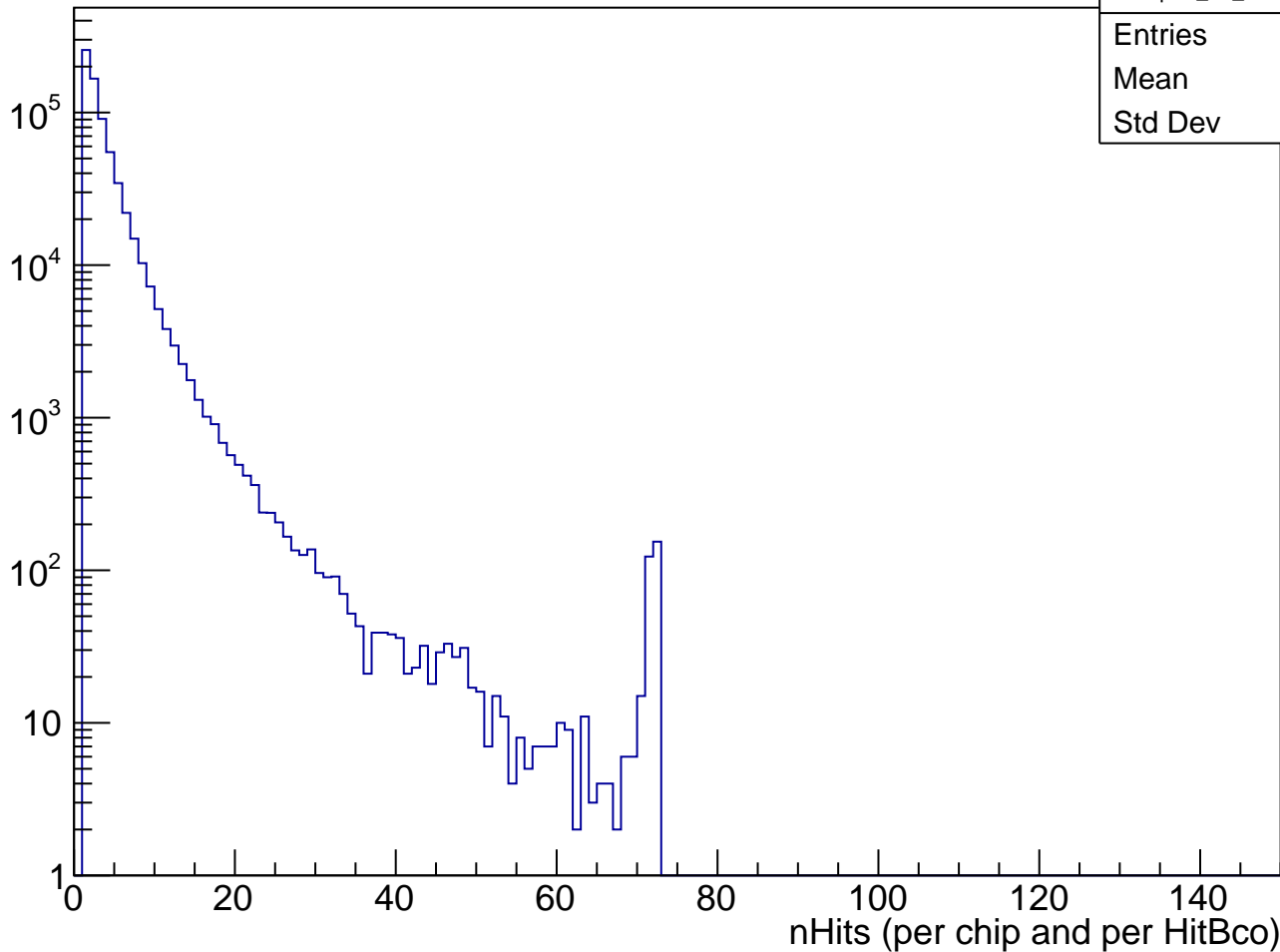


nChipHit_F7_Fch12_Chip2	
Entries	744042
Mean	3.303
Std Dev	3.794

nHits (per chip and per HitBco)

nChipHit_F7_Fch12_Chip20

Entries



nChipHit_F7_Fch12_Chip20

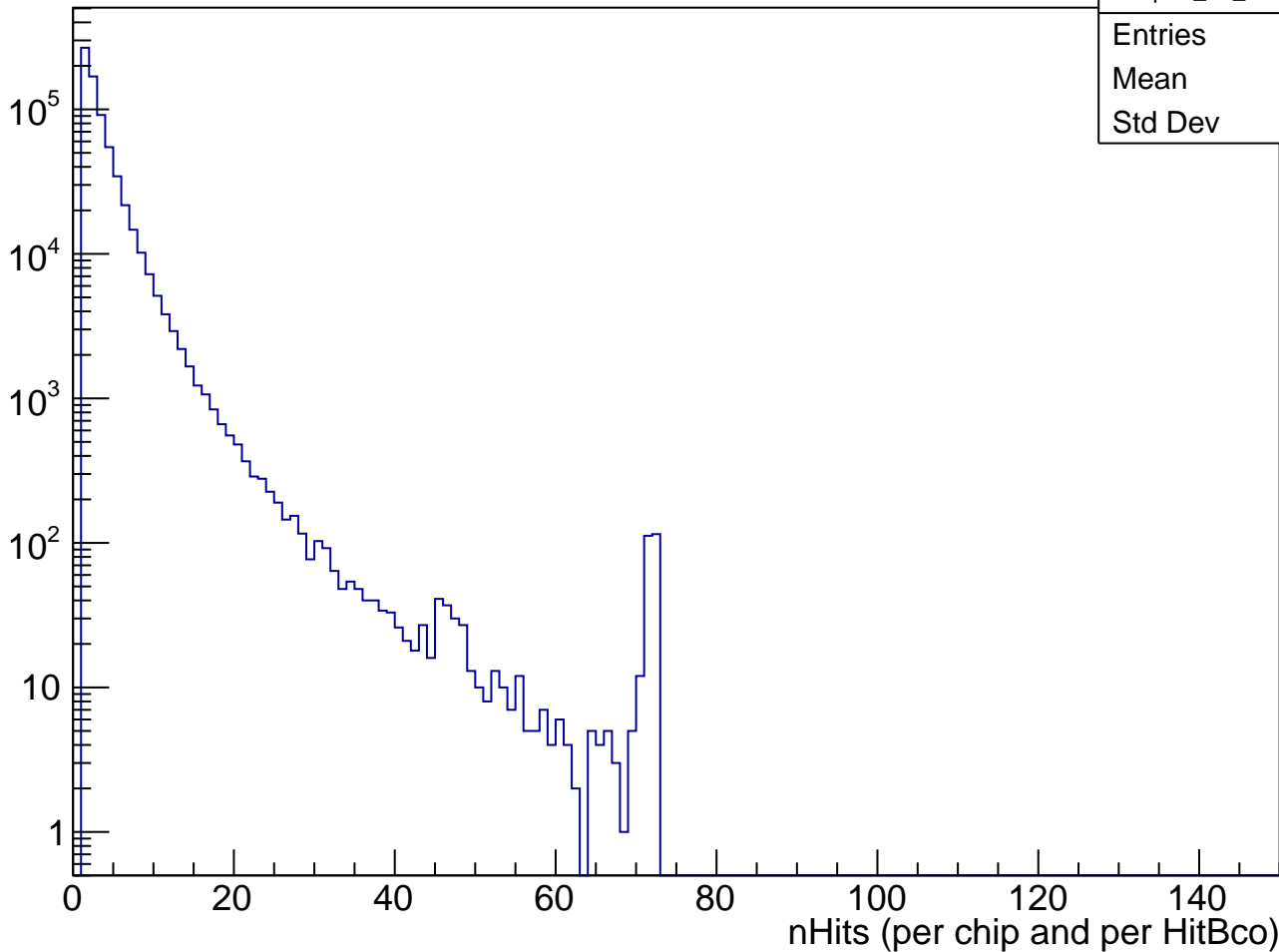
Entries 682700

Mean 2.952

Std Dev 3.466

nChipHit_F7_Fch12_Chip21

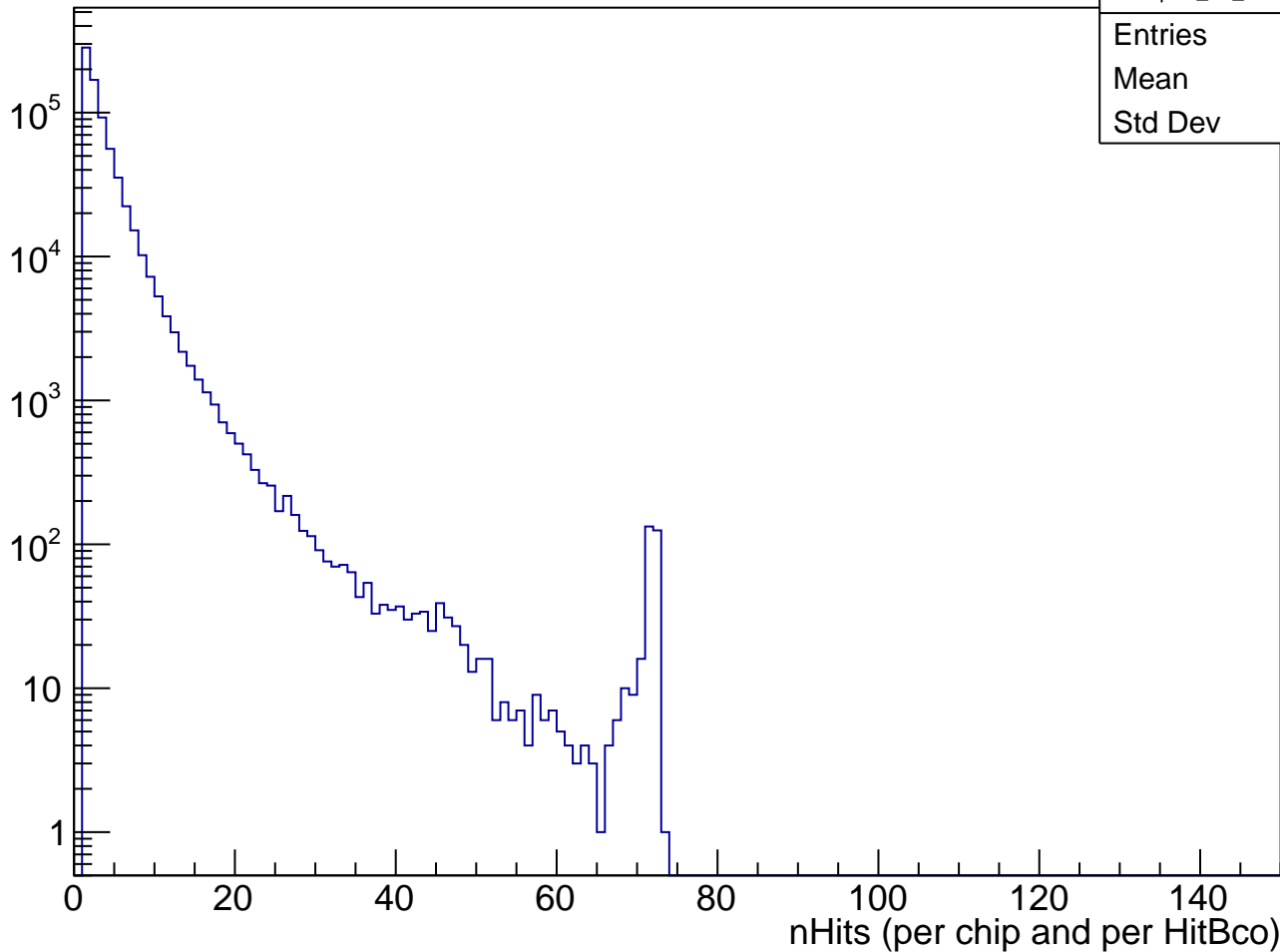
Entries



nChipHit_F7_Fch12_Chip21	
Entries	693844
Mean	2.897
Std Dev	3.334

nChipHit_F7_Fch12_Chip22

Entries



nChipHit_F7_Fch12_Chip22

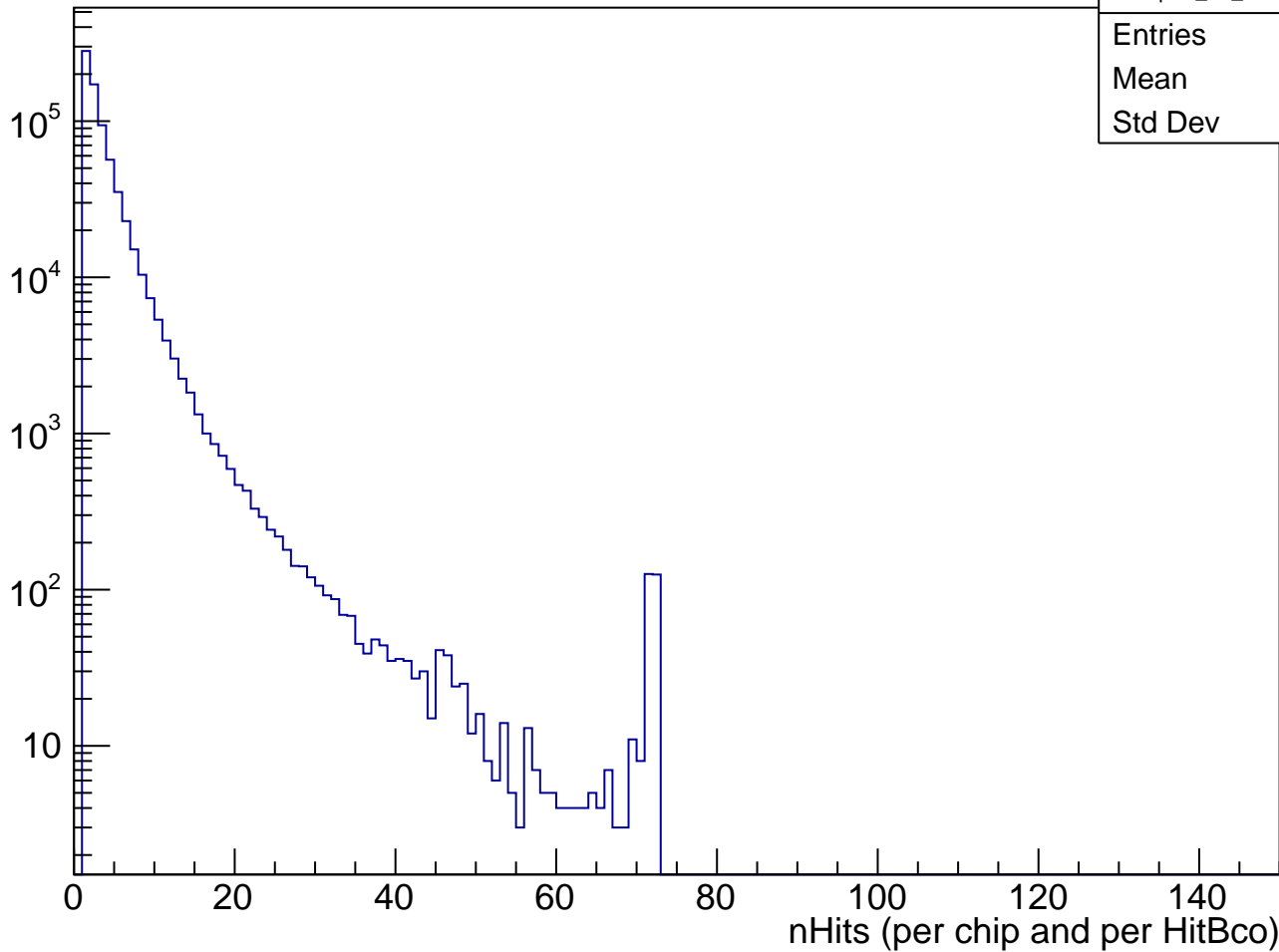
Entries 715327

Mean 2.89

Std Dev 3.407

nChipHit_F7_Fch12_Chip23

Entries



nChipHit_F7_Fch12_Chip23

Entries 719695

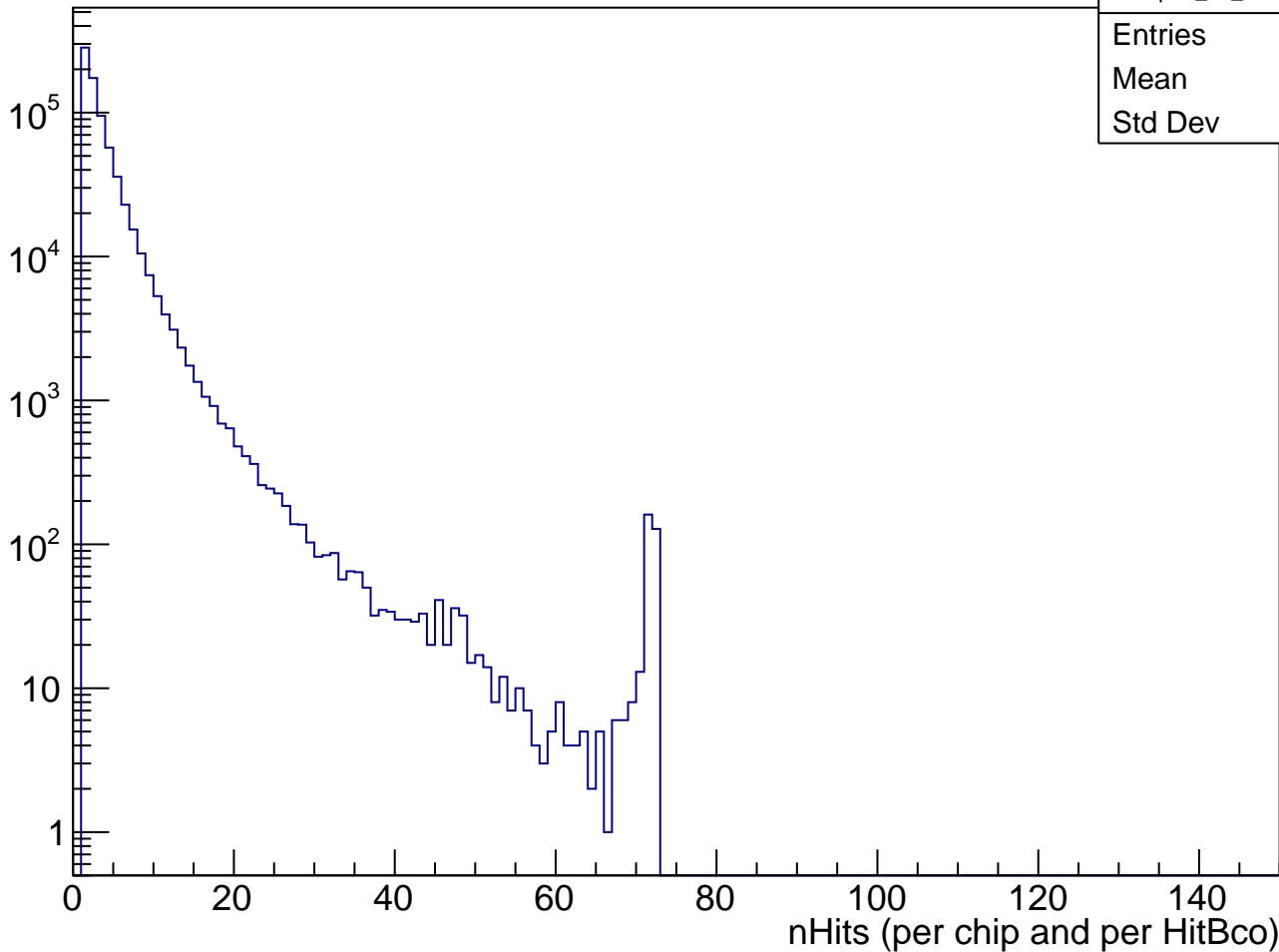
Mean 2.896

Std Dev 3.388

nHits (per chip and per HitBco)

nChipHit_F7_Fch12_Chip24

Entries



nChipHit_F7_Fch12_Chip24

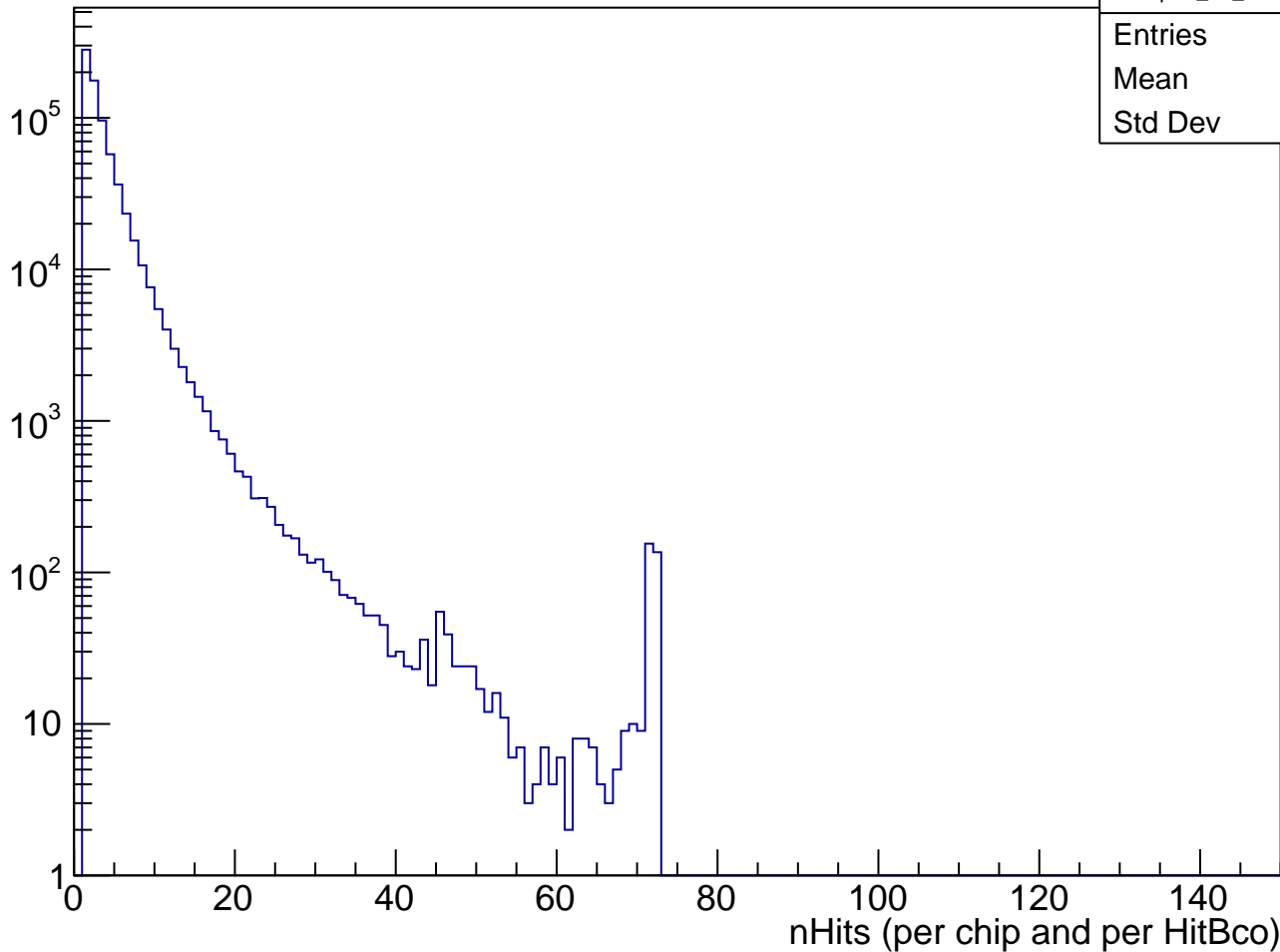
Entries 726270

Mean 2.901

Std Dev 3.418

nChipHit_F7_Fch12_Chip25

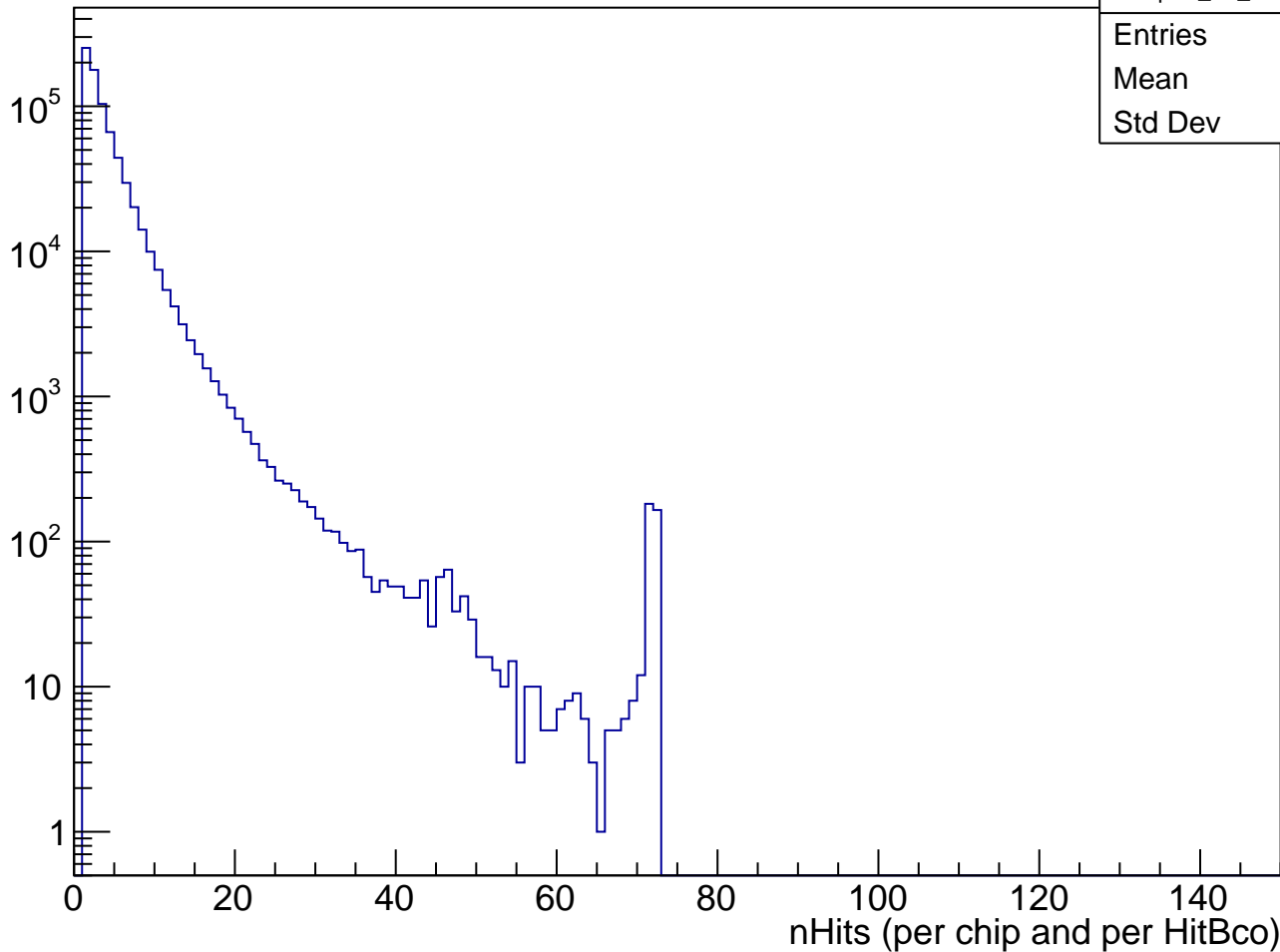
Entries



nChipHit_F7_Fch12_Chip25	
Entries	730518
Mean	2.919
Std Dev	3.452

nChipHit_F7_Fch12_Chip3

Entries



nChipHit_F7_Fch12_Chip3

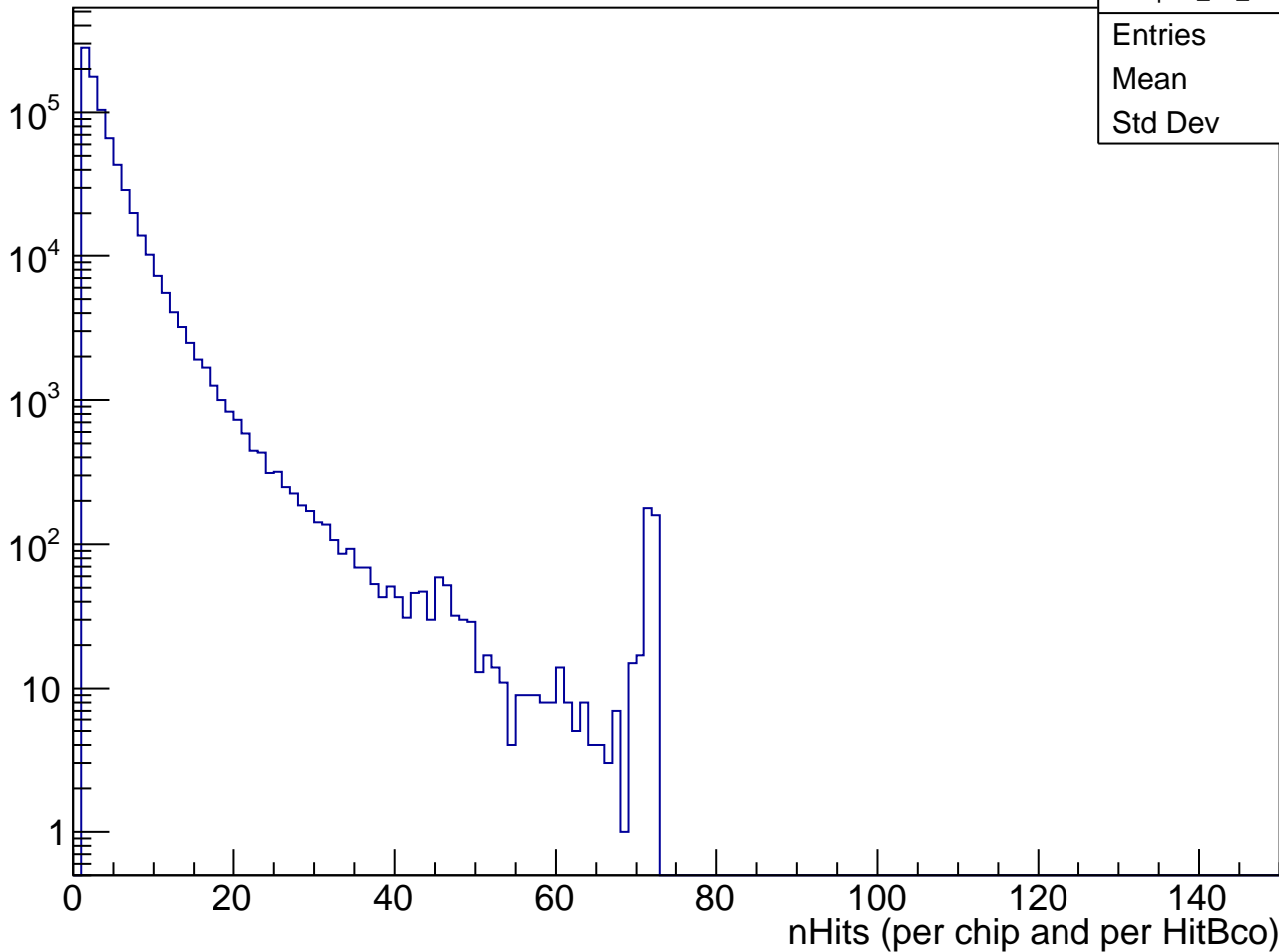
Entries 753325

Mean 3.269

Std Dev 3.78

nChipHit_F7_Fch12_Chip4

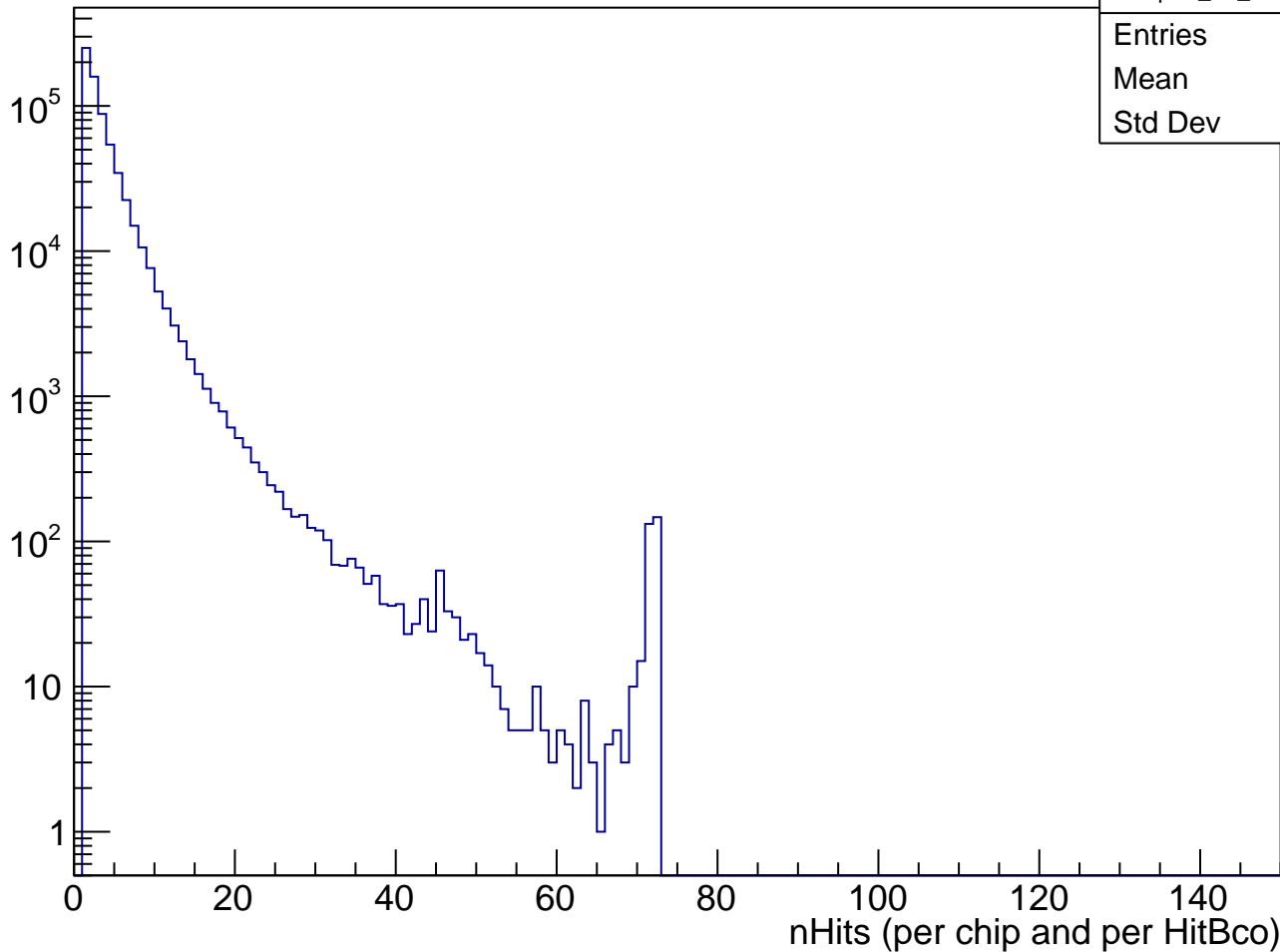
Entries



nChipHit_F7_Fch12_Chip4	
Entries	779197
Mean	3.185
Std Dev	3.738

nChipHit_F7_Fch12_Chip5

Entries

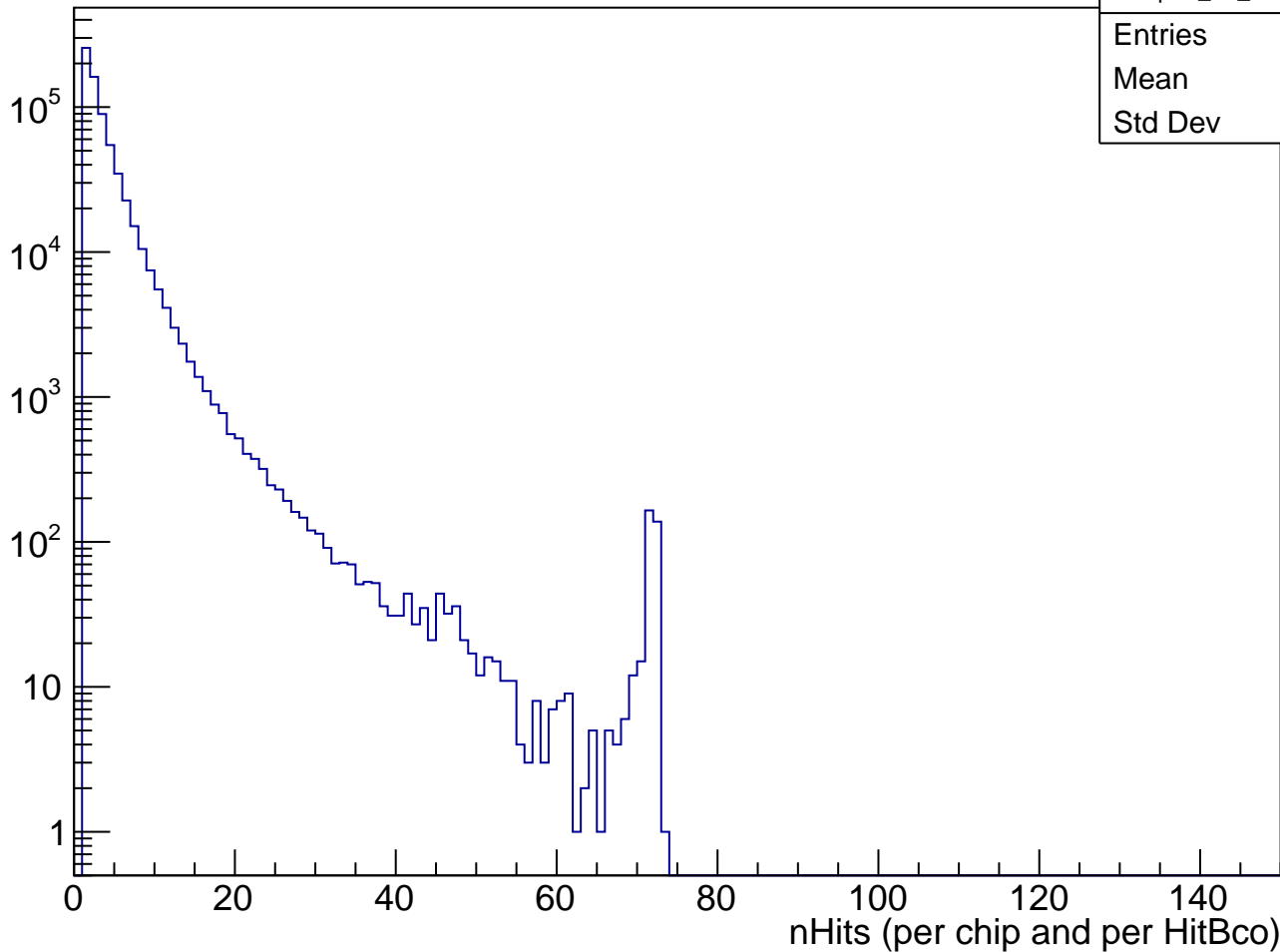


nChipHit_F7_Fch12_Chip5	
Entries	667899
Mean	3.014
Std Dev	3.571

nHits (per chip and per HitBco)

nChipHit_F7_Fch12_Chip6

Entries



nChipHit_F7_Fch12_Chip6

Entries 678035

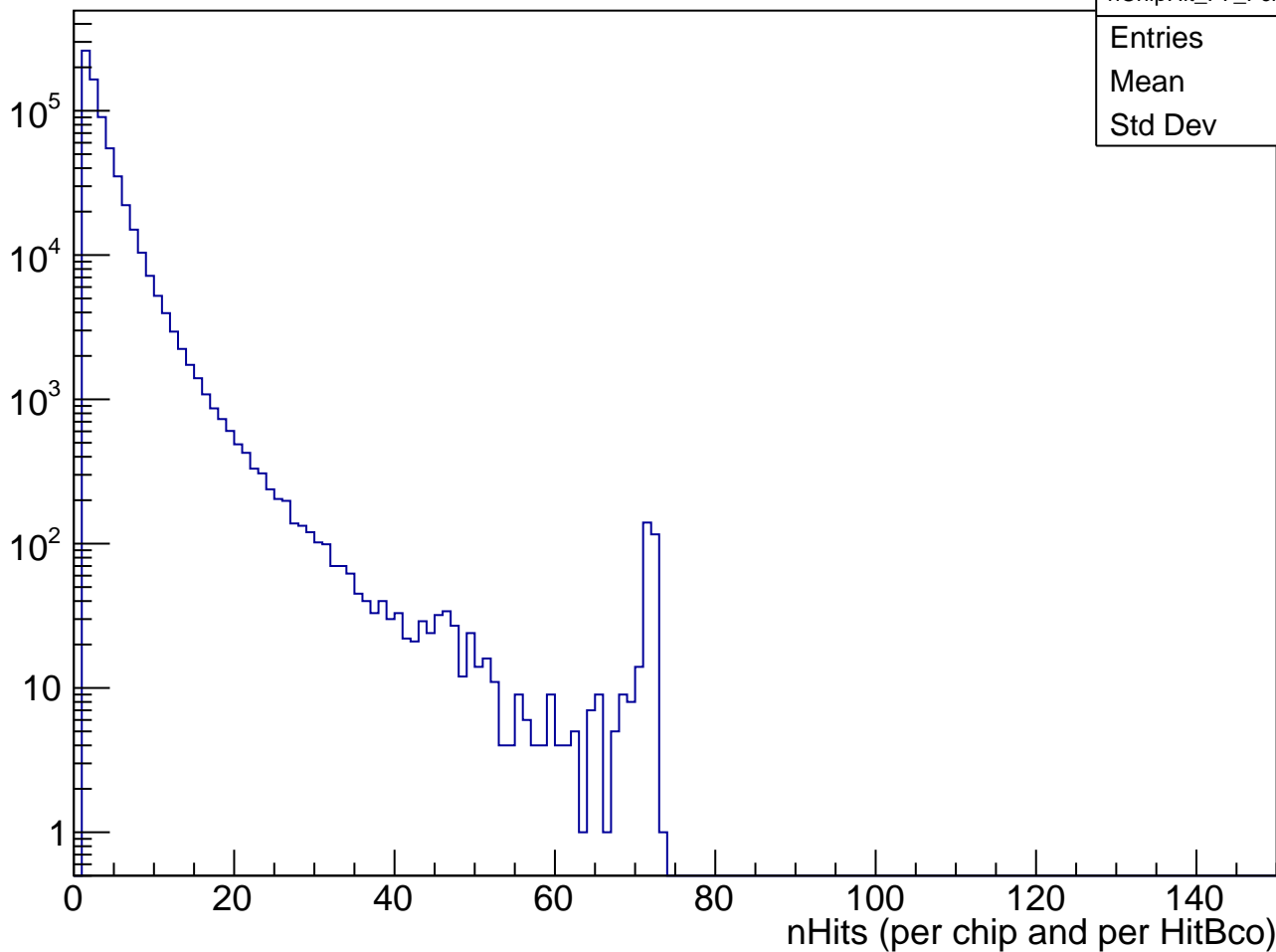
Mean 2.996

Std Dev 3.568

nHits (per chip and per HitBco)

nChipHit_F7_Fch12_Chip7

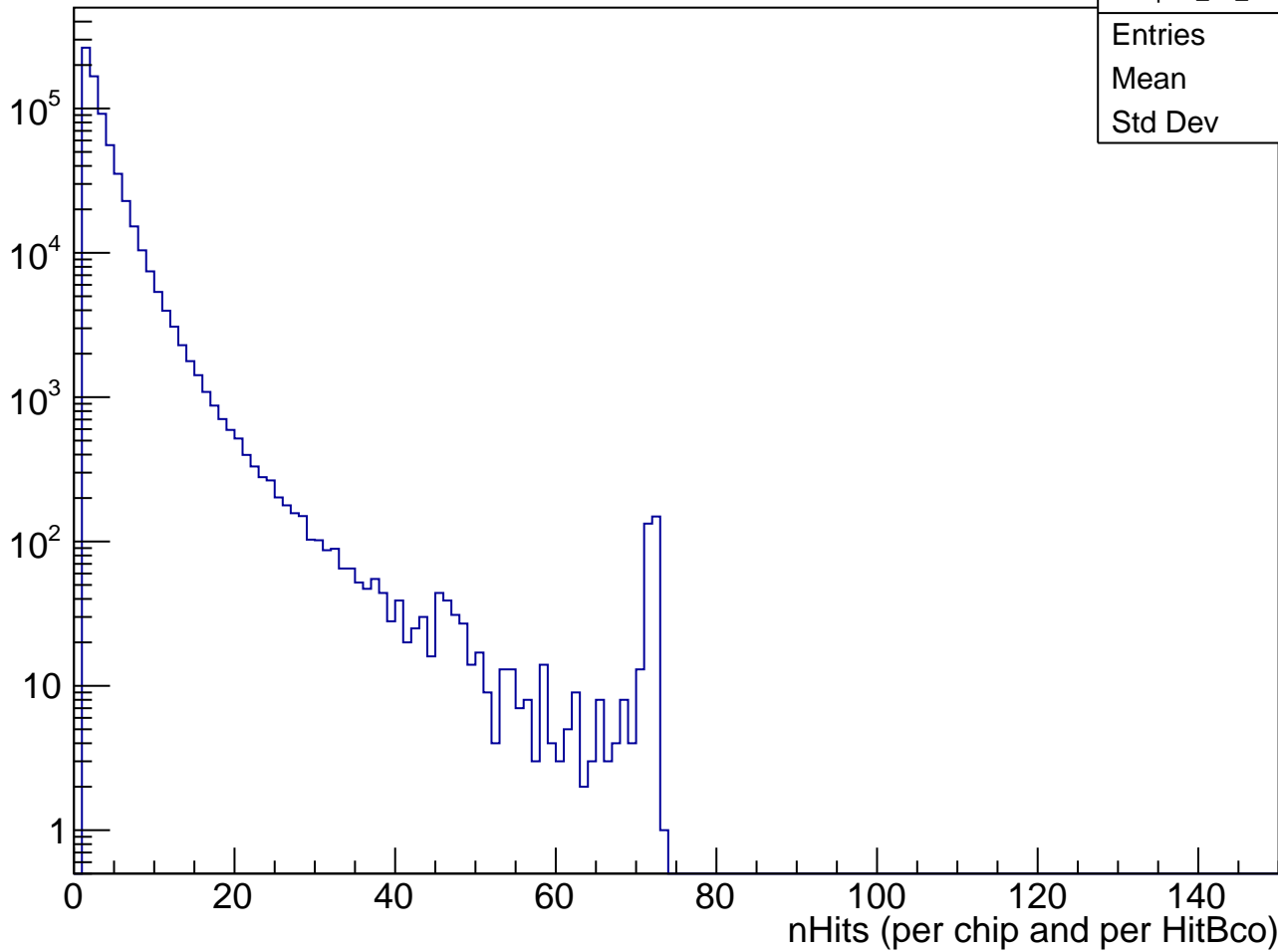
Entries



nChipHit_F7_Fch12_Chip7	
Entries	685372
Mean	2.951
Std Dev	3.45

nChipHit_F7_Fch12_Chip8

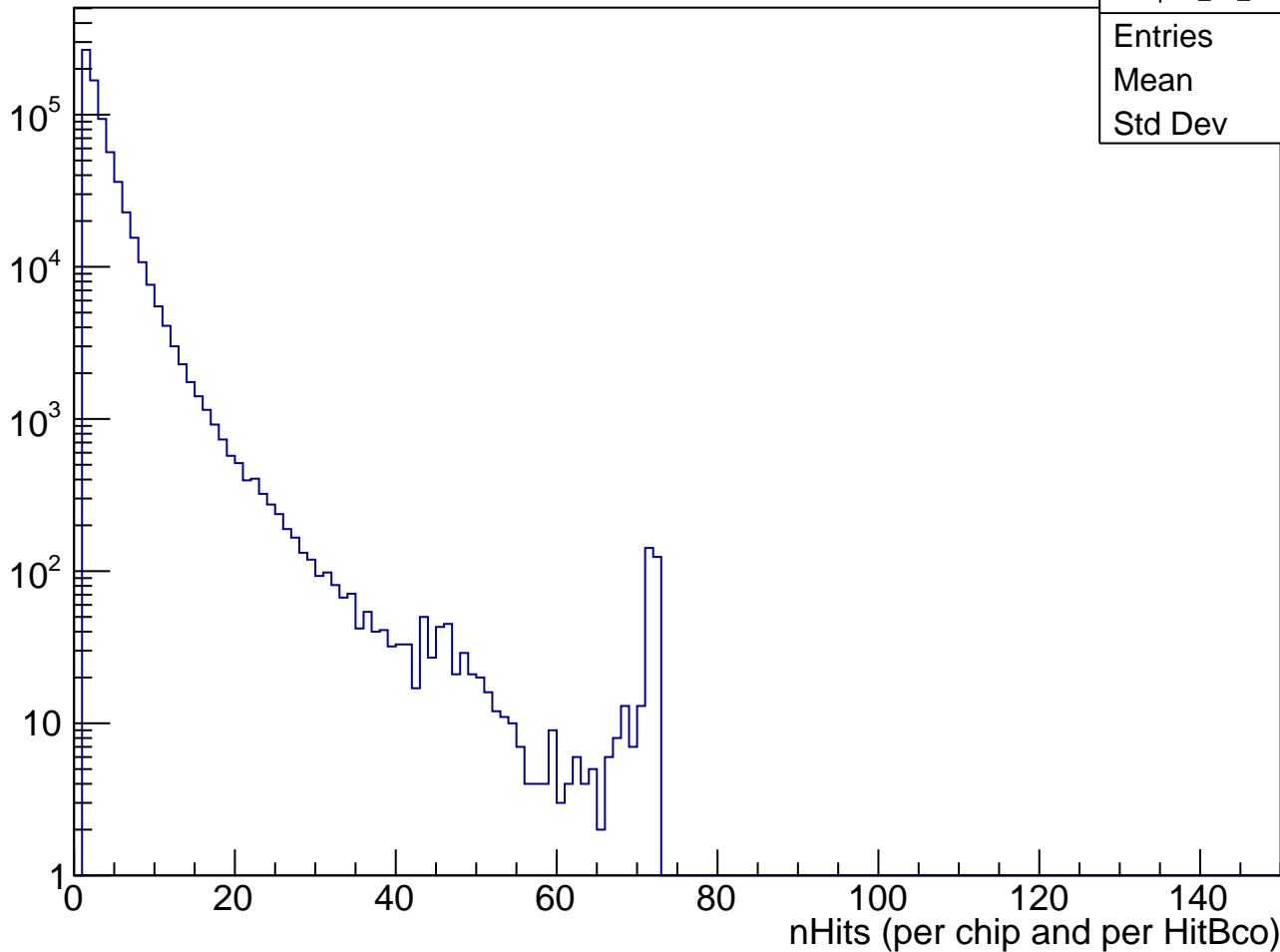
Entries



nChipHit_F7_Fch12_Chip8	
Entries	694763
Mean	2.96
Std Dev	3.487

nChipHit_F7_Fch12_Chip9

Entries

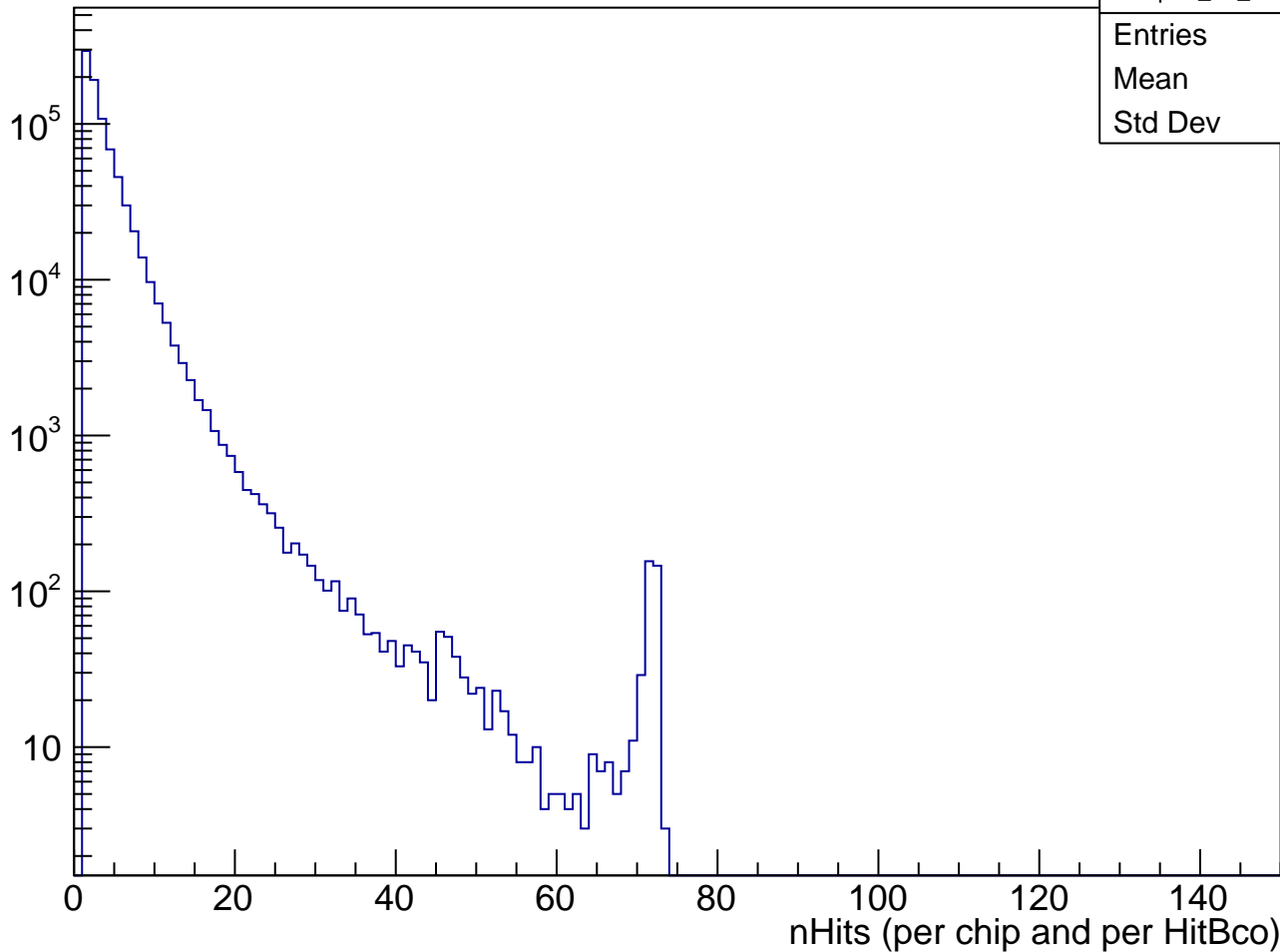


nChipHit_F7_Fch12_Chip9	
Entries	703251
Mean	2.969
Std Dev	3.494

nHits (per chip and per HitBco)

nChipHit_F7_Fch13_Chip0

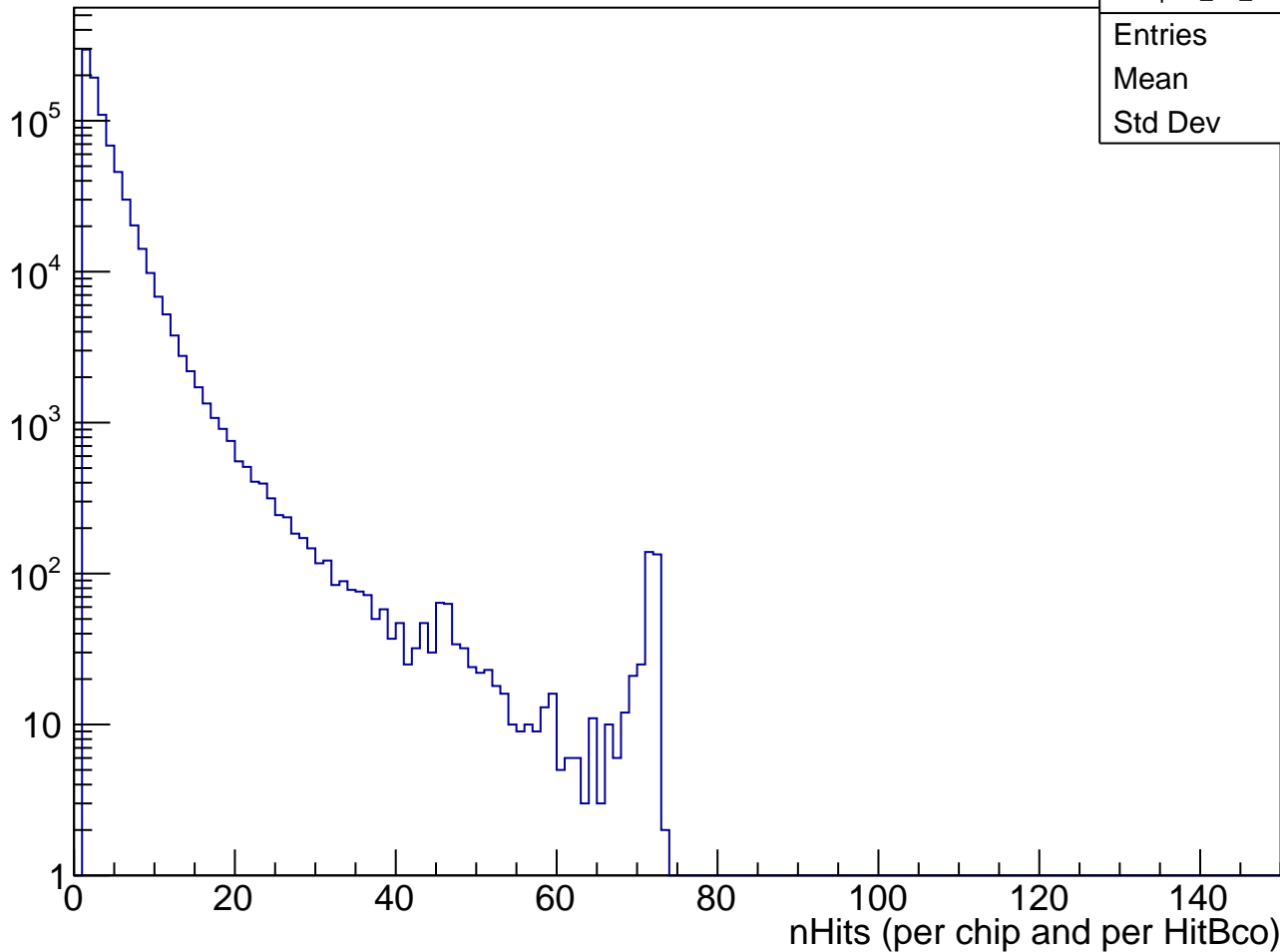
Entries



nChipHit_F7_Fch13_Chip0	
Entries	814272
Mean	3.088
Std Dev	3.559

nChipHit_F7_Fch13_Chip1

Entries

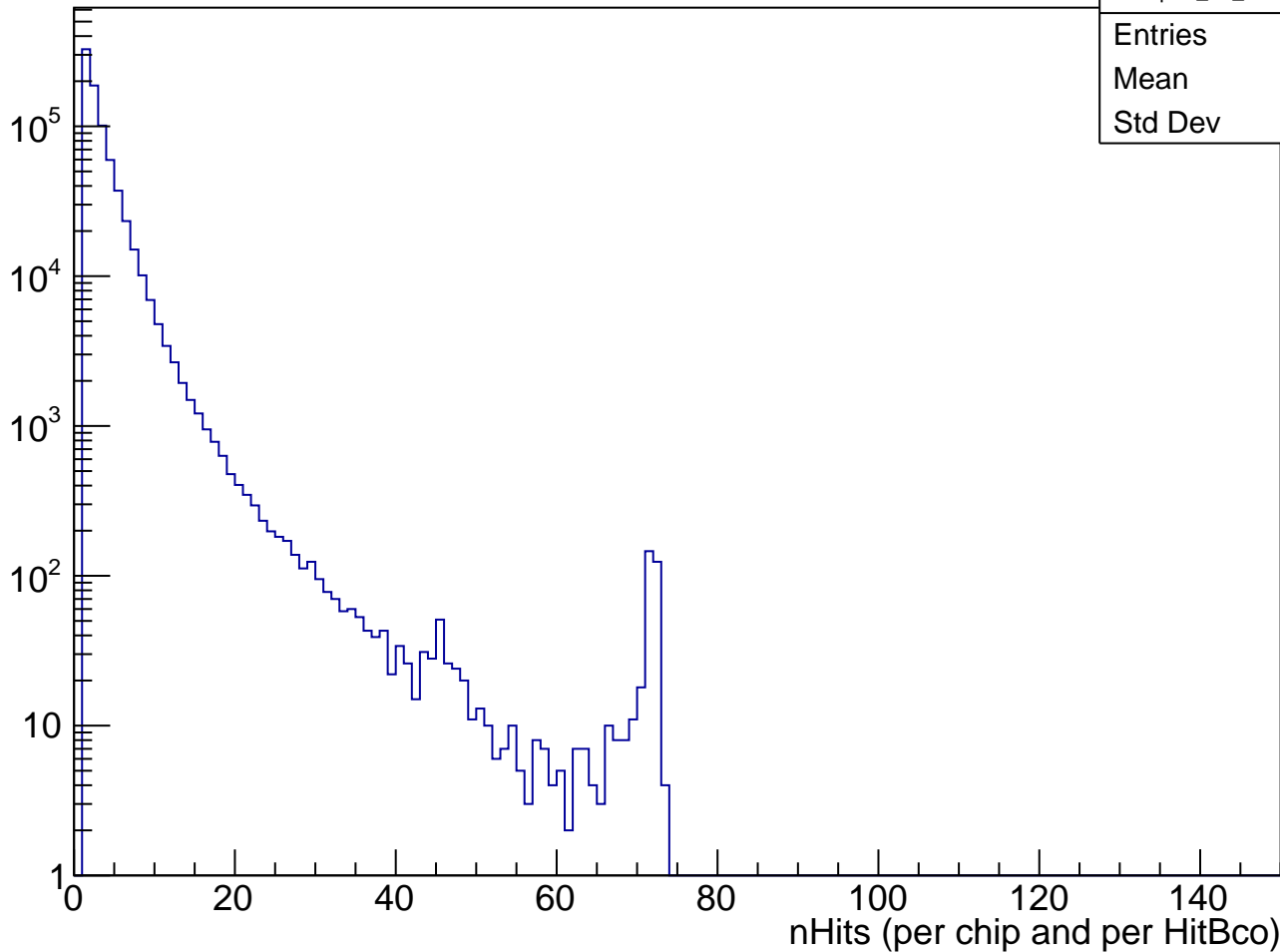


nChipHit_F7_Fch13_Chip1	
Entries	818859
Mean	3.082
Std Dev	3.561

nHits (per chip and per HitBco)

nChipHit_F7_Fch13_Chip10

Entries



nChipHit_F7_Fch13_Chip10

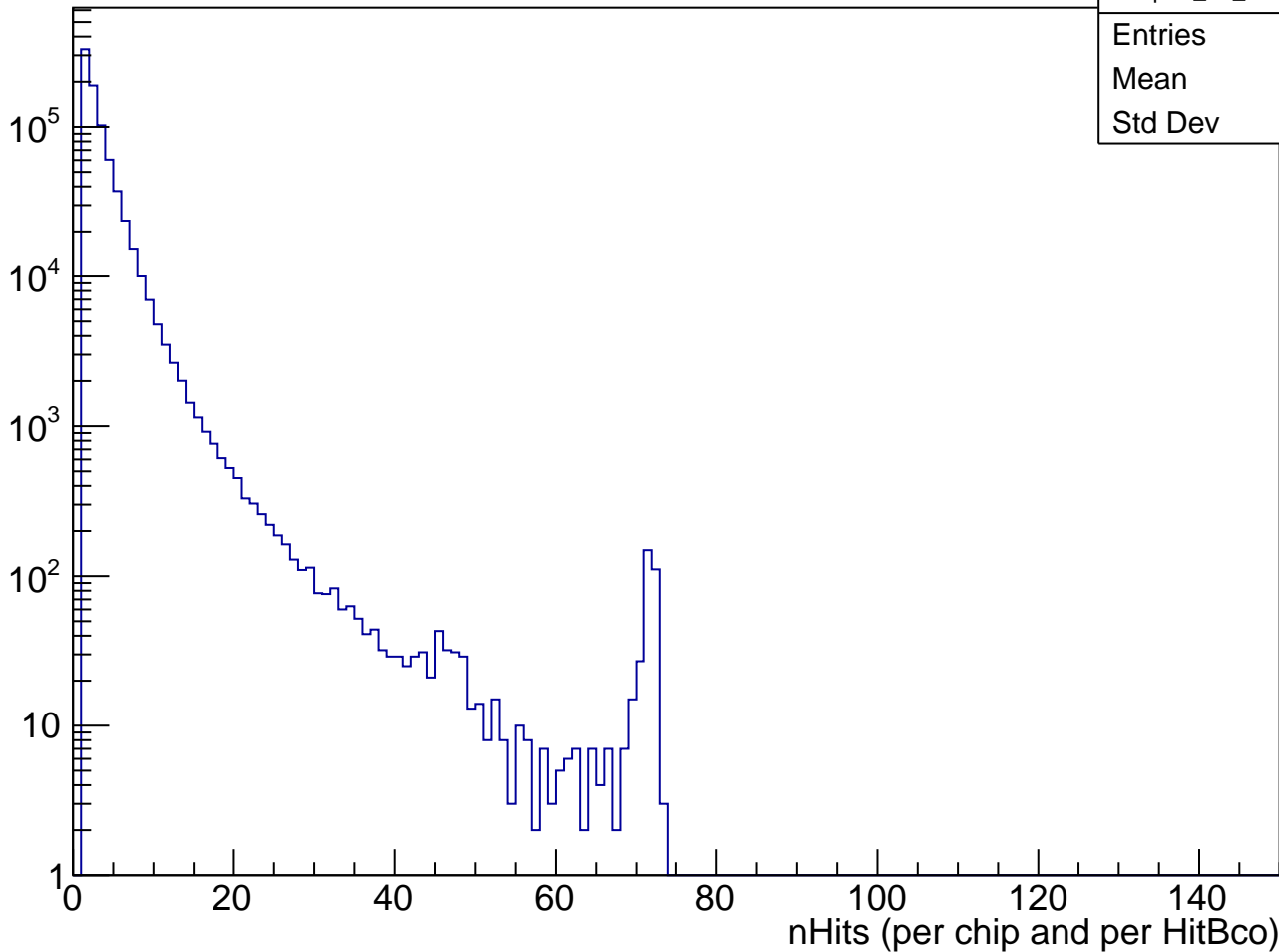
Entries 789233

Mean 2.736

Std Dev 3.206

nChipHit_F7_Fch13_Chip11

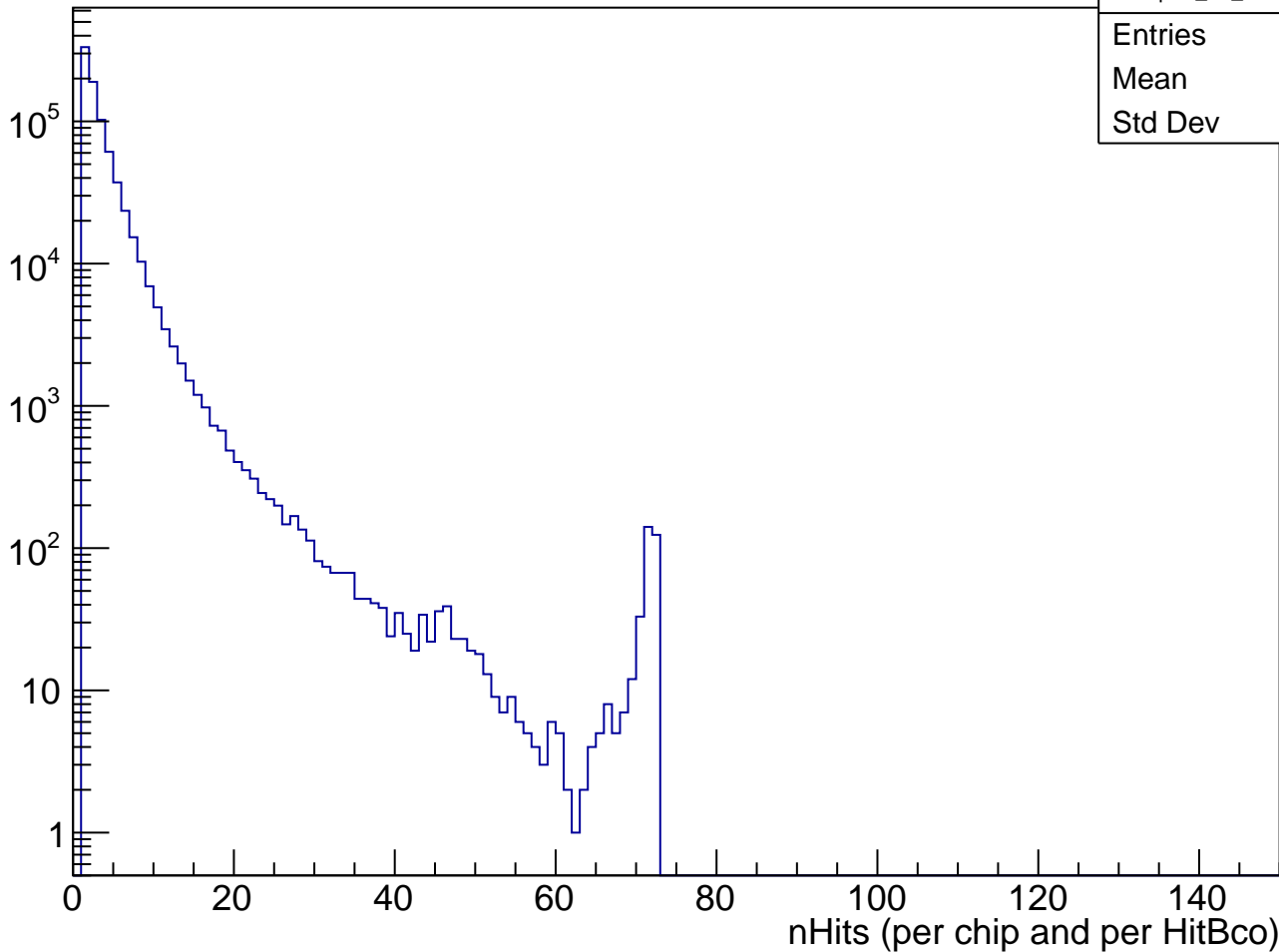
Entries



nChipHit_F7_Fch13_Chip11	
Entries	795809
Mean	2.733
Std Dev	3.2

nChipHit_F7_Fch13_Chip12

Entries



nChipHit_F7_Fch13_Chip12

Entries 800949

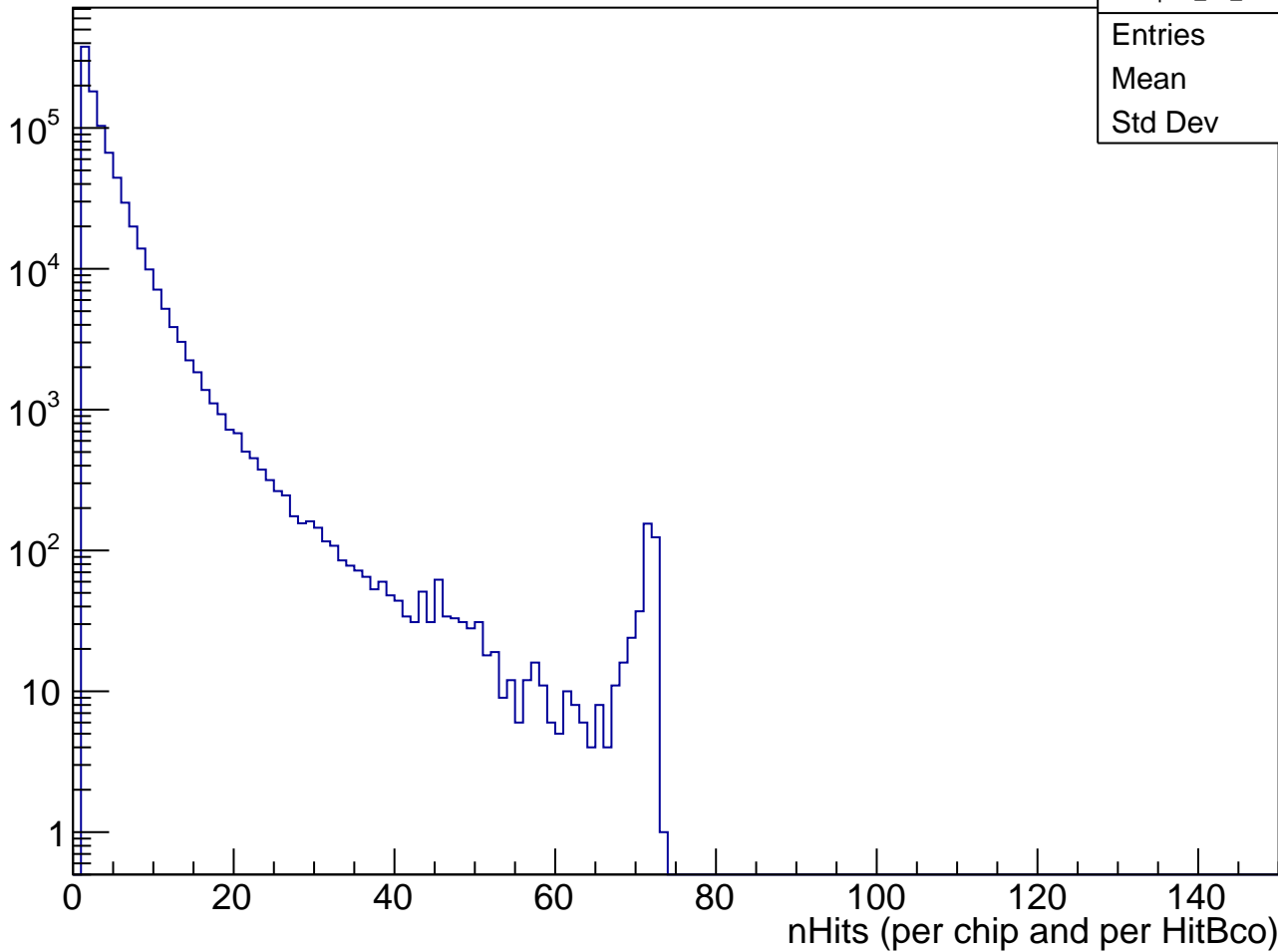
Mean 2.732

Std Dev 3.199

nHits (per chip and per HitBco)

nChipHit_F7_Fch13_Chip13

Entries



nChipHit_F7_Fch13_Chip13

Entries 879403

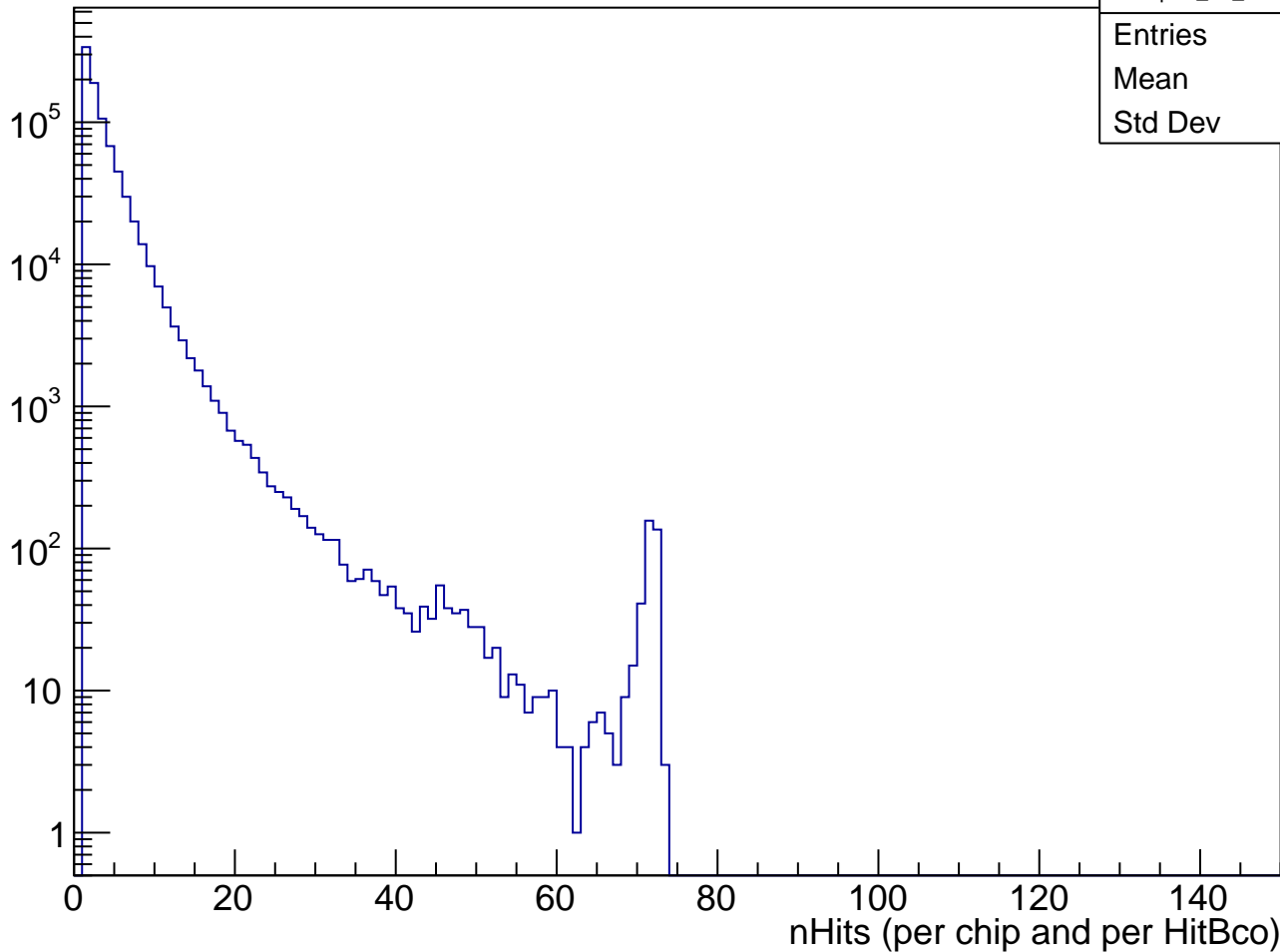
Mean 2.91

Std Dev 3.523

nHits (per chip and per HitBco)

nChipHit_F7_Fch13_Chip14

Entries



nChipHit_F7_Fch13_Chip14

Entries 850807

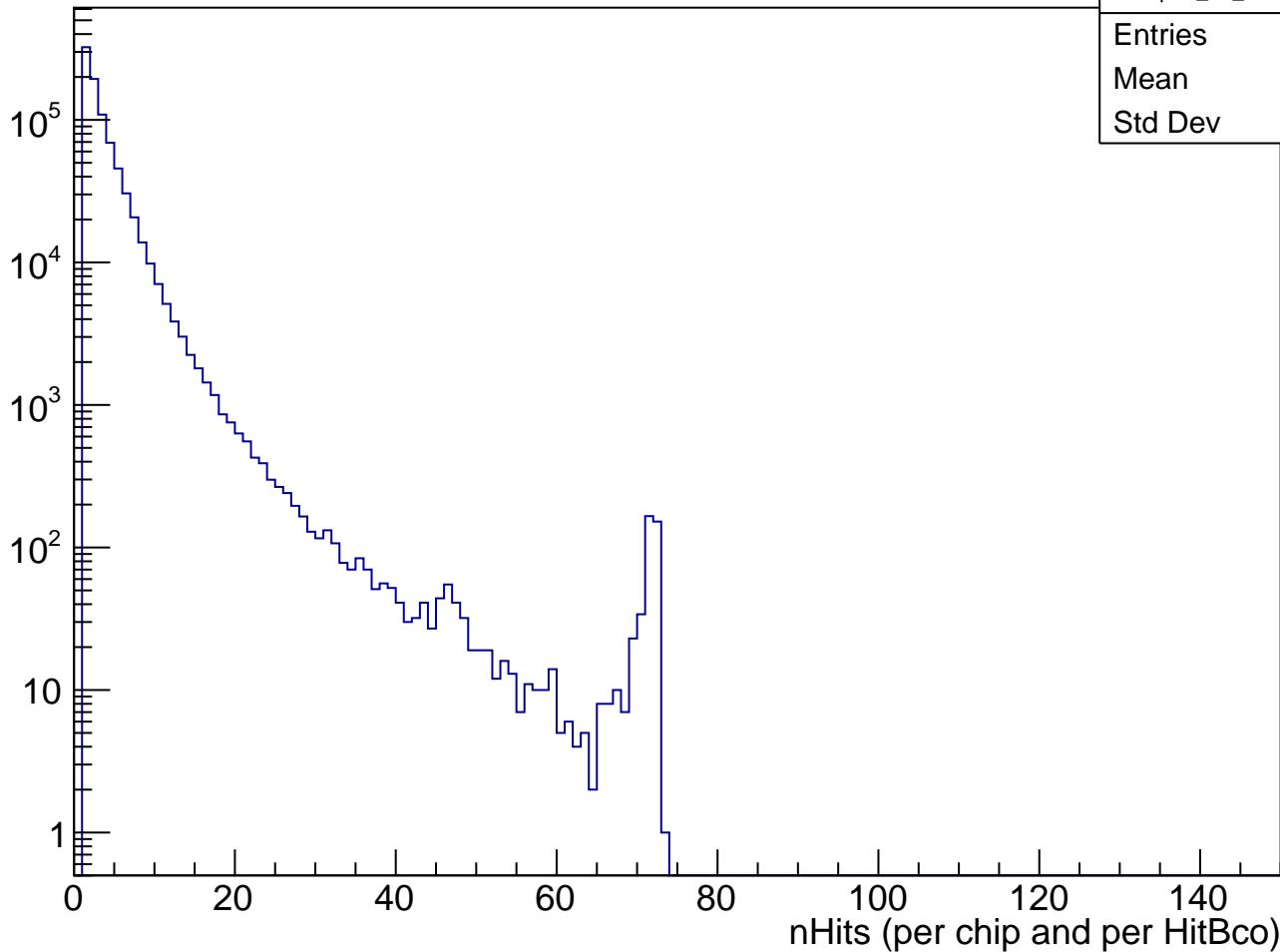
Mean 2.976

Std Dev 3.51

nHits (per chip and per HitBco)

nChipHit_F7_Fch13_Chip15

Entries



nChipHit_F7_Fch13_Chip15

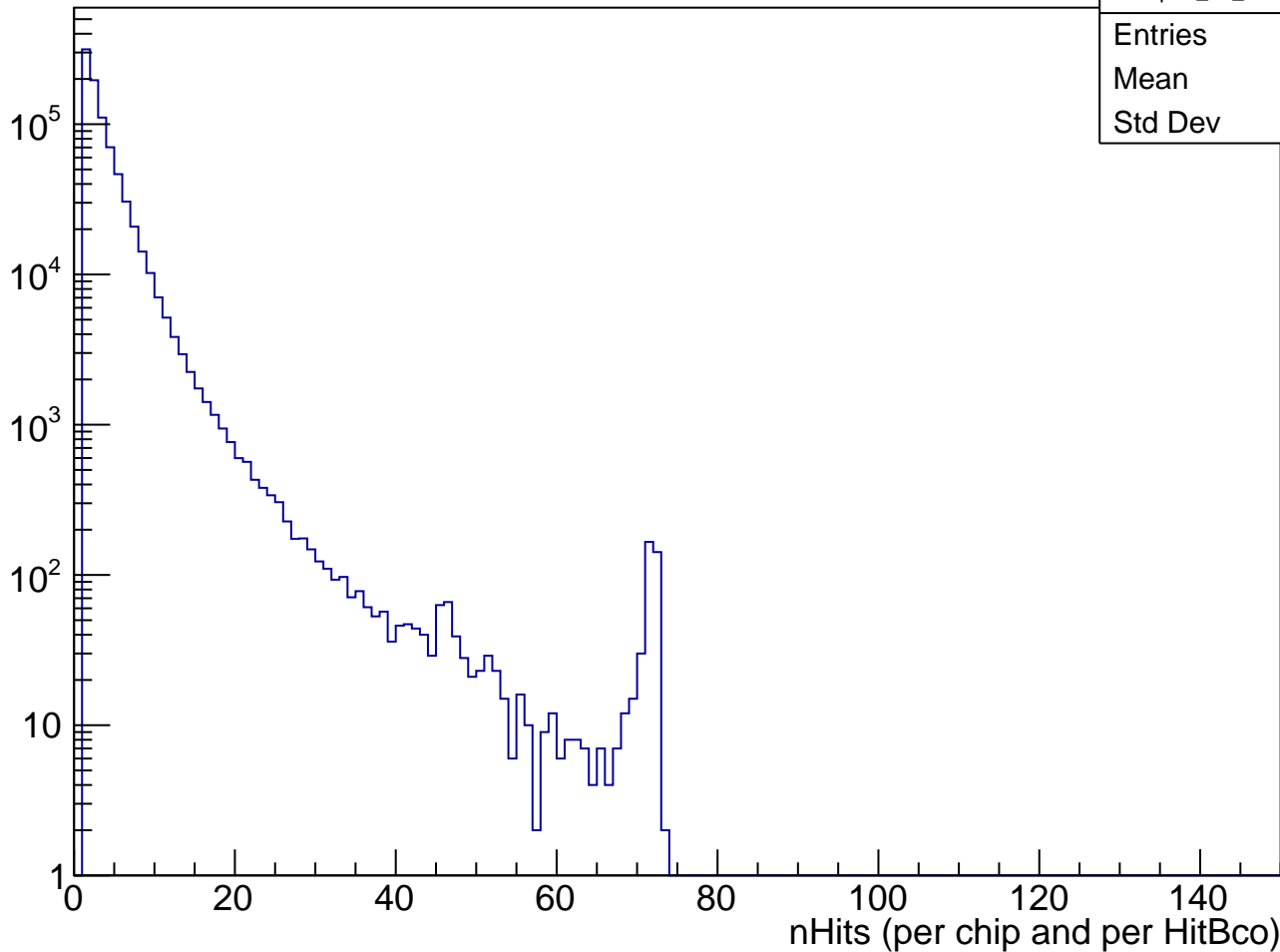
Entries 848298

Mean 3.03

Std Dev 3.565

nChipHit_F7_Fch13_Chip16

Entries



nChipHit_F7_Fch13_Chip16

Entries 846679

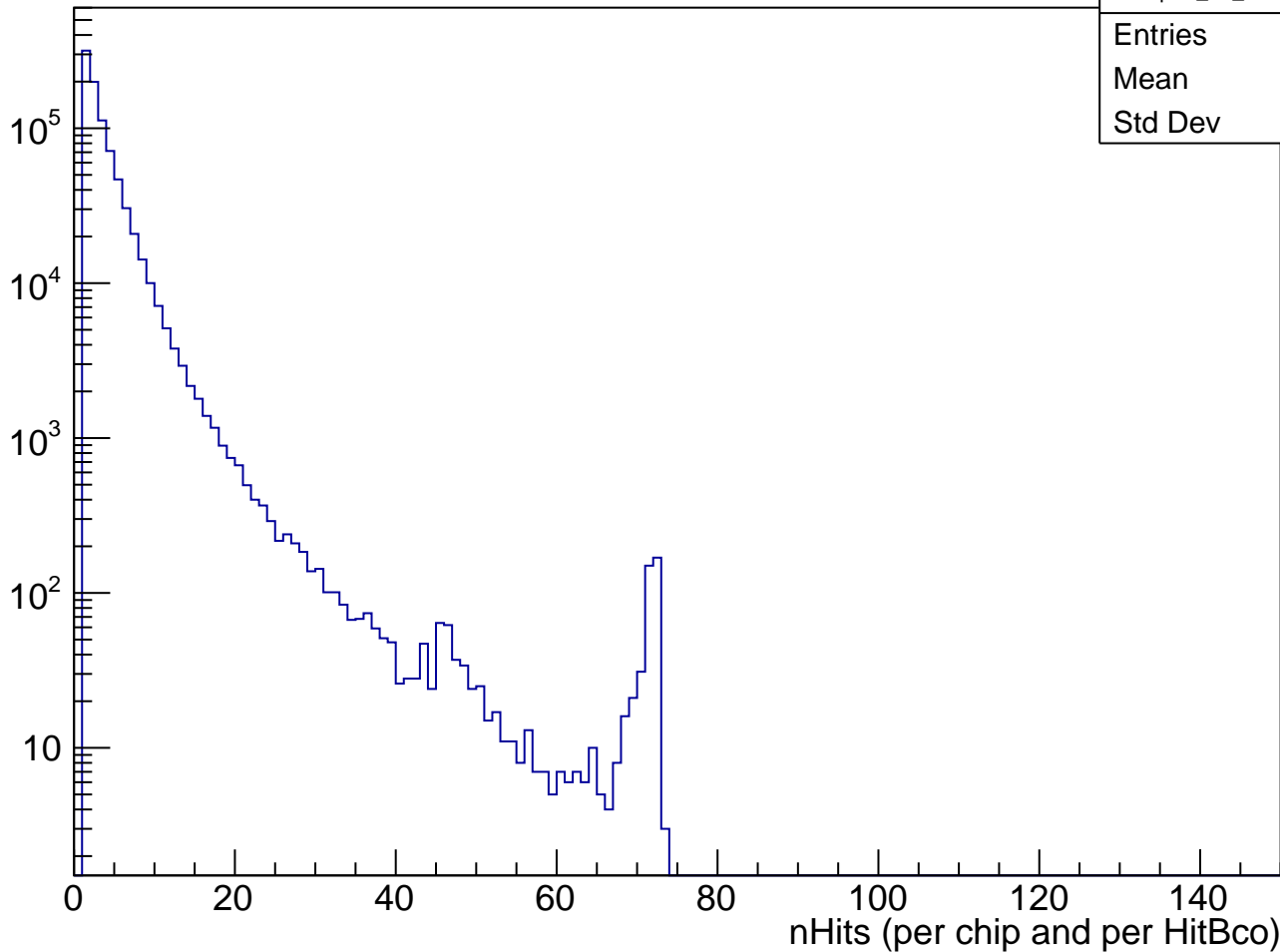
Mean 3.059

Std Dev 3.575

nHits (per chip and per HitBco)

nChipHit_F7_Fch13_Chip17

Entries



nChipHit_F7_Fch13_Chip17

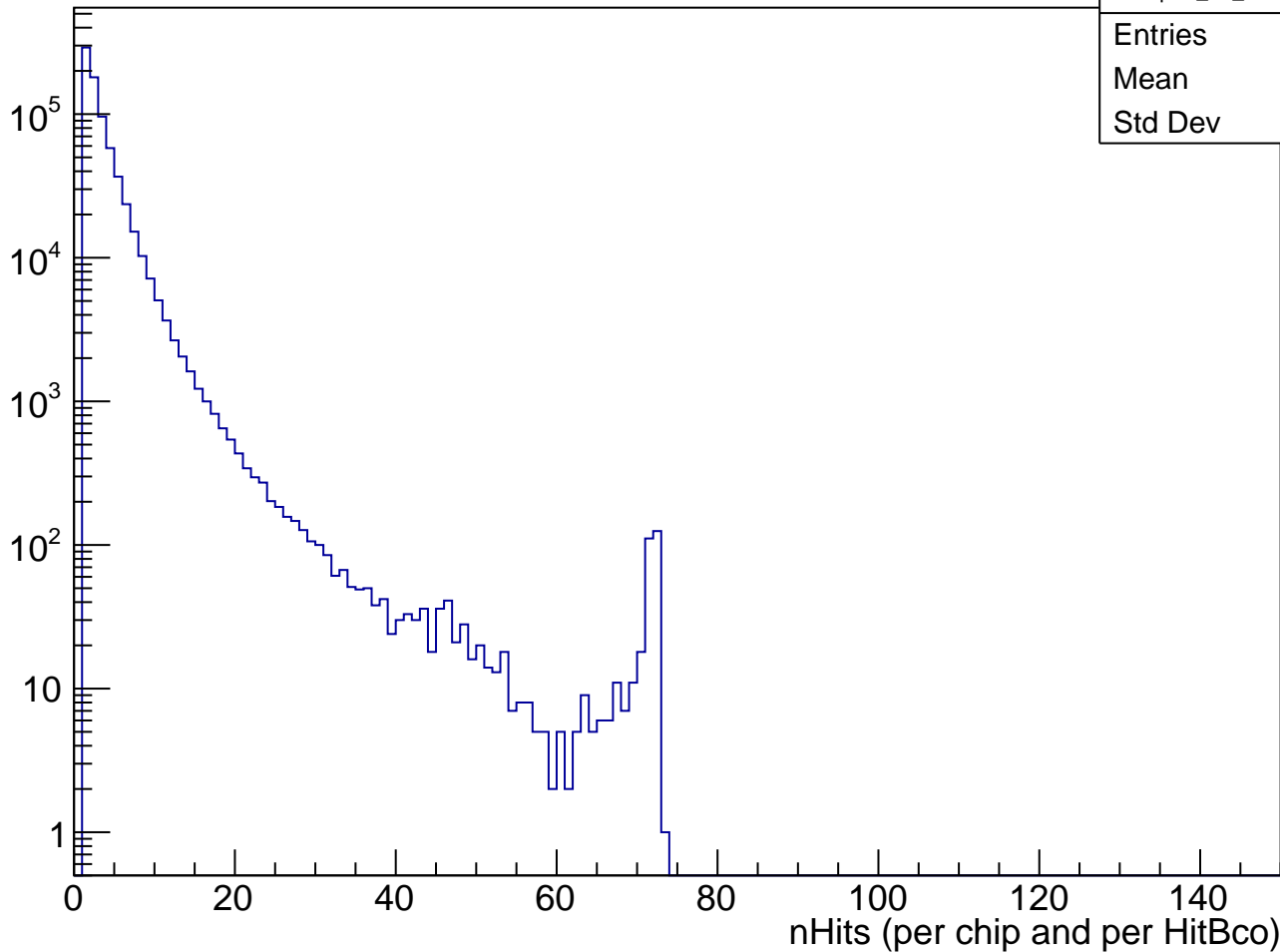
Entries 854406

Mean 3.043

Std Dev 3.546

nChipHit_F7_Fch13_Chip18

Entries



nChipHit_F7_Fch13_Chip18

Entries 741184

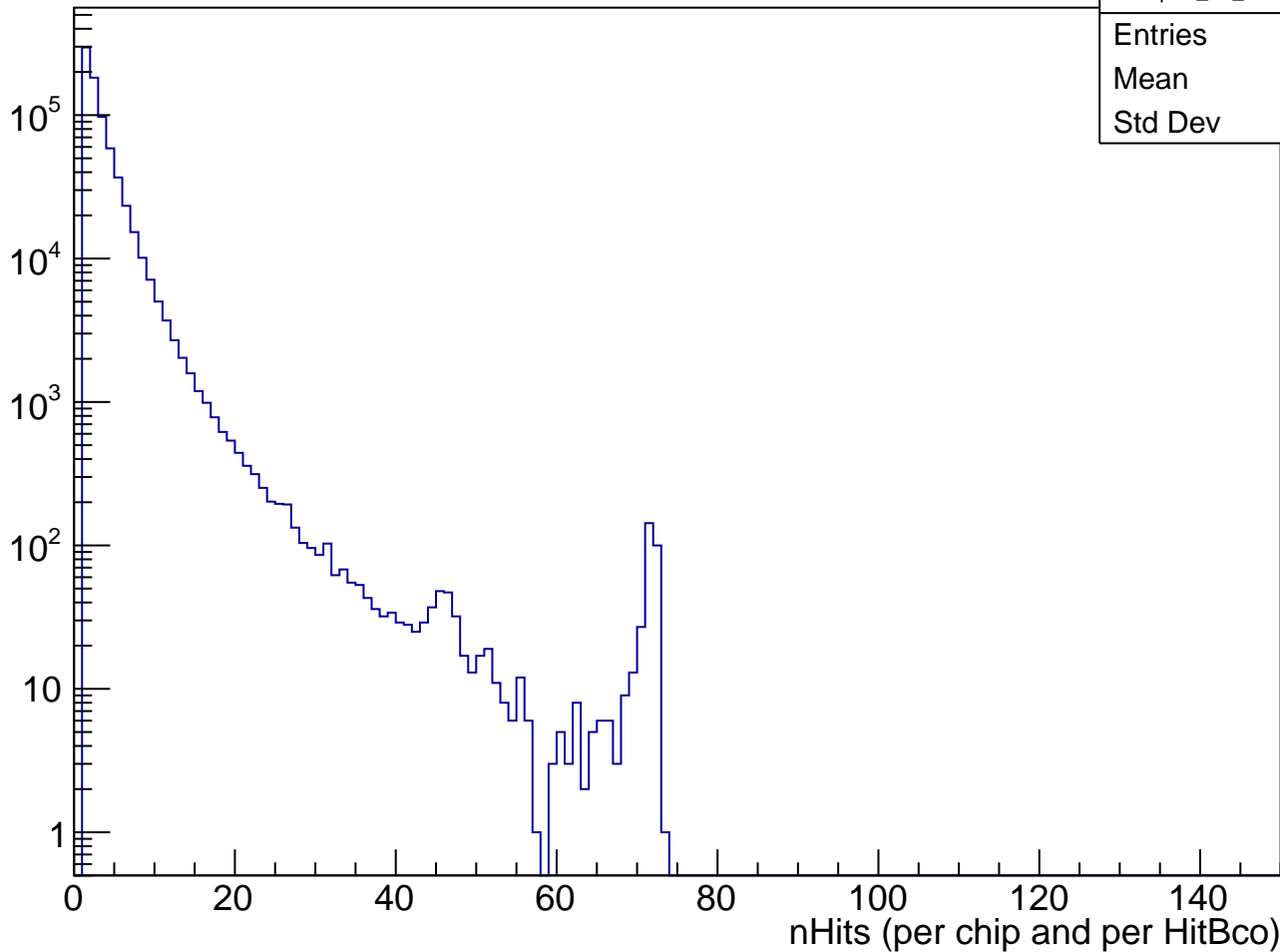
Mean 2.84

Std Dev 3.299

nHits (per chip and per HitBco)

nChipHit_F7_Fch13_Chip19

Entries



nChipHit_F7_Fch13_Chip19

Entries 750157

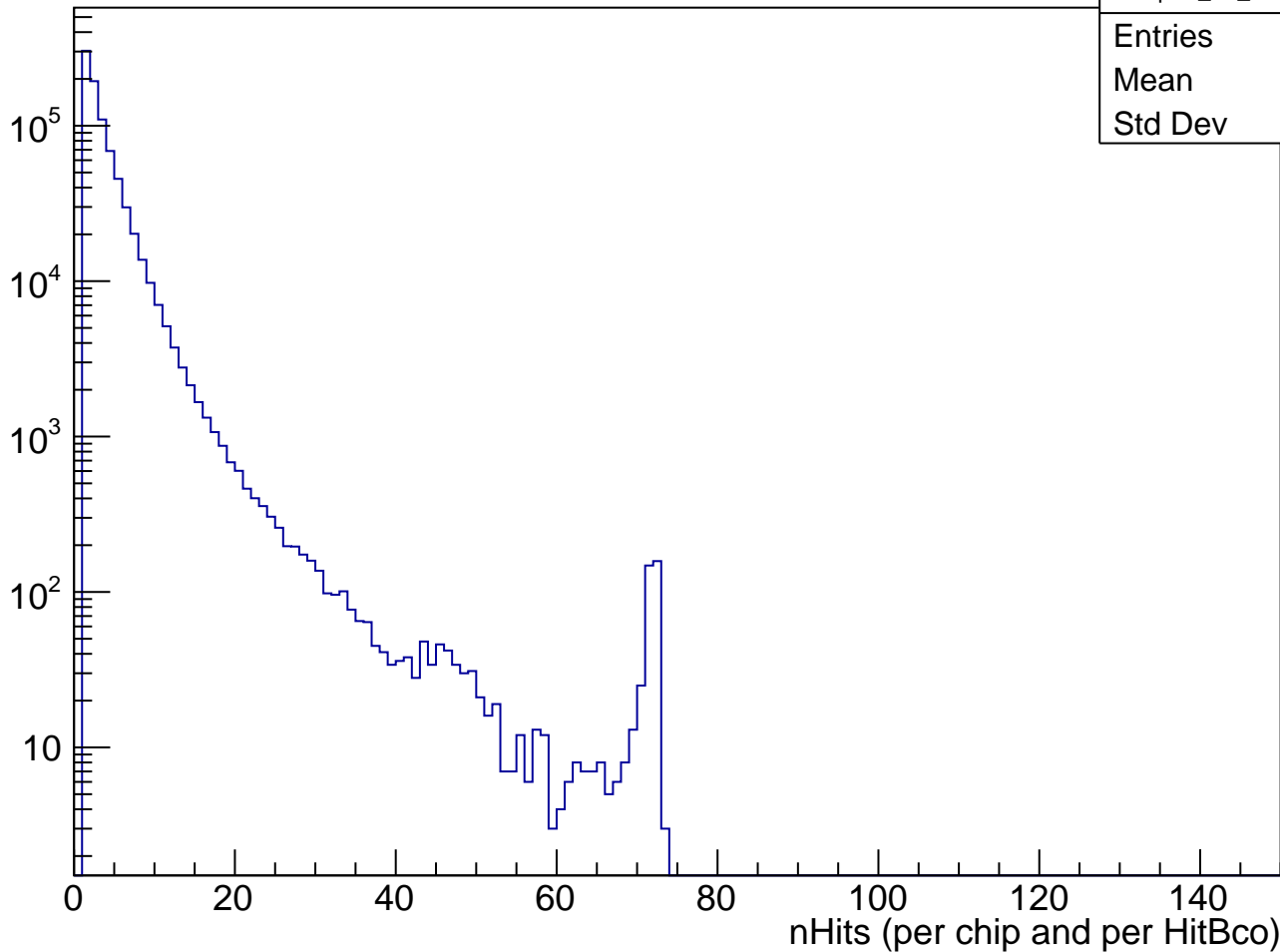
Mean 2.822

Std Dev 3.284

nHits (per chip and per HitBco)

nChipHit_F7_Fch13_Chip2

Entries

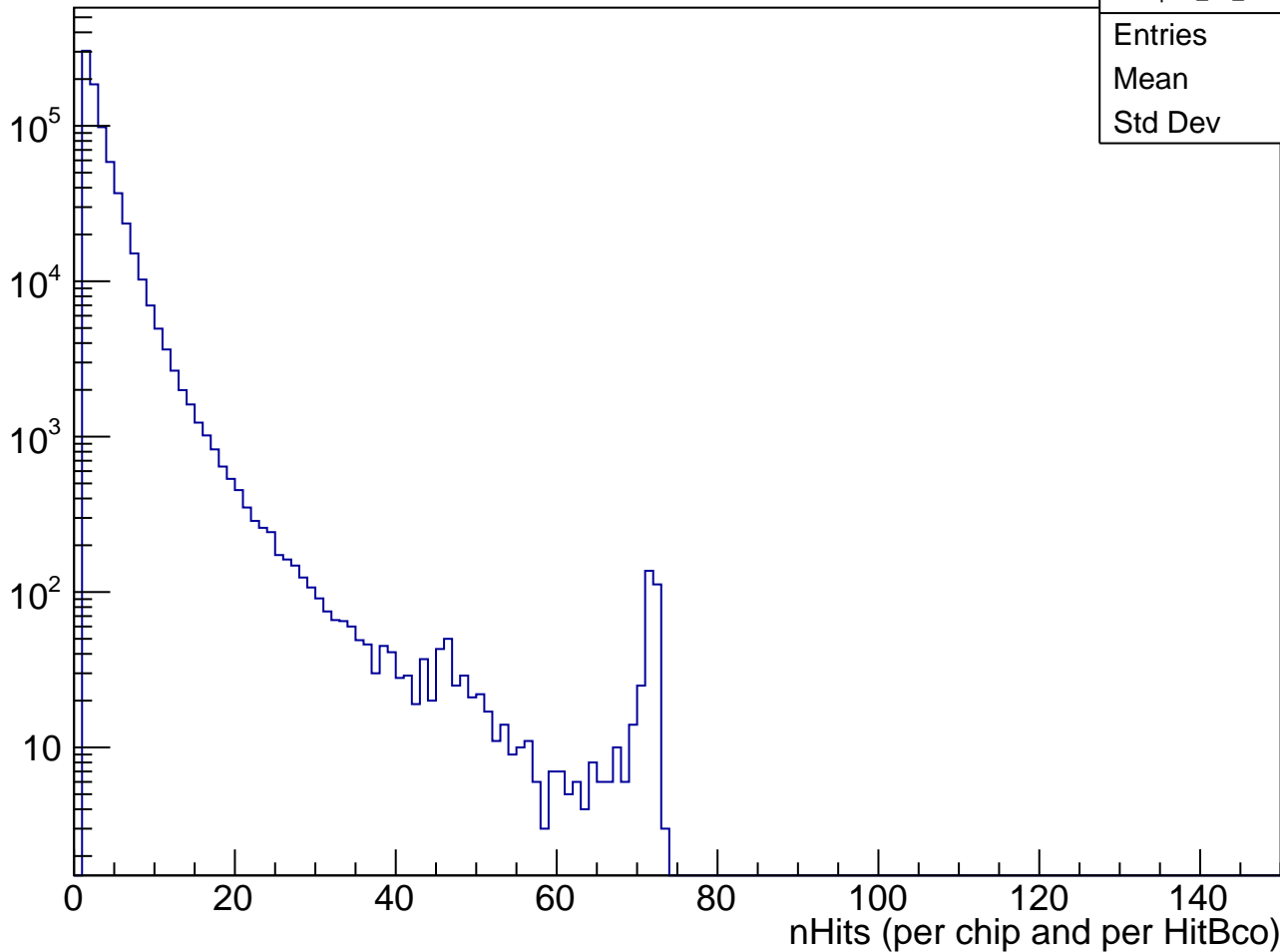


nChipHit_F7_Fch13_Chip2	
Entries	825581
Mean	3.052
Std Dev	3.528

nHits (per chip and per HitBco)

nChipHit_F7_Fch13_Chip20

Entries



nChipHit_F7_Fch13_Chip20

Entries 761568

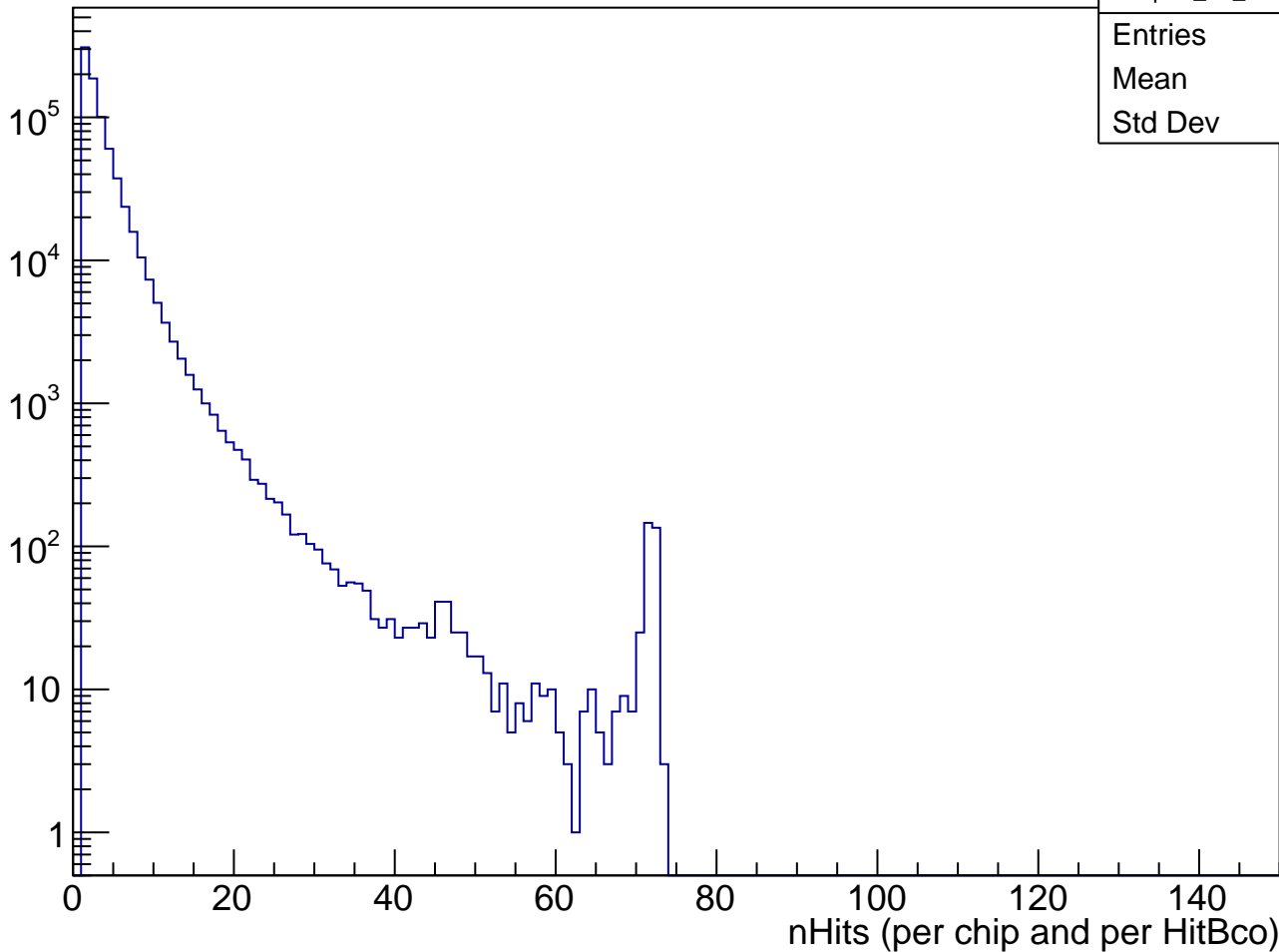
Mean 2.805

Std Dev 3.3

nHits (per chip and per HitBco)

nChipHit_F7_Fch13_Chip21

Entries

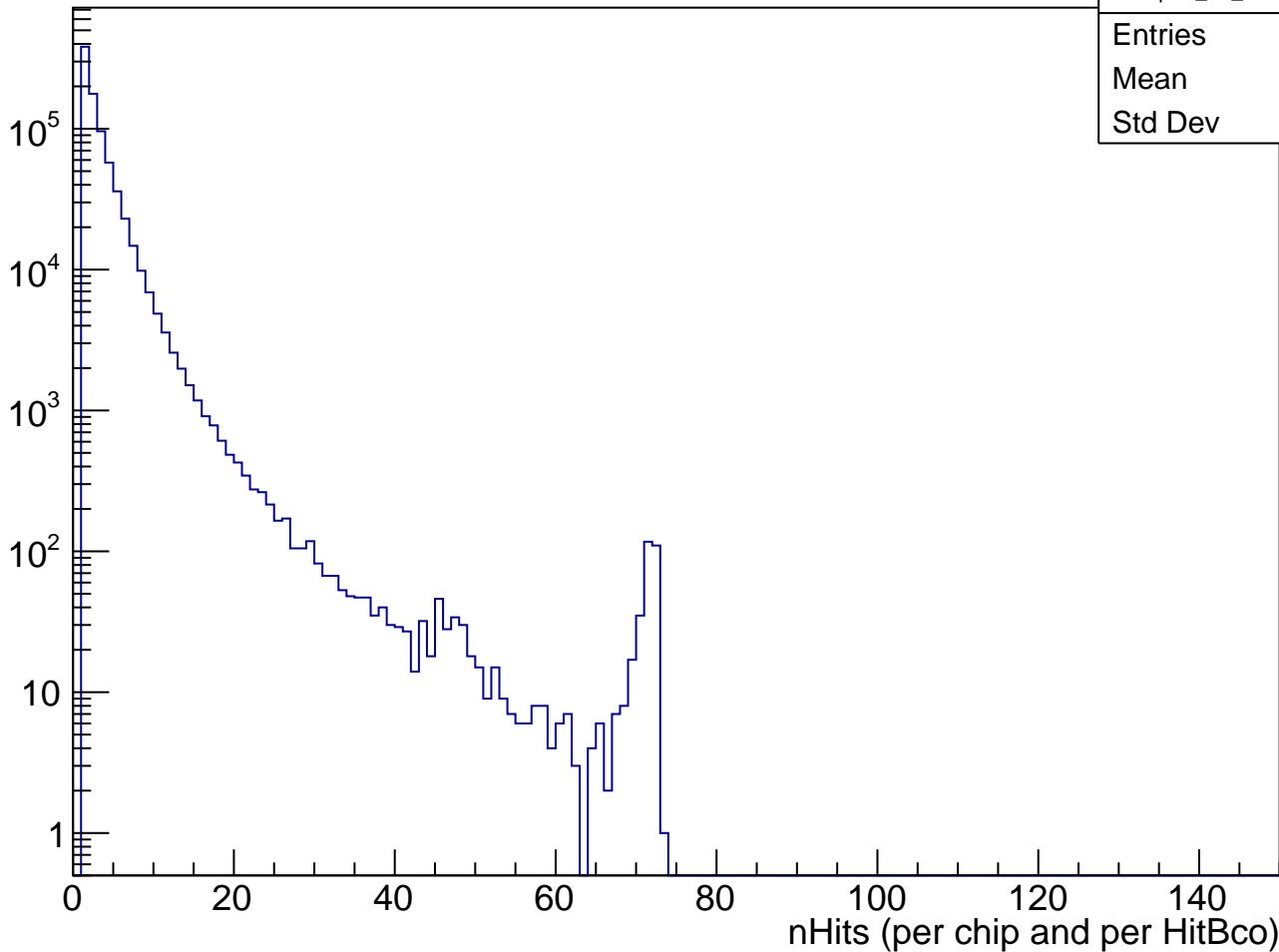


nChipHit_F7_Fch13_Chip21	
Entries	774085
Mean	2.809
Std Dev	3.289

nHits (per chip and per HitBco)

nChipHit_F7_Fch13_Chip22

Entries



nChipHit_F7_Fch13_Chip22

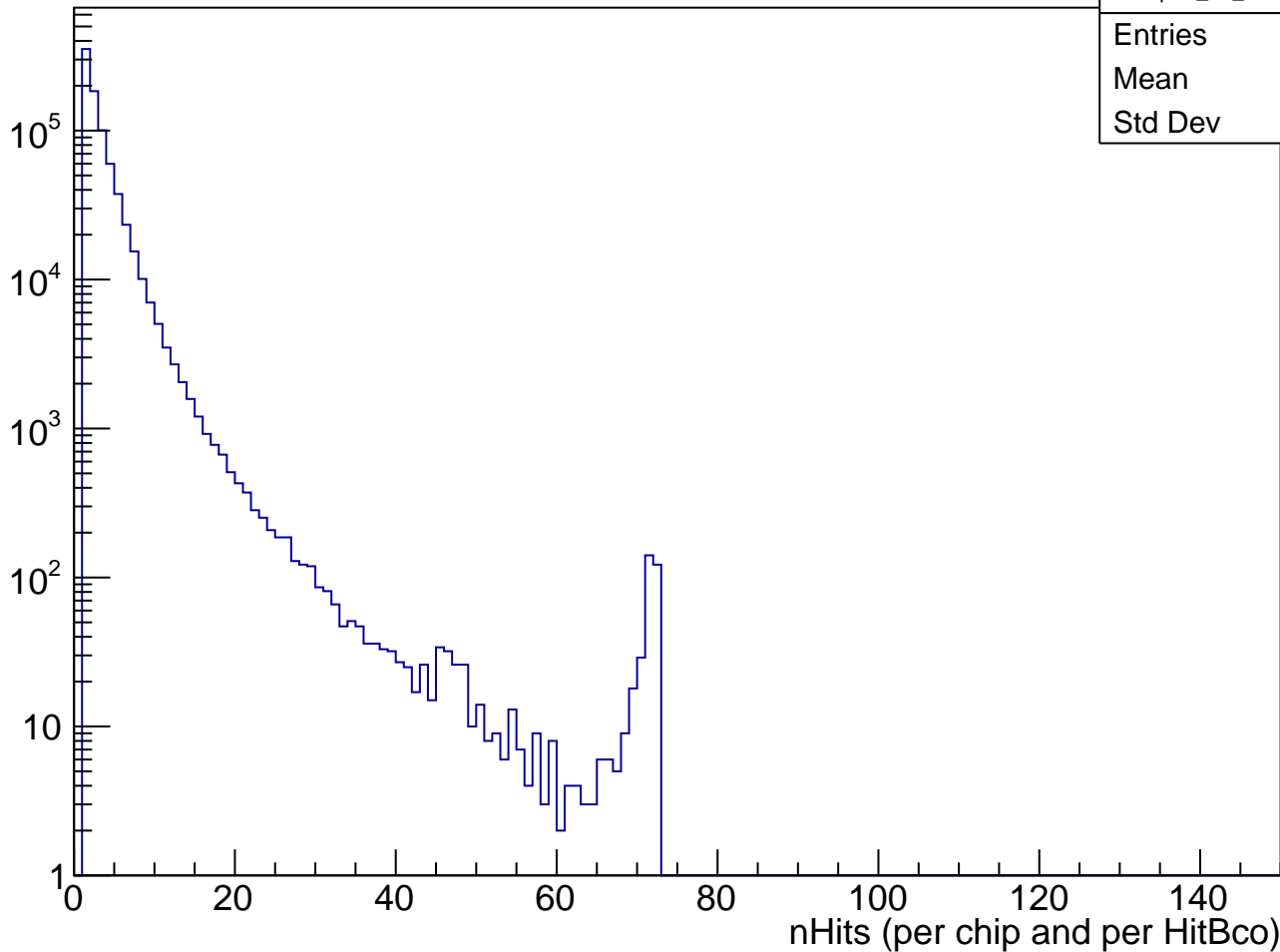
Entries 825206

Mean 2.612

Std Dev 3.129

nChipHit_F7_Fch13_Chip23

Entries



nChipHit_F7_Fch13_Chip23

Entries 813002

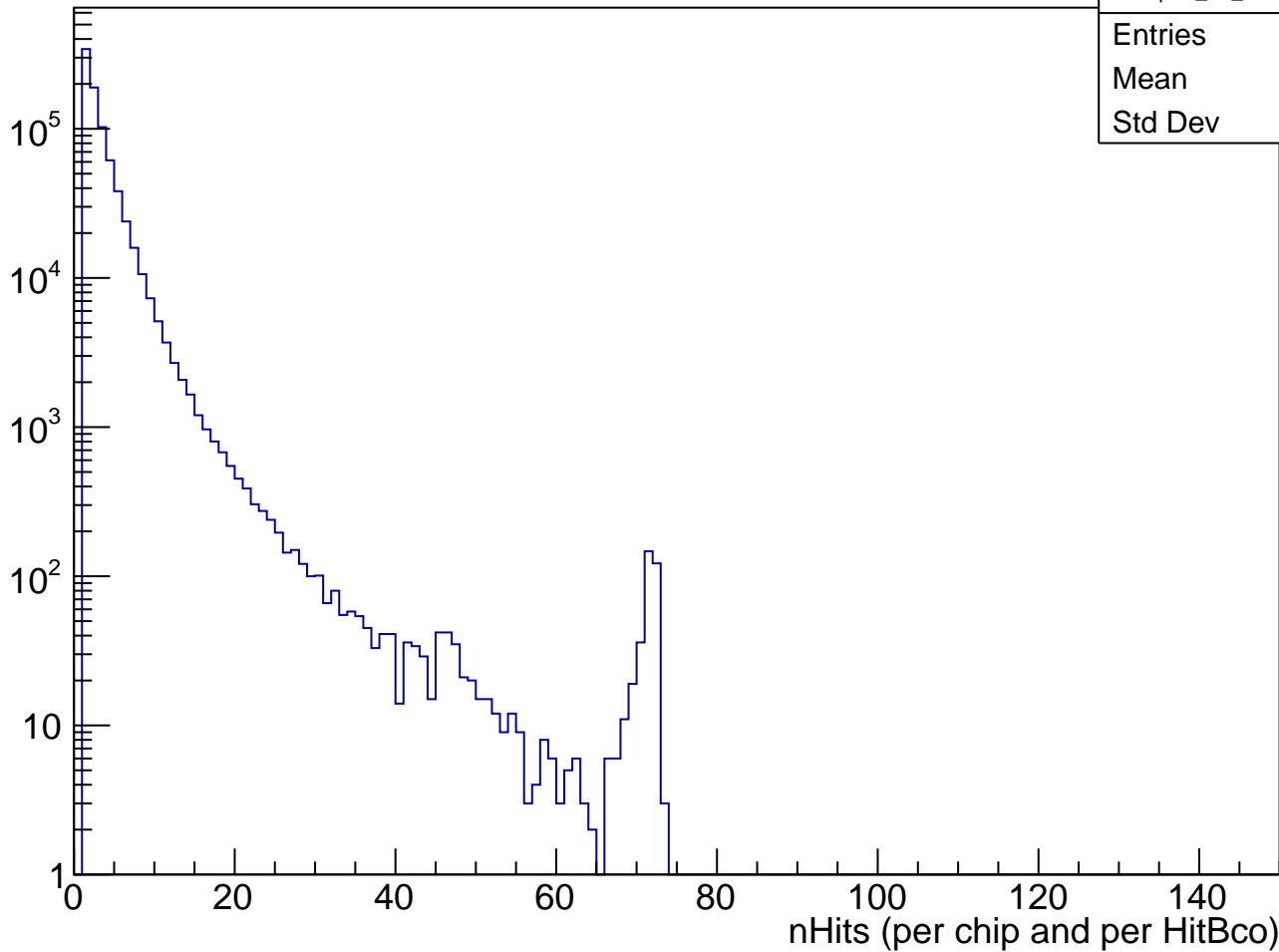
Mean 2.692

Std Dev 3.176

nHits (per chip and per HitBco)

nChipHit_F7_Fch13_Chip24

Entries



nChipHit_F7_Fch13_Chip24

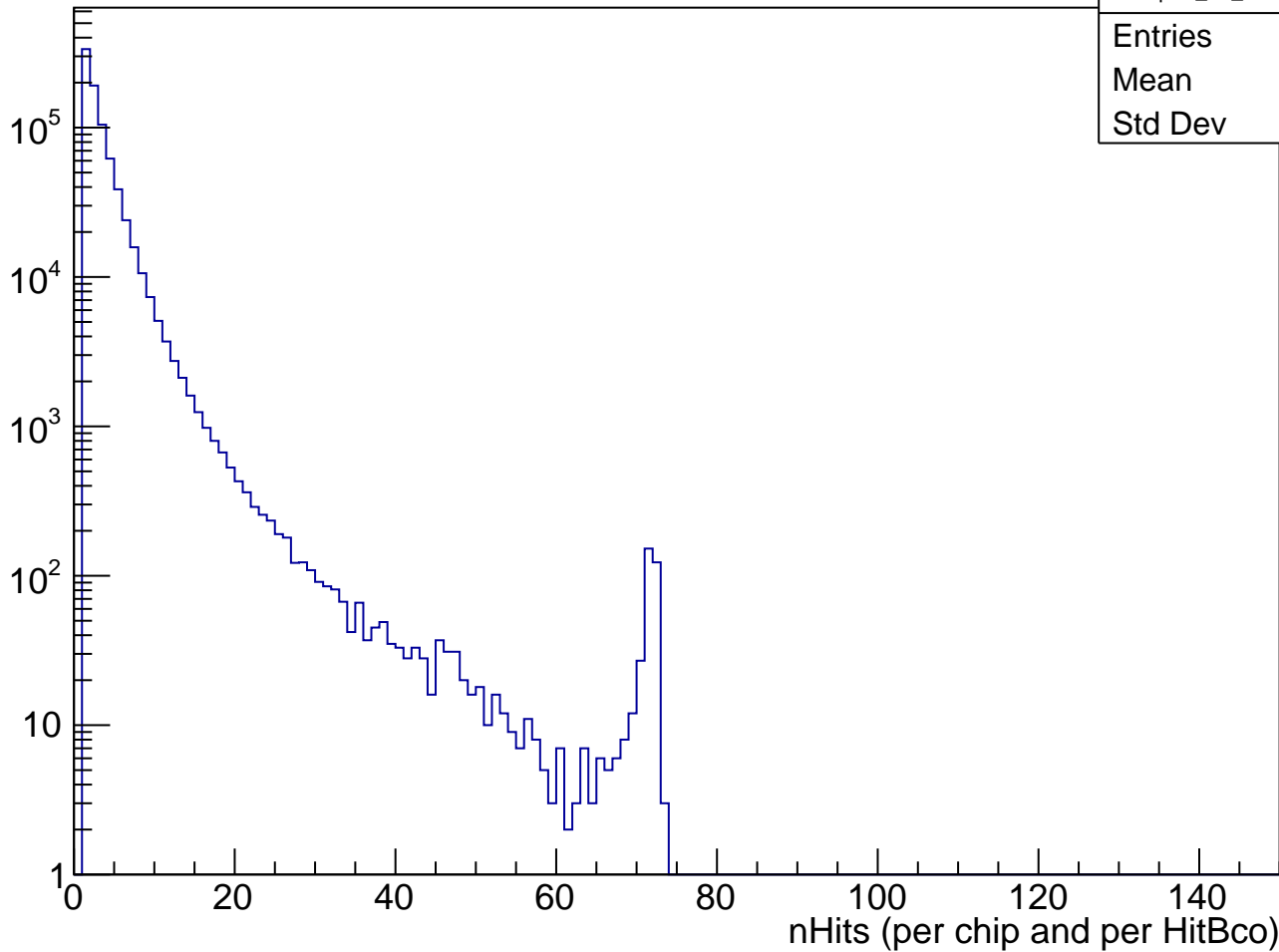
Entries 815093

Mean 2.74

Std Dev 3.24

nChipHit_F7_Fch13_Chip25

Entries



nChipHit_F7_Fch13_Chip25

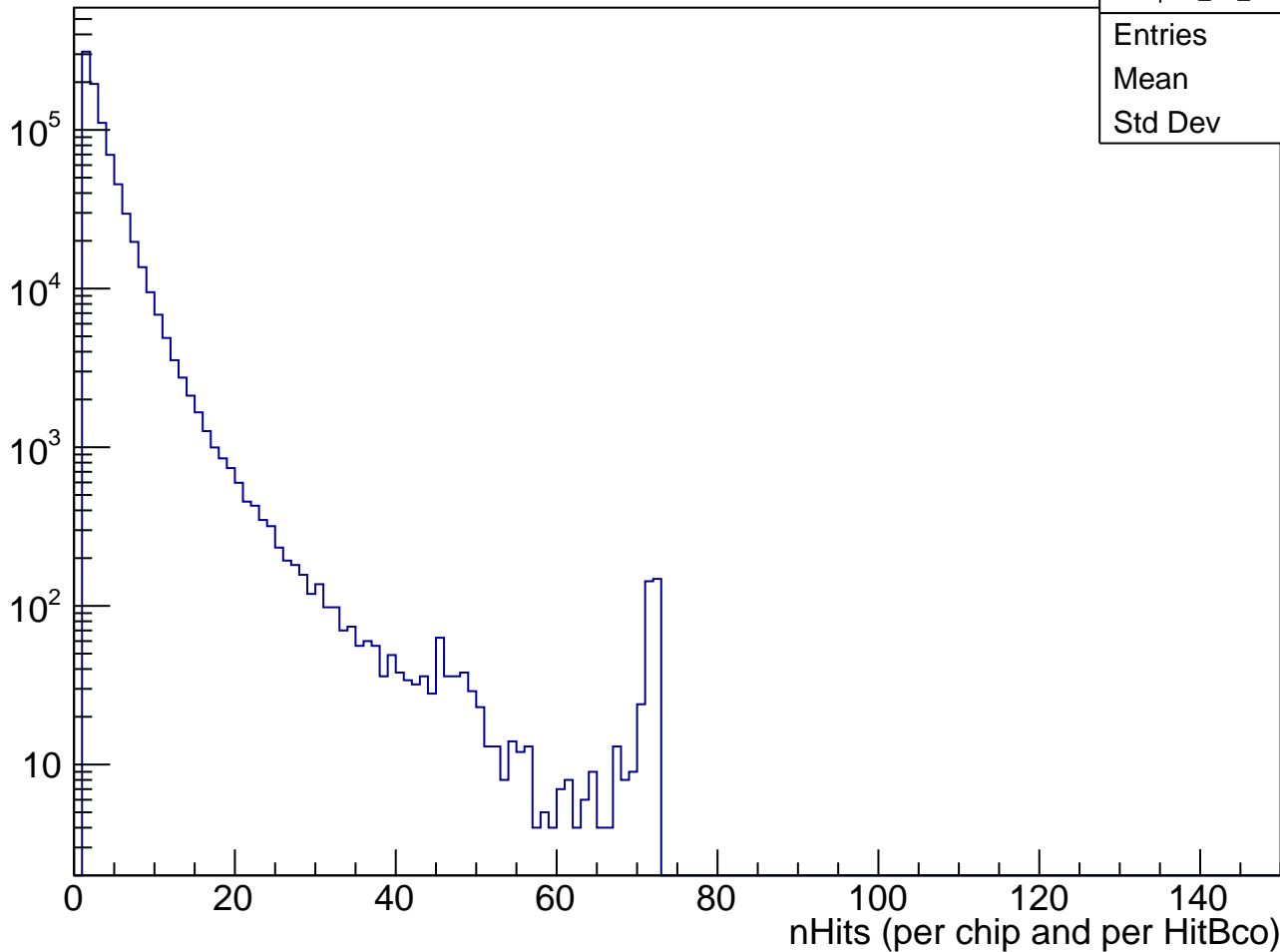
Entries 812601

Mean 2.755

Std Dev 3.234

nChipHit_F7_Fch13_Chip3

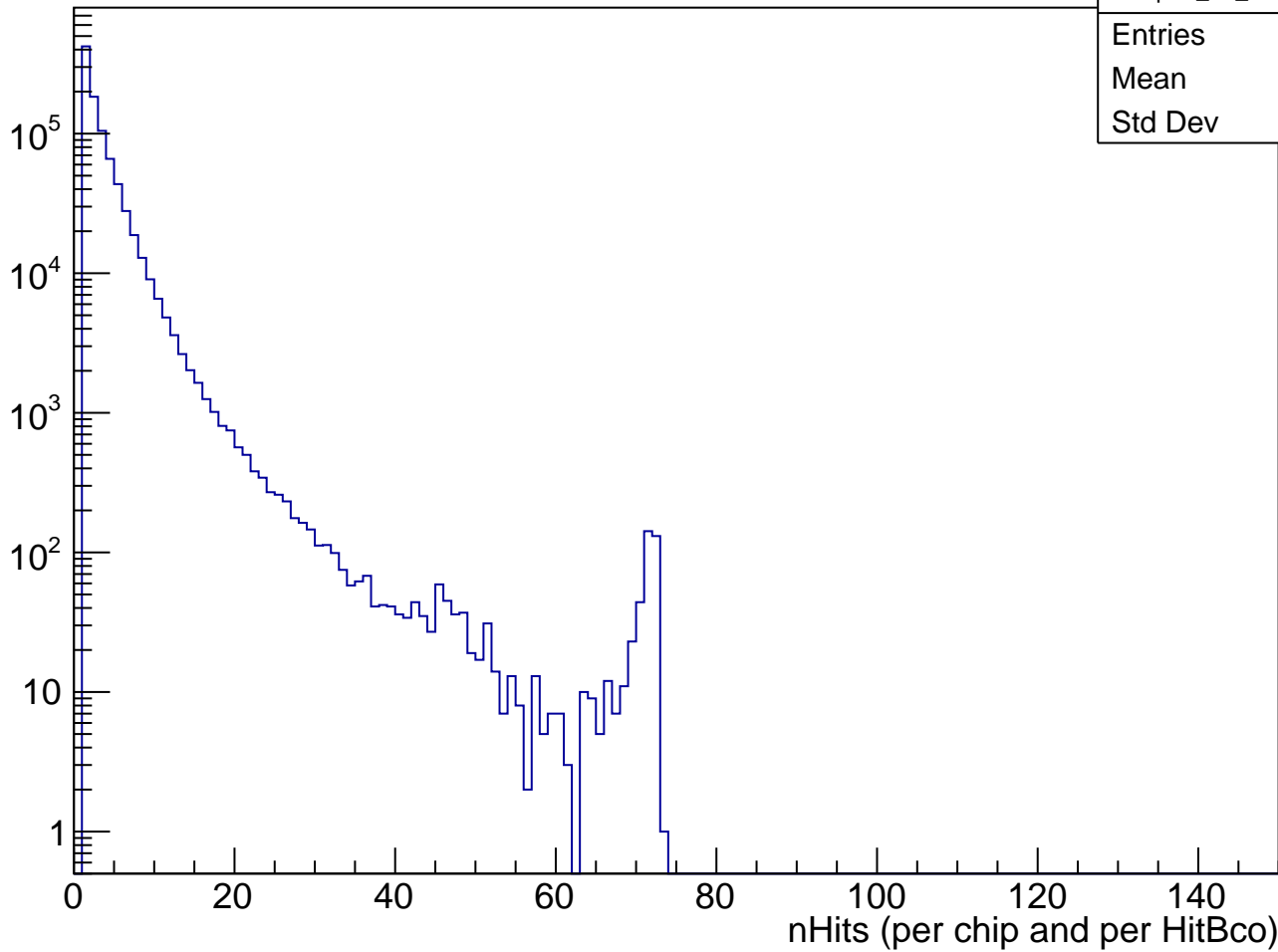
Entries



nChipHit_F7_Fch13_Chip3	
Entries	834833
Mean	3.013
Std Dev	3.471

nChipHit_F7_Fch13_Chip4

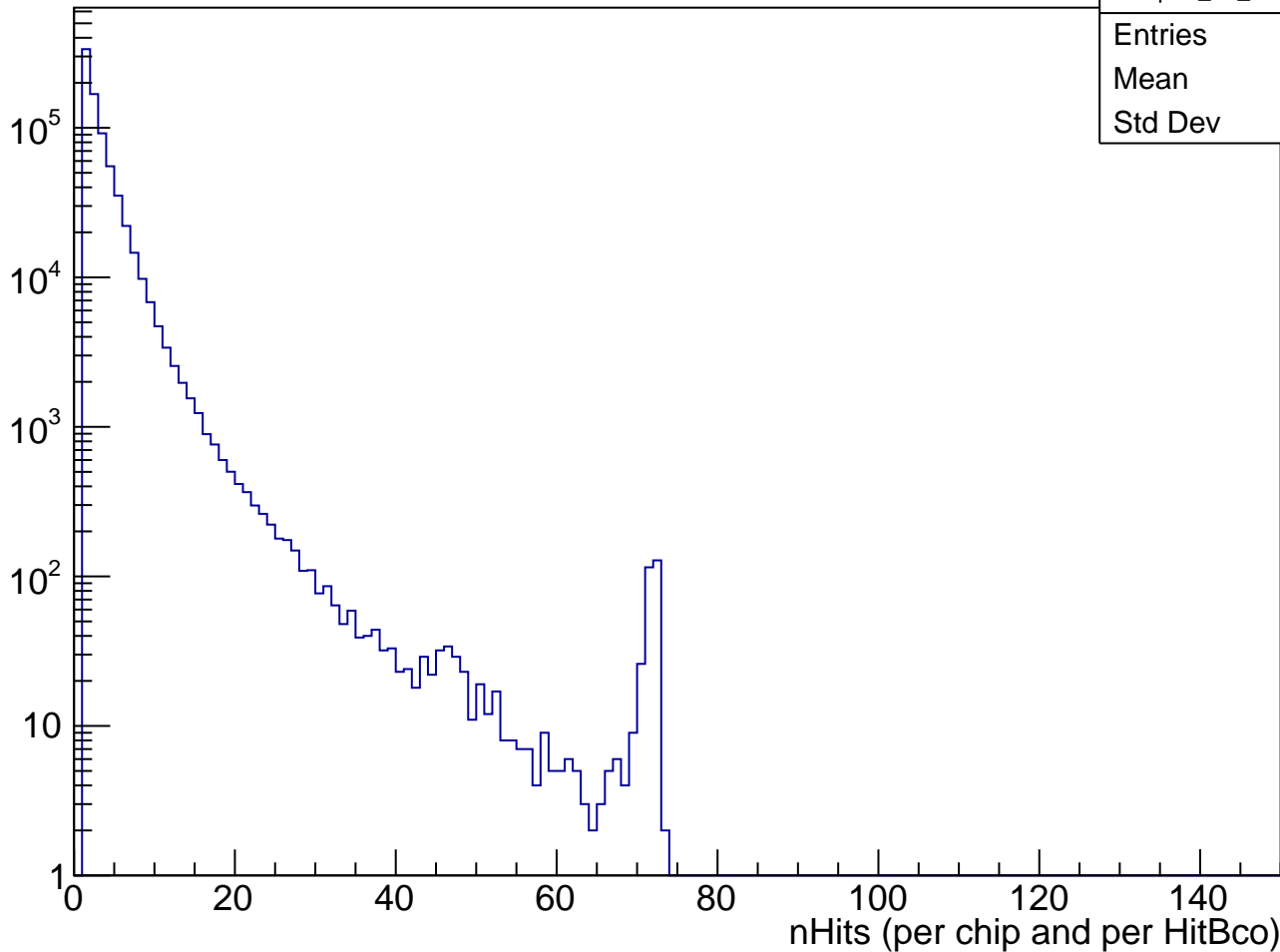
Entries



nChipHit_F7_Fch13_Chip4	
Entries	918170
Mean	2.754
Std Dev	3.366

nChipHit_F7_Fch13_Chip5

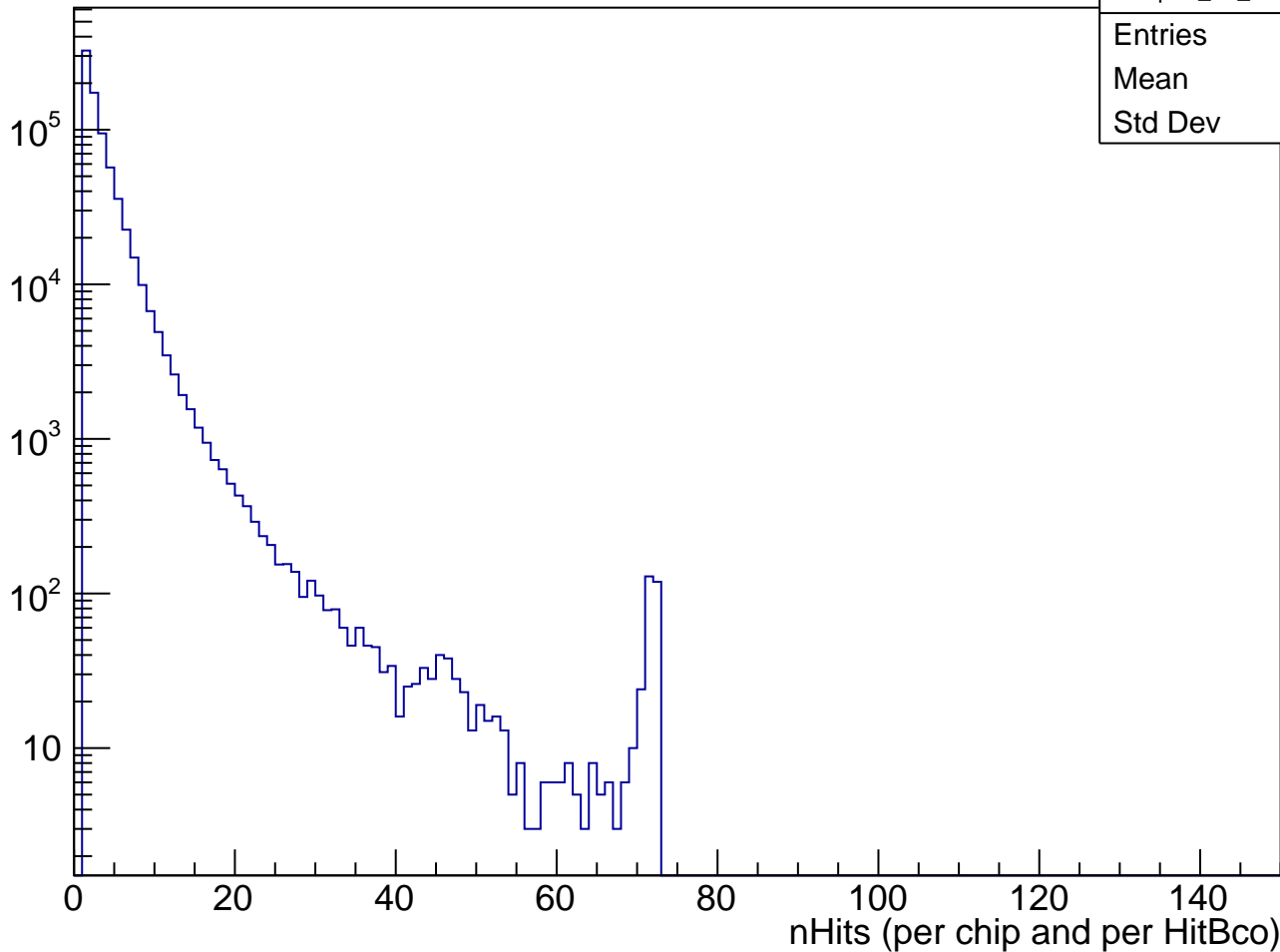
Entries



nChipHit_F7_Fch13_Chip5	
Entries	761210
Mean	2.702
Std Dev	3.224

nChipHit_F7_Fch13_Chip6

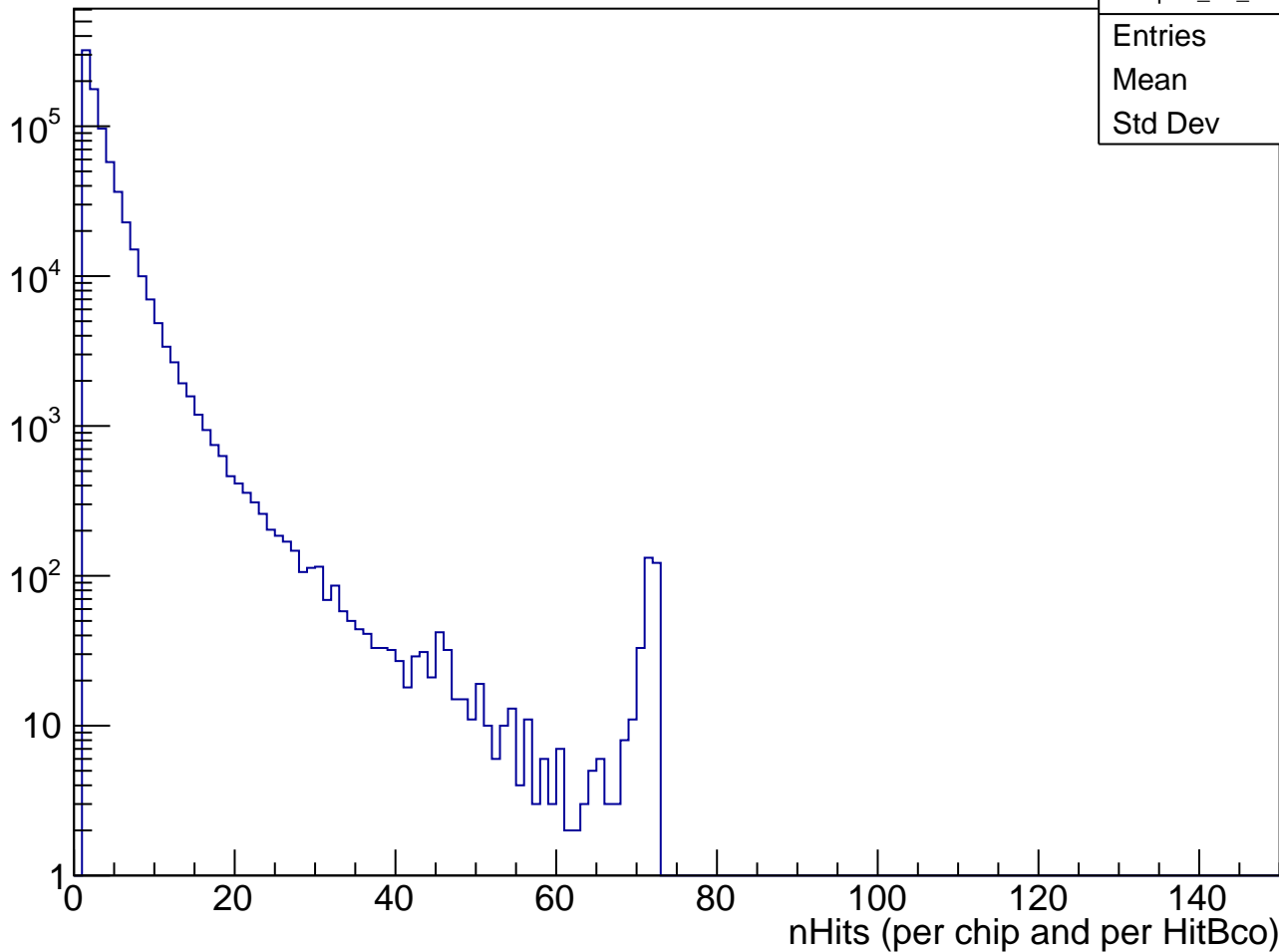
Entries



nChipHit_F7_Fch13_Chip6	
Entries	762049
Mean	2.735
Std Dev	3.242

nChipHit_F7_Fch13_Chip7

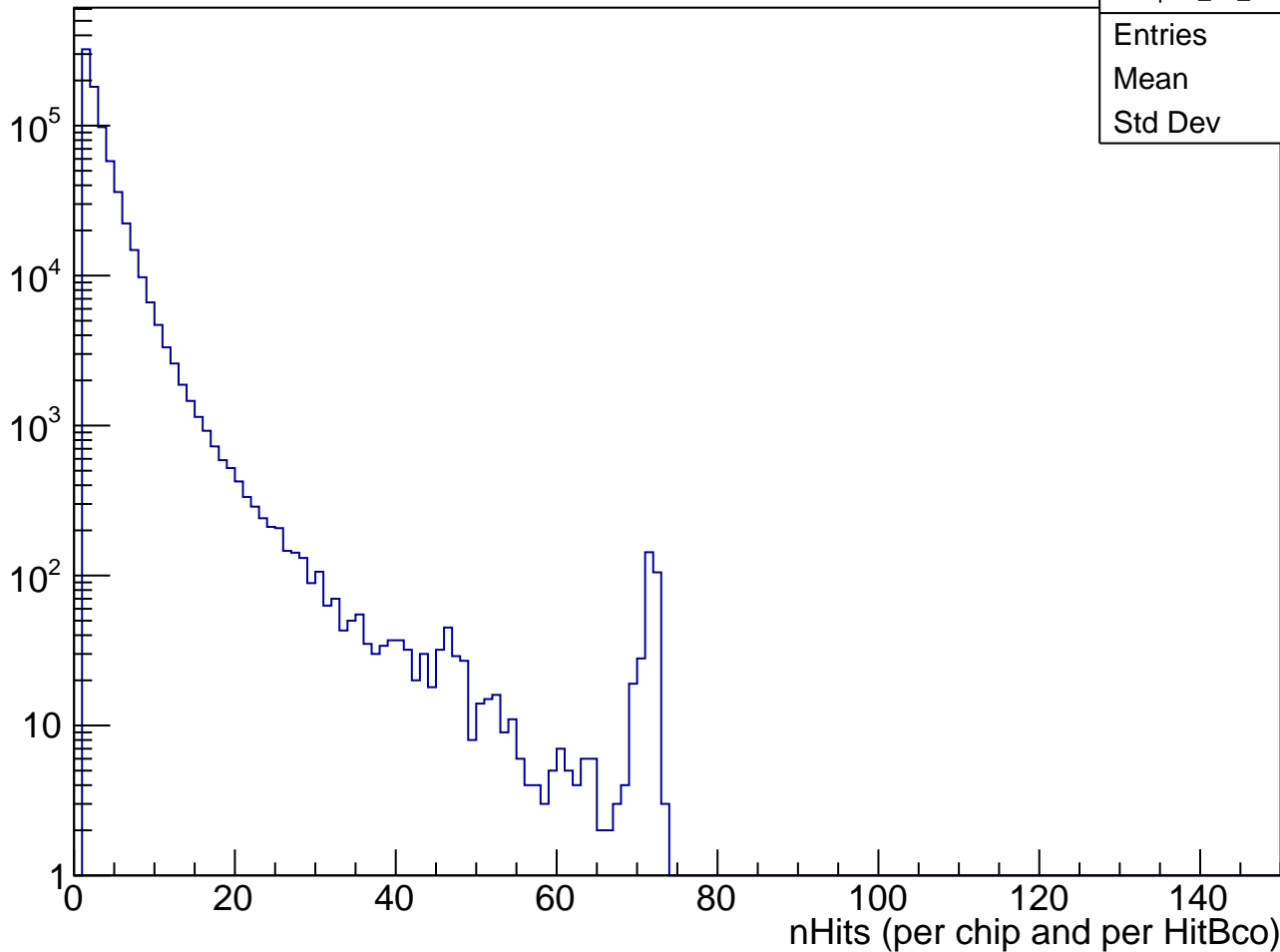
Entries



nChipHit_F7_Fch13_Chip7	
Entries	765537
Mean	2.747
Std Dev	3.228

nChipHit_F7_Fch13_Chip8

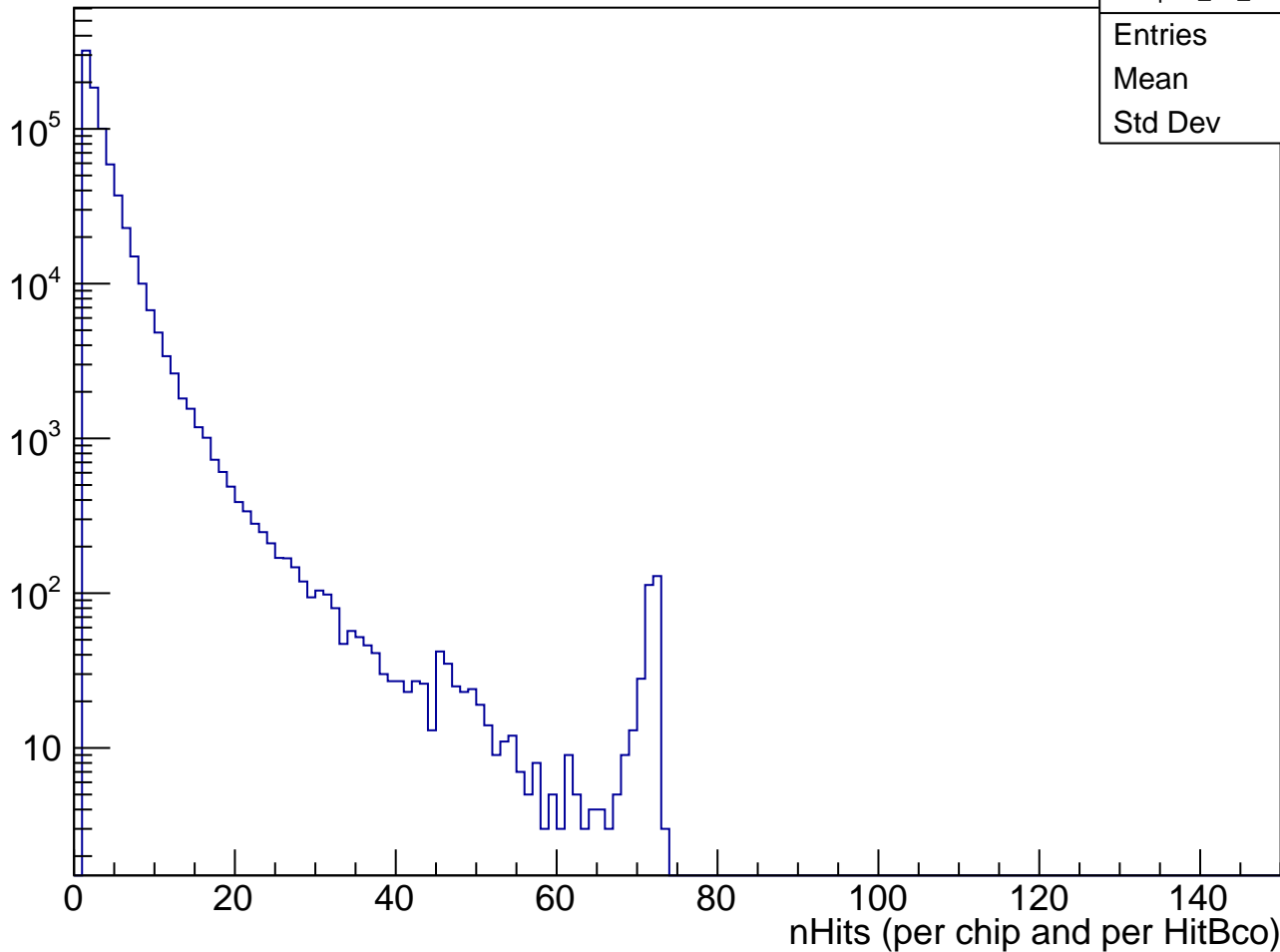
Entries



nChipHit_F7_Fch13_Chip8	
Entries	771086
Mean	2.724
Std Dev	3.204

nChipHit_F7_Fch13_Chip9

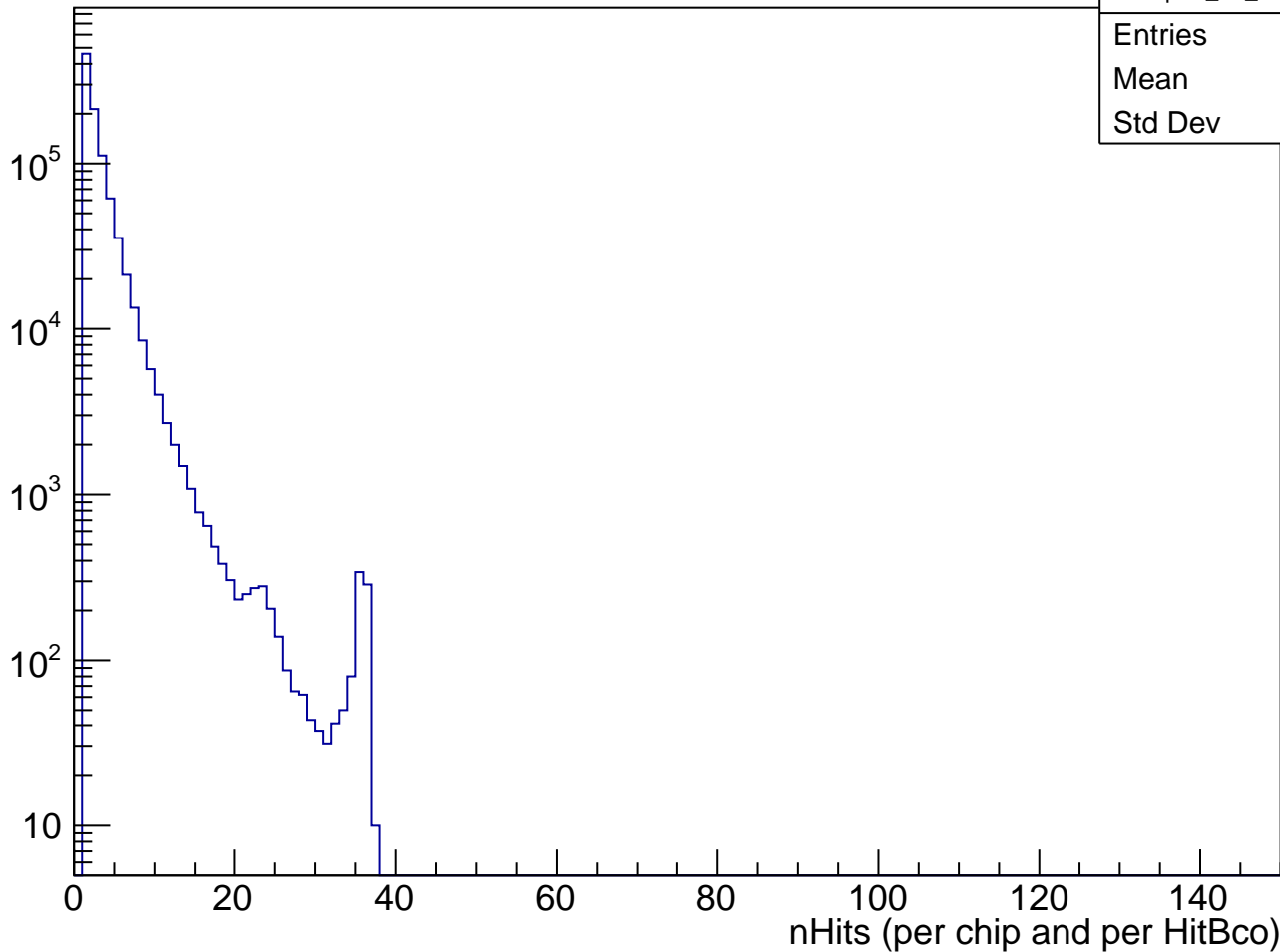
Entries



nChipHit_F7_Fch13_Chip9	
Entries	778008
Mean	2.743
Std Dev	3.204

nChipHit_F7_Fch1_Chip0

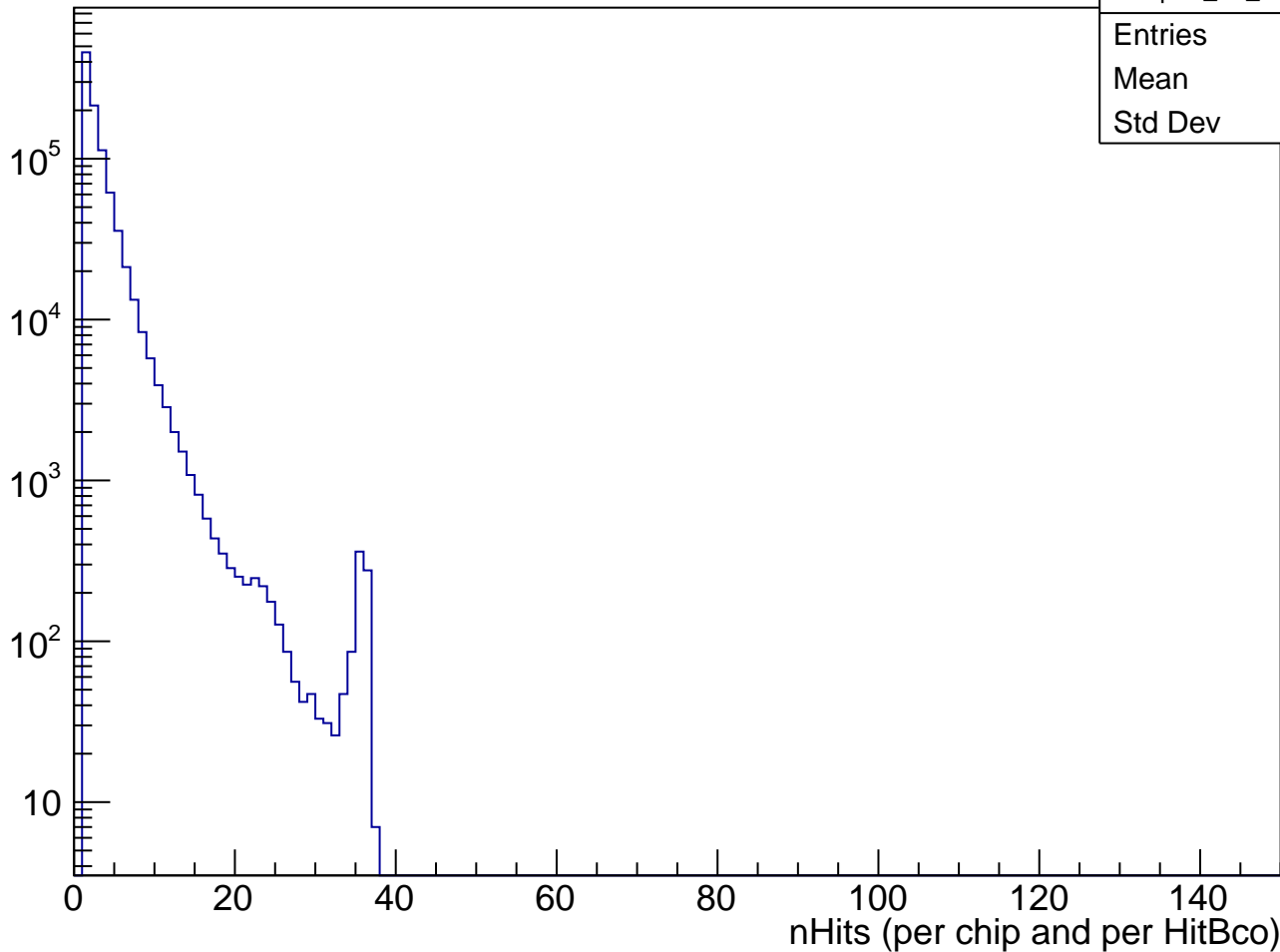
Entries



nChipHit_F7_Fch1_Chip0	
Entries	947698
Mean	2.348
Std Dev	2.413

nChipHit_F7_Fch1_Chip1

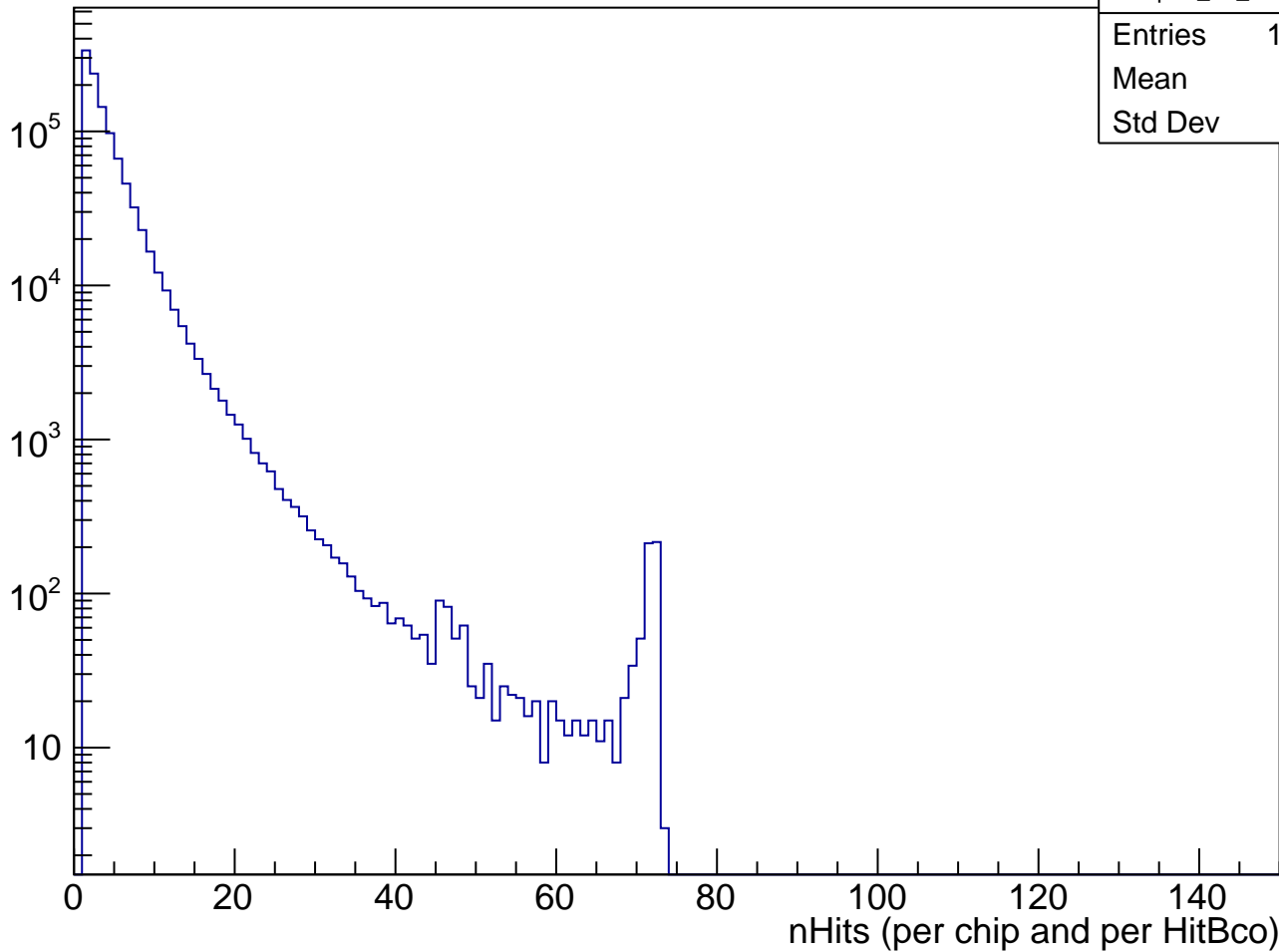
Entries



nChipHit_F7_Fch1_Chip1	
Entries	948308
Mean	2.345
Std Dev	2.388

nChipHit_F7_Fch1_Chip10

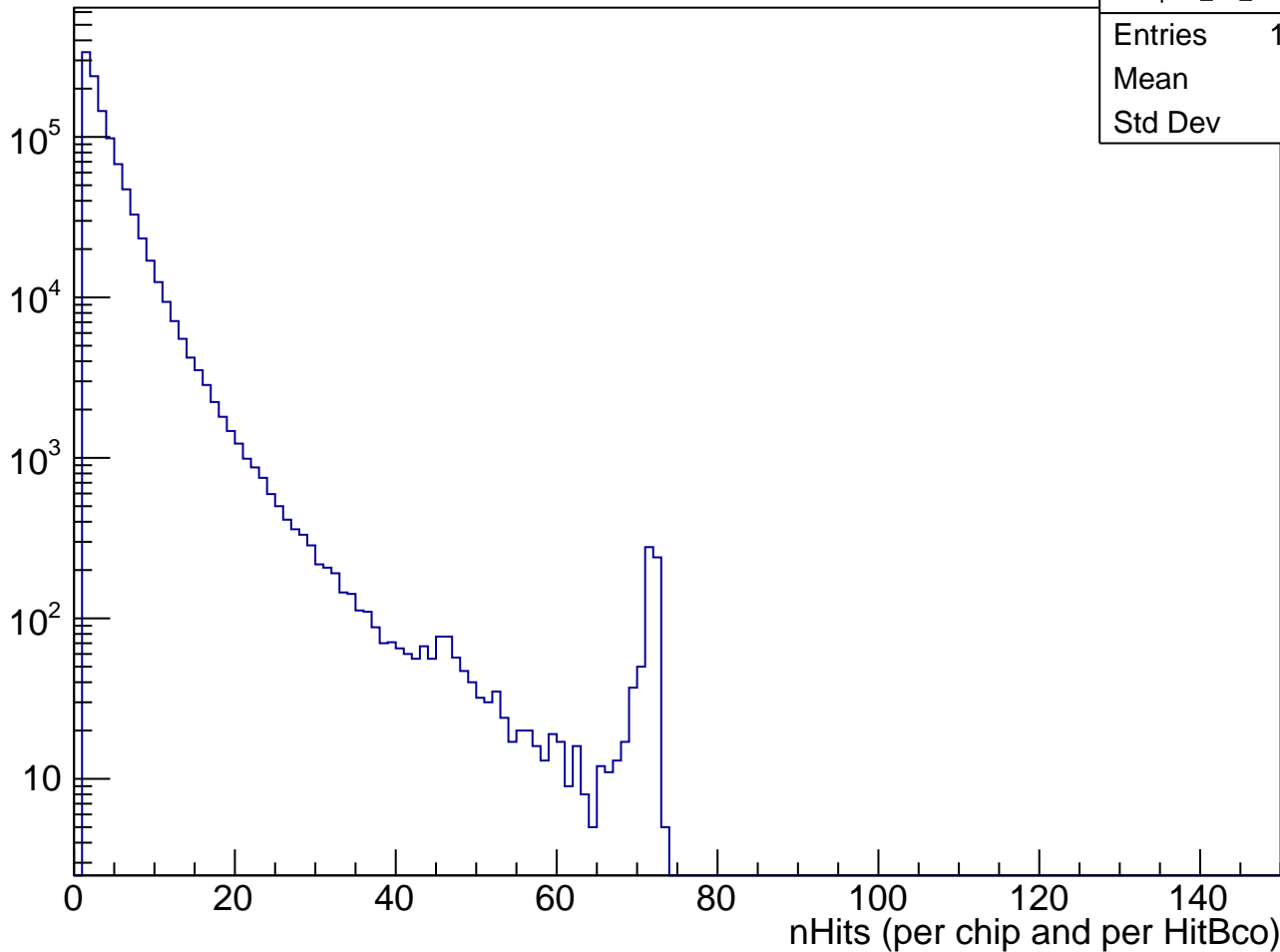
Entries



nChipHit_F7_Fch1_Chip10	
Entries	1056499
Mean	3.493
Std Dev	3.998

nChipHit_F7_Fch1_Chip11

Entries



nChipHit_F7_Fch1_Chip11

Entries 1067453

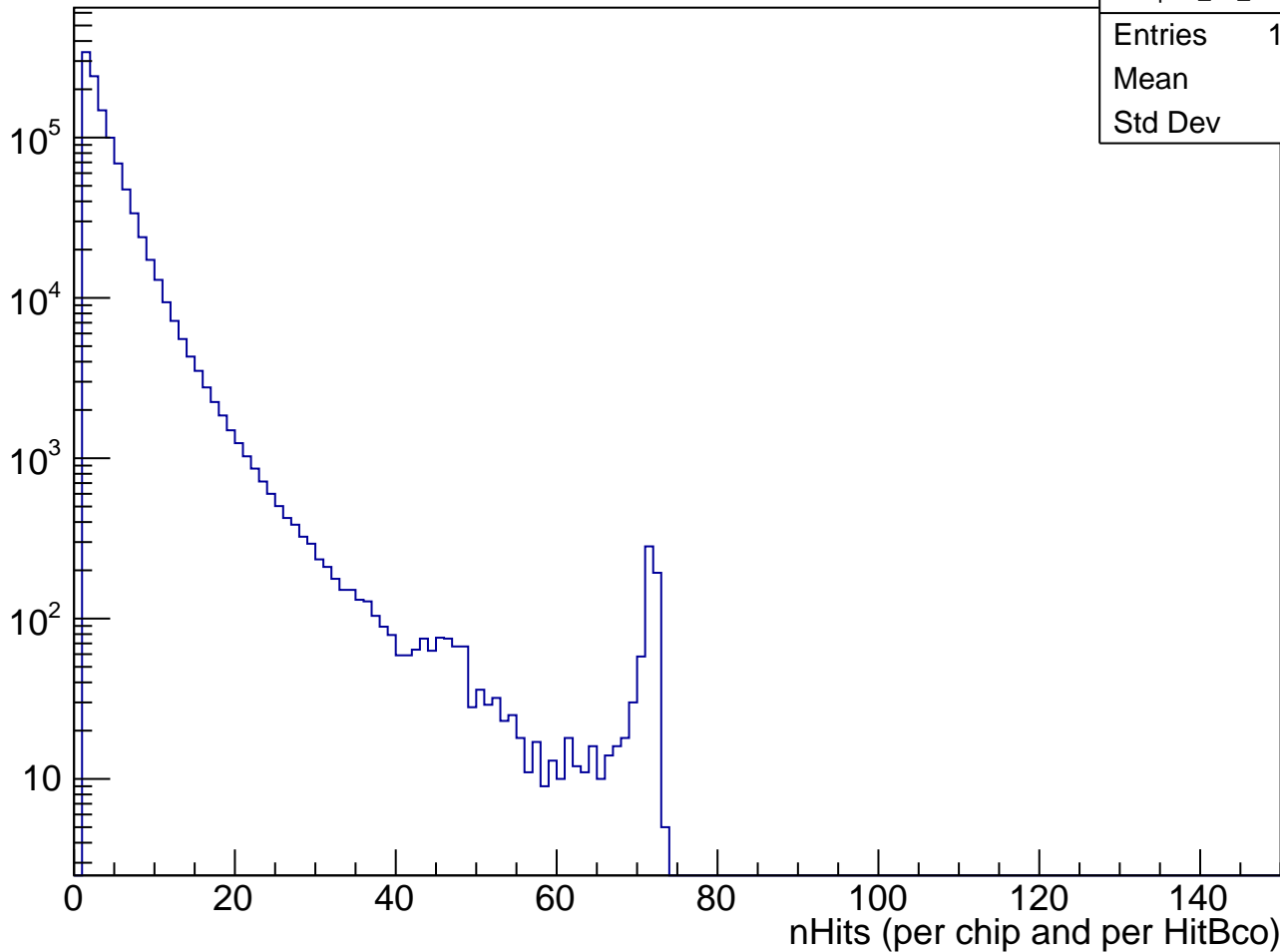
Mean 3.518

Std Dev 4.062

nHits (per chip and per HitBco)

nChipHit_F7_Fch1_Chip12

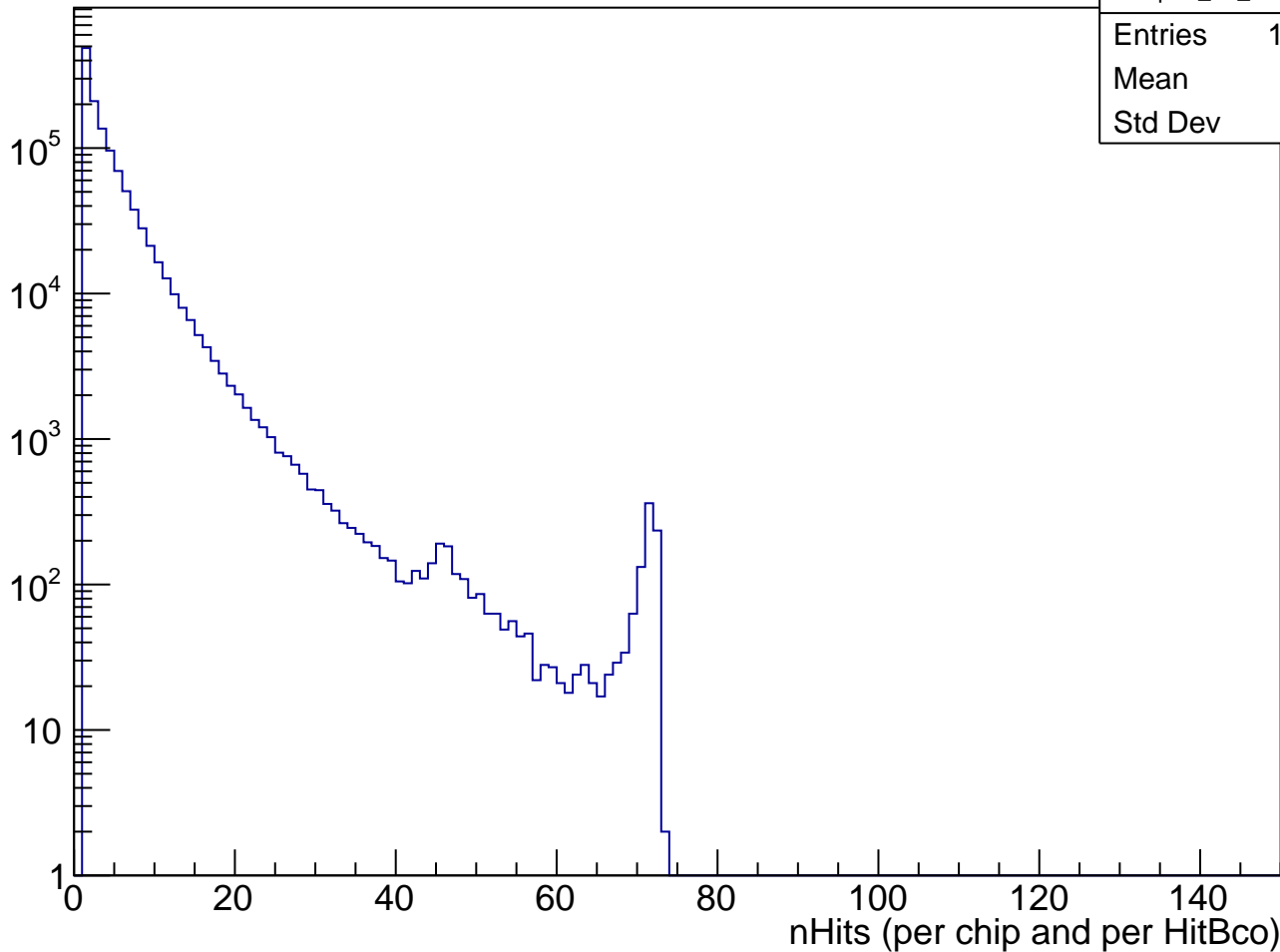
Entries



nChipHit_F7_Fch1_Chip12	
Entries	1081832
Mean	3.523
Std Dev	4.048

nChipHit_F7_Fch1_Chip13

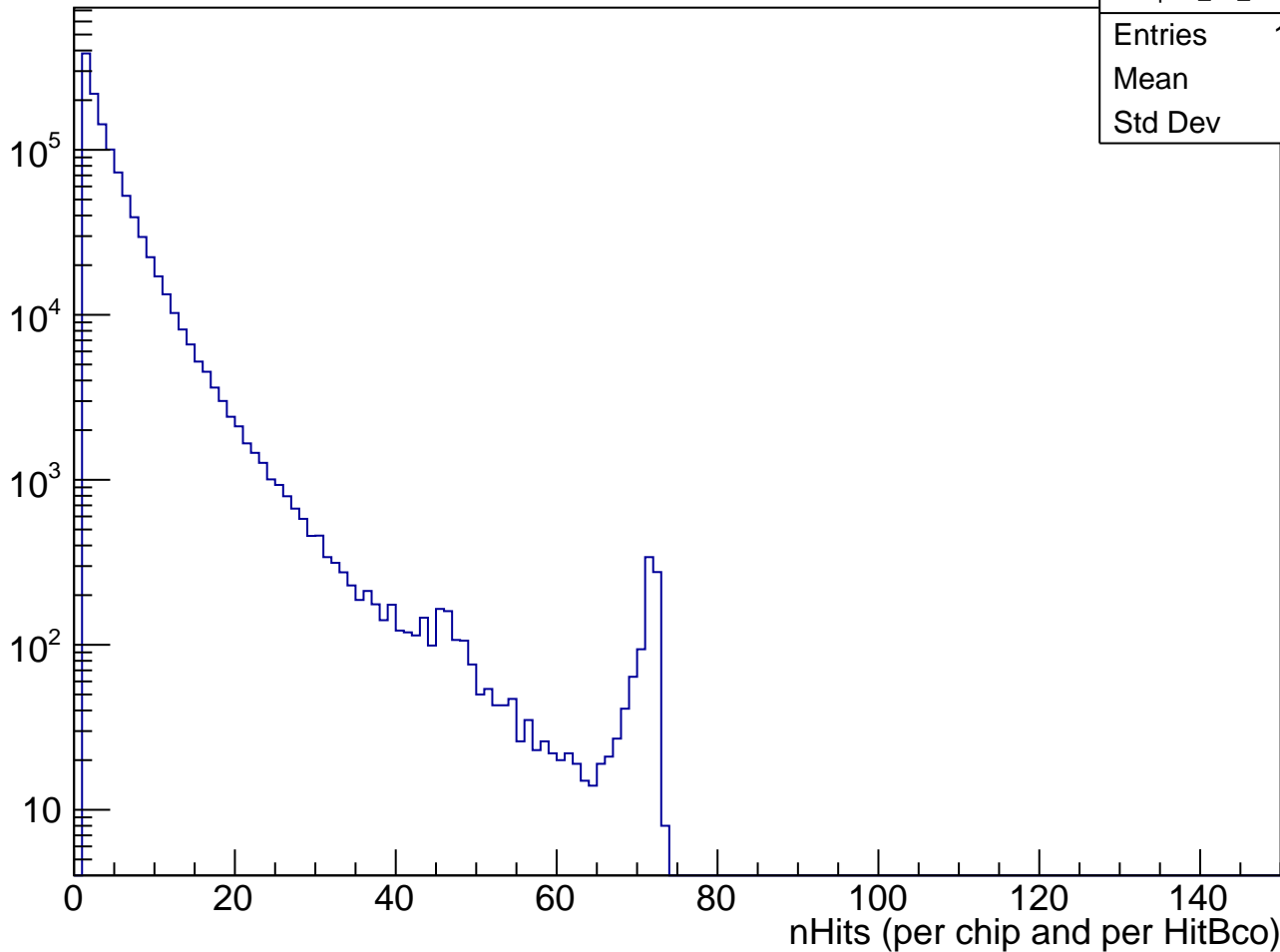
Entries



nChipHit_F7_Fch1_Chip13	
Entries	1224284
Mean	3.62
Std Dev	4.665

nChipHit_F7_Fch1_Chip14

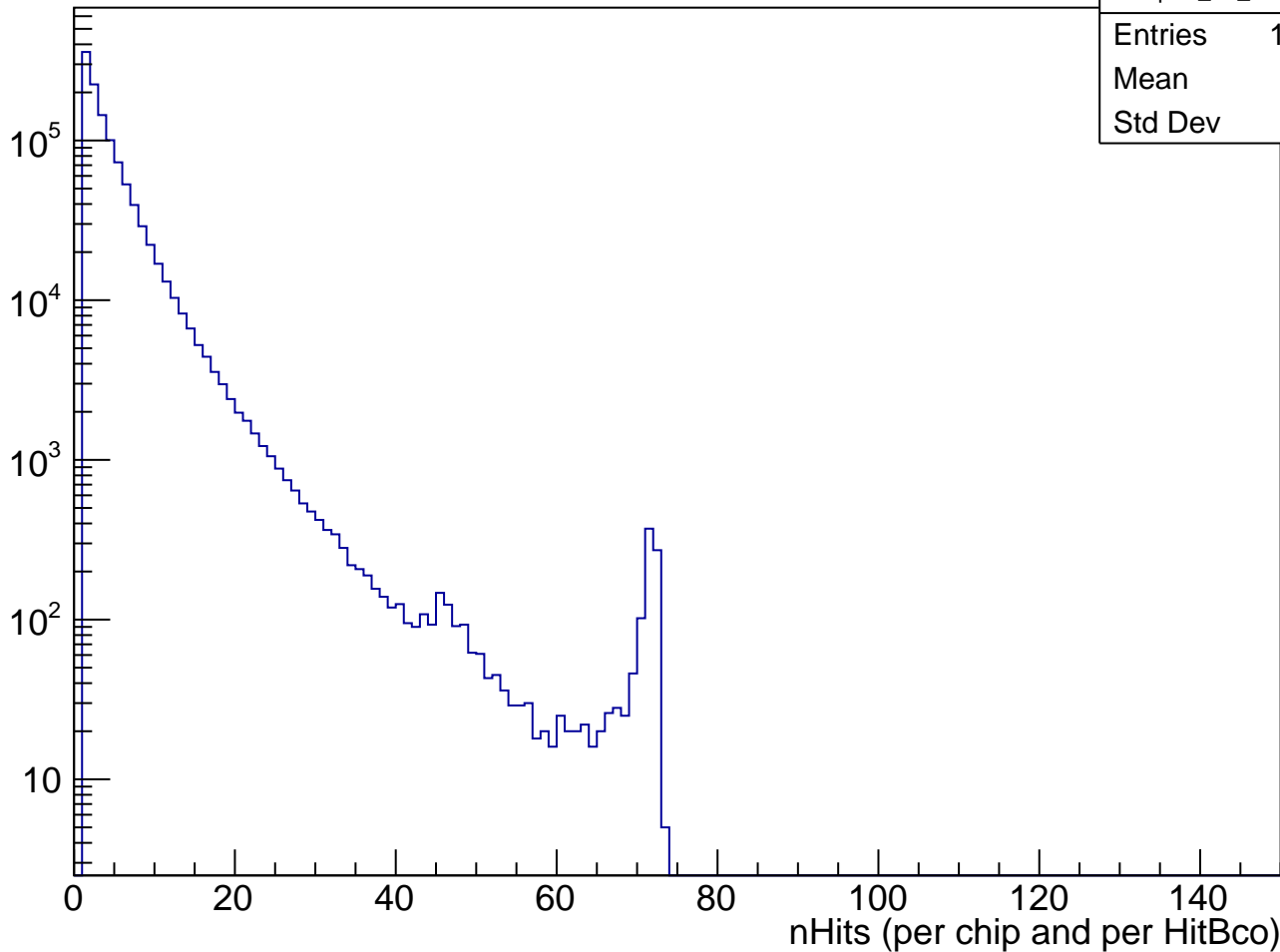
Entries



nChipHit_F7_Fch1_Chip14	
Entries	1152901
Mean	3.882
Std Dev	4.737

nChipHit_F7_Fch1_Chip15

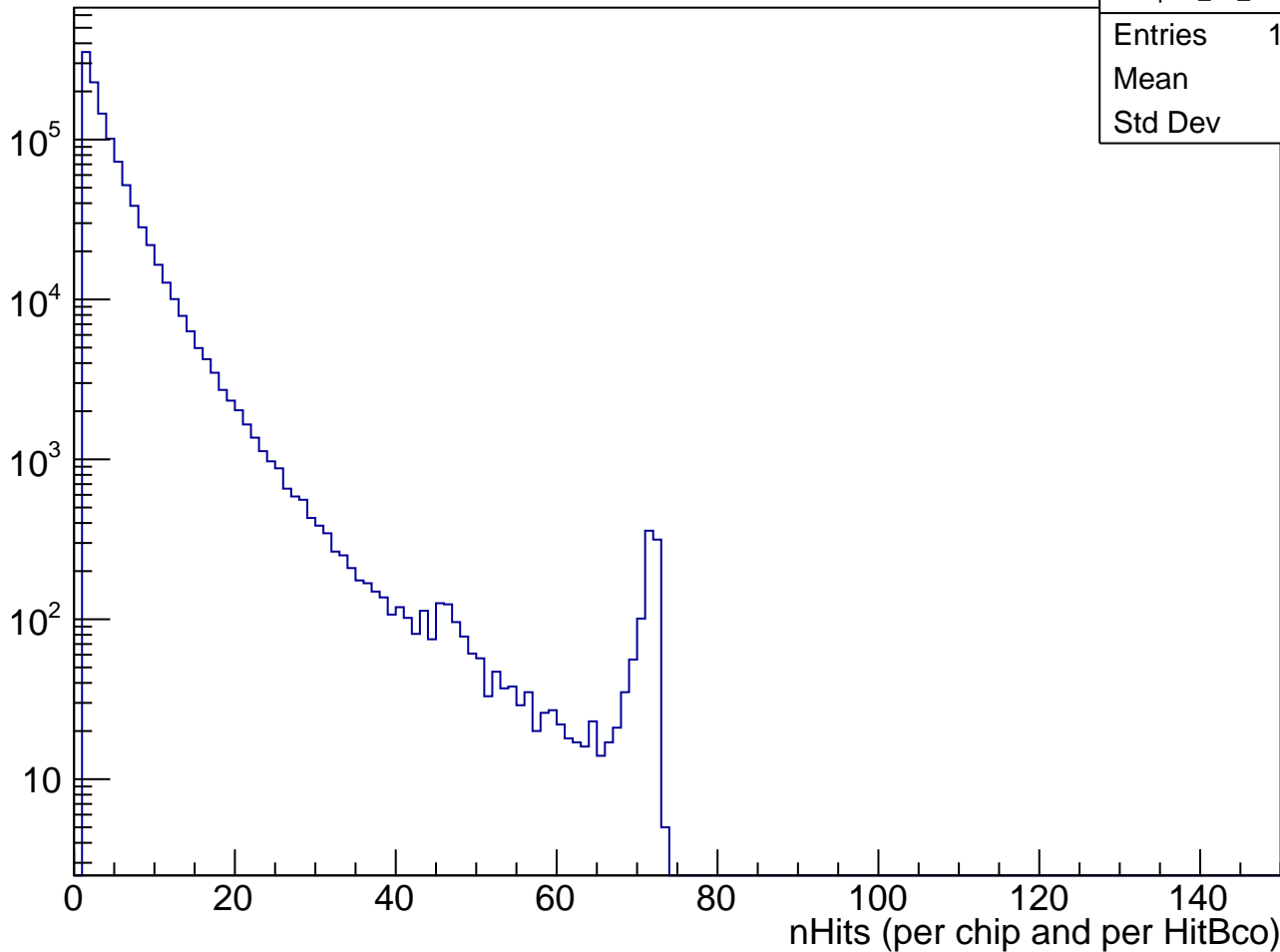
Entries



nChipHit_F7_Fch1_Chip15	
Entries	1133440
Mean	3.92
Std Dev	4.707

nChipHit_F7_Fch1_Chip16

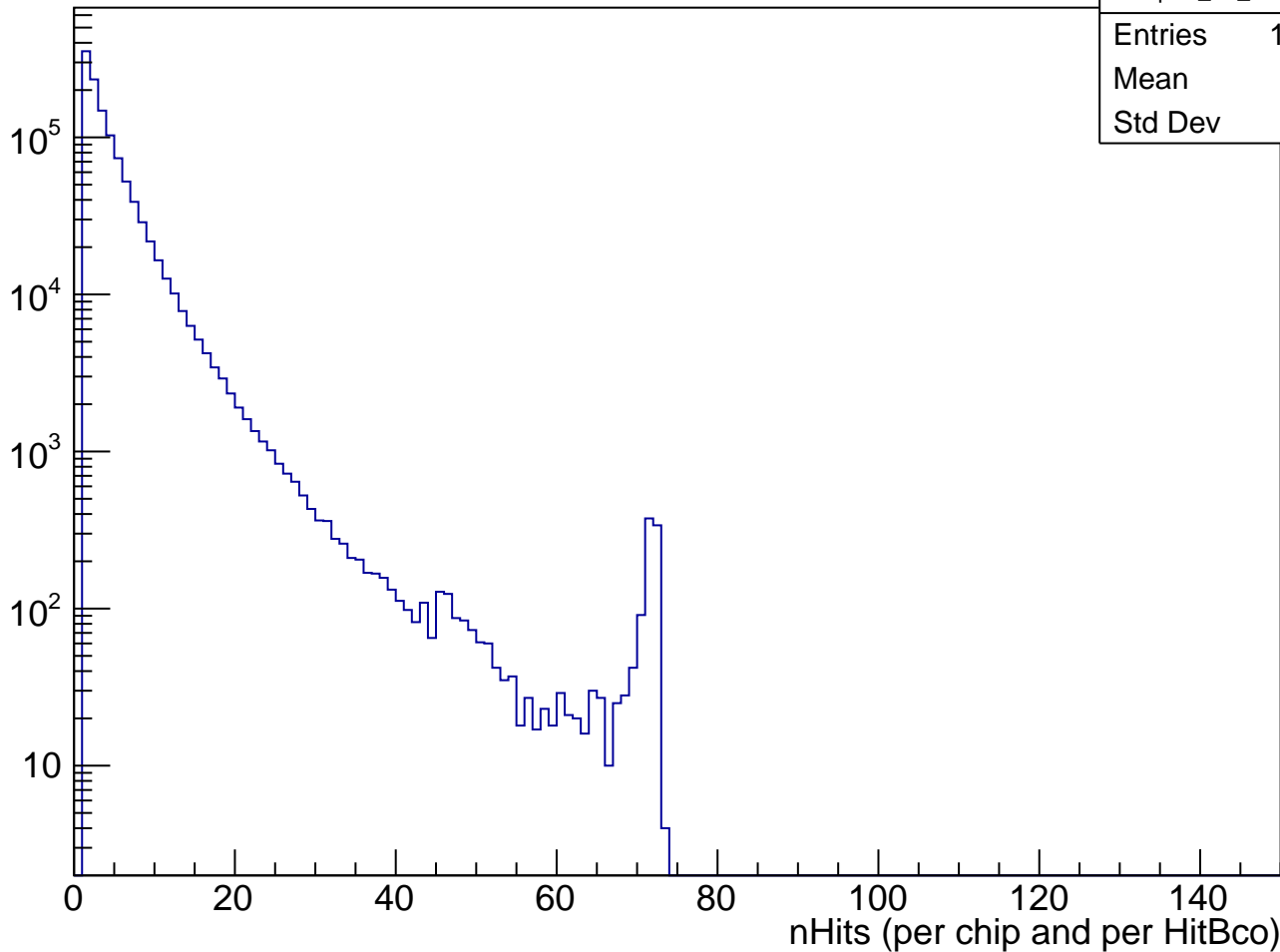
Entries



nChipHit_F7_Fch1_Chip16	
Entries	1126416
Mean	3.881
Std Dev	4.66

nChipHit_F7_Fch1_Chip17

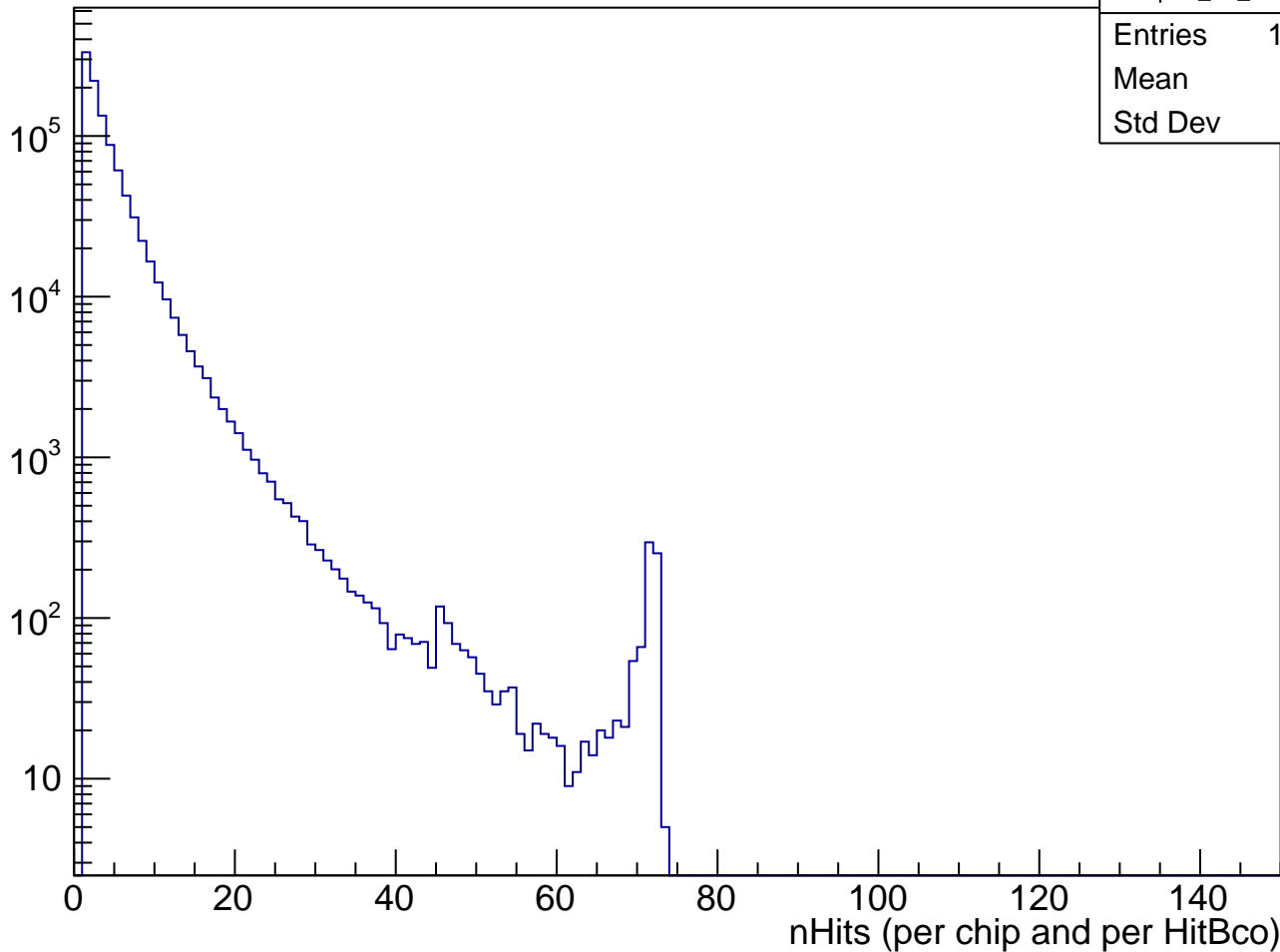
Entries



nChipHit_F7_Fch1_Chip17	
Entries	1139893
Mean	3.879
Std Dev	4.662

nChipHit_F7_Fch1_Chip18

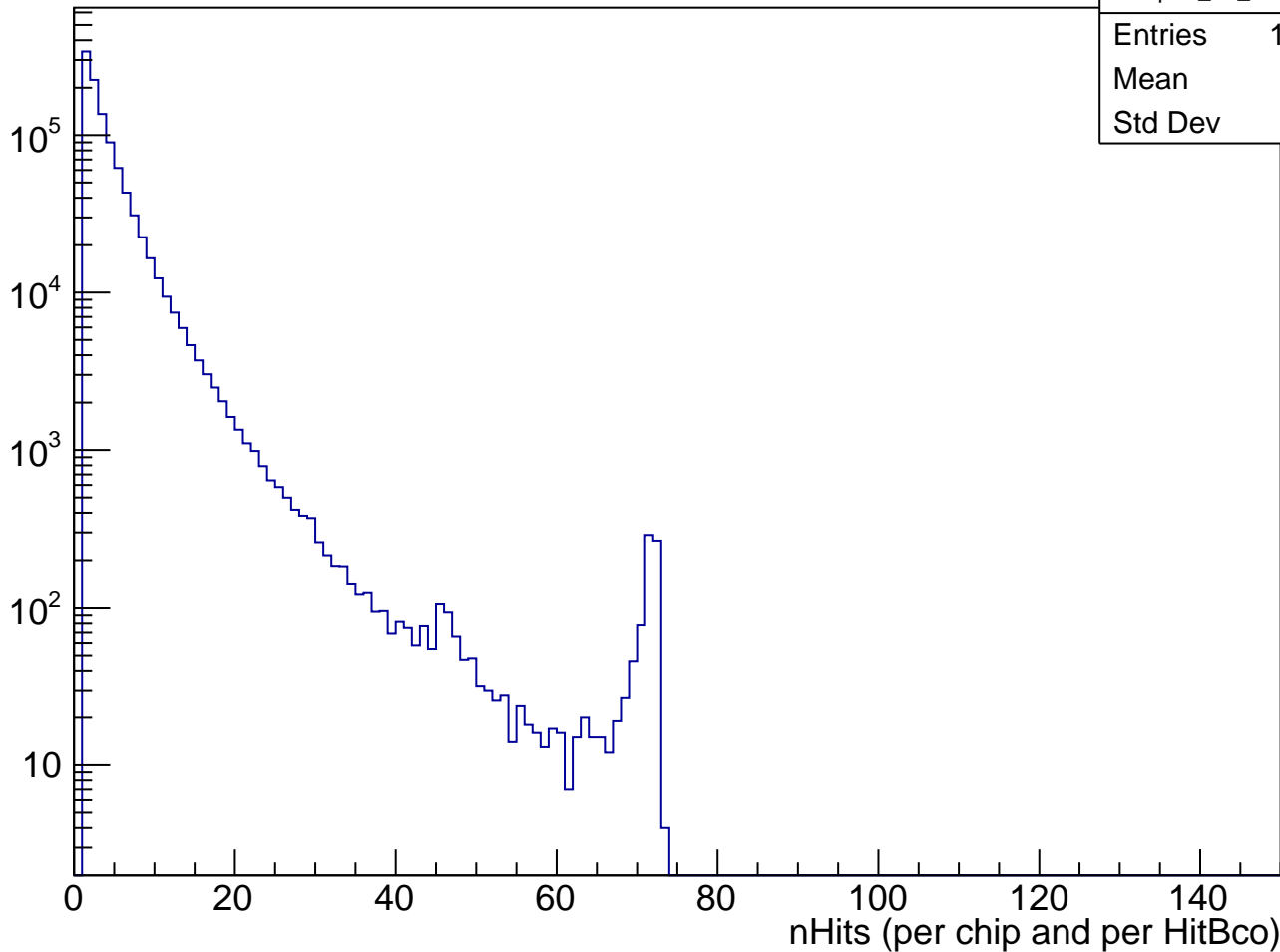
Entries



nChipHit_F7_Fch1_Chip18	
Entries	1011345
Mean	3.585
Std Dev	4.336

nChipHit_F7_Fch1_Chip19

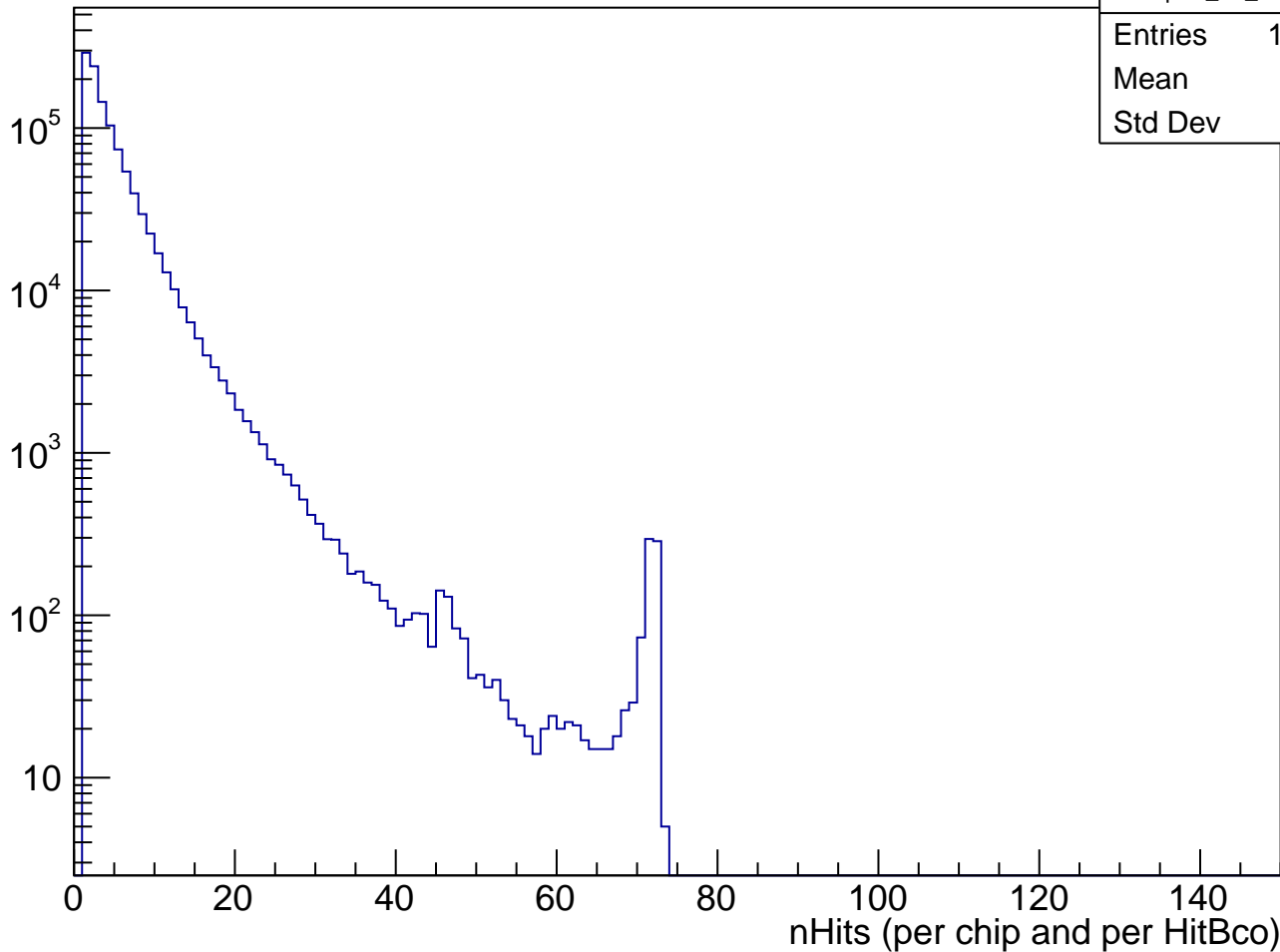
Entries



nChipHit_F7_Fch1_Chip19	
Entries	1027810
Mean	3.559
Std Dev	4.286

nChipHit_F7_Fch1_Chip2

Entries



nChipHit_F7_Fch1_Chip2

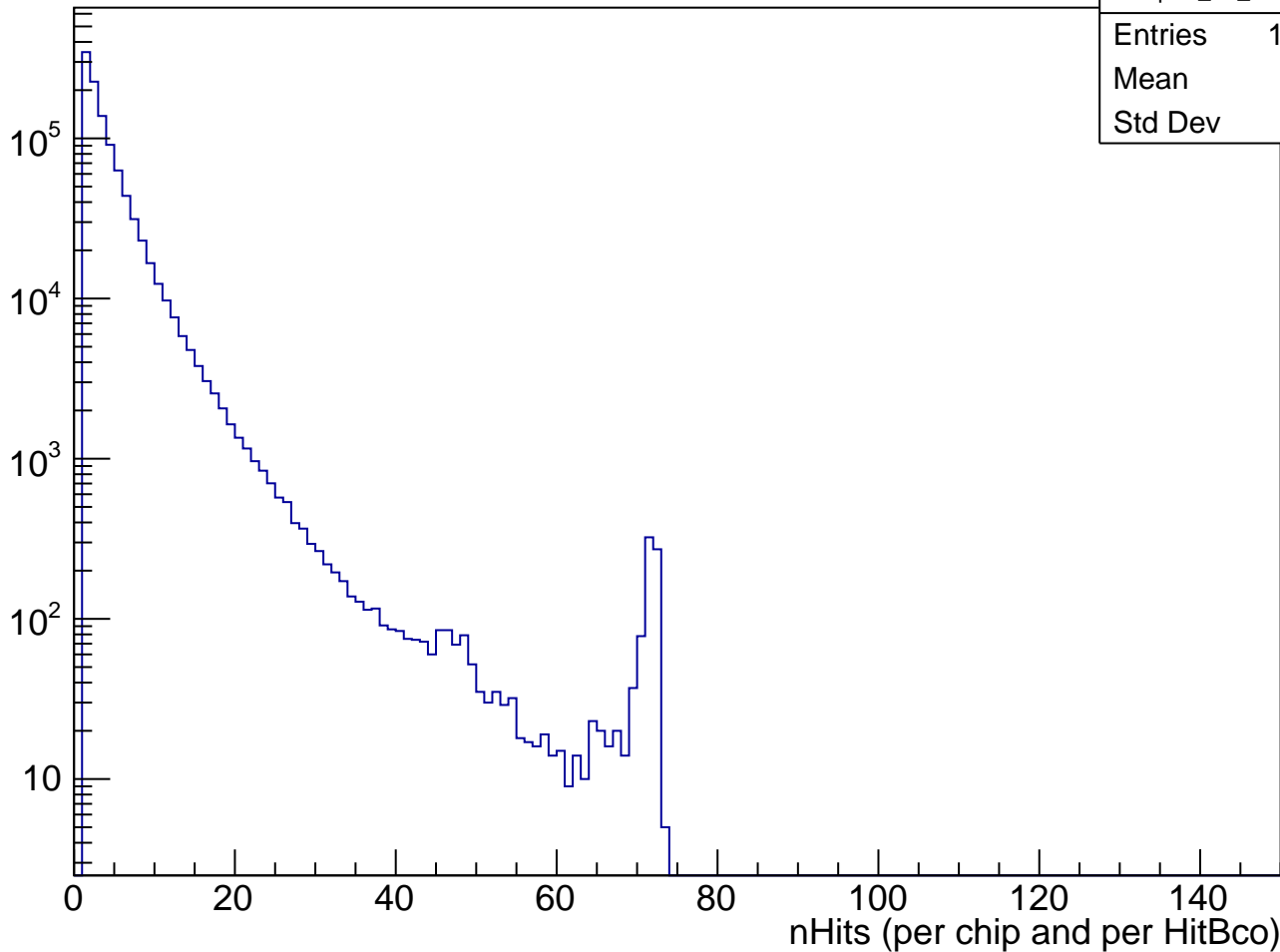
Entries 1085038

Mean 4.02

Std Dev 4.59

nChipHit_F7_Fch1_Chip20

Entries

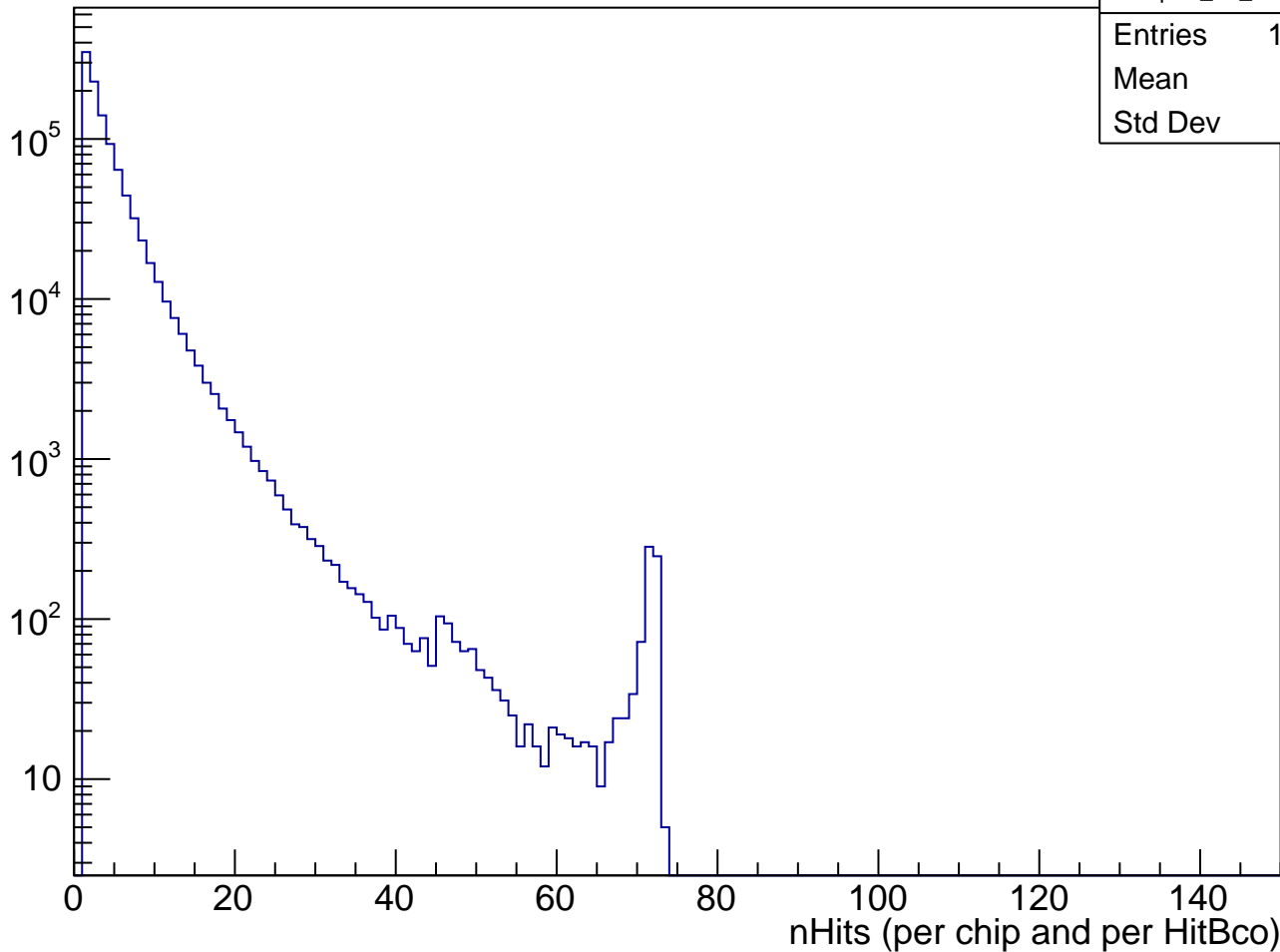


nChipHit_F7_Fch1_Chip20	
Entries	1042472
Mean	3.561
Std Dev	4.303

nHits (per chip and per HitBco)

nChipHit_F7_Fch1_Chip21

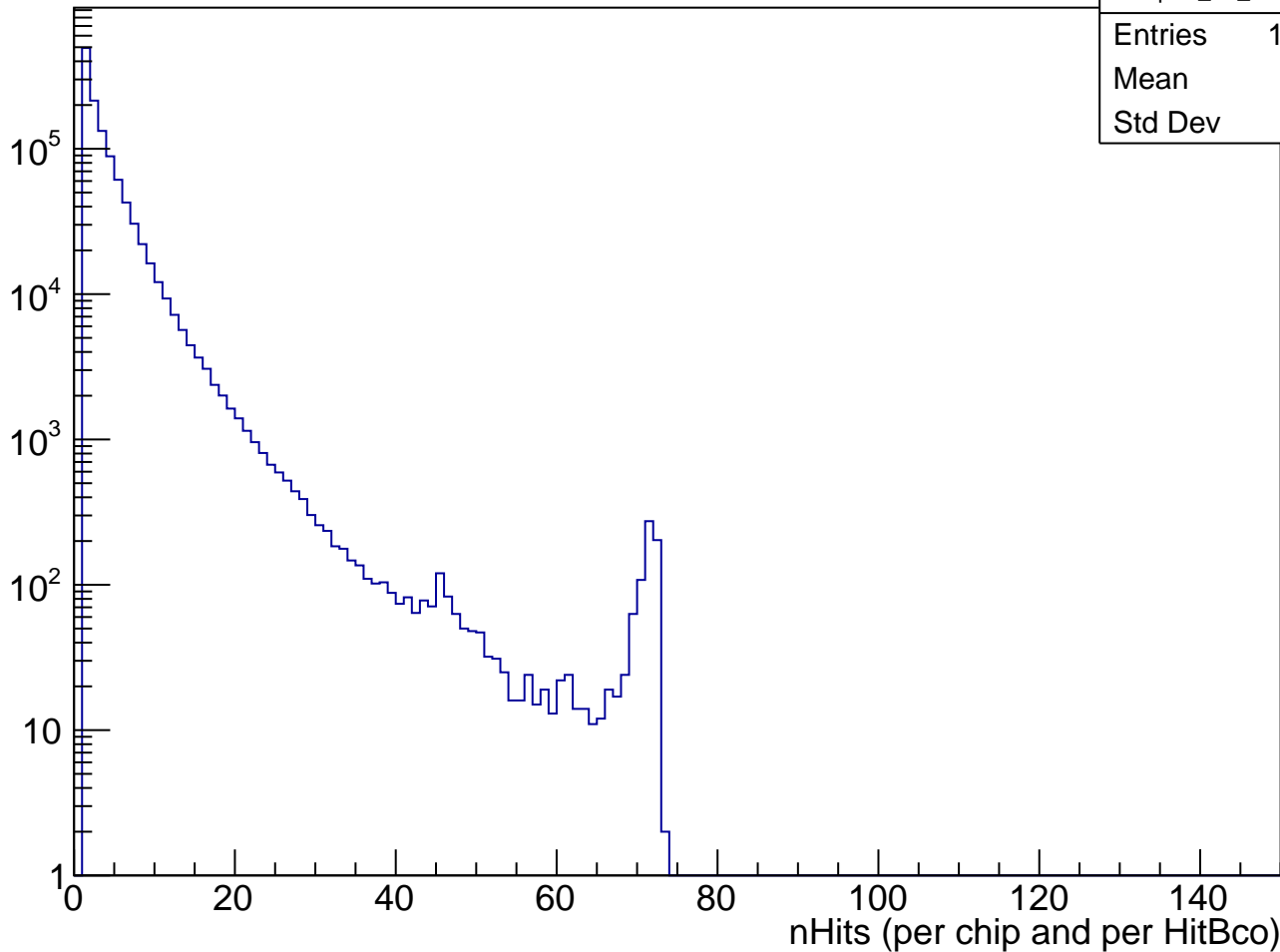
Entries



nChipHit_F7_Fch1_Chip21	
Entries	1056382
Mean	3.564
Std Dev	4.281

nChipHit_F7_Fch1_Chip22

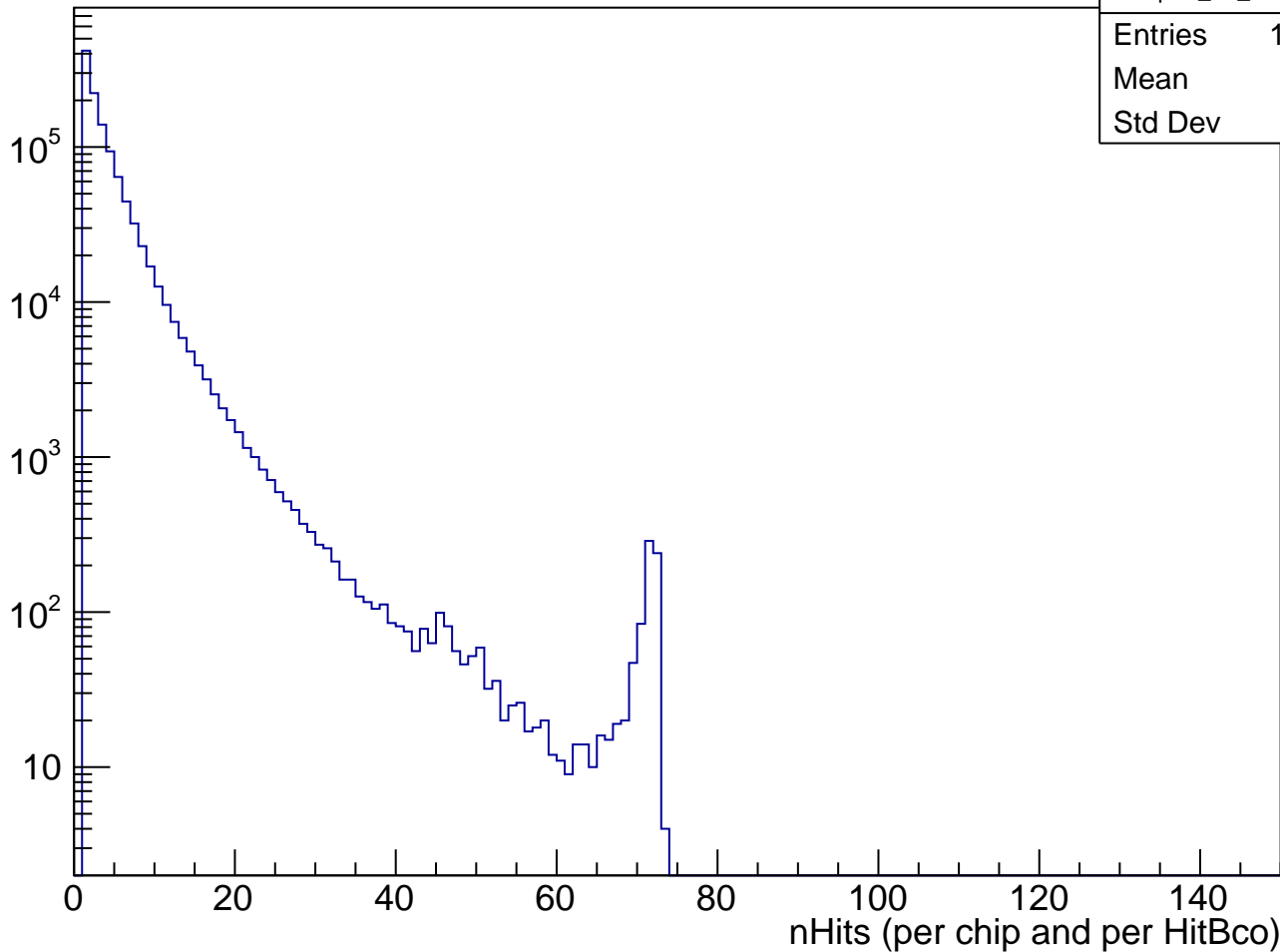
Entries



nChipHit_F7_Fch1_Chip22	
Entries	1164782
Mean	3.222
Std Dev	4.105

nChipHit_F7_Fch1_Chip23

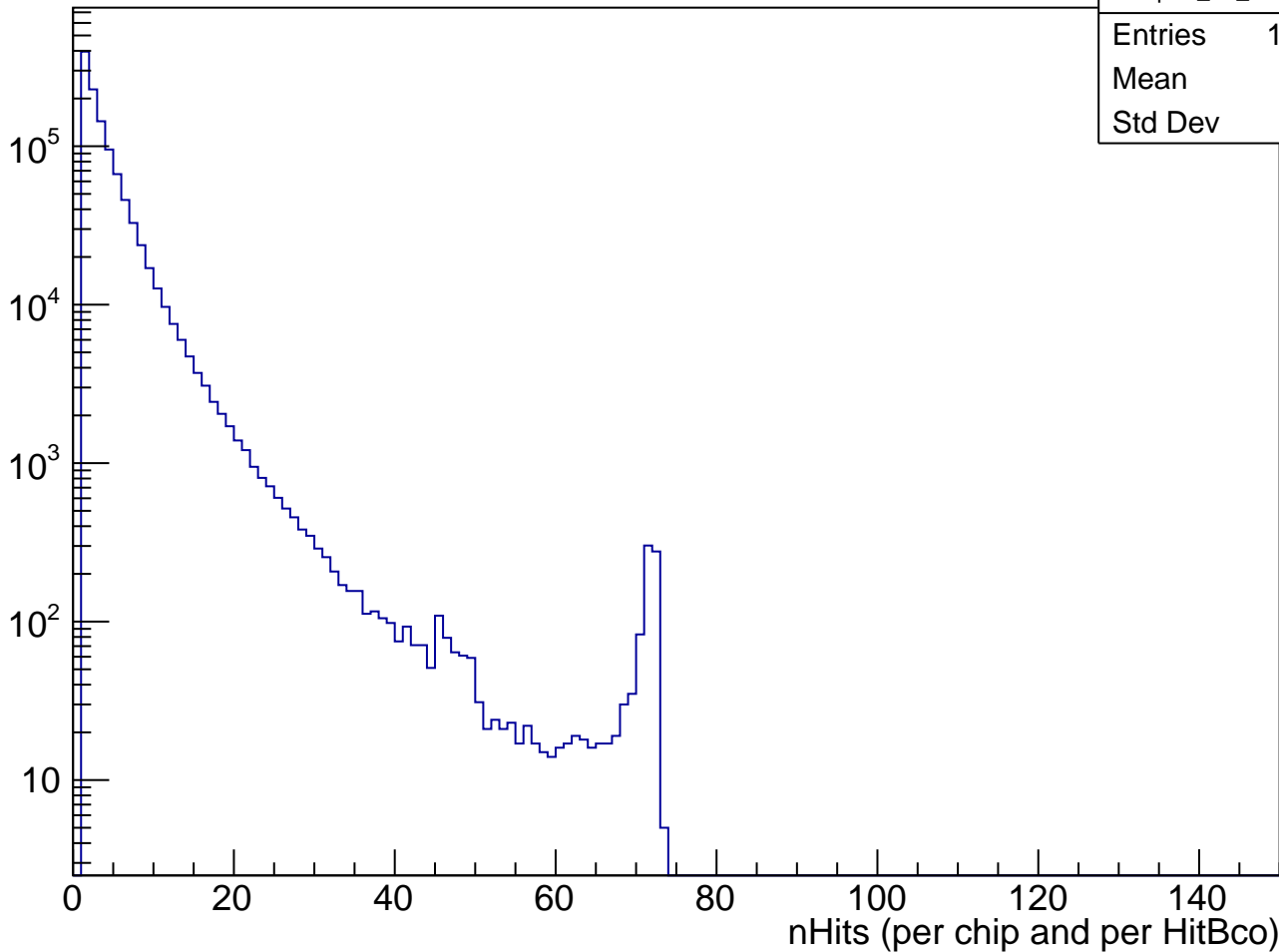
Entries



nChipHit_F7_Fch1_Chip23	
Entries	1119523
Mean	3.411
Std Dev	4.189

nChipHit_F7_Fch1_Chip24

Entries

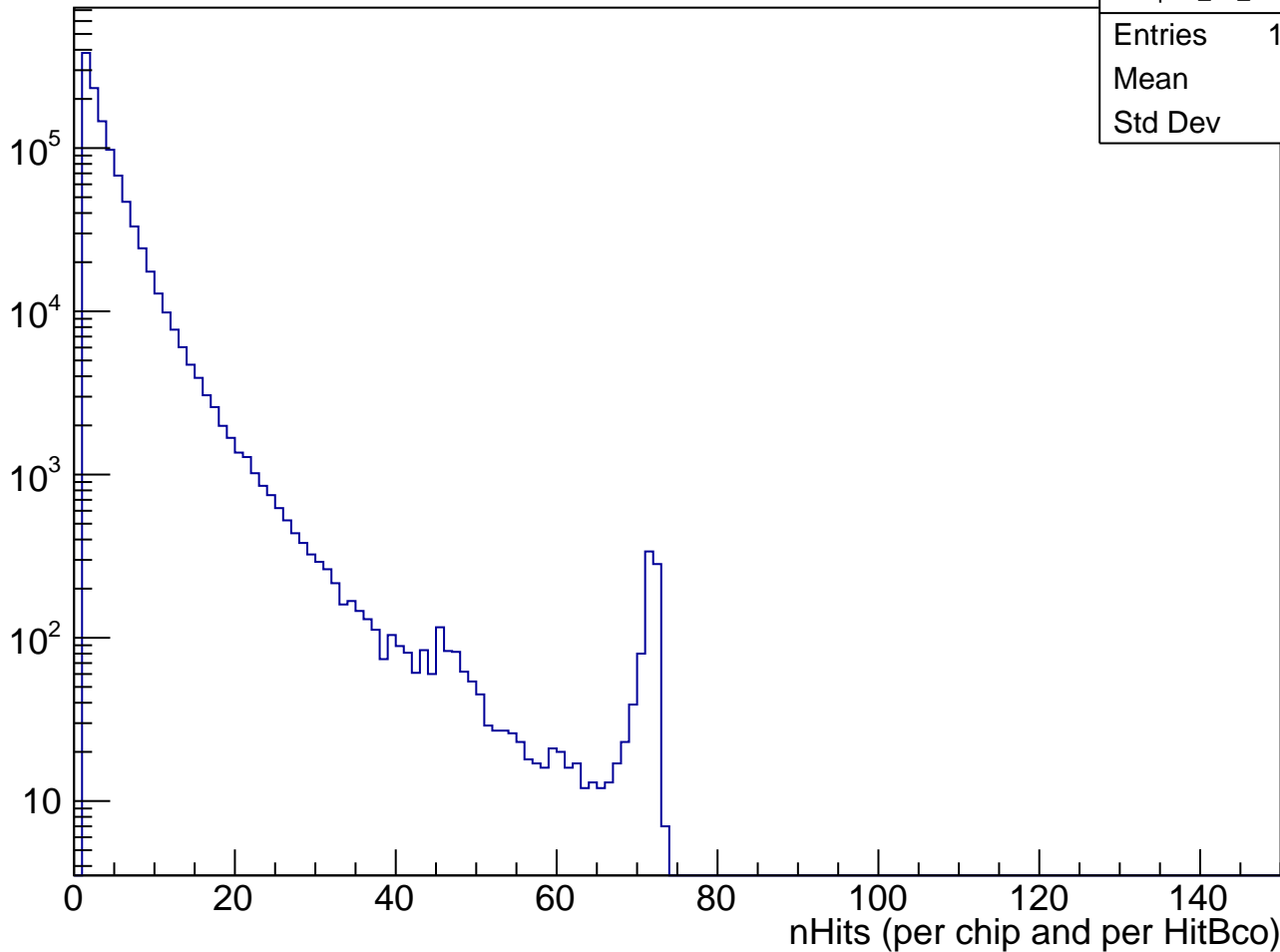


nChipHit_F7_Fch1_Chip24	
Entries	1113218
Mean	3.468
Std Dev	4.222

nHits (per chip and per HitBco)

nChipHit_F7_Fch1_Chip25

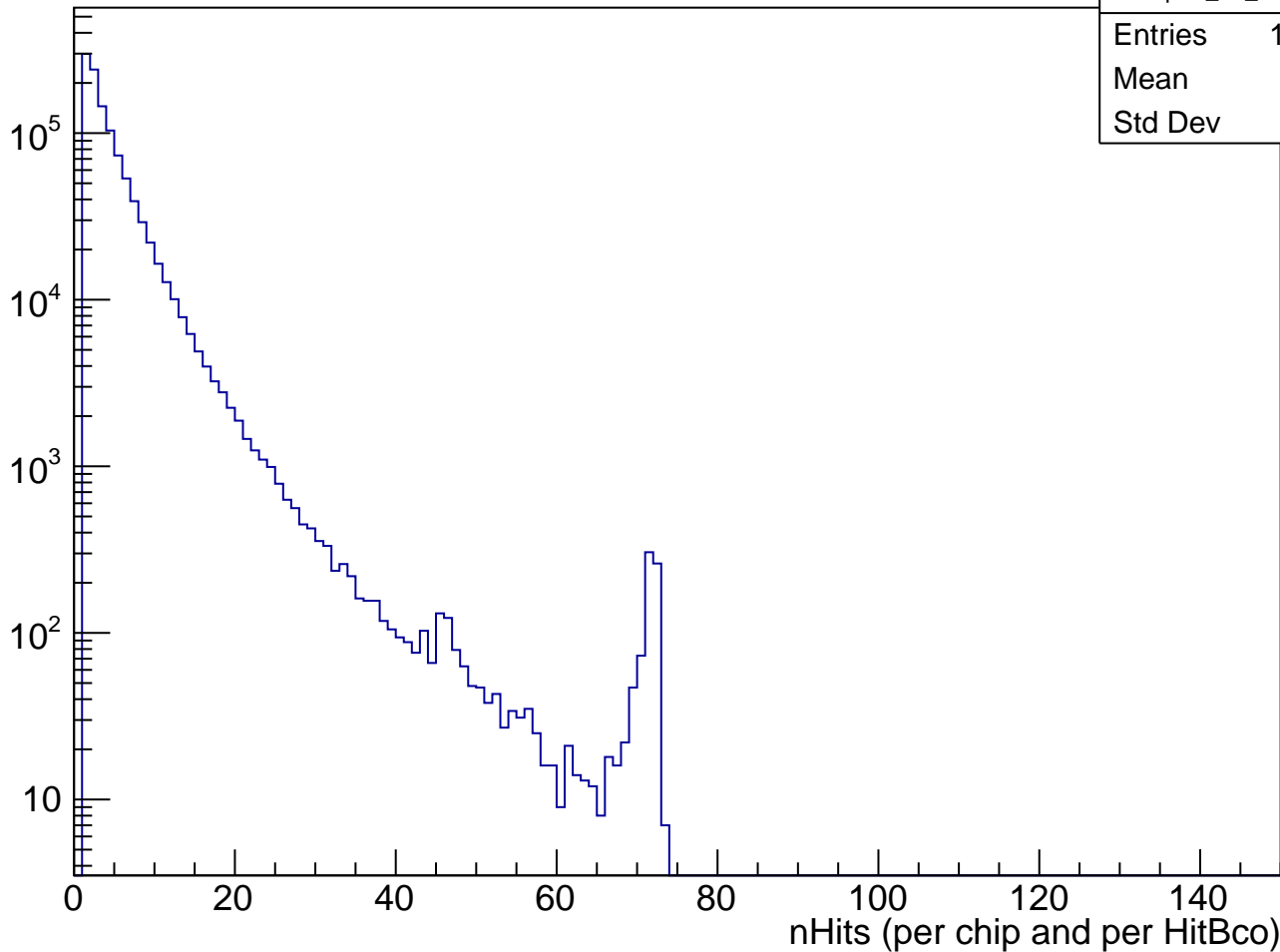
Entries



nChipHit_F7_Fch1_Chip25	
Entries	1114574
Mean	3.513
Std Dev	4.259

nChipHit_F7_Fch1_Chip3

Entries

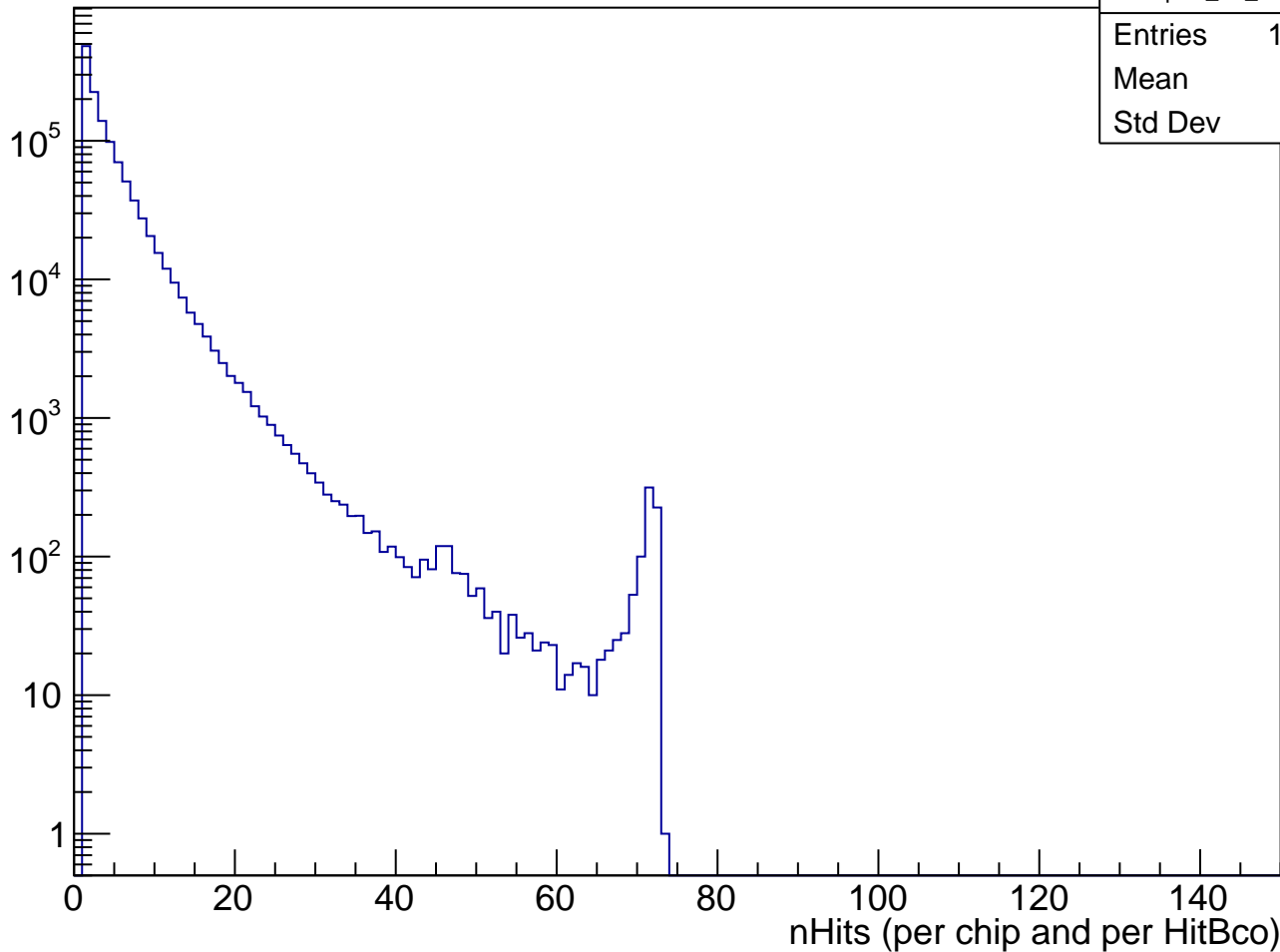


nChipHit_F7_Fch1_Chip3	
Entries	1089460
Mean	3.971
Std Dev	4.548

nHits (per chip and per HitBco)

nChipHit_F7_Fch1_Chip4

Entries



nChipHit_F7_Fch1_Chip4

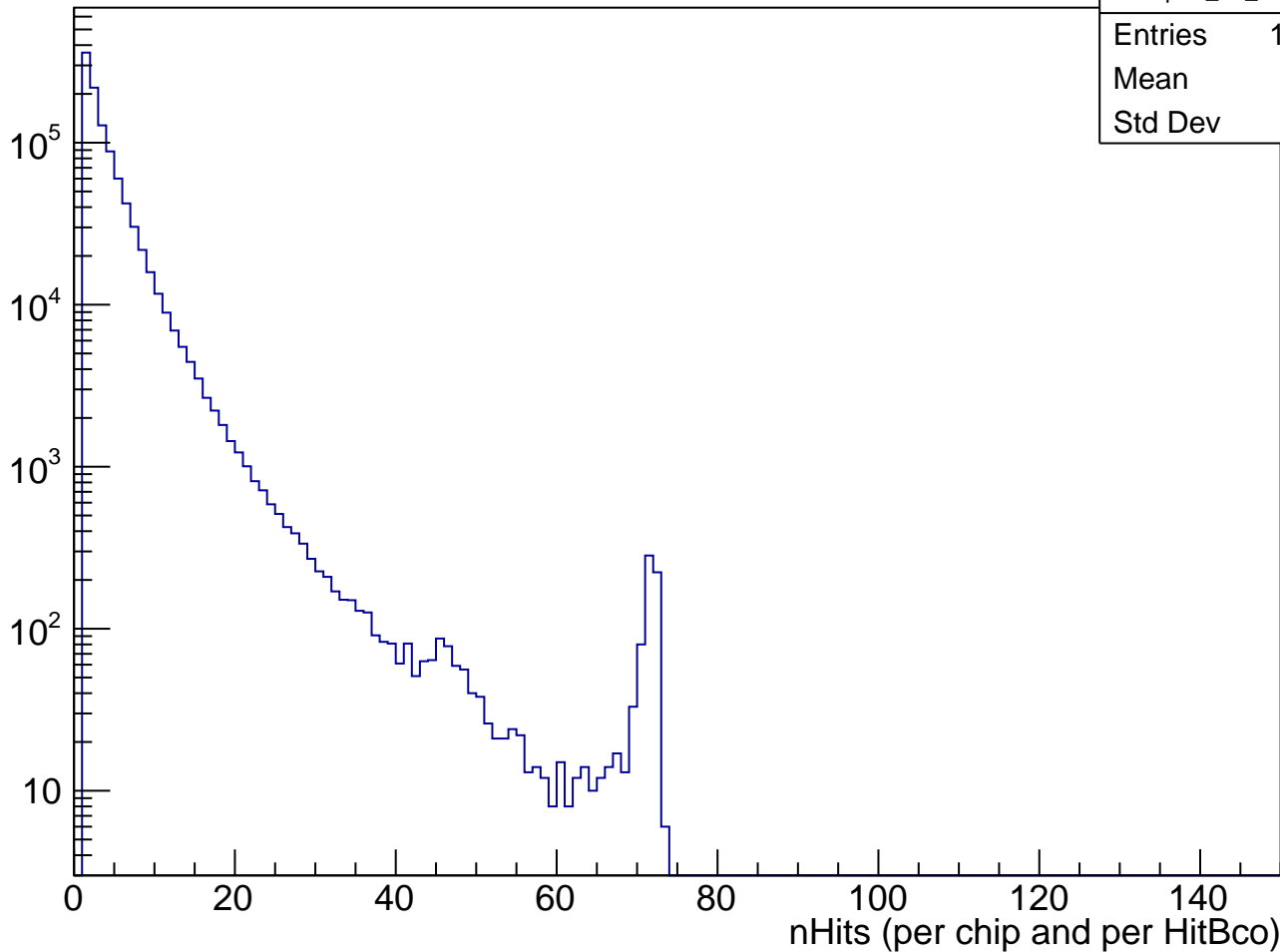
Entries 1230525

Mean 3.5

Std Dev 4.354

nChipHit_F7_Fch1_Chip5

Entries



nChipHit_F7_Fch1_Chip5

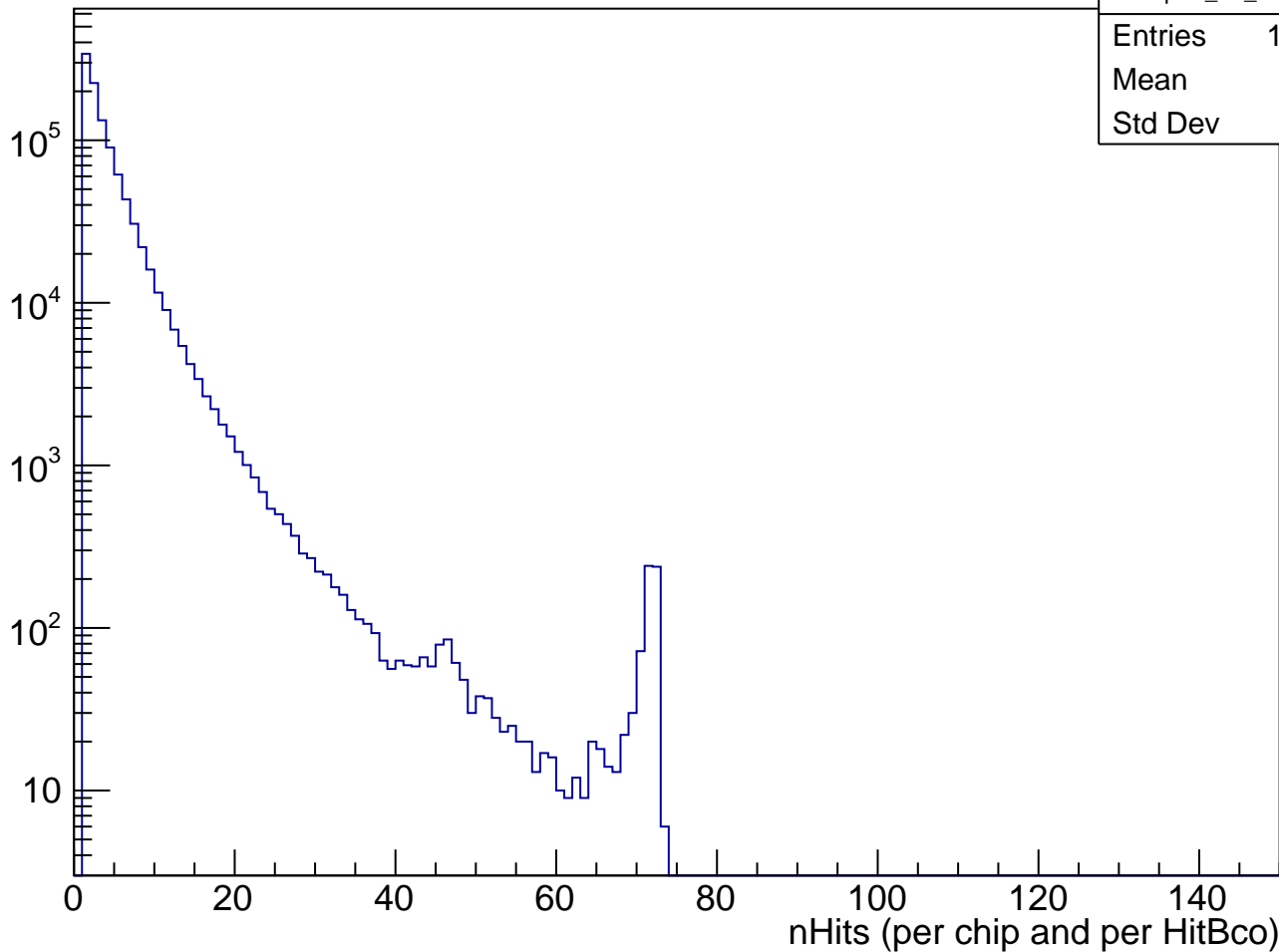
Entries 1023103

Mean 3.445

Std Dev 4.147

nChipHit_F7_Fch1_Chip6

Entries



nChipHit_F7_Fch1_Chip6

Entries 1019498

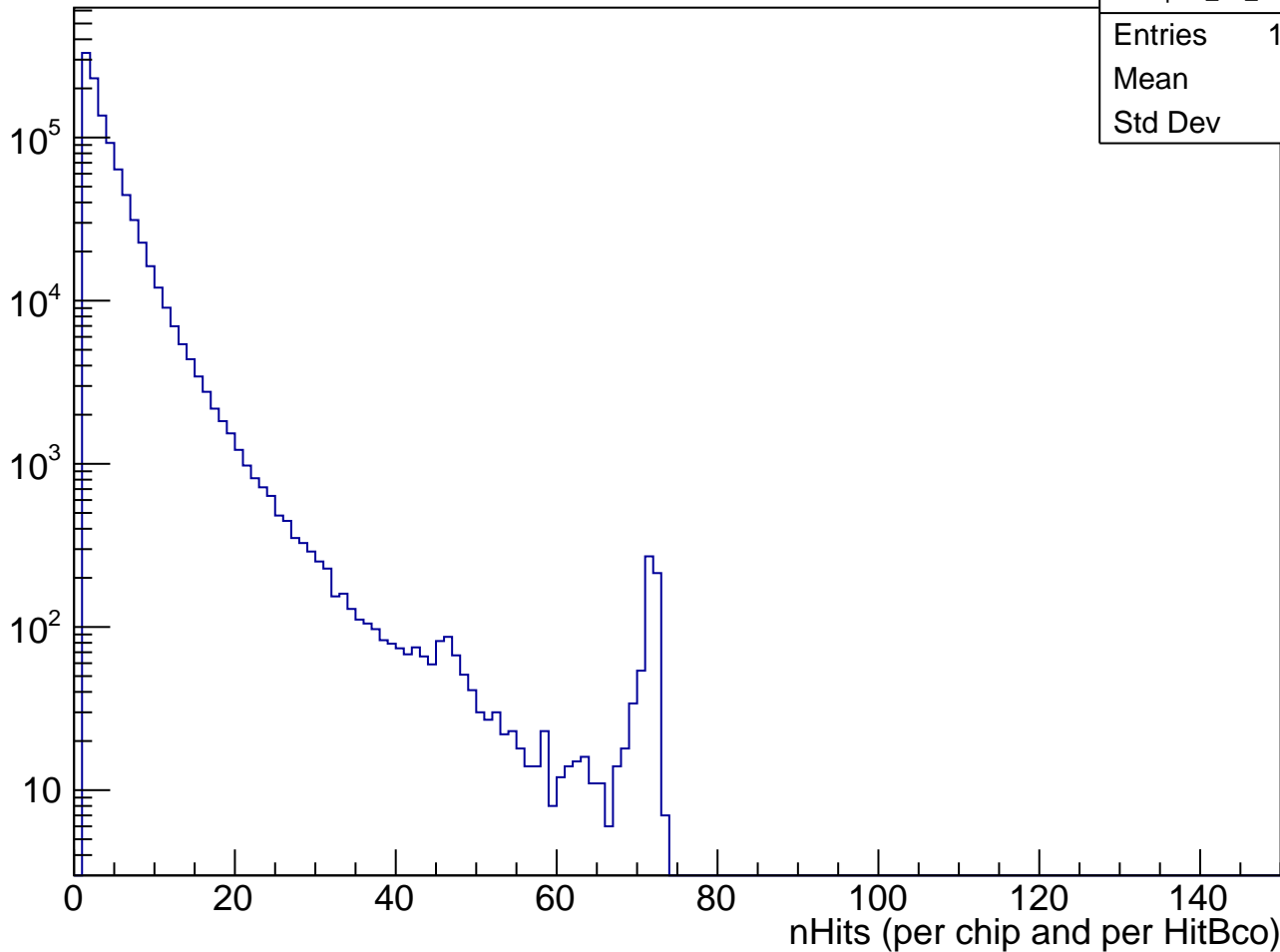
Mean 3.478

Std Dev 4.112

nHits (per chip and per HitBco)

nChipHit_F7_Fch1_Chip7

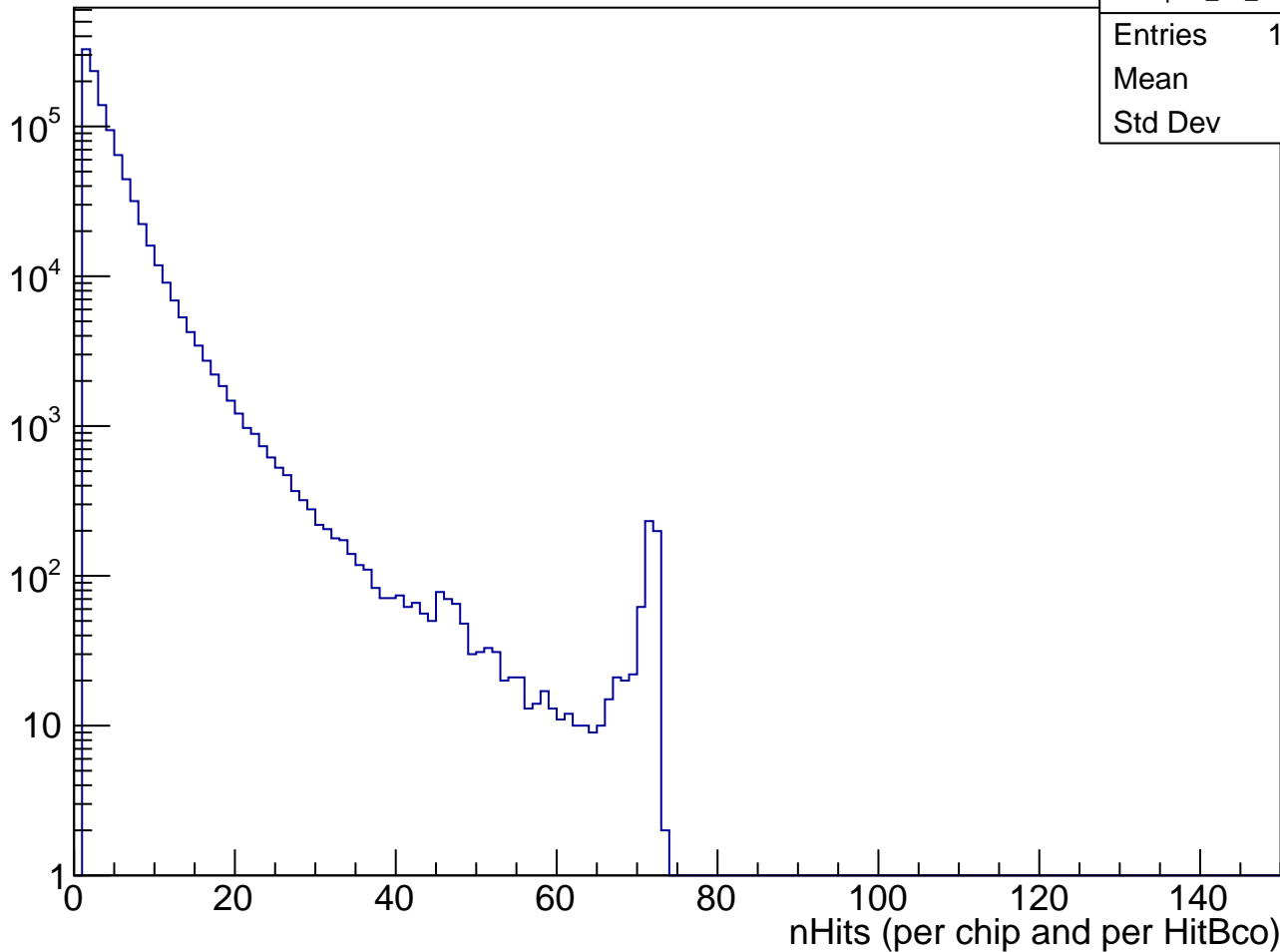
Entries



nChipHit_F7_Fch1_Chip7	
Entries	1026697
Mean	3.52
Std Dev	4.116

nChipHit_F7_Fch1_Chip8

Entries



nChipHit_F7_Fch1_Chip8

Entries 1032168

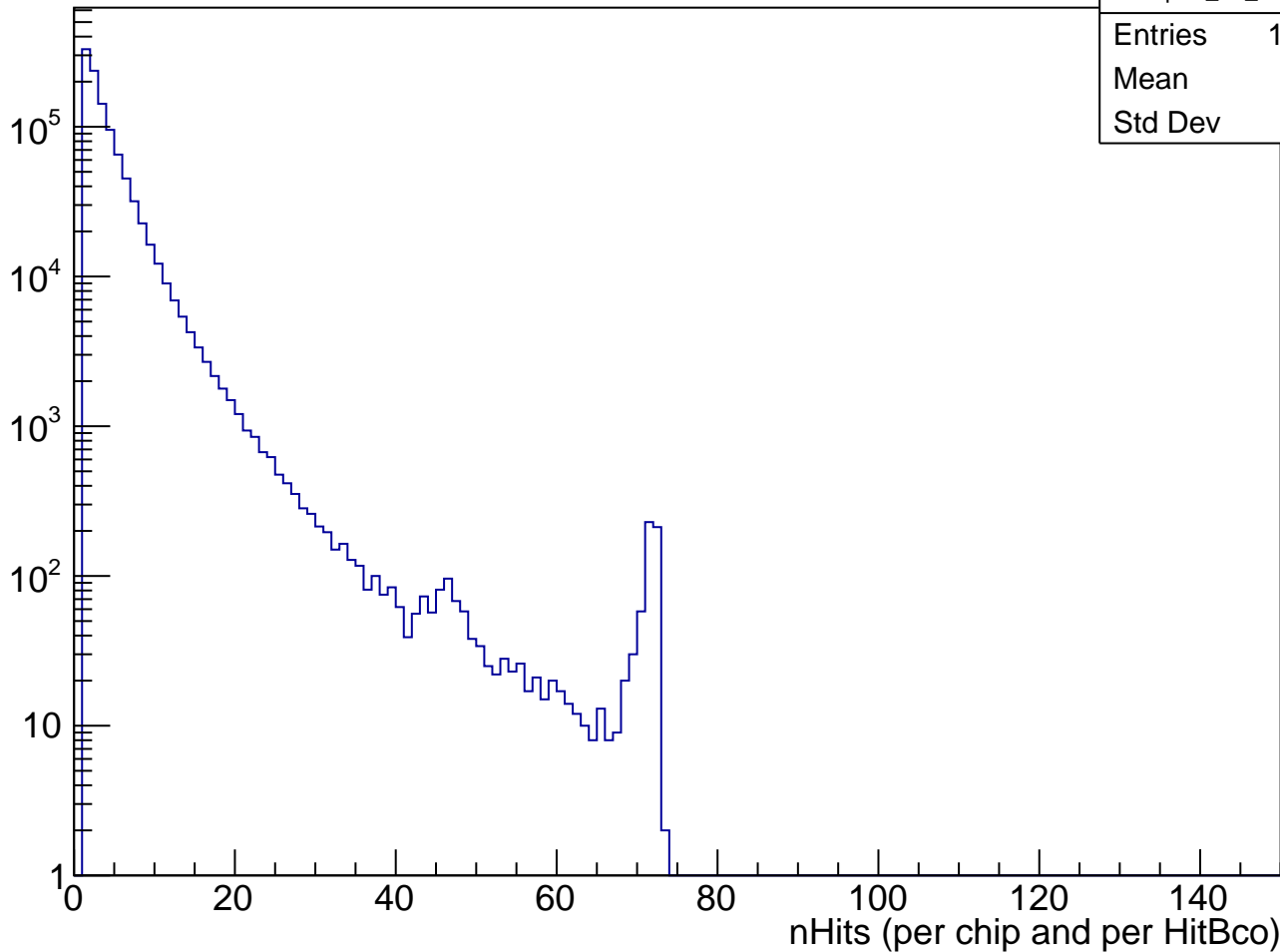
Mean 3.51

Std Dev 4.054

nHits (per chip and per HitBco)

nChipHit_F7_Fch1_Chip9

Entries



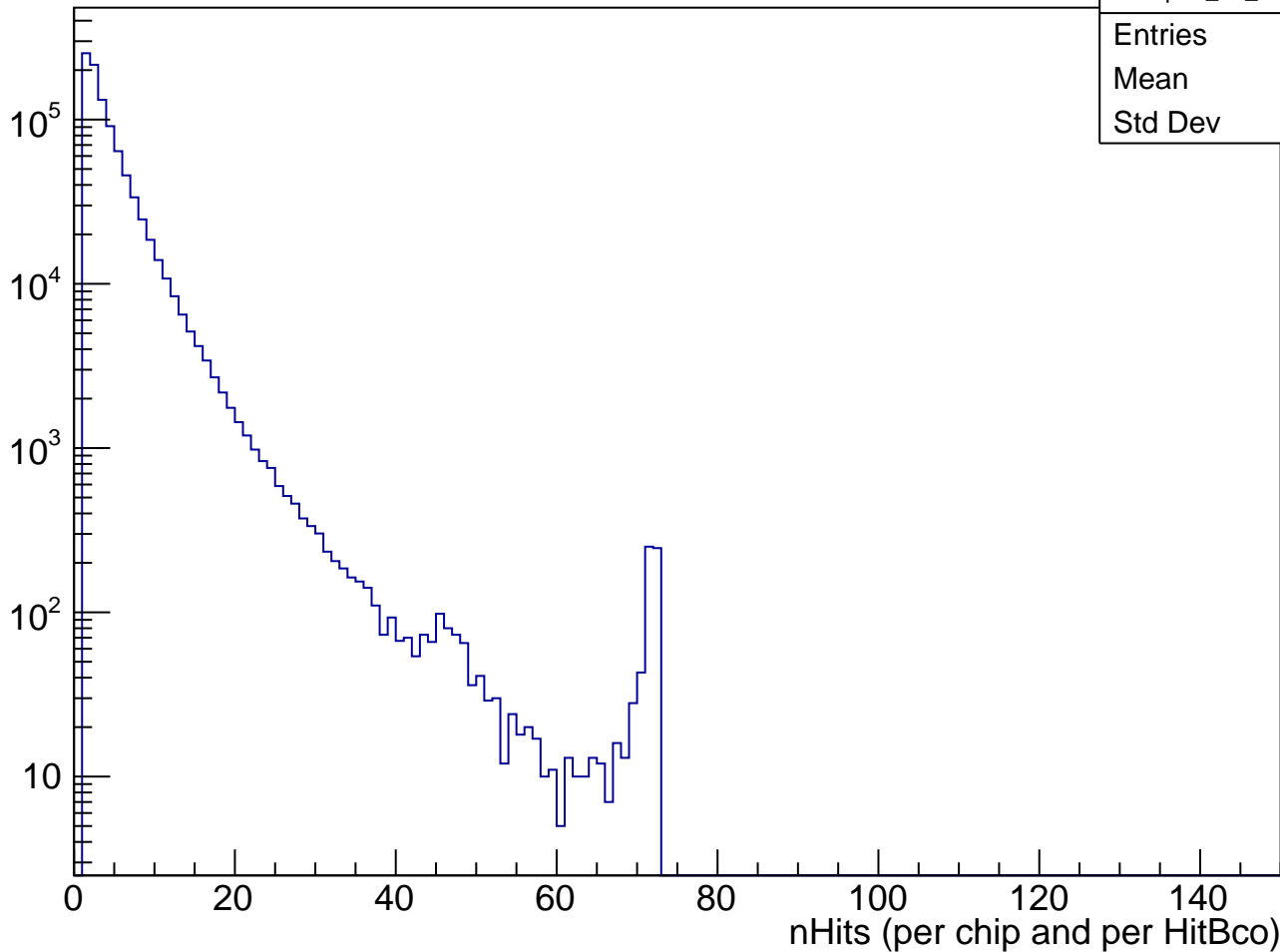
nChipHit_F7_Fch1_Chip9

Entries	1043015
Mean	3.502
Std Dev	4.032

nHits (per chip and per HitBco)

nChipHit_F7_Fch2_Chip0

Entries

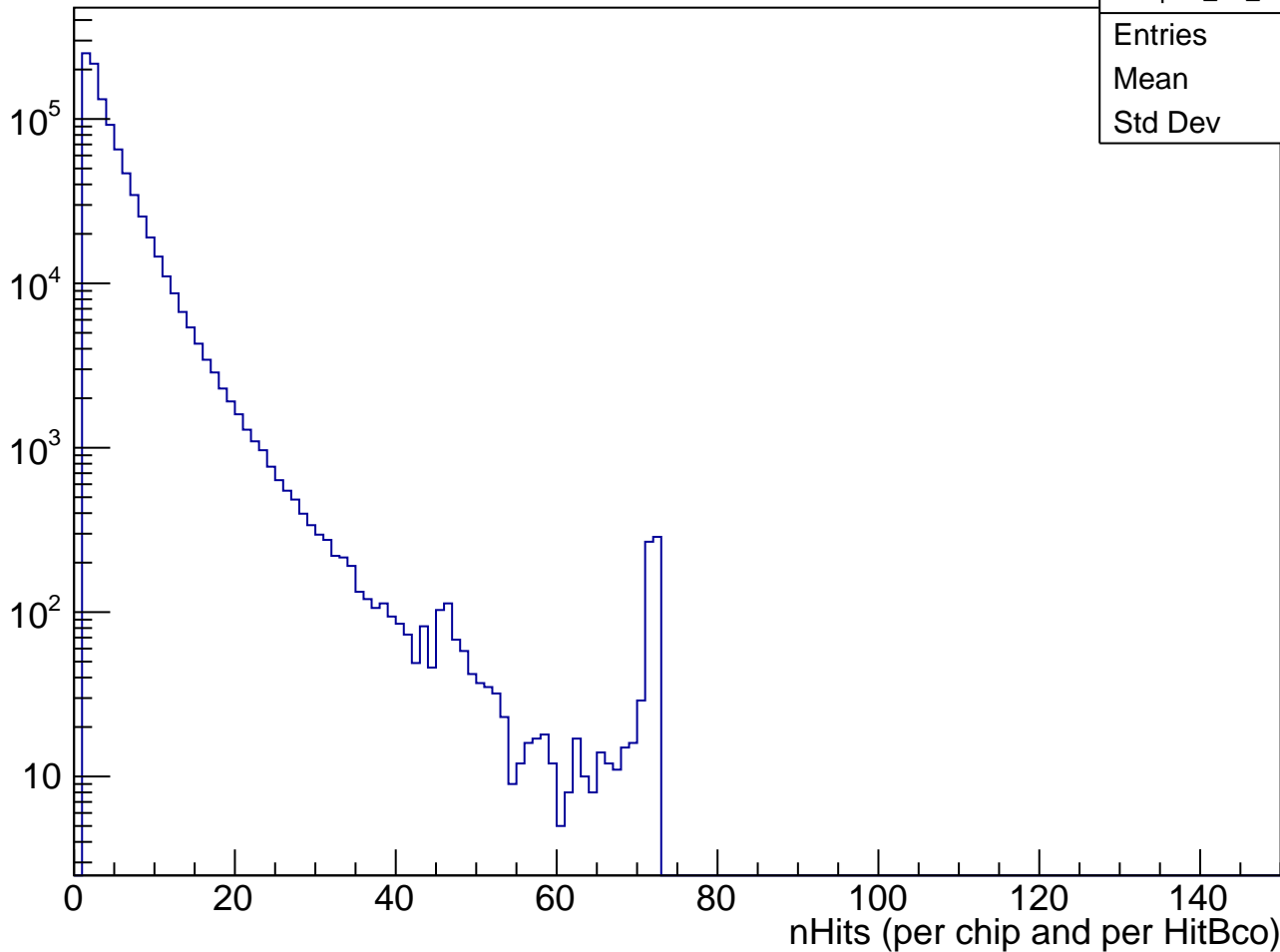


nChipHit_F7_Fch2_Chip0	
Entries	948608
Mean	3.909
Std Dev	4.402

nHits (per chip and per HitBco)

nChipHit_F7_Fch2_Chip1

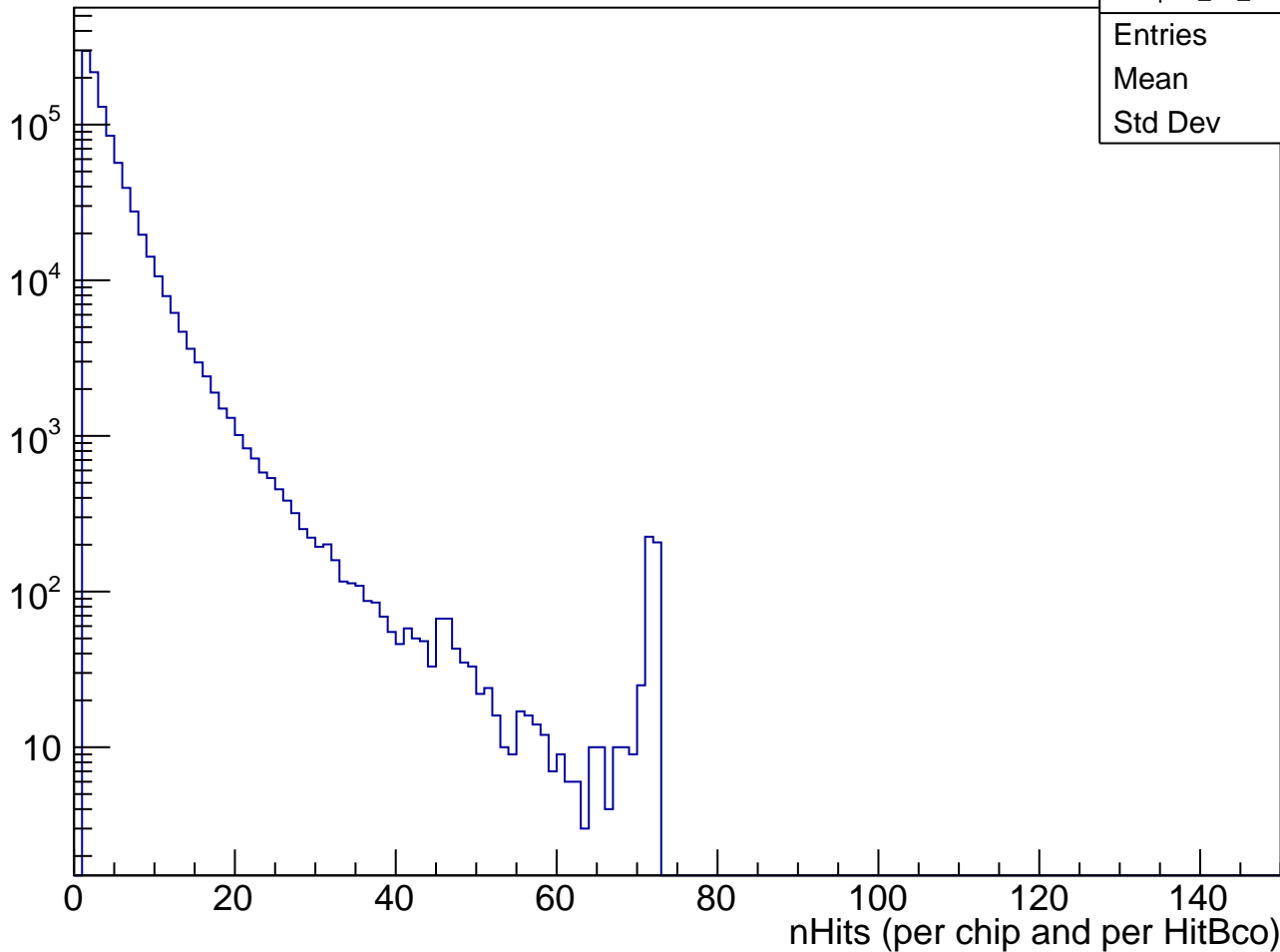
Entries



nChipHit_F7_Fch2_Chip1	
Entries	955850
Mean	3.967
Std Dev	4.475

nChipHit_F7_Fch2_Chip10

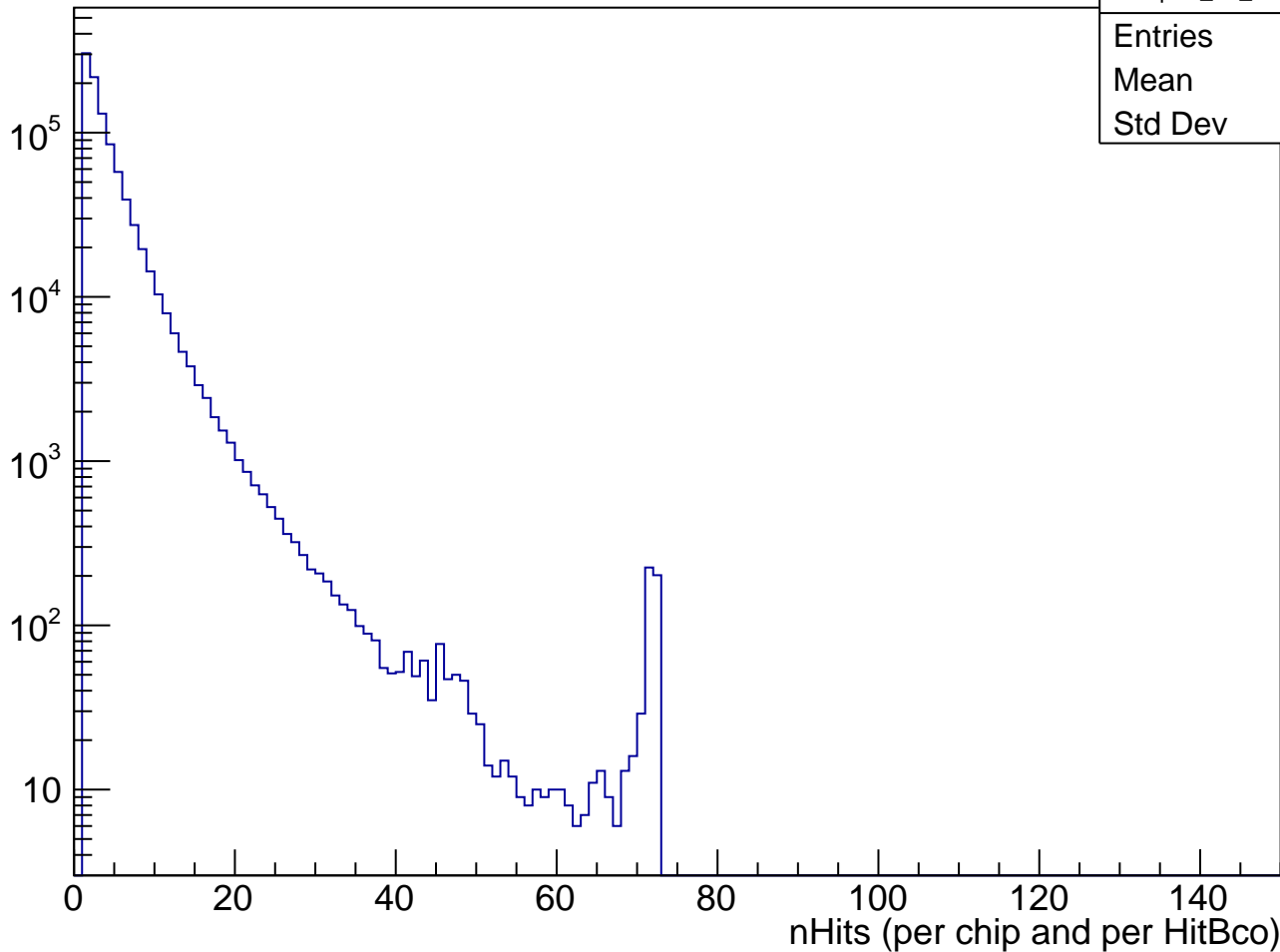
Entries



nChipHit_F7_Fch2_Chip10	
Entries	938113
Mean	3.448
Std Dev	3.943

nChipHit_F7_Fch2_Chip11

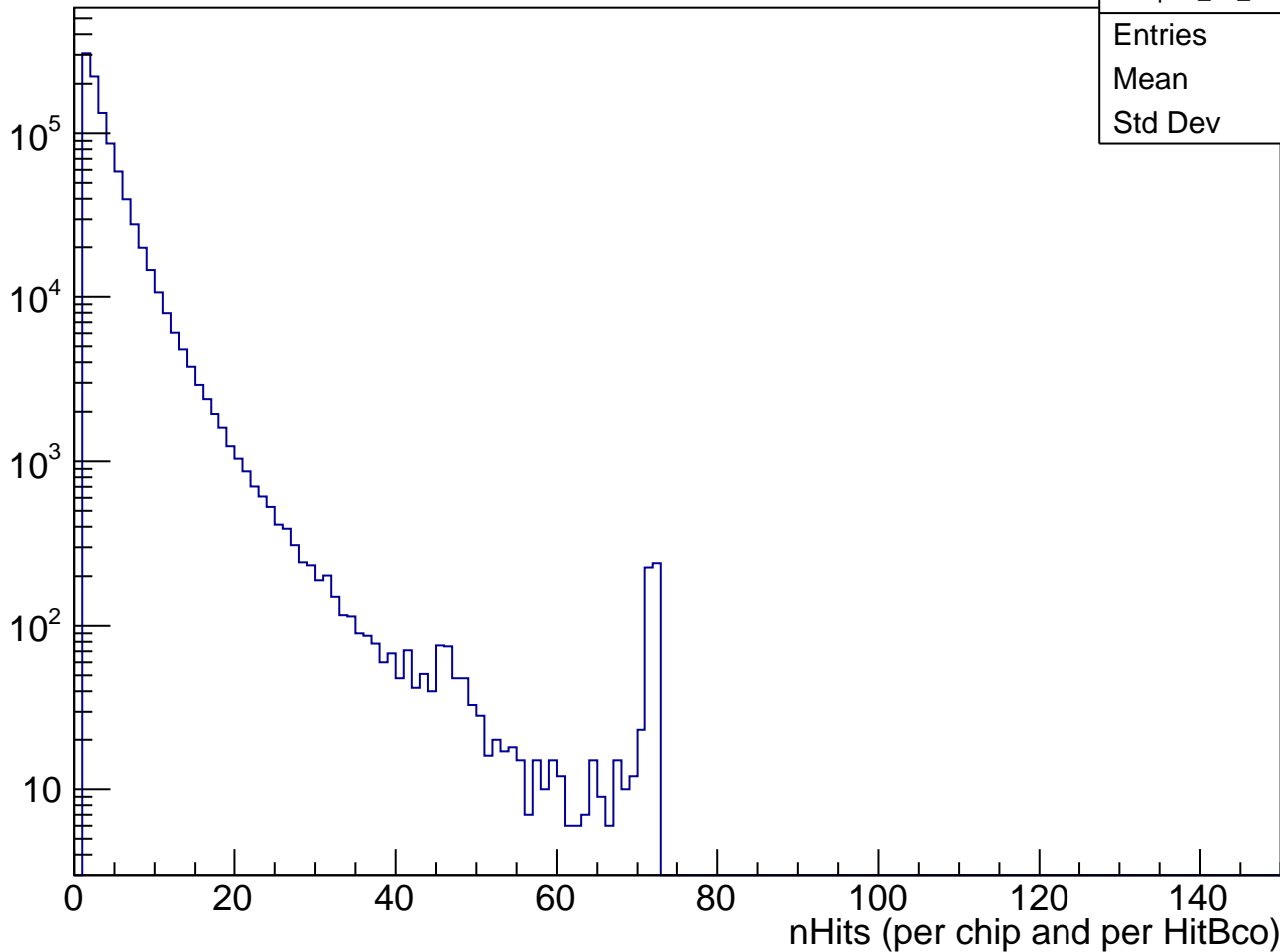
Entries



nChipHit_F7_Fch2_Chip11	
Entries	946161
Mean	3.429
Std Dev	3.938

nChipHit_F7_Fch2_Chip12

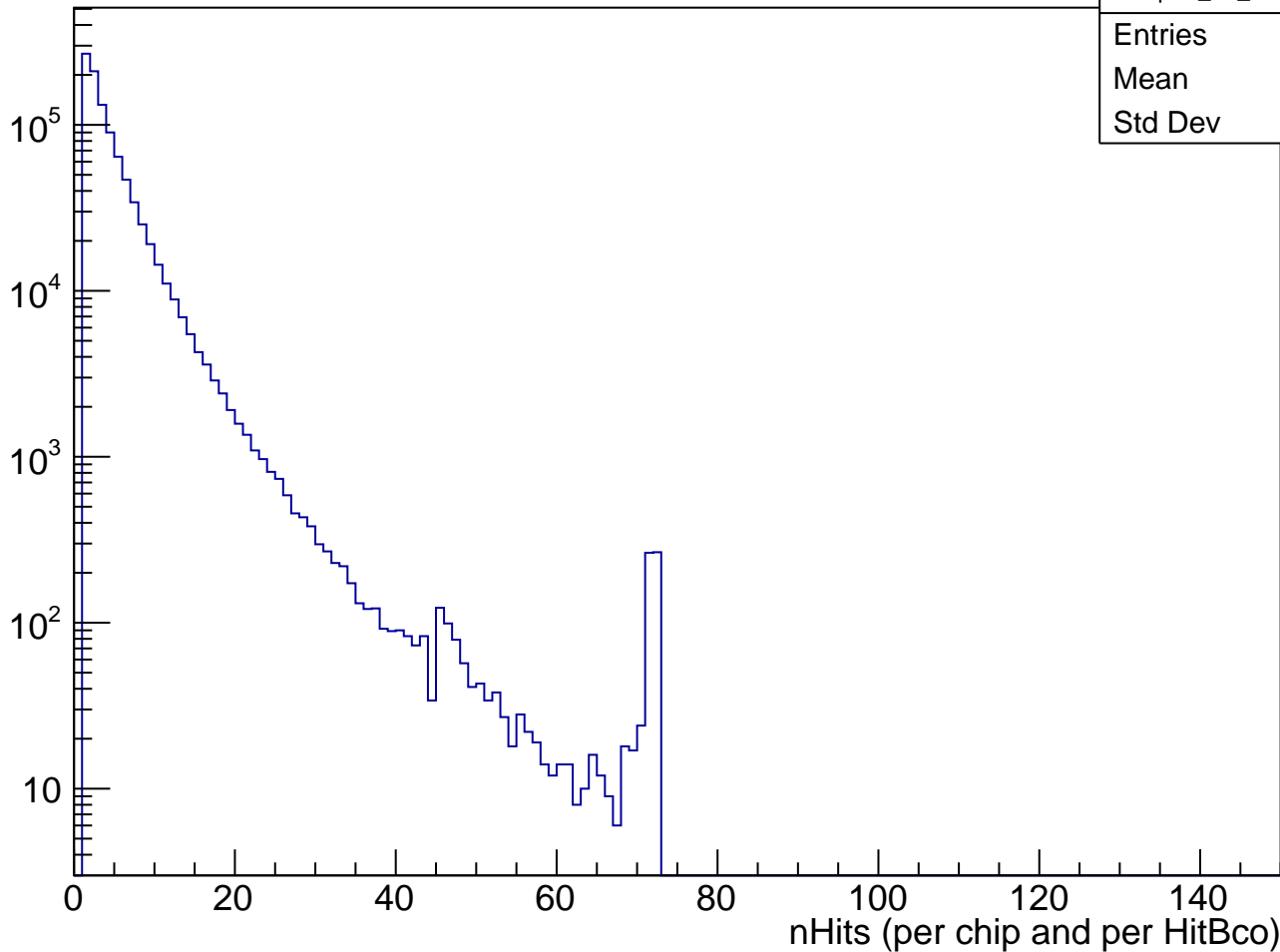
Entries



nChipHit_F7_Fch2_Chip12	
Entries	959222
Mean	3.436
Std Dev	3.957

nChipHit_F7_Fch2_Chip13

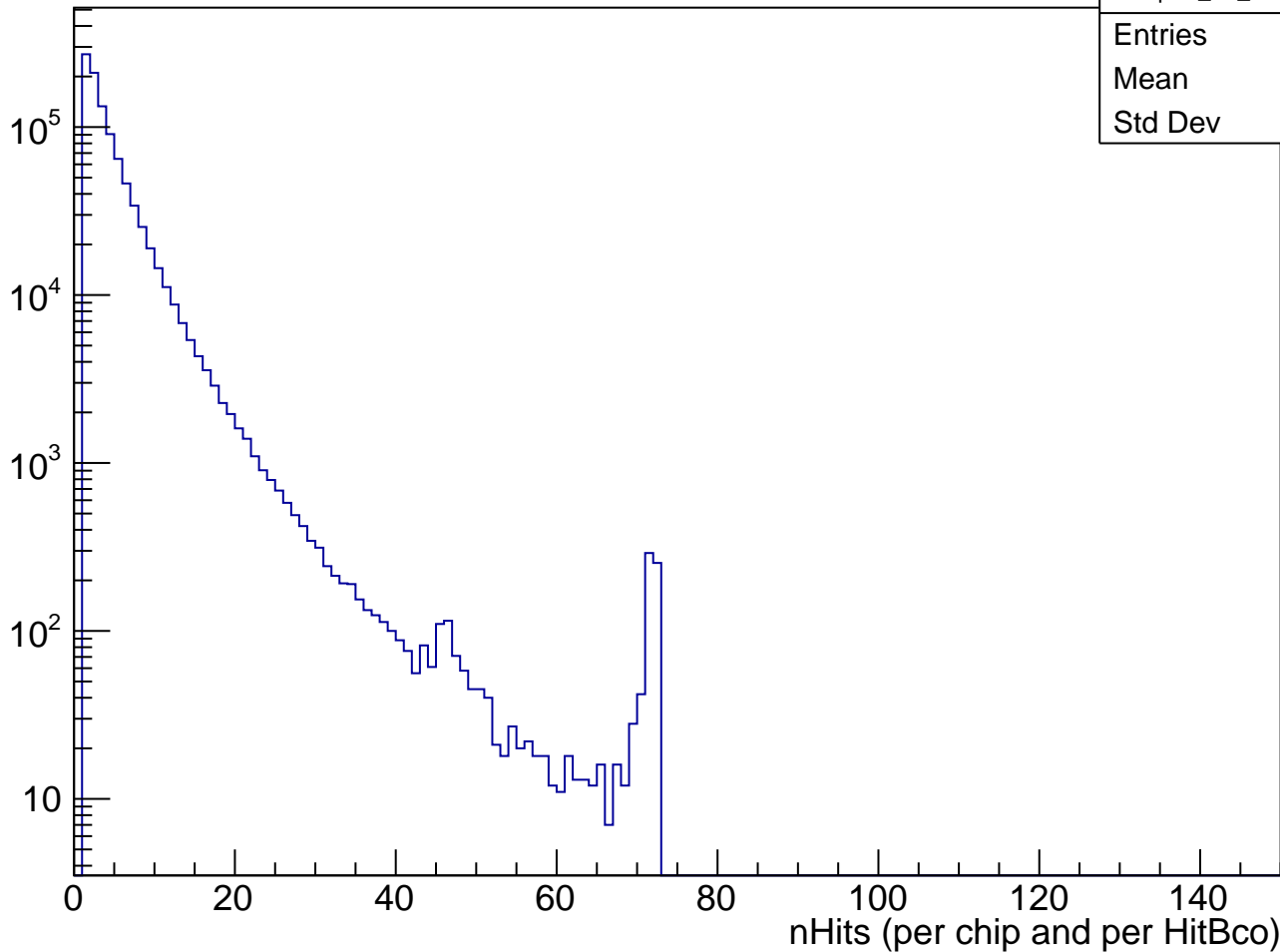
Entries



nChipHit_F7_Fch2_Chip13	
Entries	963975
Mean	3.938
Std Dev	4.498

nChipHit_F7_Fch2_Chip14

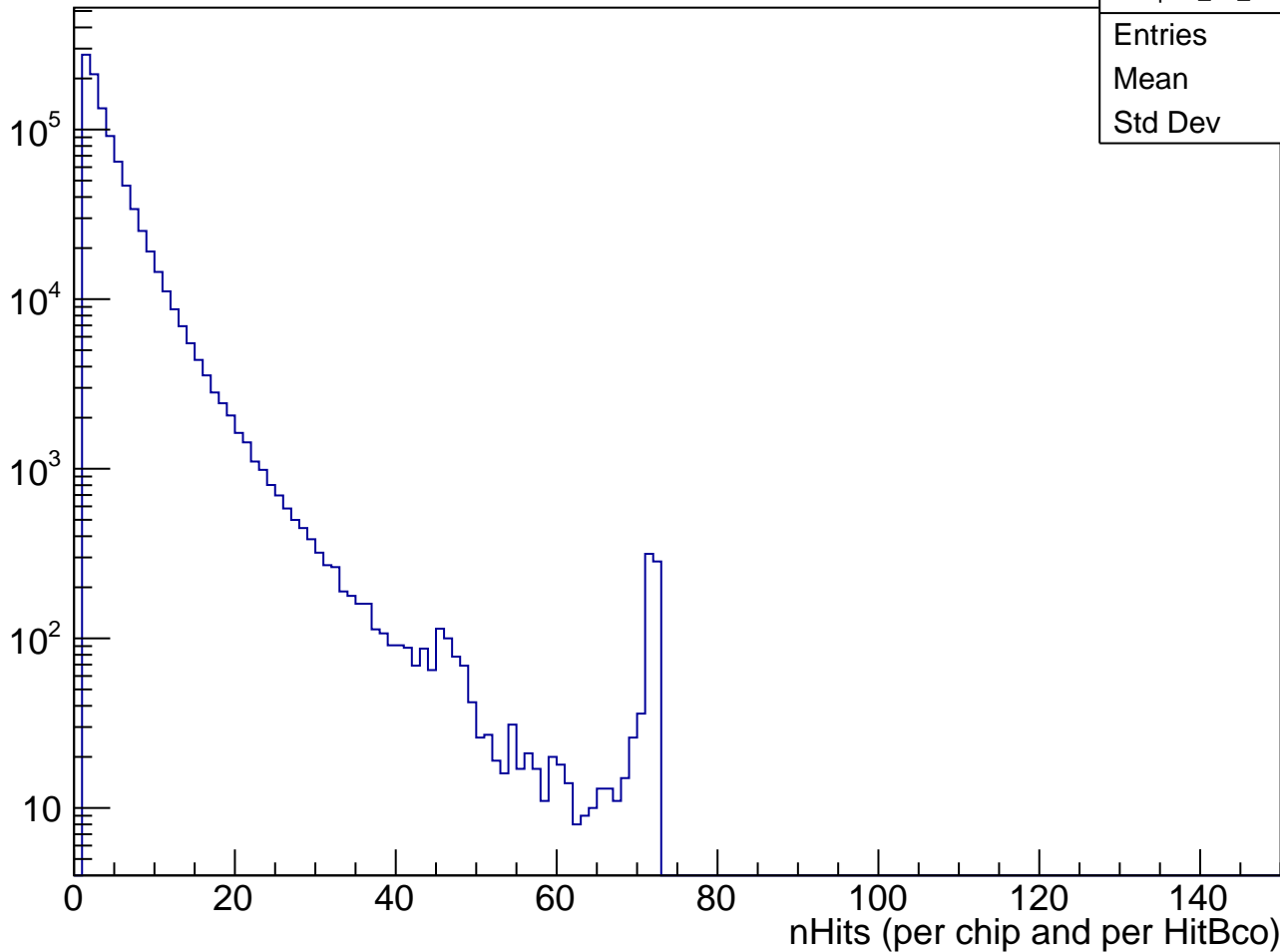
Entries



nChipHit_F7_Fch2_Chip14	
Entries	968185
Mean	3.928
Std Dev	4.51

nChipHit_F7_Fch2_Chip15

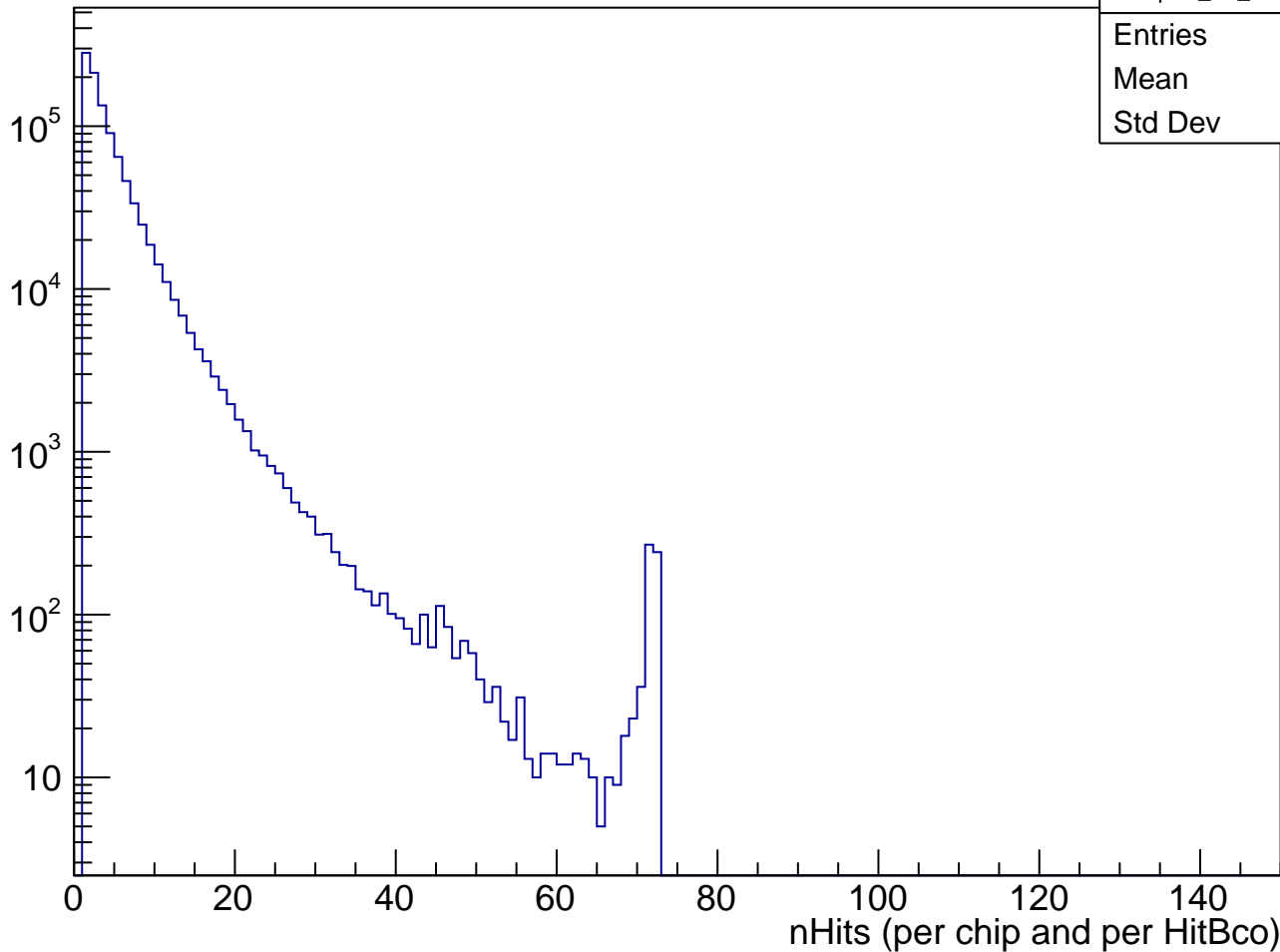
Entries



nChipHit_F7_Fch2_Chip15	
Entries	976704
Mean	3.925
Std Dev	4.54

nChipHit_F7_Fch2_Chip16

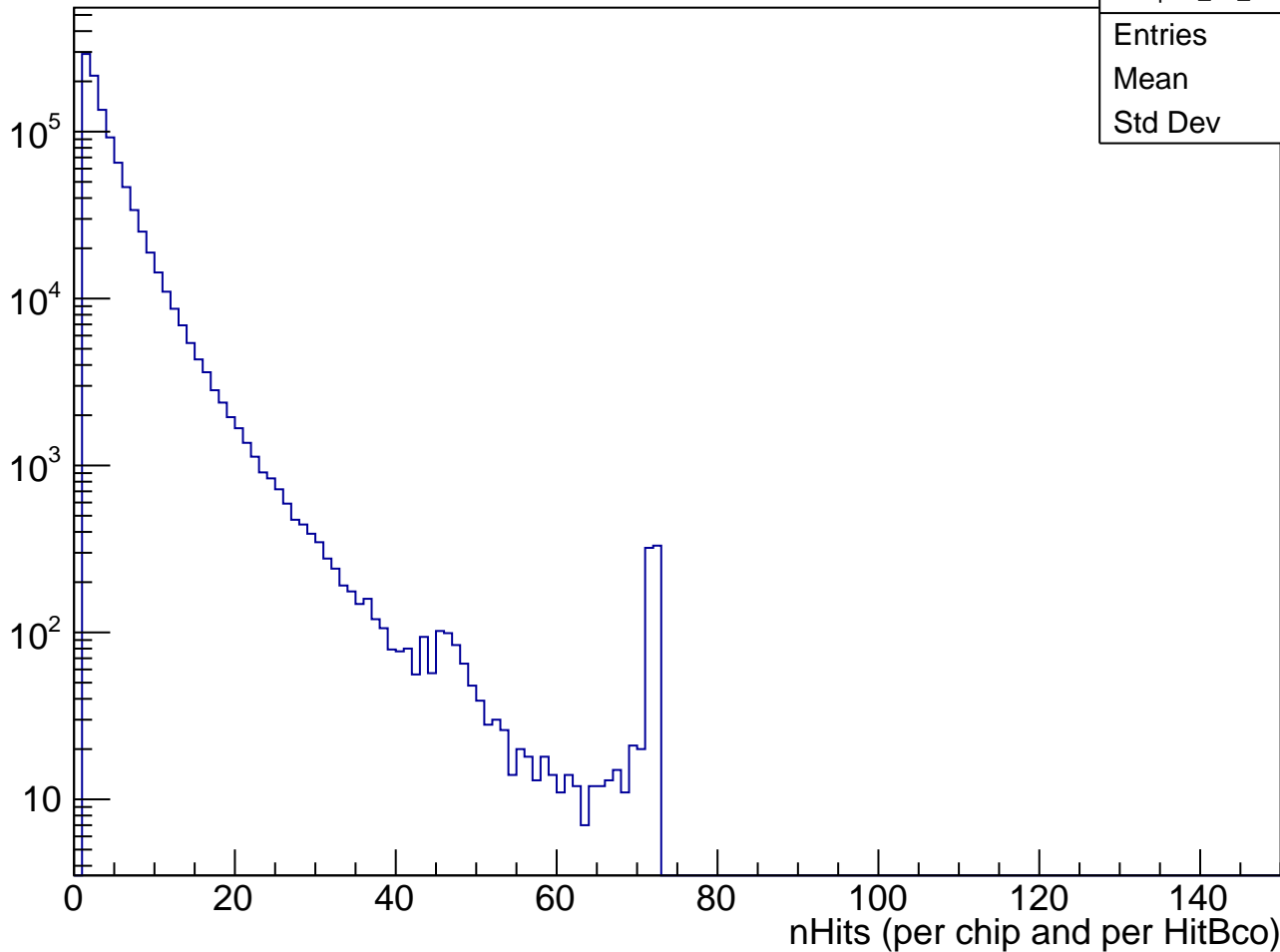
Entries



nChipHit_F7_Fch2_Chip16	
Entries	980745
Mean	3.883
Std Dev	4.476

nChipHit_F7_Fch2_Chip17

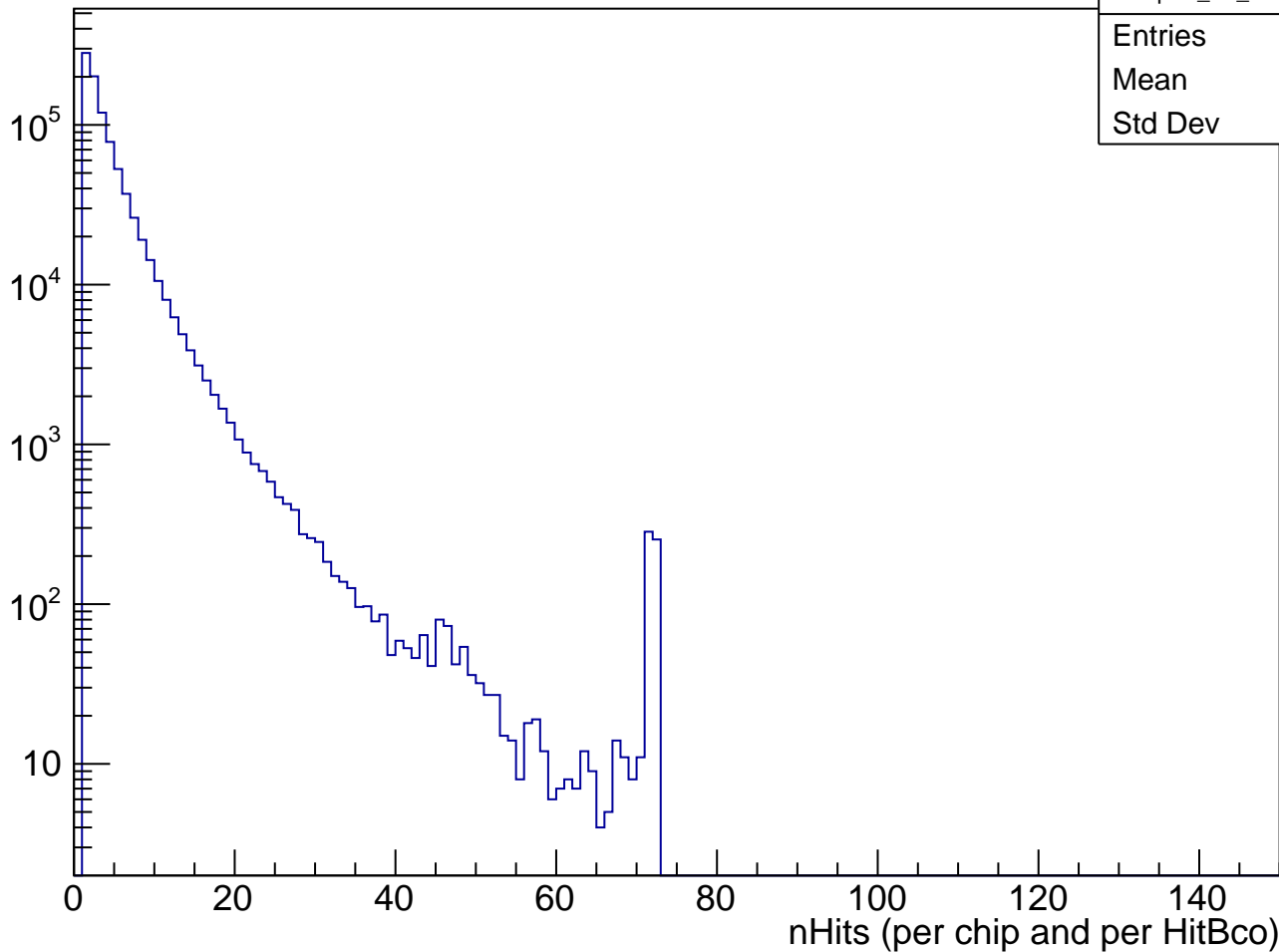
Entries



nChipHit_F7_Fch2_Chip17	
Entries	998808
Mean	3.863
Std Dev	4.509

nChipHit_F7_Fch2_Chip18

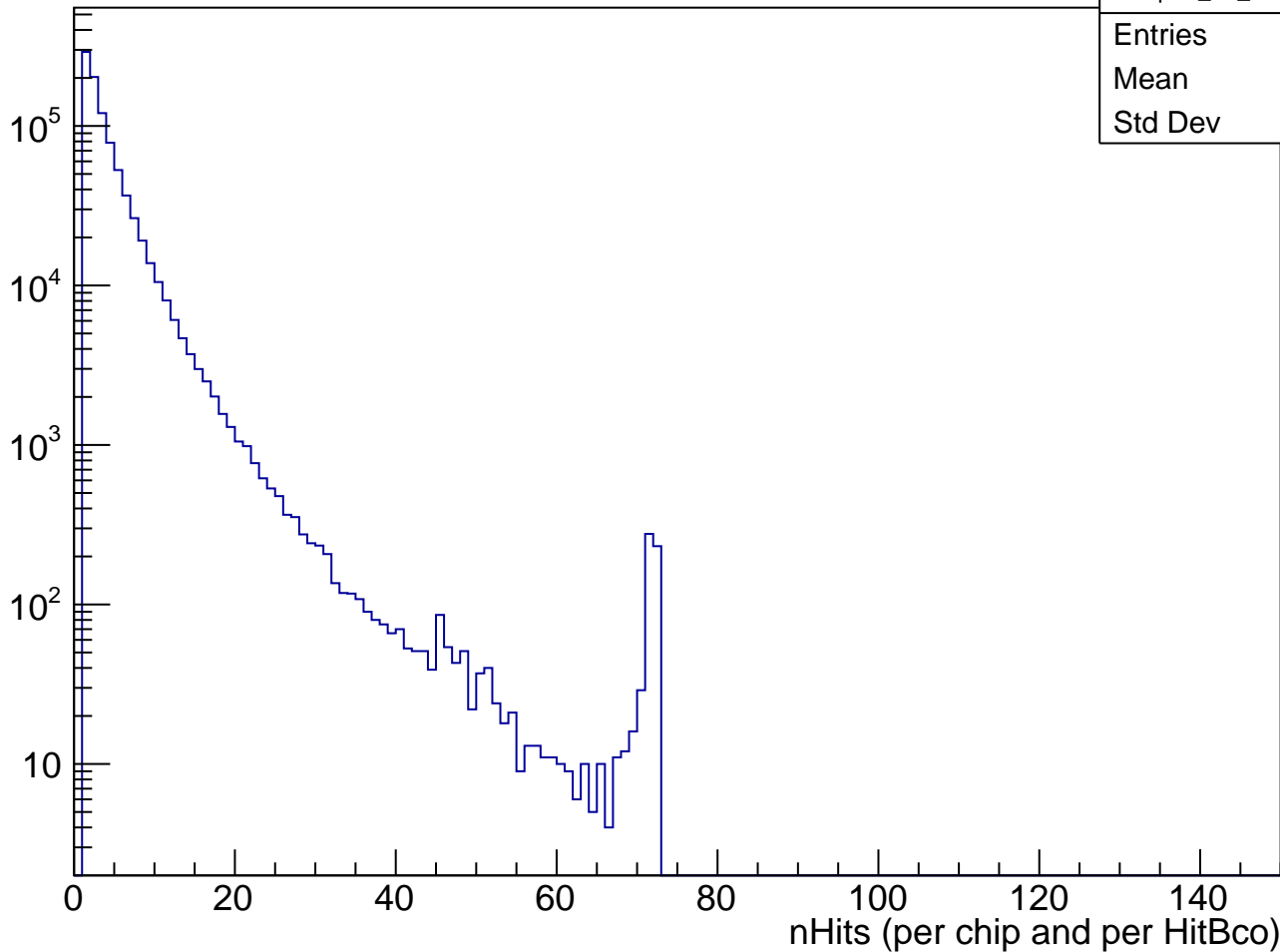
Entries



nChipHit_F7_Fch2_Chip18	
Entries	883226
Mean	3.54
Std Dev	4.185

nChipHit_F7_Fch2_Chip19

Entries

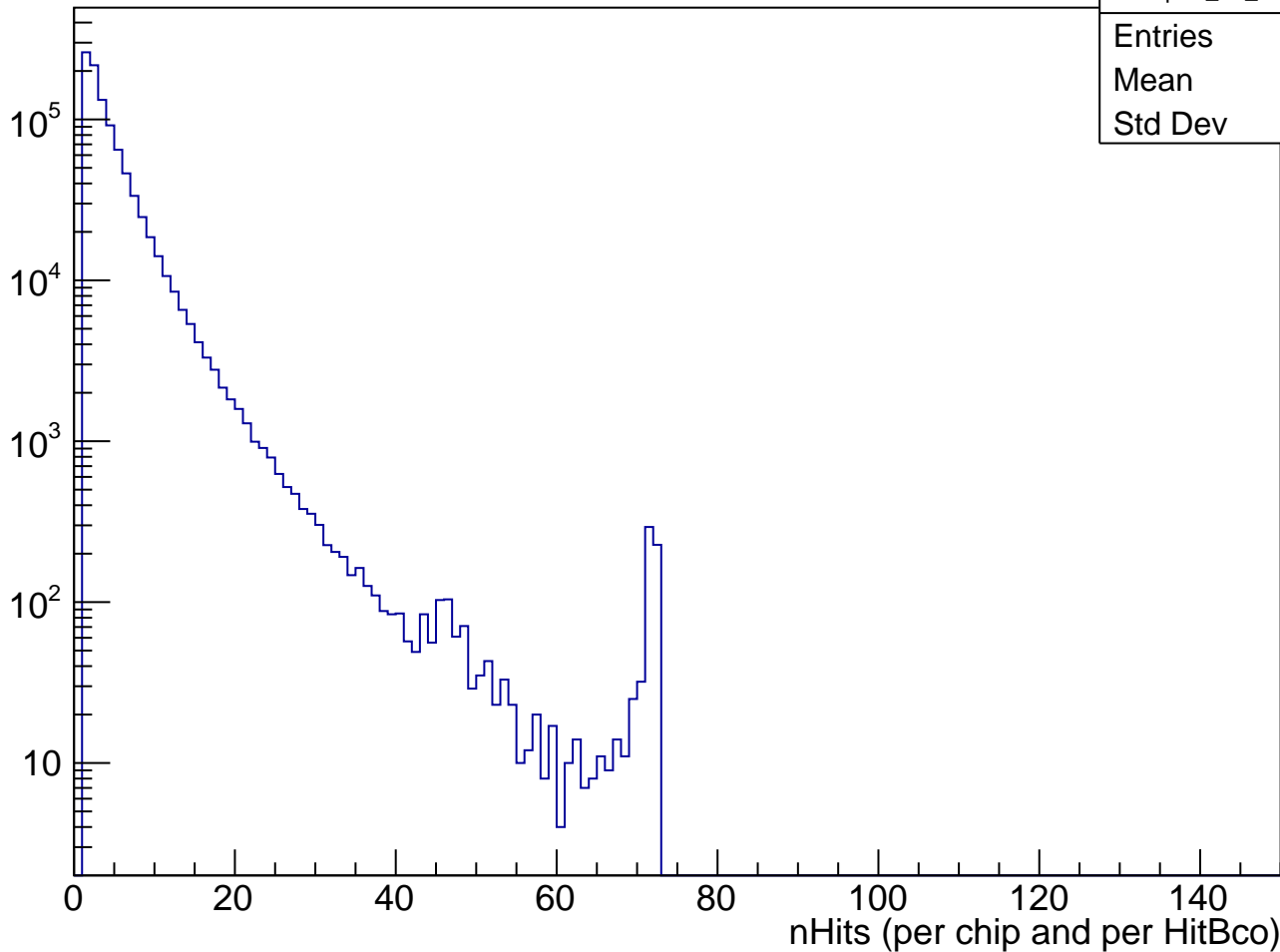


nChipHit_F7_Fch2_Chip19	
Entries	893454
Mean	3.492
Std Dev	4.136

nHits (per chip and per HitBco)

nChipHit_F7_Fch2_Chip2

Entries



nChipHit_F7_Fch2_Chip2

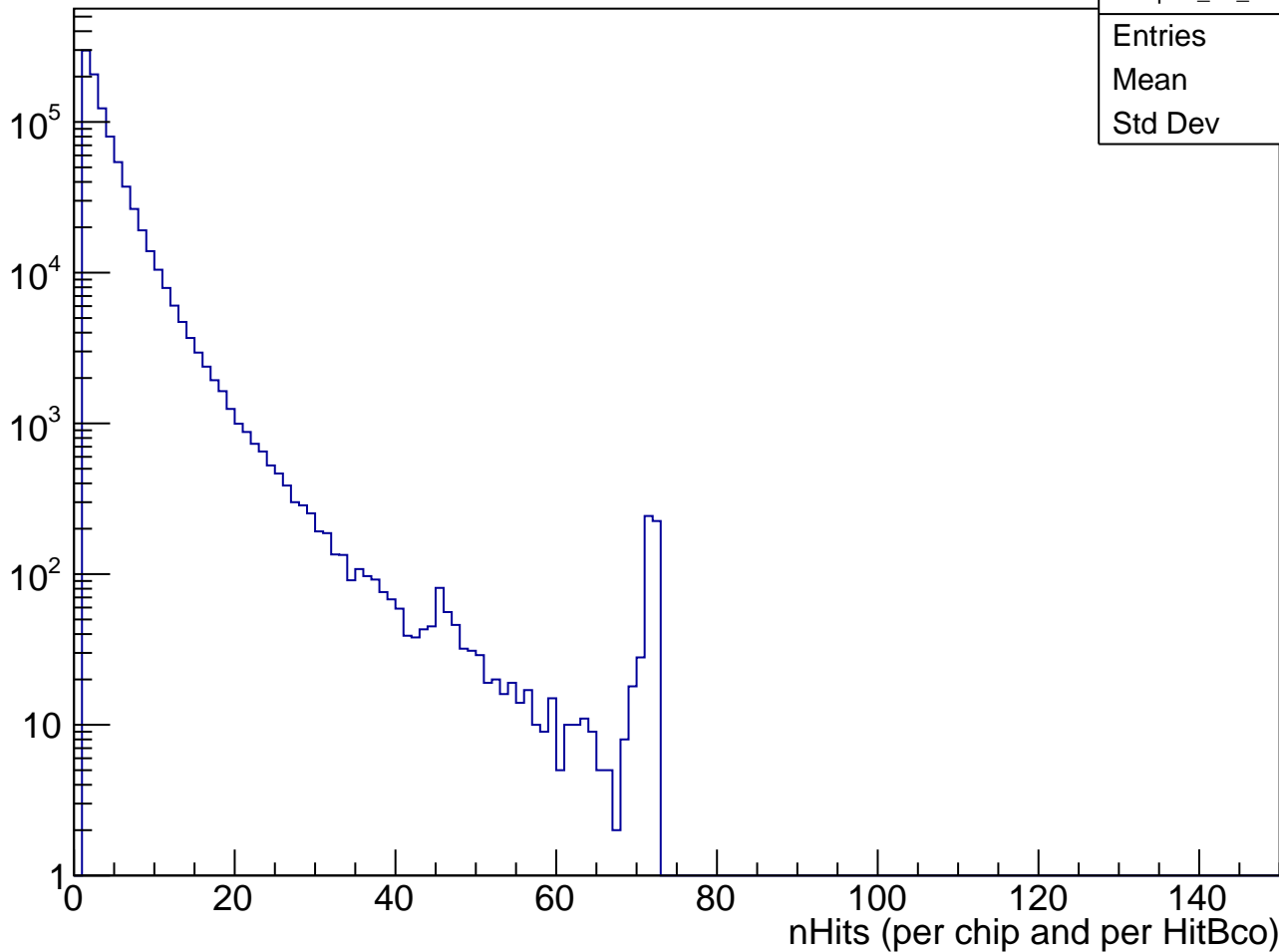
Entries 960951

Mean 3.896

Std Dev 4.41

nChipHit_F7_Fch2_Chip20

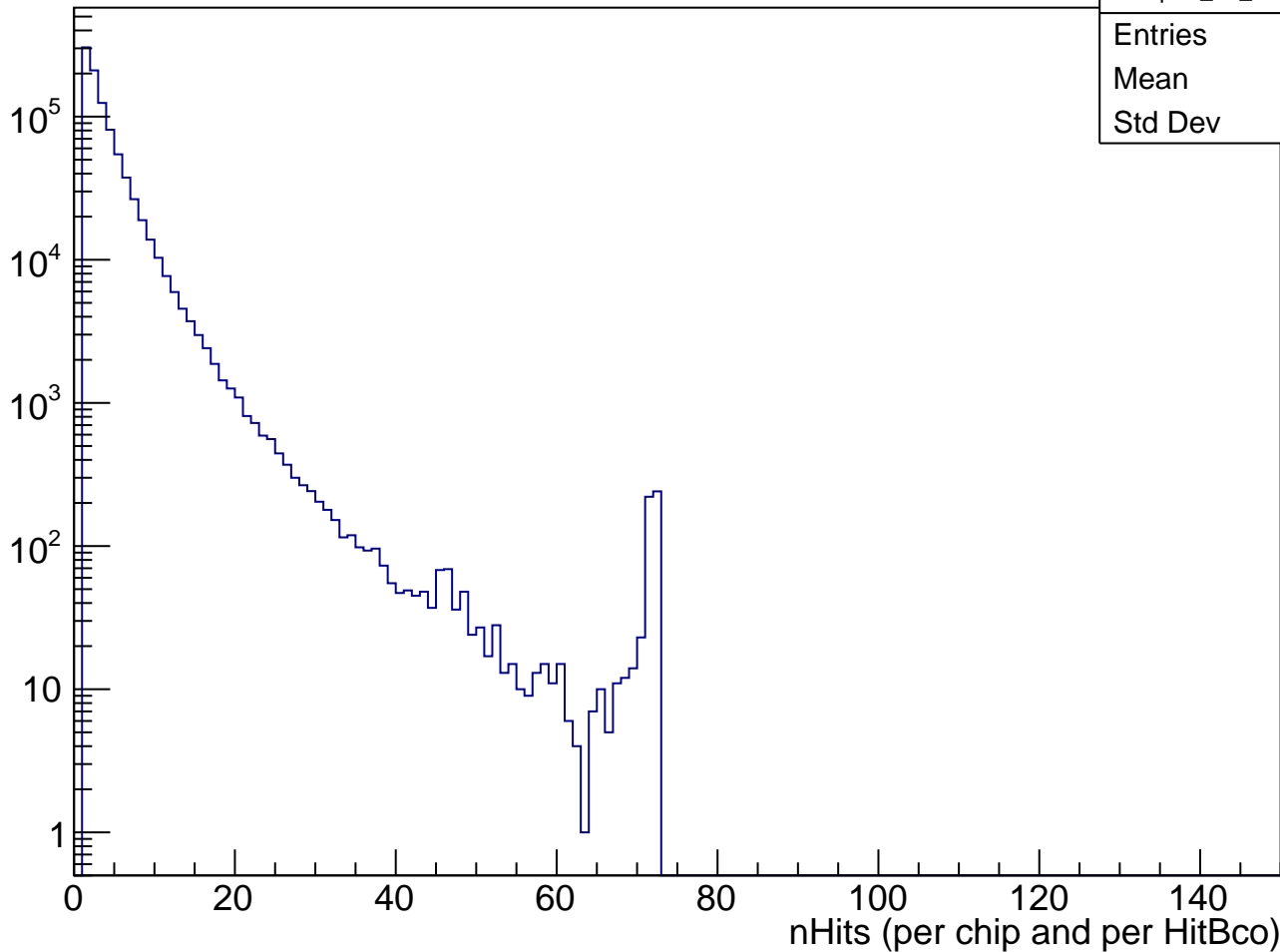
Entries



nChipHit_F7_Fch2_Chip20	
Entries	909769
Mean	3.453
Std Dev	4.038

nChipHit_F7_Fch2_Chip21

Entries

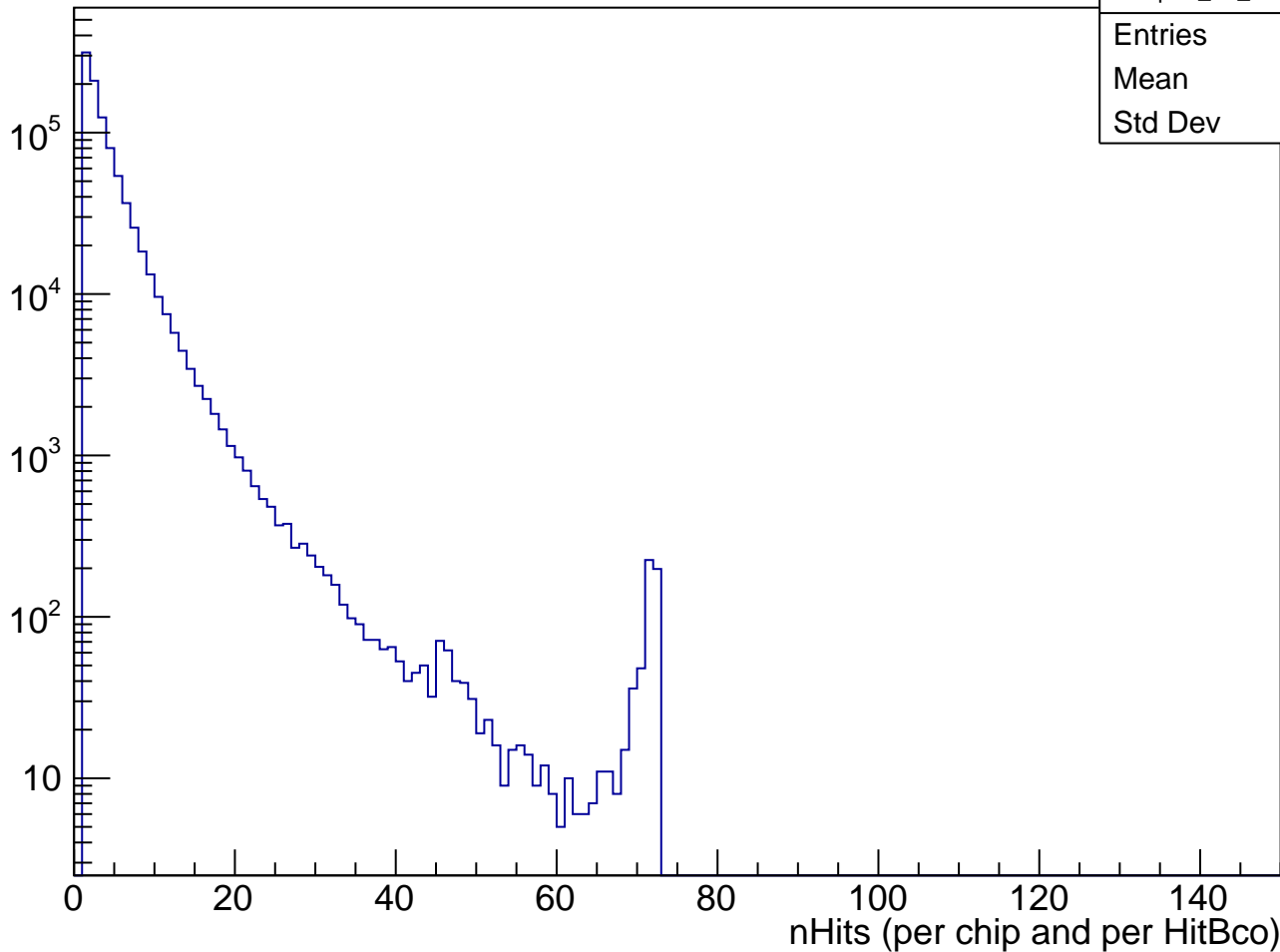


nChipHit_F7_Fch2_Chip21	
Entries	921836
Mean	3.419
Std Dev	3.992

nHits (per chip and per HitBco)

nChipHit_F7_Fch2_Chip22

Entries

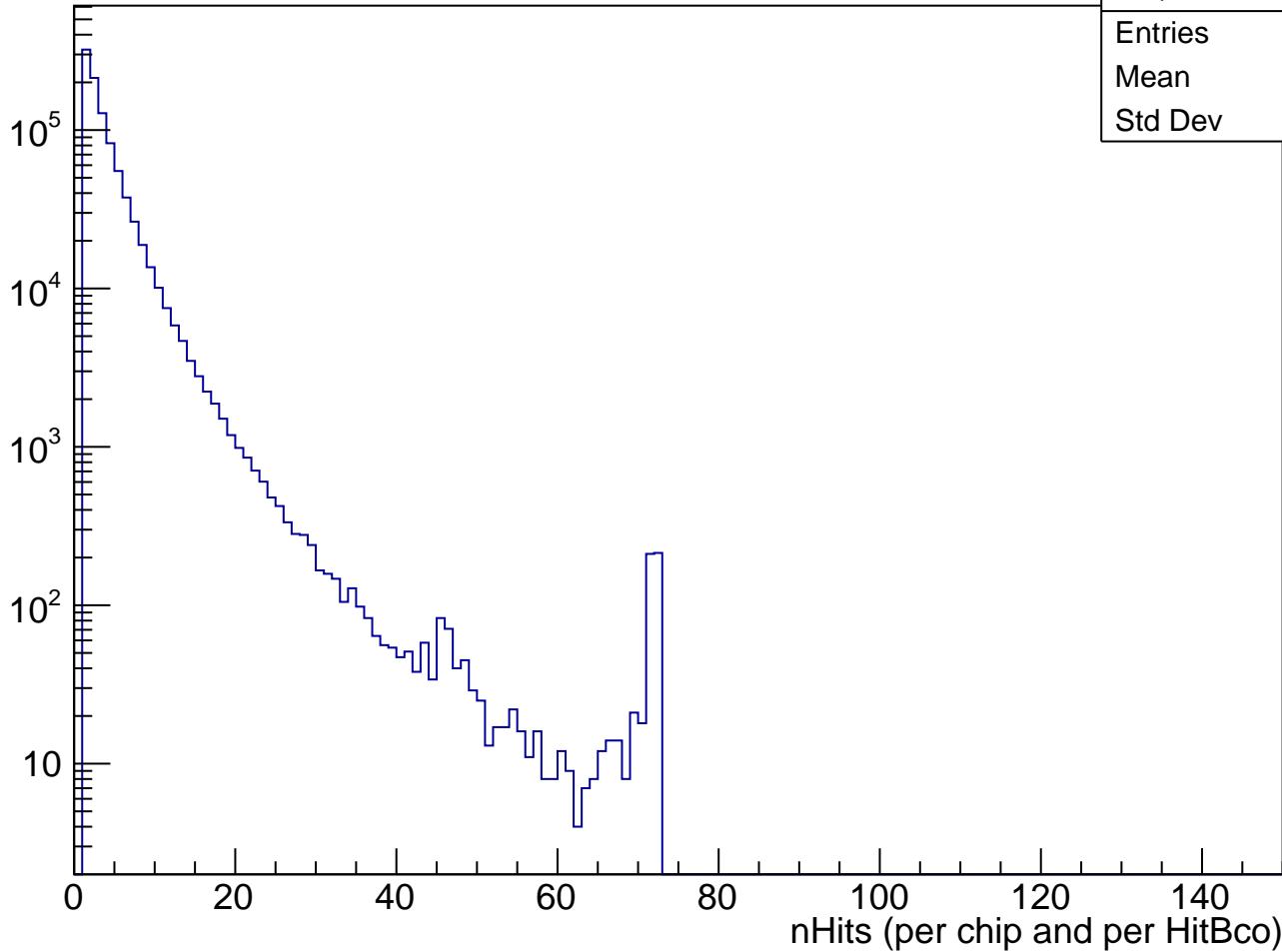


nChipHit_F7_Fch2_Chip22	
Entries	923105
Mean	3.35
Std Dev	3.932

nHits (per chip and per HitBco)

nChipHit_F7_Fch2_Chip23

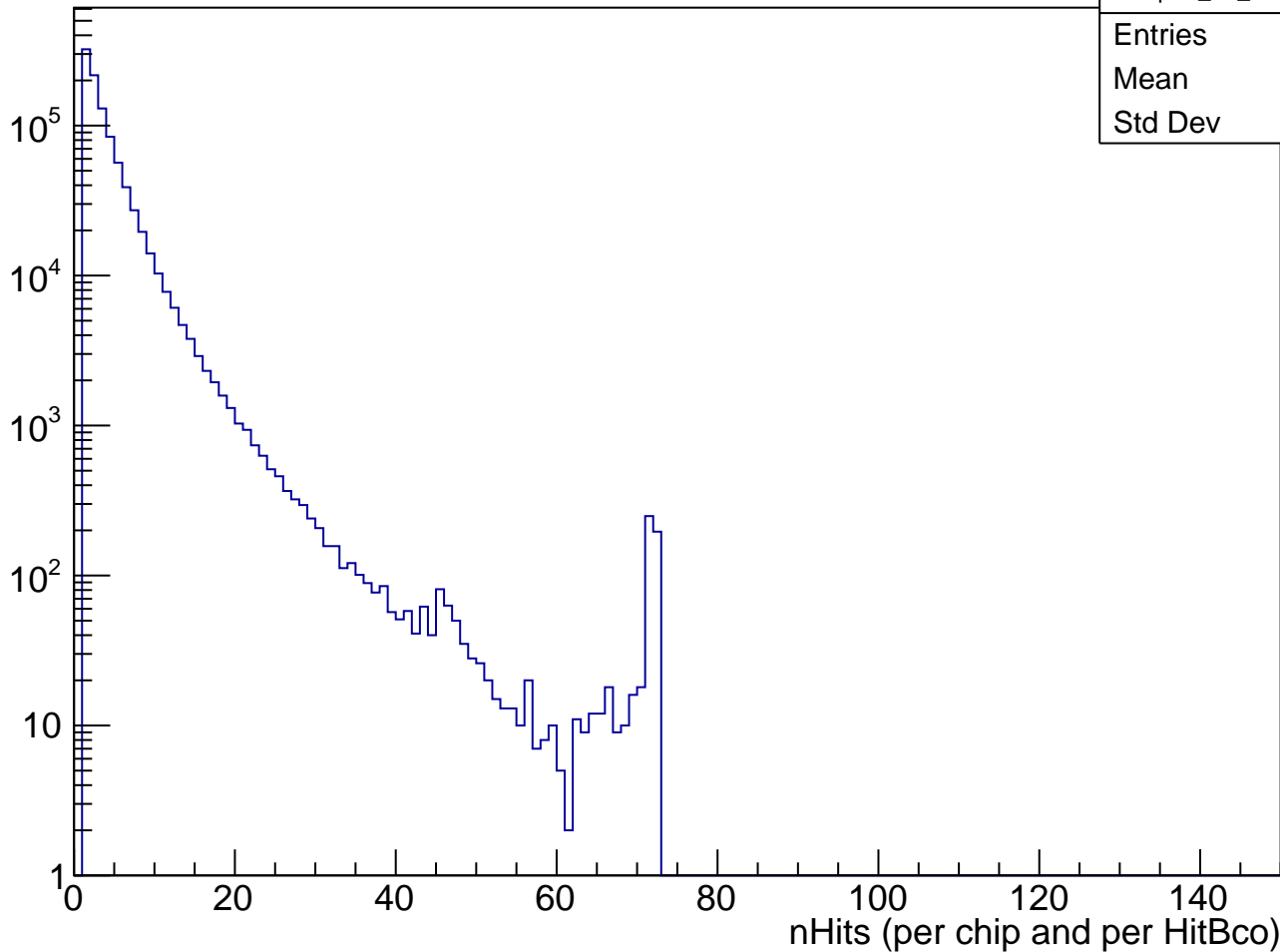
Entries



nChipHit_F7_Fch2_Chip23	
Entries	945905
Mean	3.349
Std Dev	3.905

nChipHit_F7_Fch2_Chip24

Entries

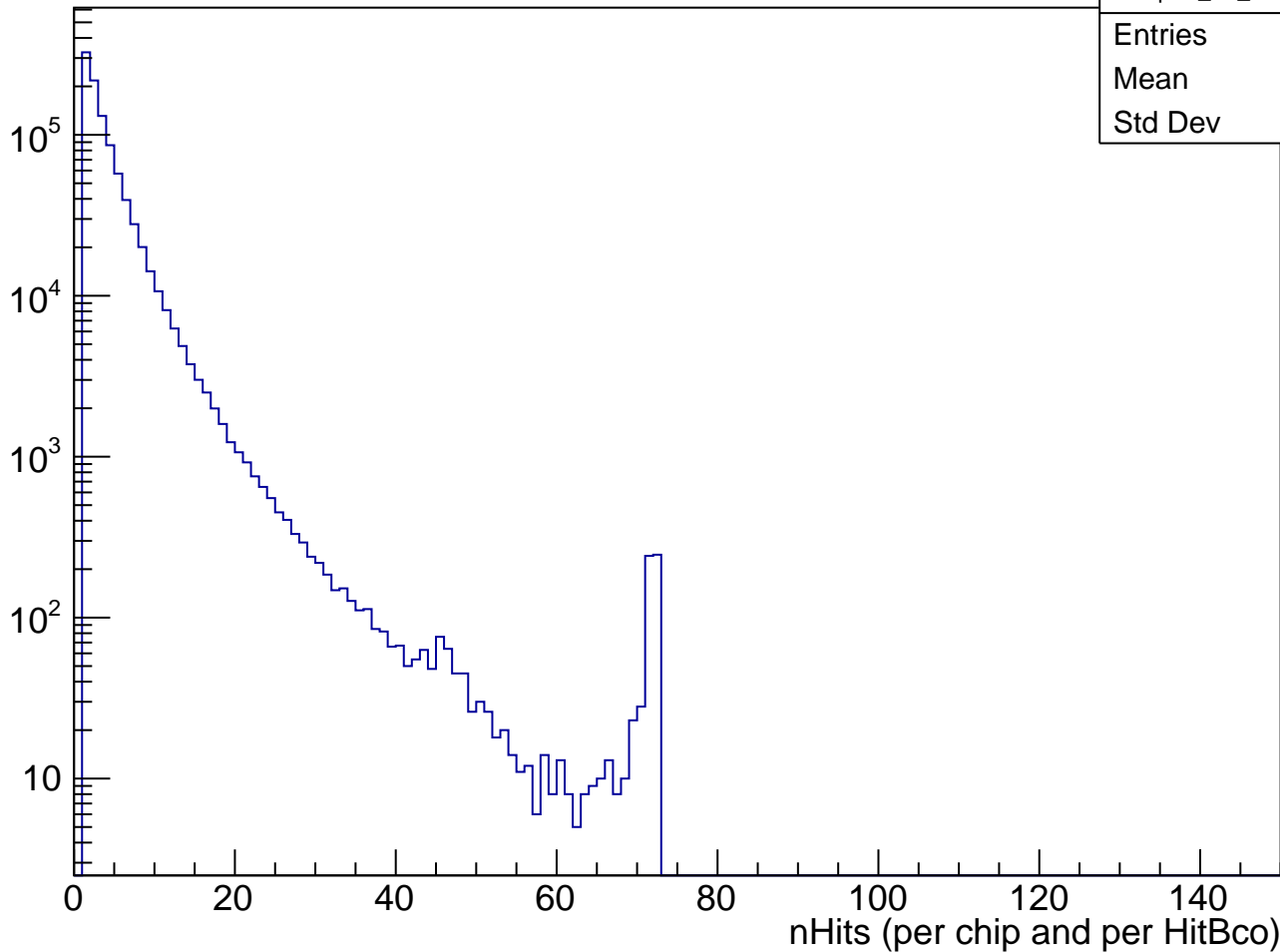


nChipHit_F7_Fch2_Chip24	
Entries	960546
Mean	3.386
Std Dev	3.945

nHits (per chip and per HitBco)

nChipHit_F7_Fch2_Chip25

Entries

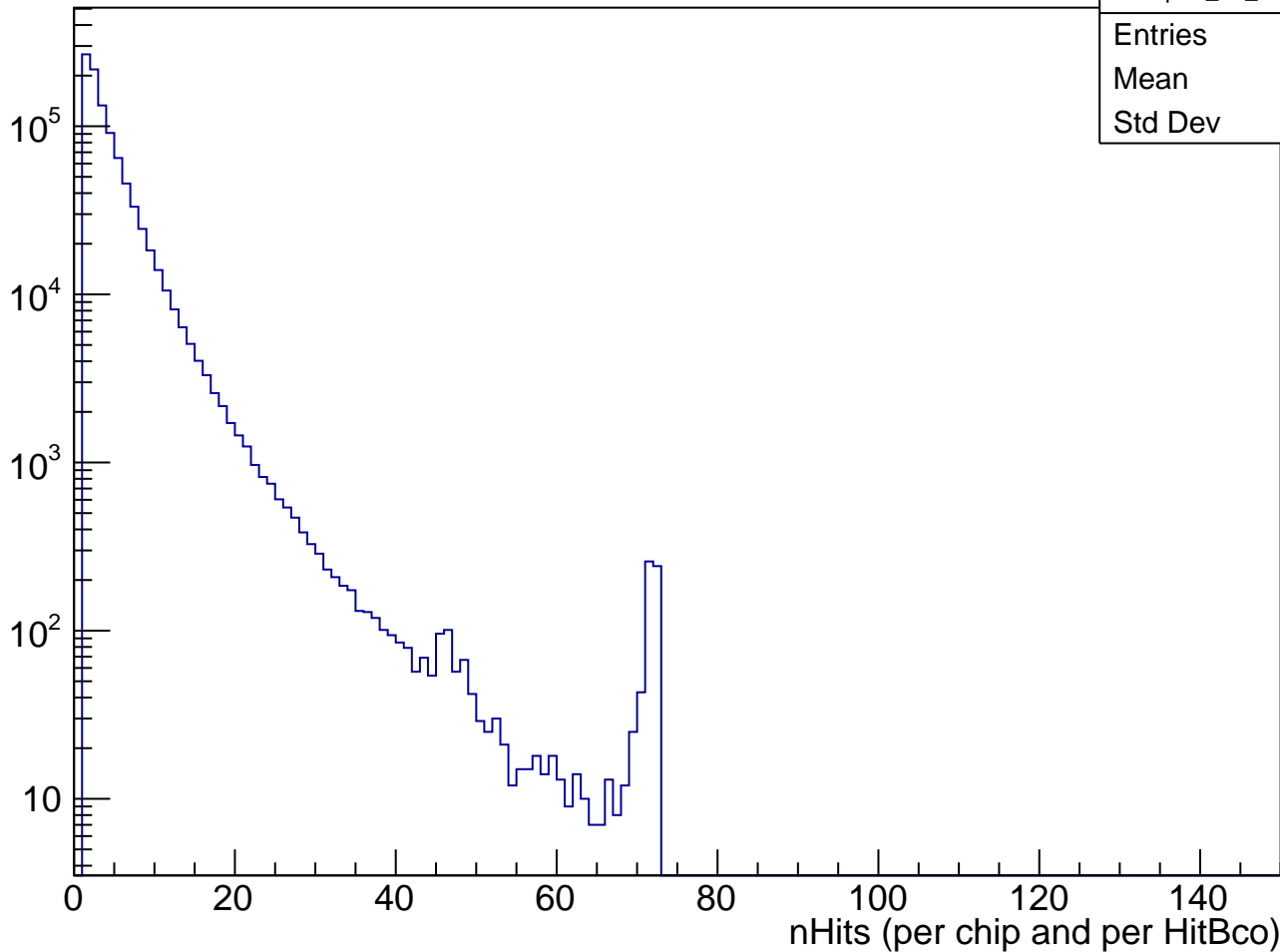


nChipHit_F7_Fch2_Chip25	
Entries	971719
Mean	3.411
Std Dev	4.008

nHits (per chip and per HitBco)

nChipHit_F7_Fch2_Chip3

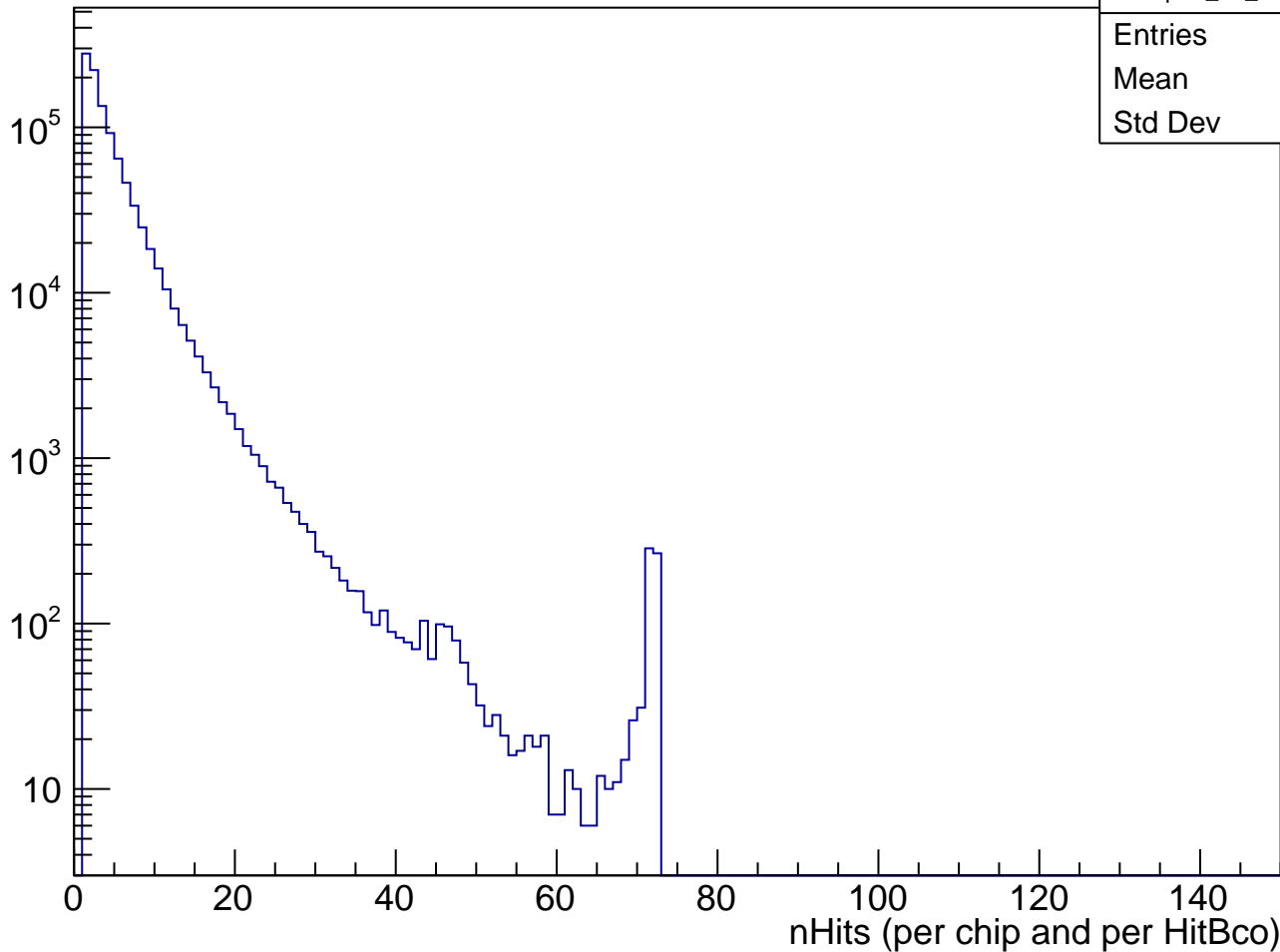
Entries



nChipHit_F7_Fch2_Chip3	
Entries	964693
Mean	3.848
Std Dev	4.368

nChipHit_F7_Fch2_Chip4

Entries



nChipHit_F7_Fch2_Chip4

Entries 985093

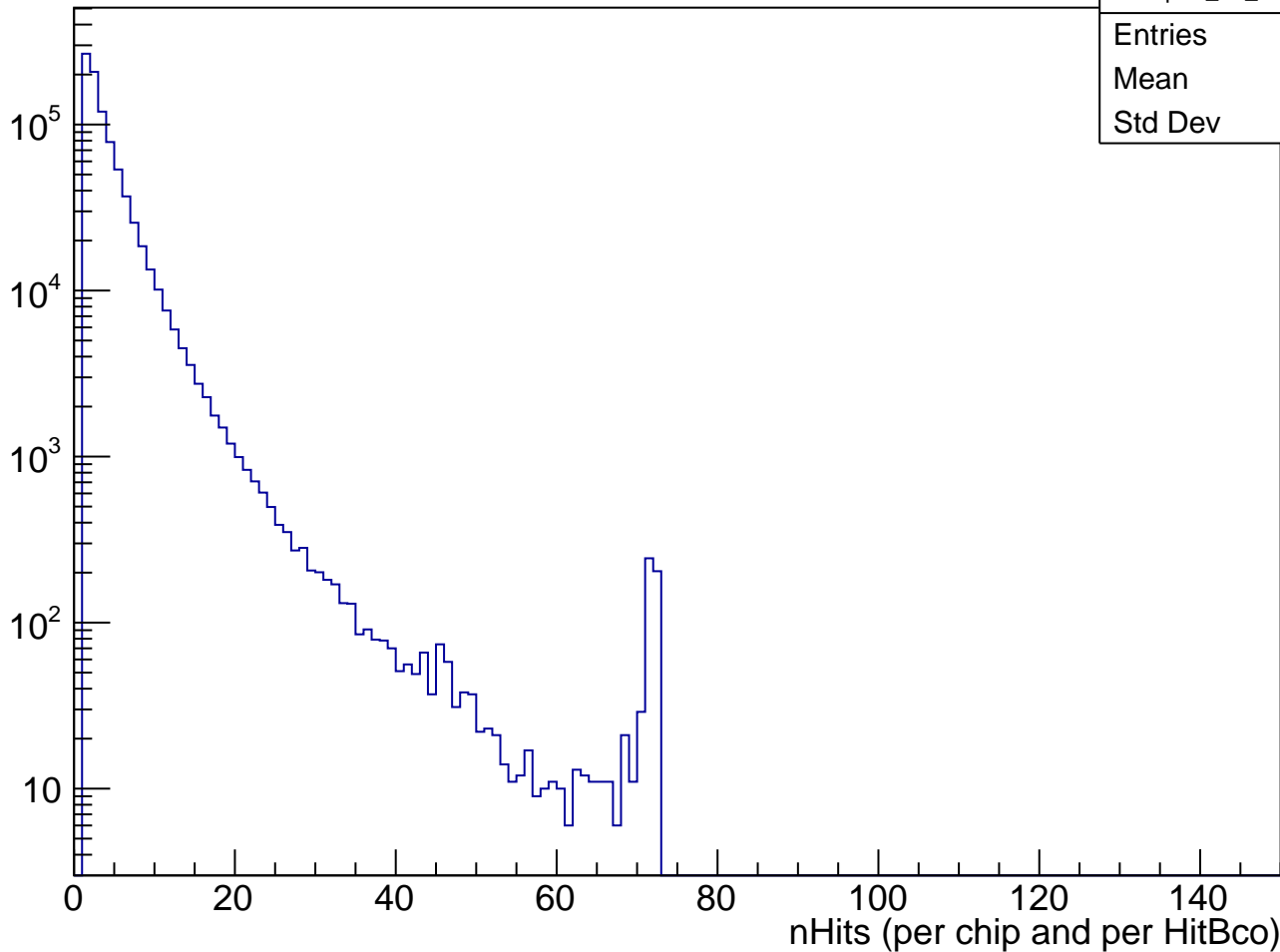
Mean 3.825

Std Dev 4.389

nHits (per chip and per HitBco)

nChipHit_F7_Fch2_Chip5

Entries



nChipHit_F7_Fch2_Chip5

Entries 869279

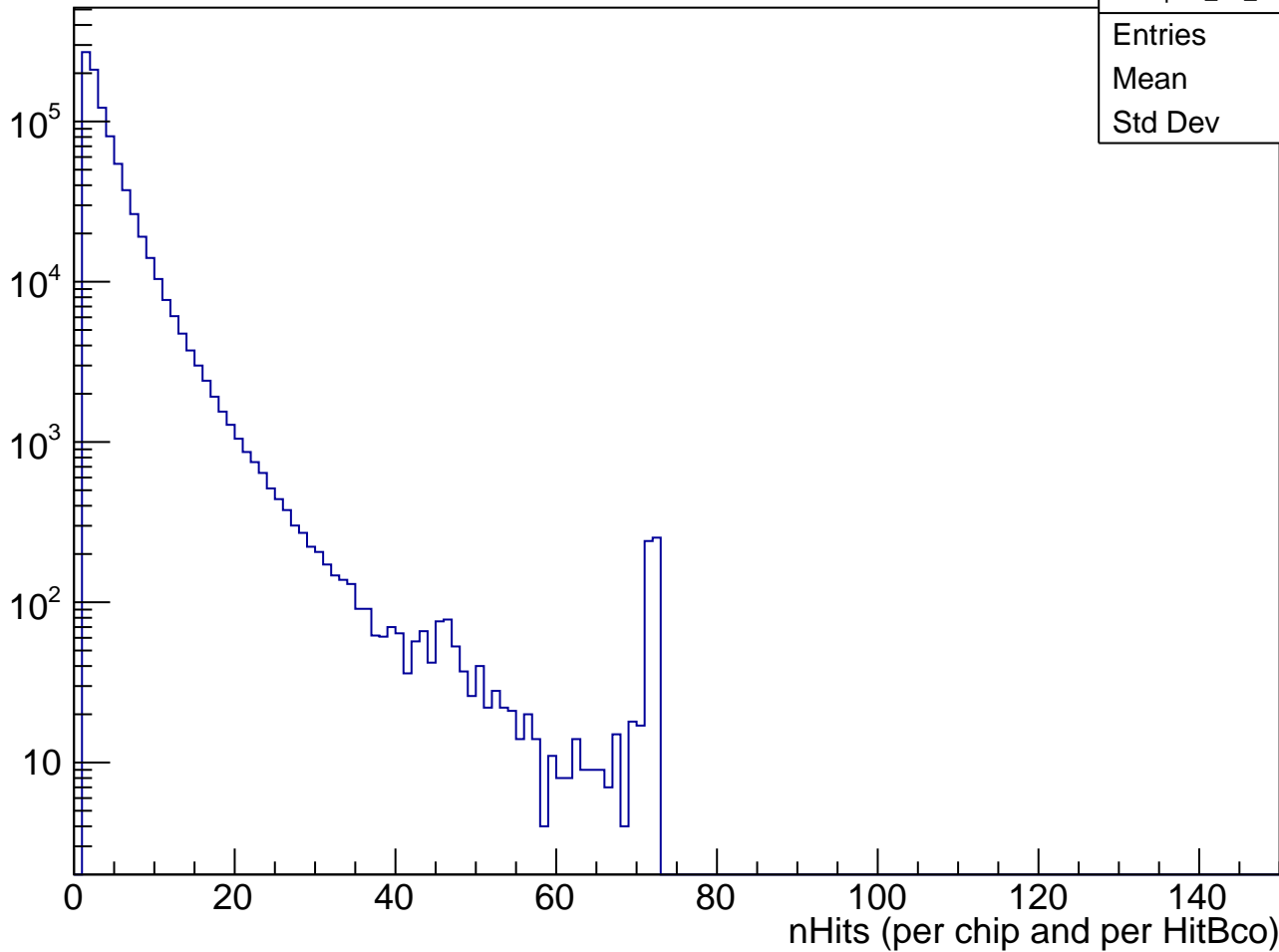
Mean 3.501

Std Dev 4.055

nHits (per chip and per HitBco)

nChipHit_F7_Fch2_Chip6

Entries



nChipHit_F7_Fch2_Chip6

Entries 885834

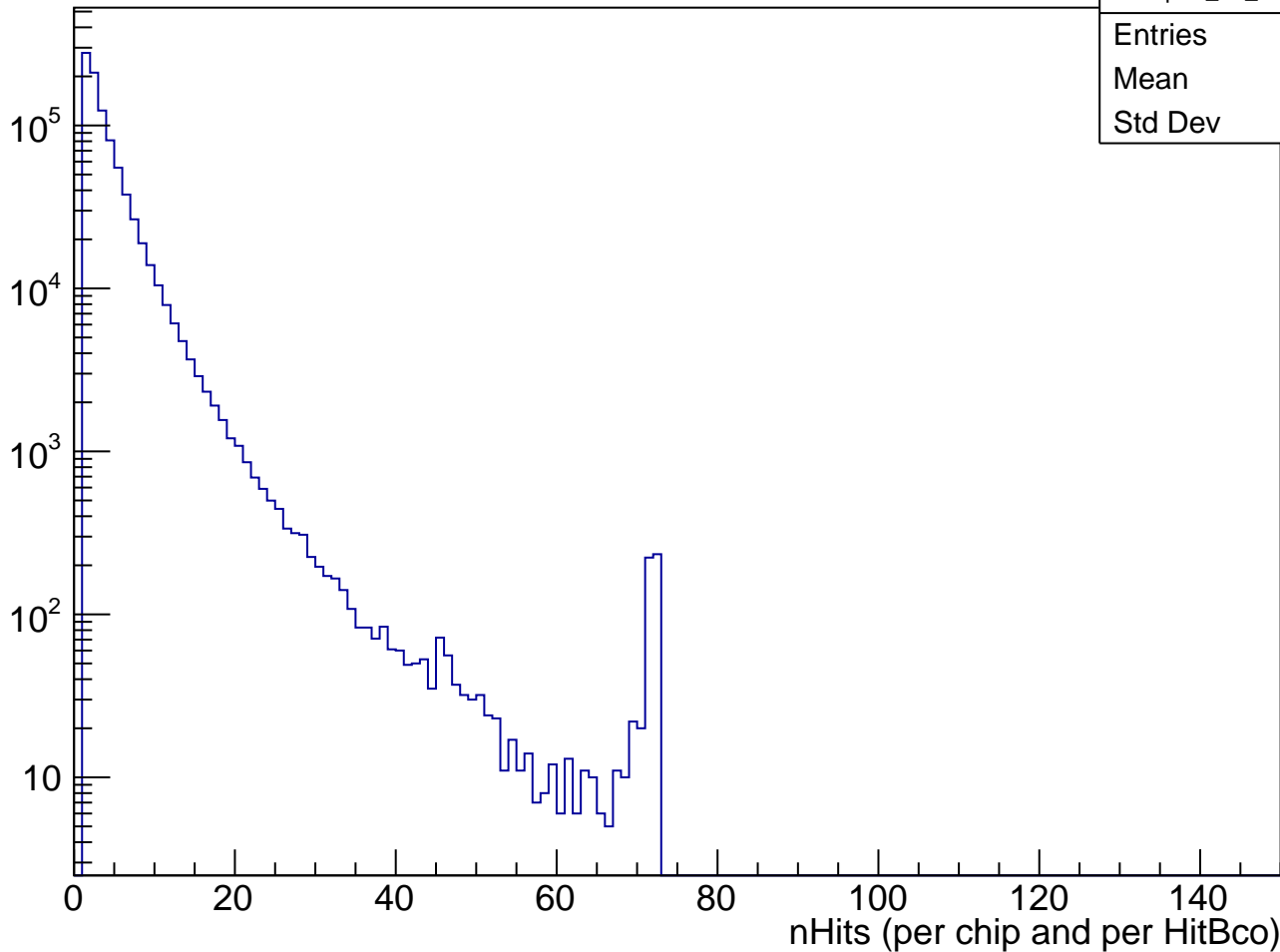
Mean 3.529

Std Dev 4.104

nHits (per chip and per HitBco)

nChipHit_F7_Fch2_Chip7

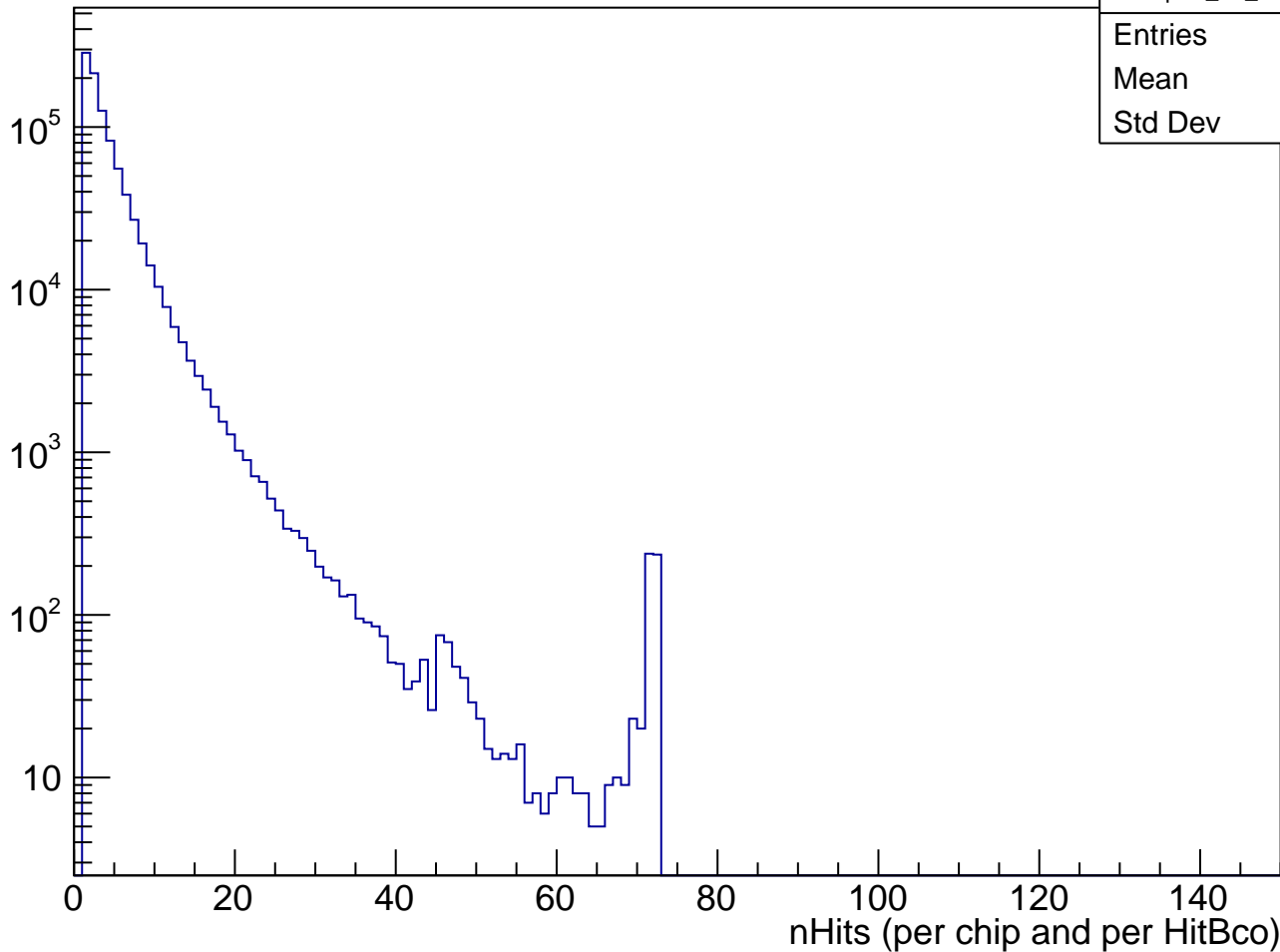
Entries



nChipHit_F7_Fch2_Chip7	
Entries	896840
Mean	3.494
Std Dev	4.028

nChipHit_F7_Fch2_Chip8

Entries



nChipHit_F7_Fch2_Chip8

Entries 913455

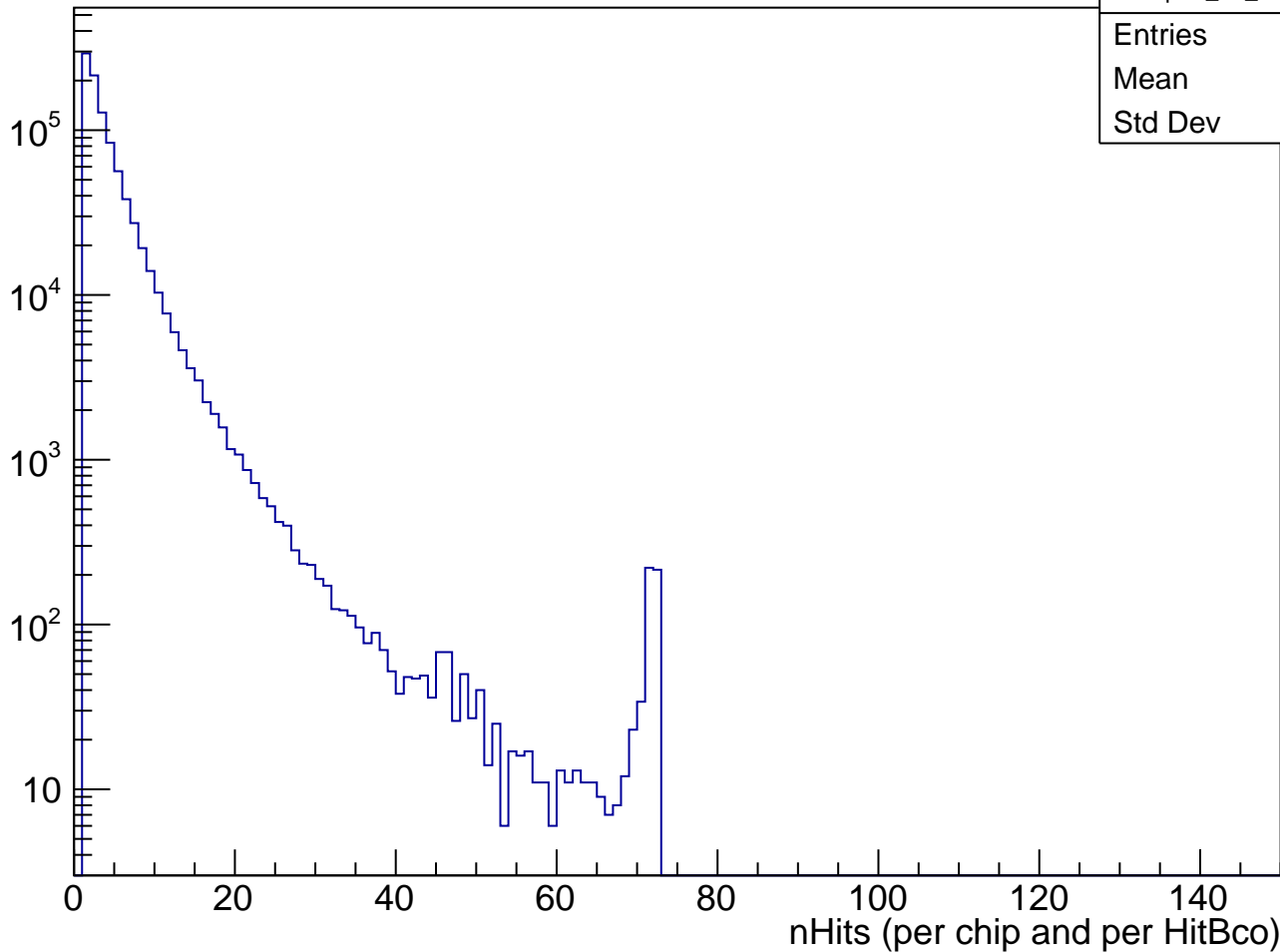
Mean 3.477

Std Dev 4.01

nHits (per chip and per HitBco)

nChipHit_F7_Fch2_Chip9

Entries



nChipHit_F7_Fch2_Chip9

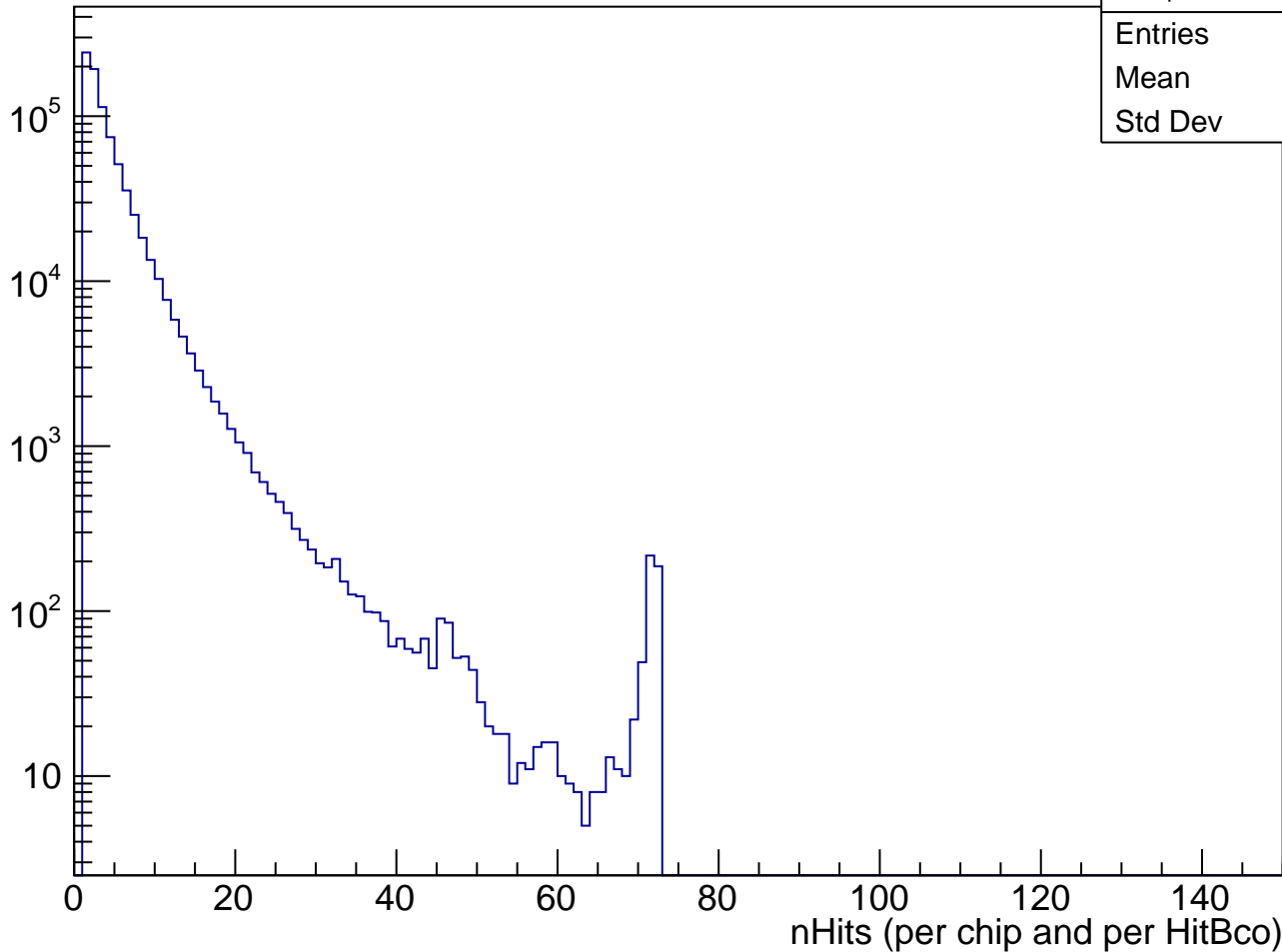
Entries 924052

Mean 3.449

Std Dev 3.97

nChipHit_F7_Fch3_Chip0

Entries



nChipHit_F7_Fch3_Chip0

Entries 818448

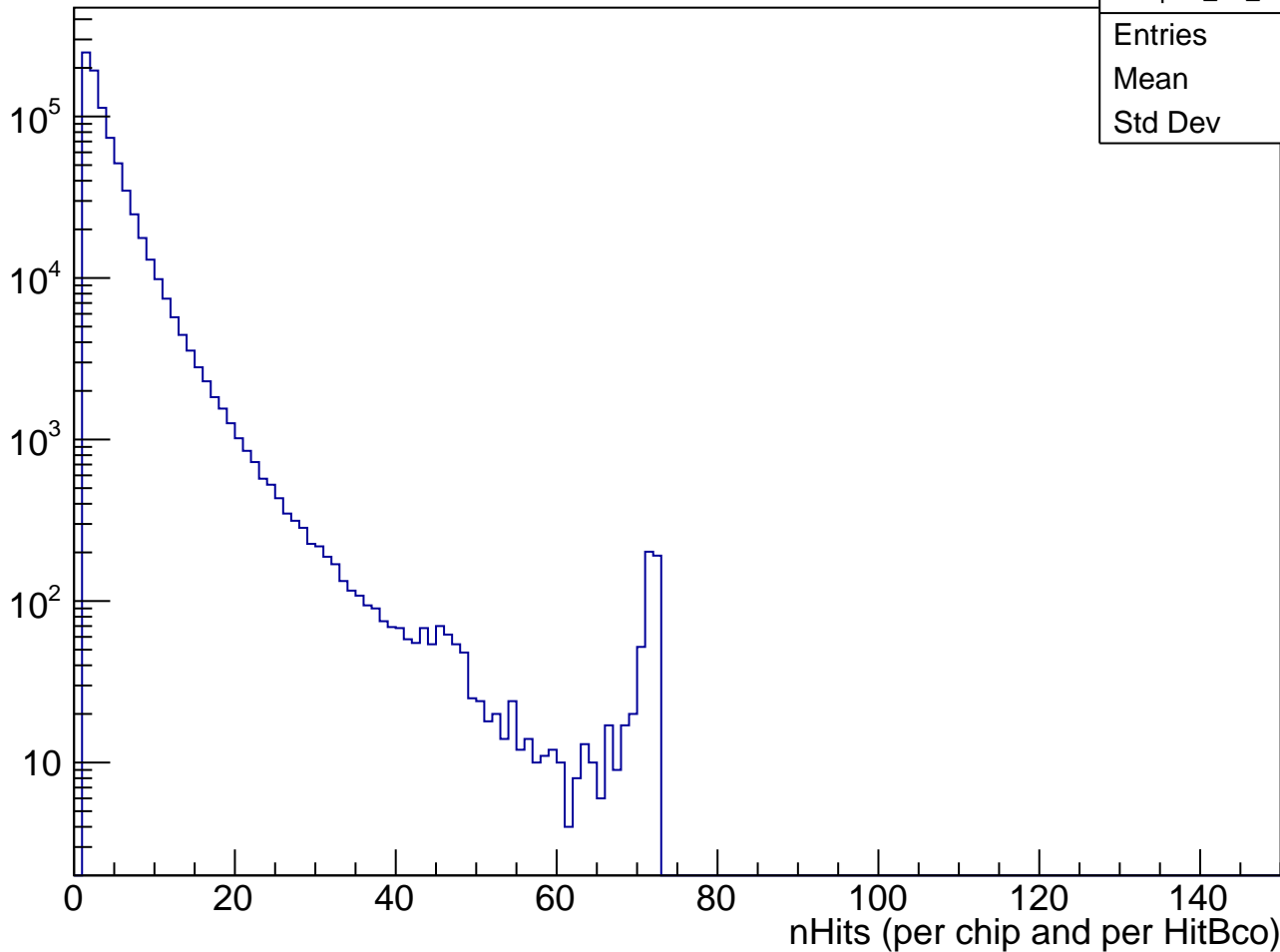
Mean 3.617

Std Dev 4.214

nHits (per chip and per HitBco)

nChipHit_F7_Fch3_Chip1

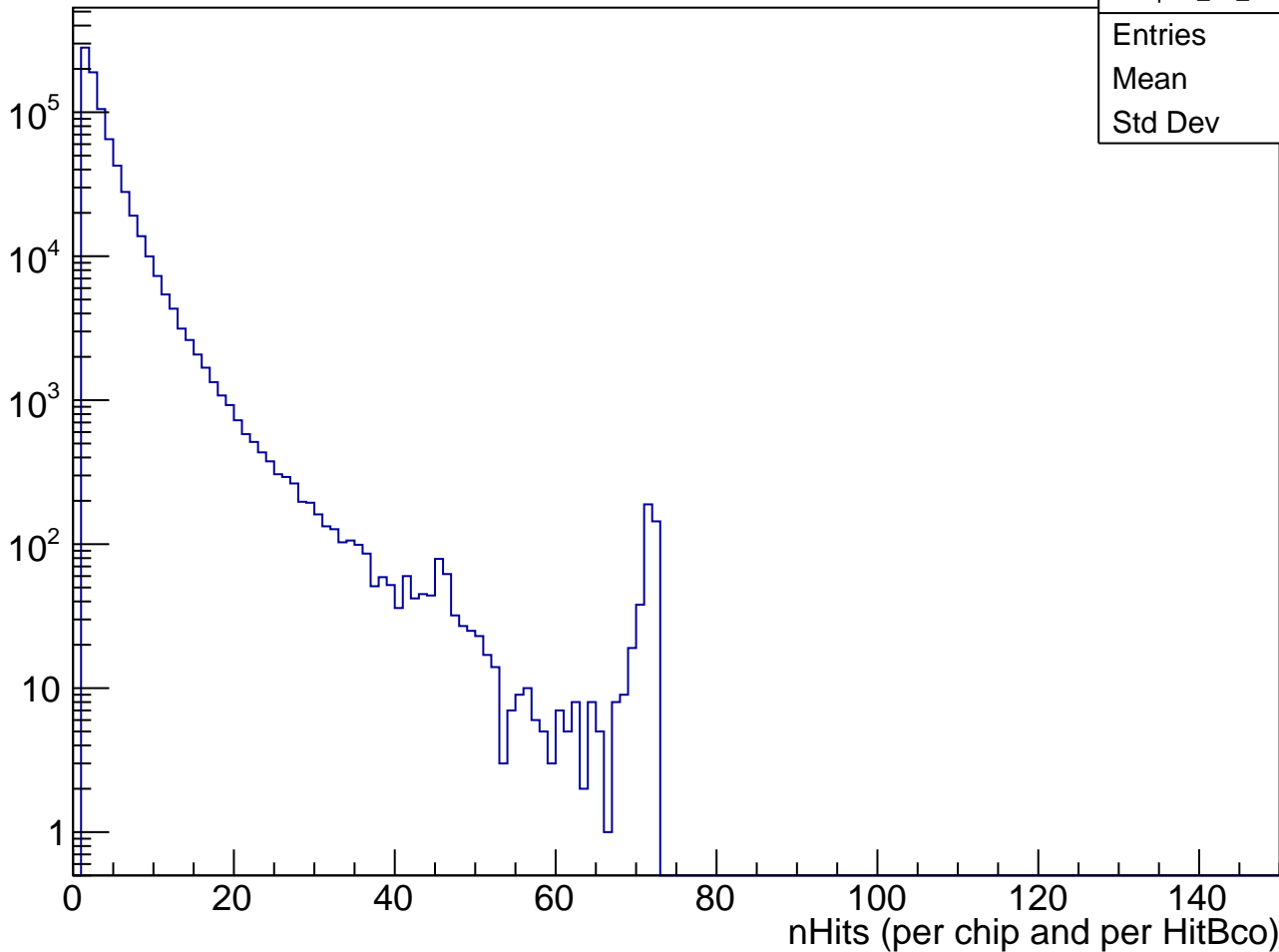
Entries



nChipHit_F7_Fch3_Chip1	
Entries	818494
Mean	3.568
Std Dev	4.165

nChipHit_F7_Fch3_Chip10

Entries

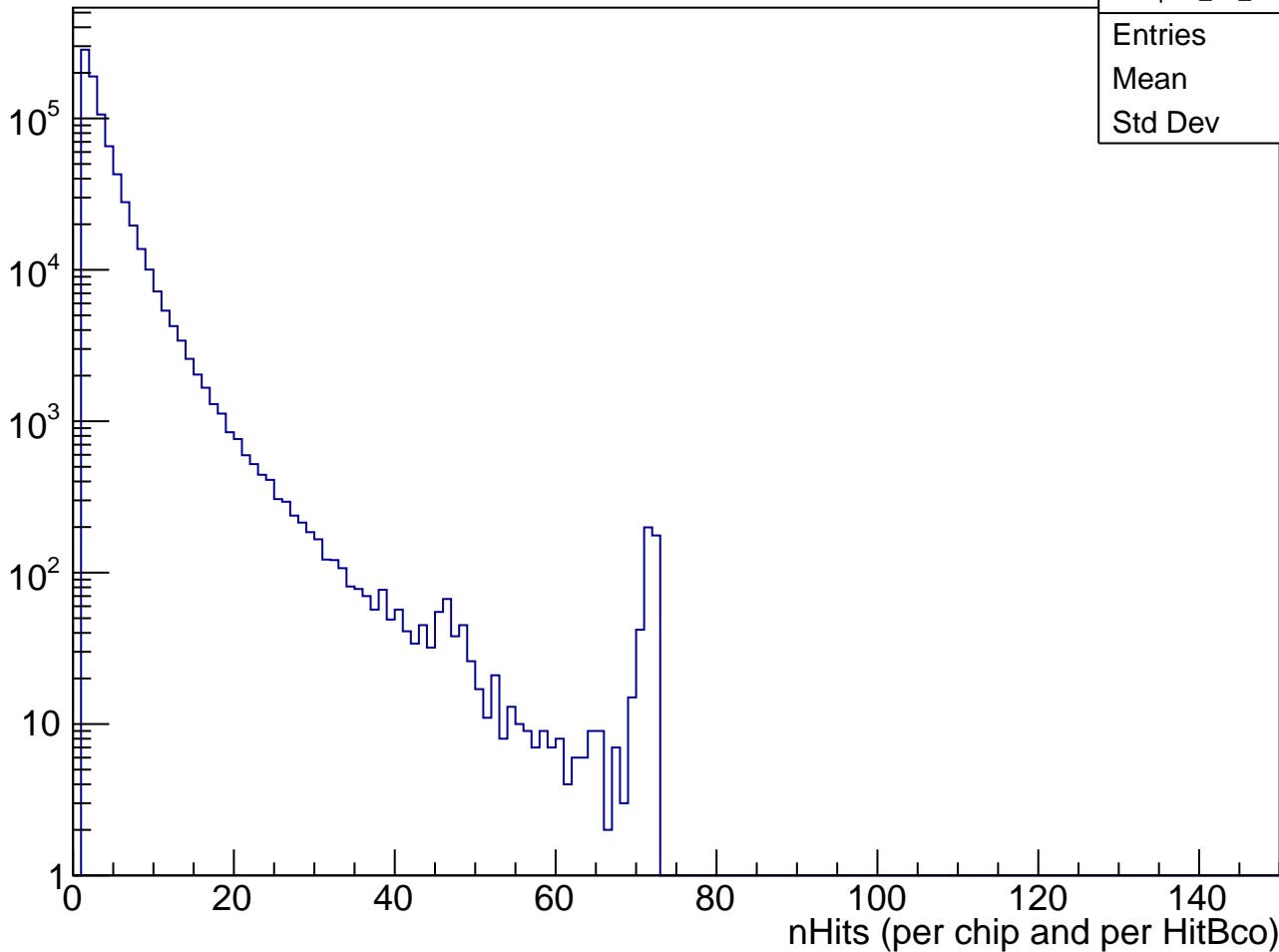


nChipHit_F7_Fch3_Chip10	
Entries	789837
Mean	3.176
Std Dev	3.794

nHits (per chip and per HitBco)

nChipHit_F7_Fch3_Chip11

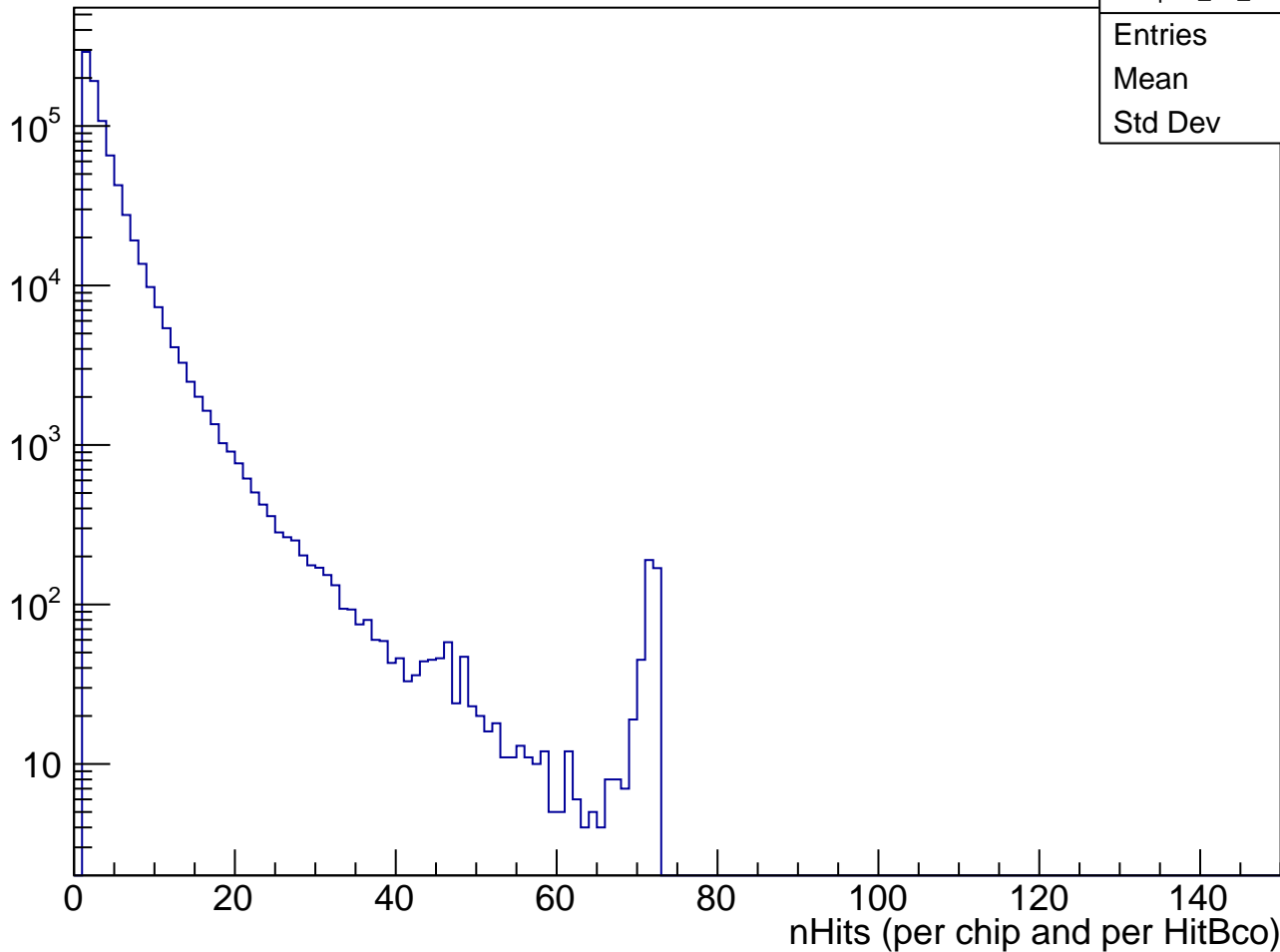
Entries



nChipHit_F7_Fch3_Chip11	
Entries	795277
Mean	3.174
Std Dev	3.817

nChipHit_F7_Fch3_Chip12

Entries

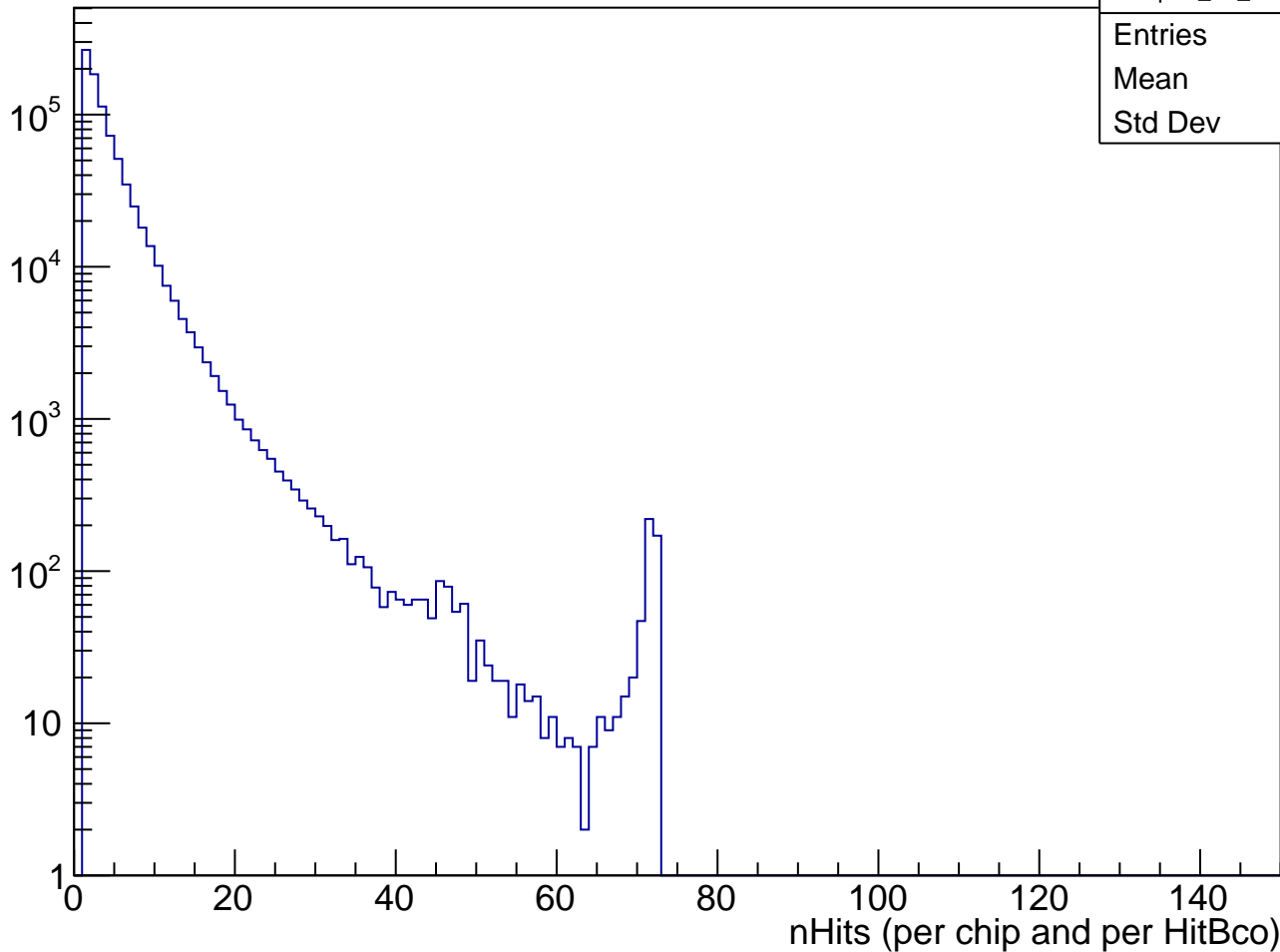


nChipHit_F7_Fch3_Chip12	
Entries	803838
Mean	3.138
Std Dev	3.779

nHits (per chip and per HitBco)

nChipHit_F7_Fch3_Chip13

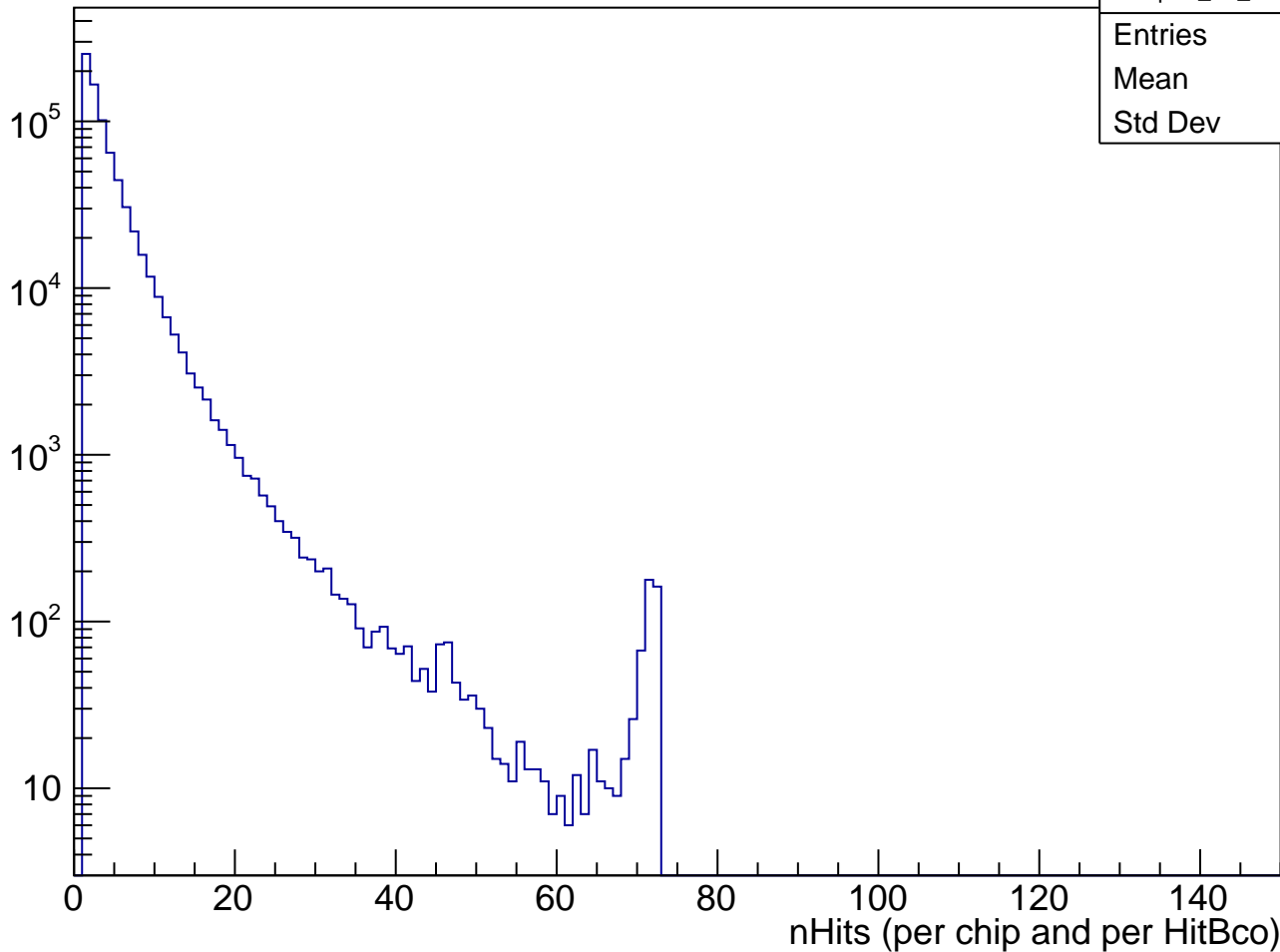
Entries



nChipHit_F7_Fch3_Chip13	
Entries	828634
Mean	3.557
Std Dev	4.19

nChipHit_F7_Fch3_Chip14

Entries

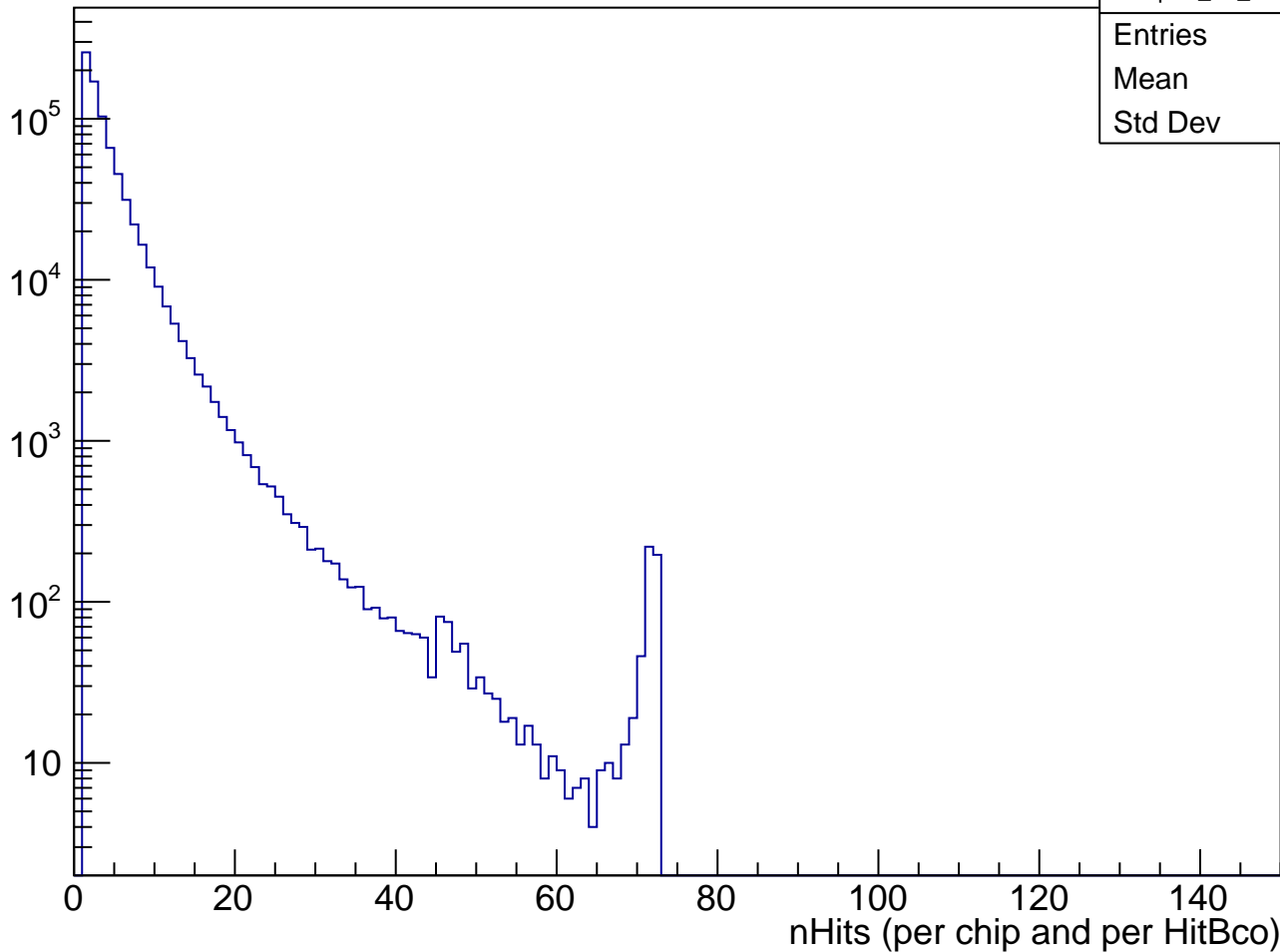


nChipHit_F7_Fch3_Chip14	
Entries	755057
Mean	3.491
Std Dev	4.2

nHits (per chip and per HitBco)

nChipHit_F7_Fch3_Chip15

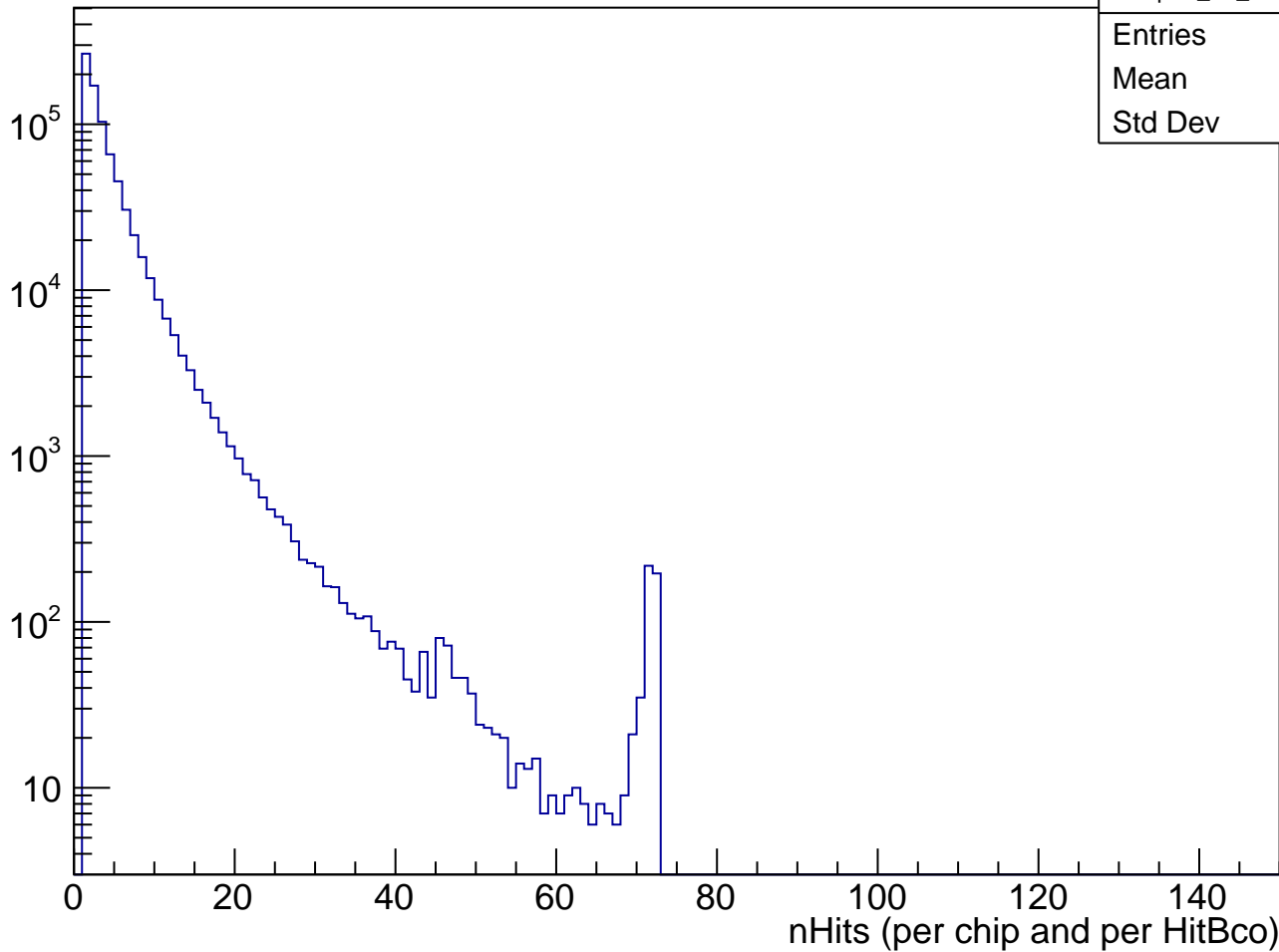
Entries



nChipHit_F7_Fch3_Chip15	
Entries	771076
Mean	3.502
Std Dev	4.244

nChipHit_F7_Fch3_Chip16

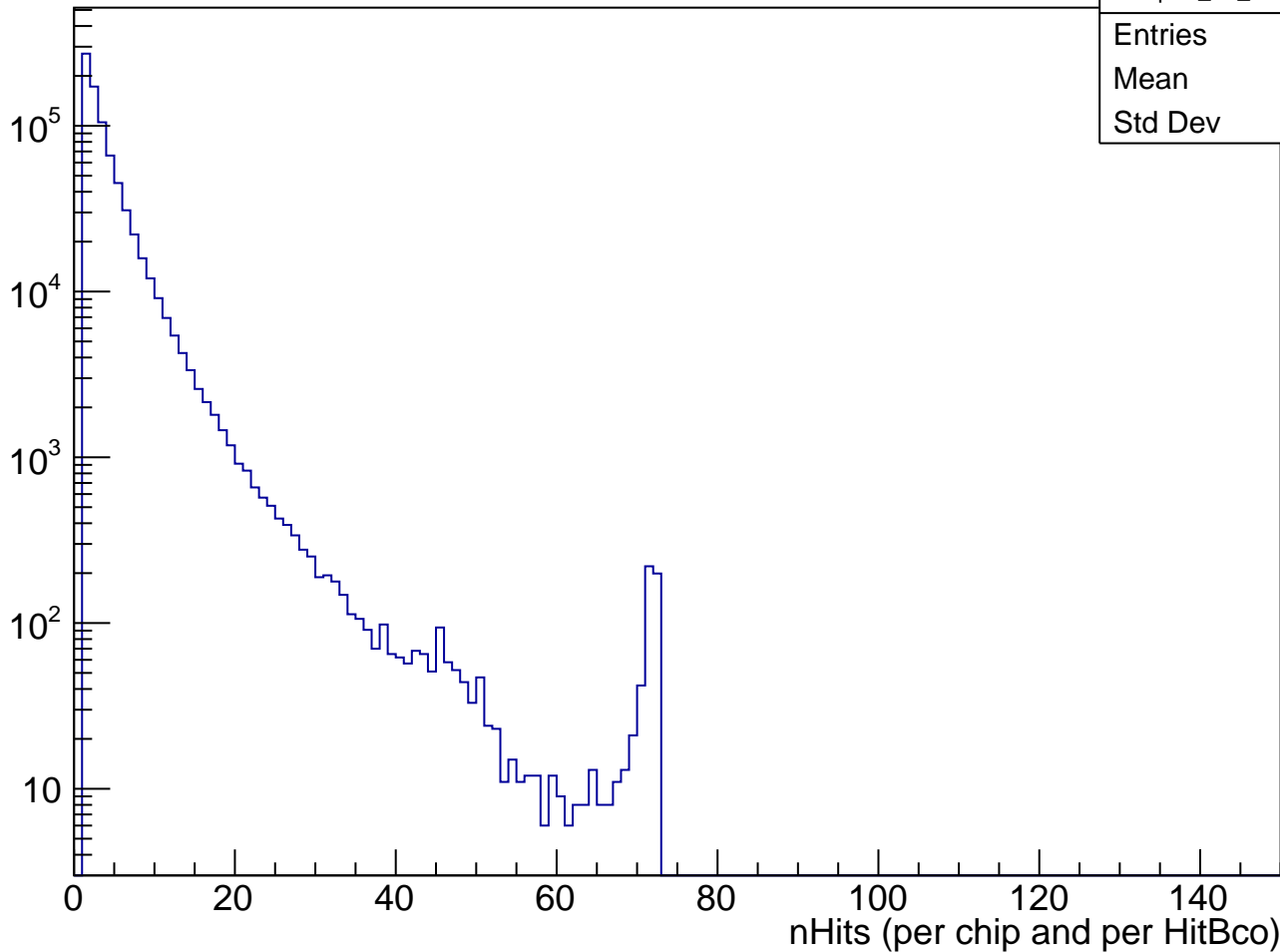
Entries



nChipHit_F7_Fch3_Chip16	
Entries	776184
Mean	3.447
Std Dev	4.179

nChipHit_F7_Fch3_Chip17

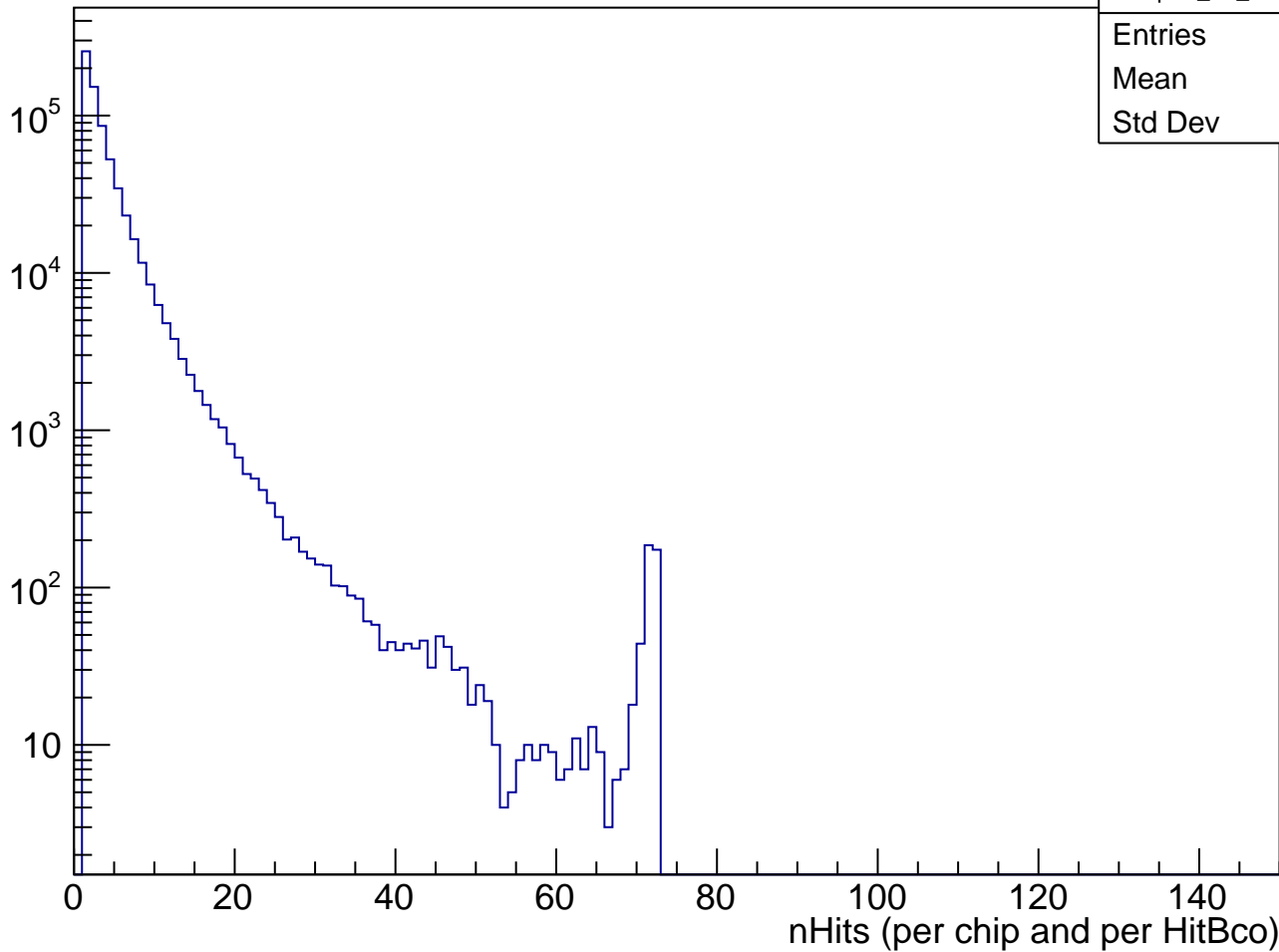
Entries



nChipHit_F7_Fch3_Chip17	
Entries	788119
Mean	3.455
Std Dev	4.219

nChipHit_F7_Fch3_Chip18

Entries

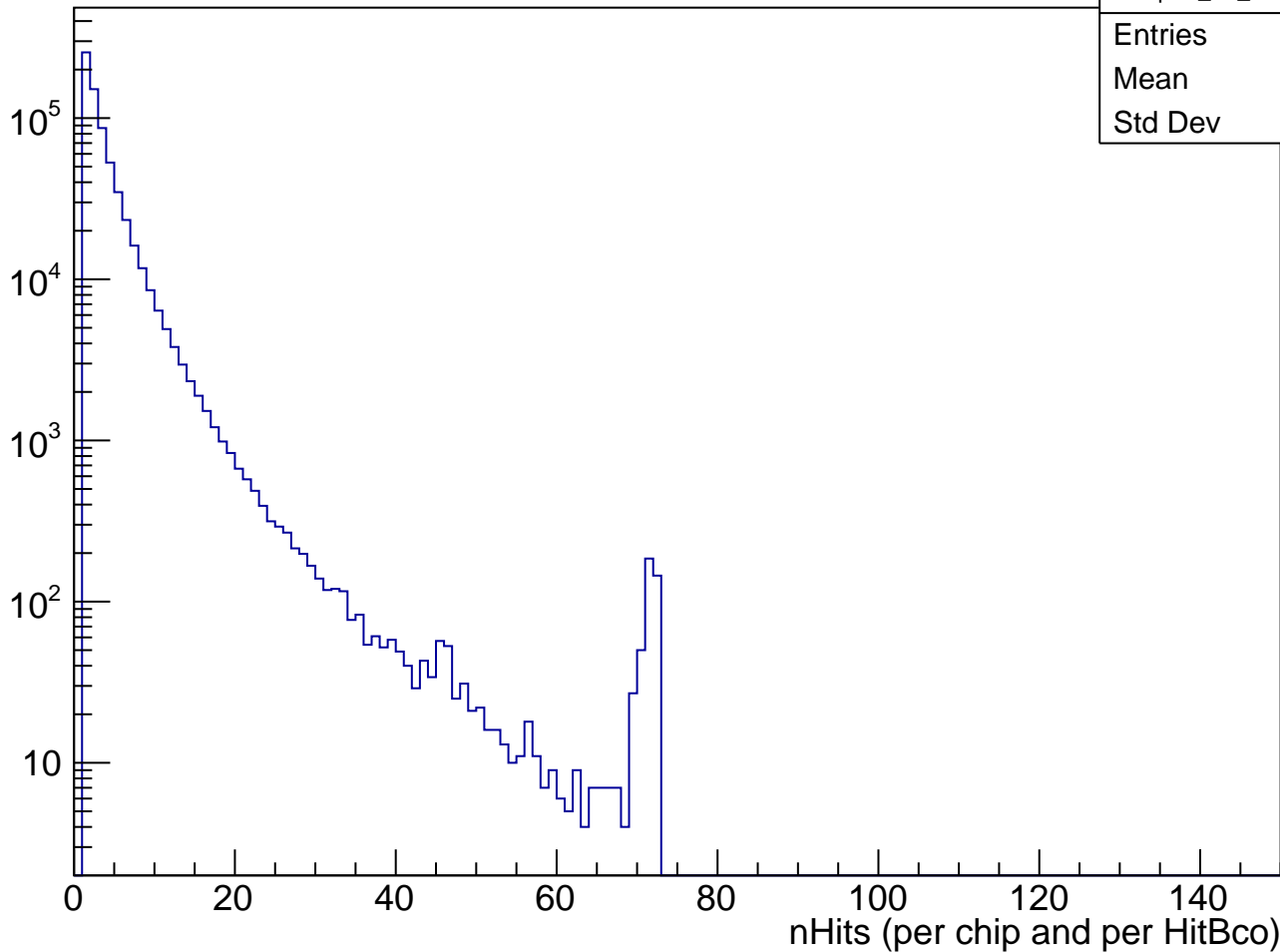


nChipHit_F7_Fch3_Chip18	
Entries	672608
Mean	3.157
Std Dev	3.938

nHits (per chip and per HitBco)

nChipHit_F7_Fch3_Chip19

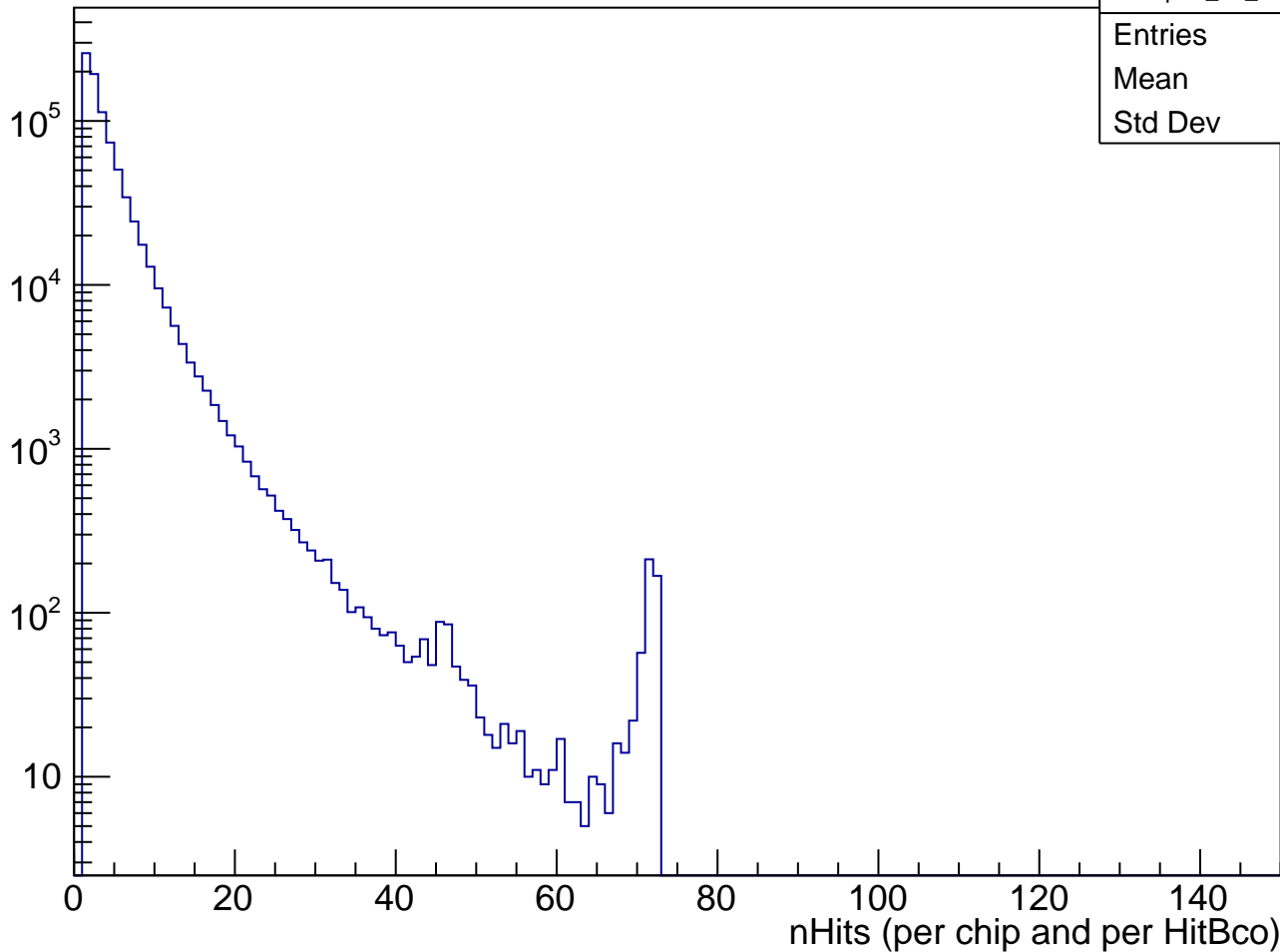
Entries



nChipHit_F7_Fch3_Chip19	
Entries	673422
Mean	3.176
Std Dev	3.956

nChipHit_F7_Fch3_Chip2

Entries



nChipHit_F7_Fch3_Chip2

Entries 826497

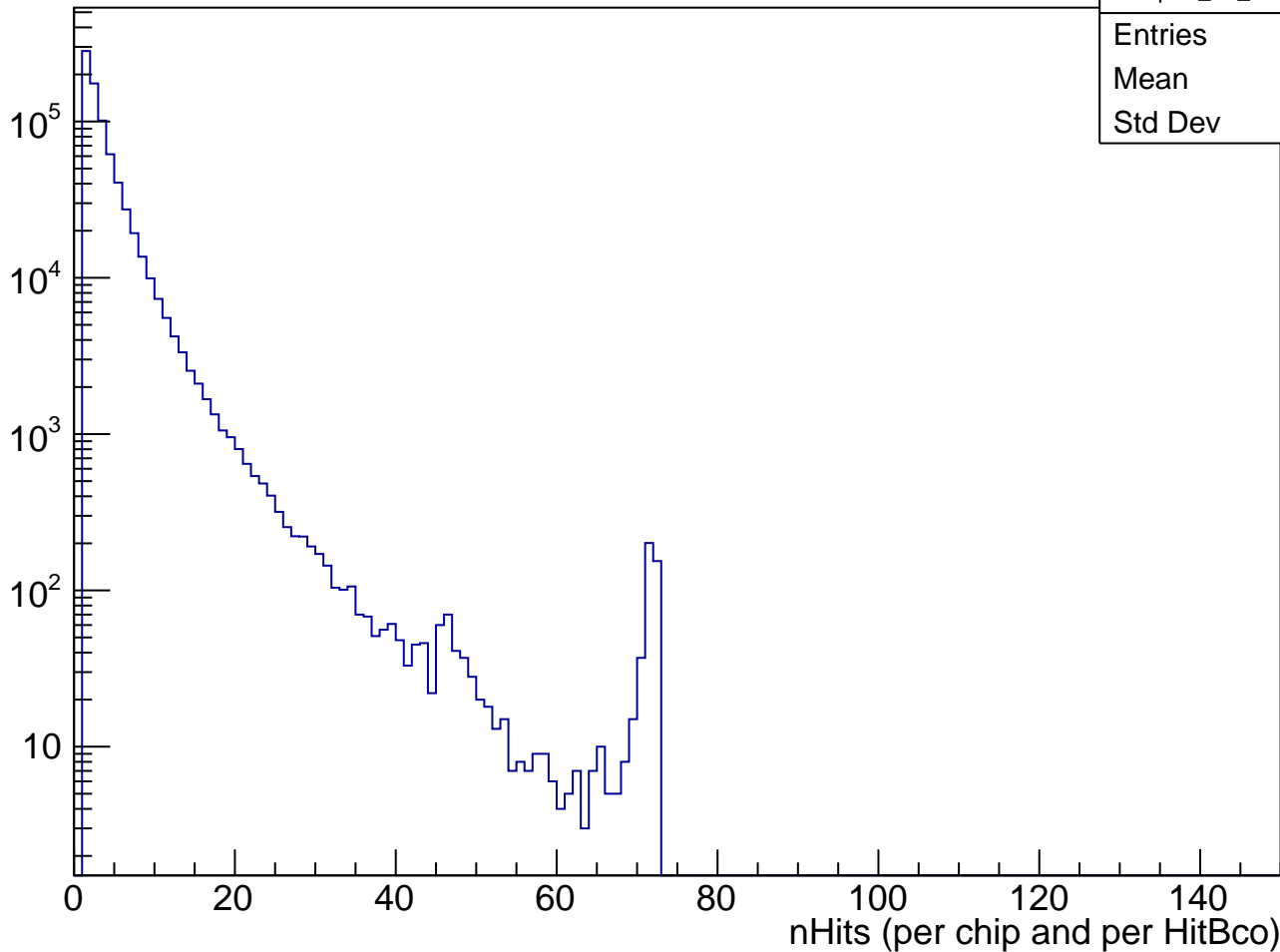
Mean 3.517

Std Dev 4.131

nHits (per chip and per HitBco)

nChipHit_F7_Fch3_Chip20

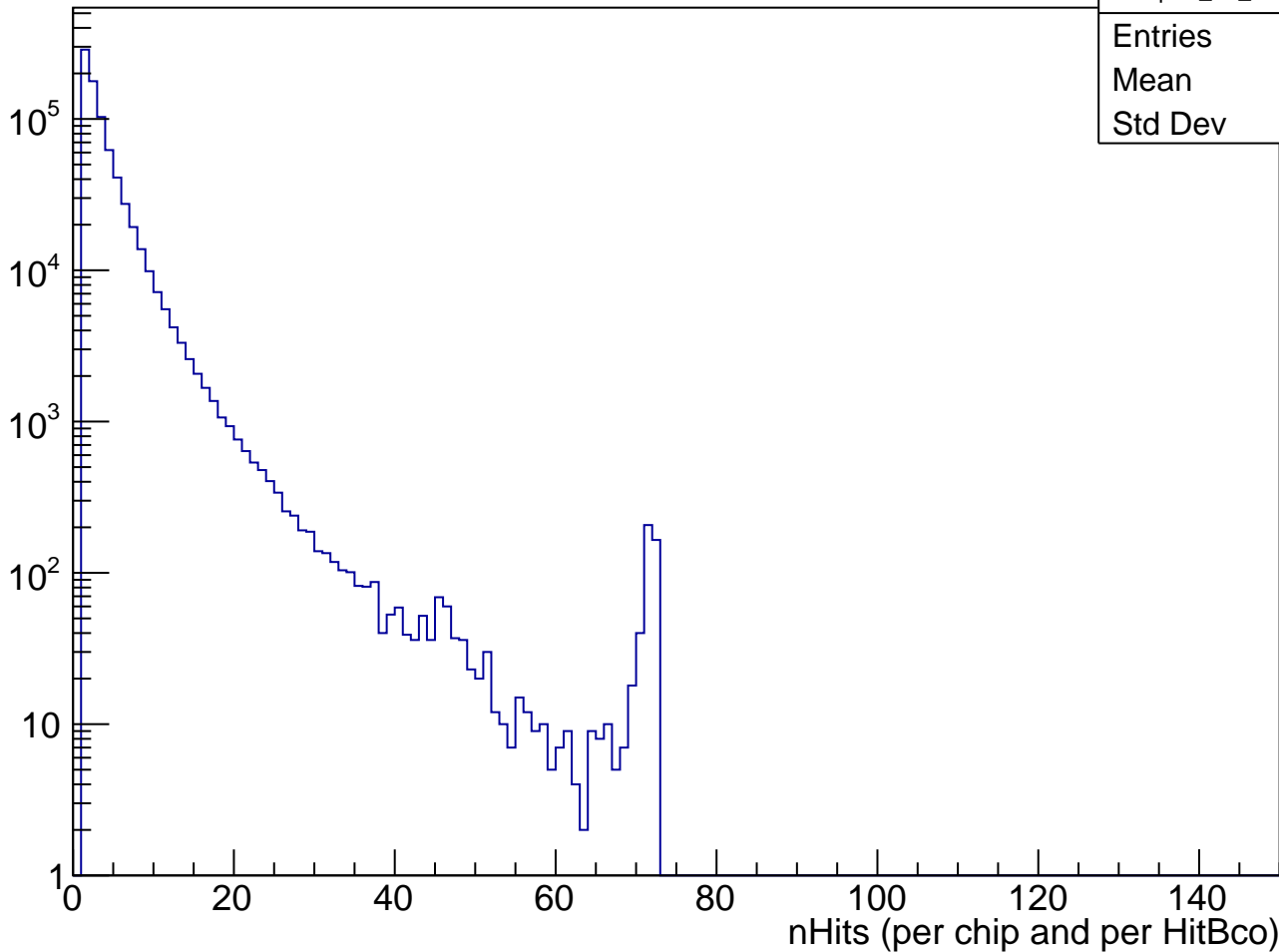
Entries



nChipHit_F7_Fch3_Chip20	
Entries	767482
Mean	3.191
Std Dev	3.865

nChipHit_F7_Fch3_Chip21

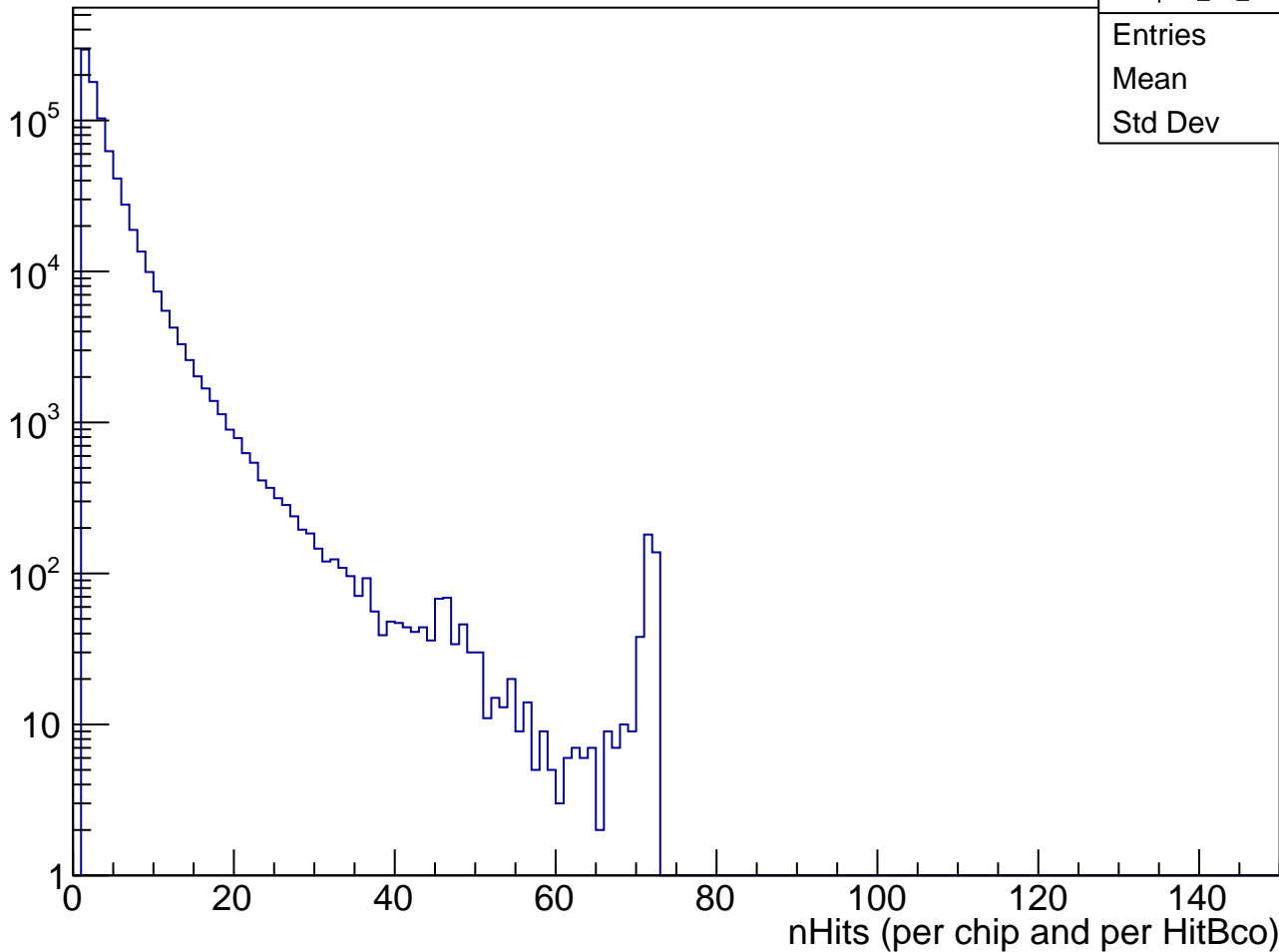
Entries



nChipHit_F7_Fch3_Chip21	
Entries	777581
Mean	3.178
Std Dev	3.876

nChipHit_F7_Fch3_Chip22

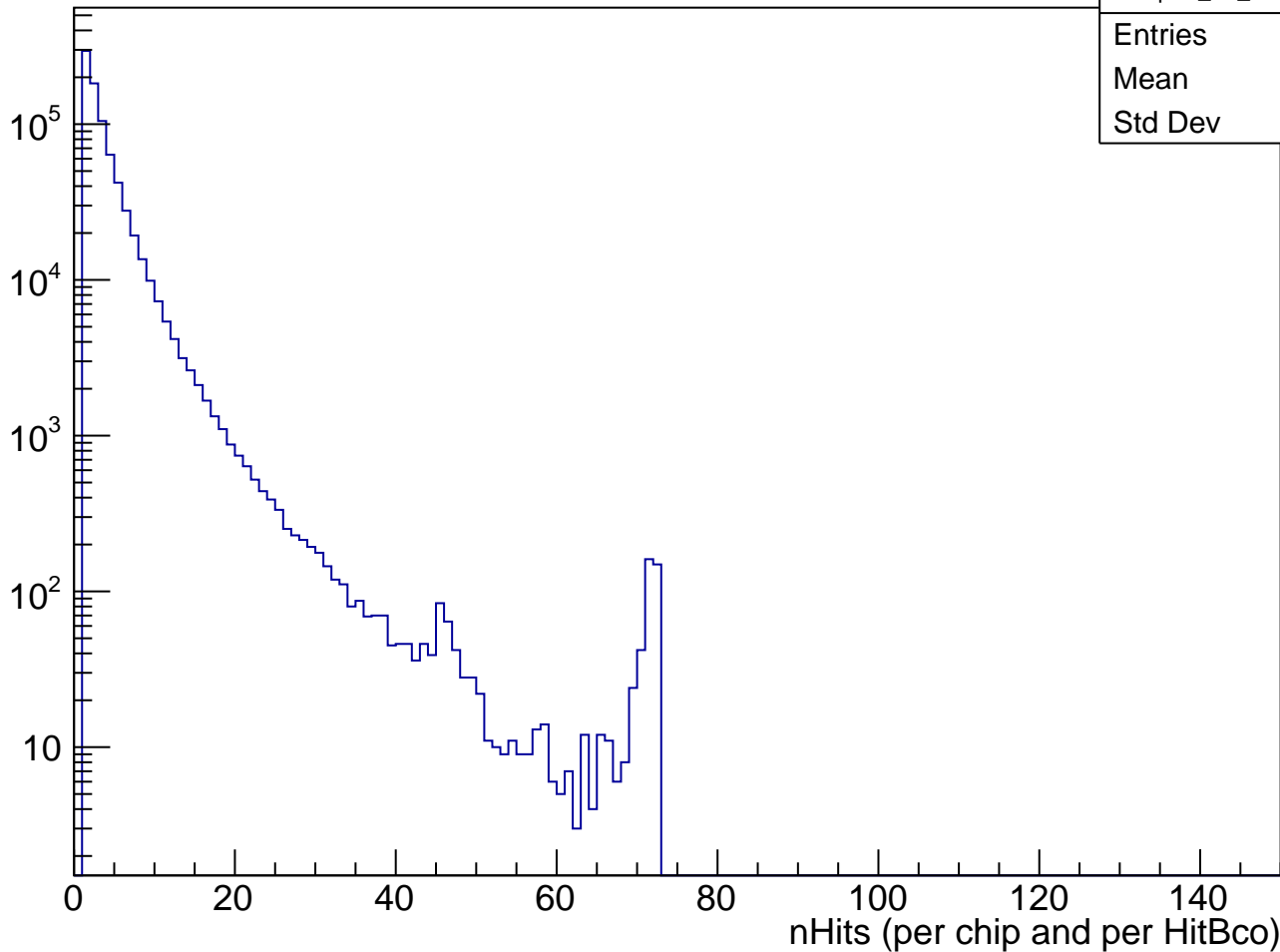
Entries



nChipHit_F7_Fch3_Chip22	
Entries	787466
Mean	3.146
Std Dev	3.801

nChipHit_F7_Fch3_Chip23

Entries

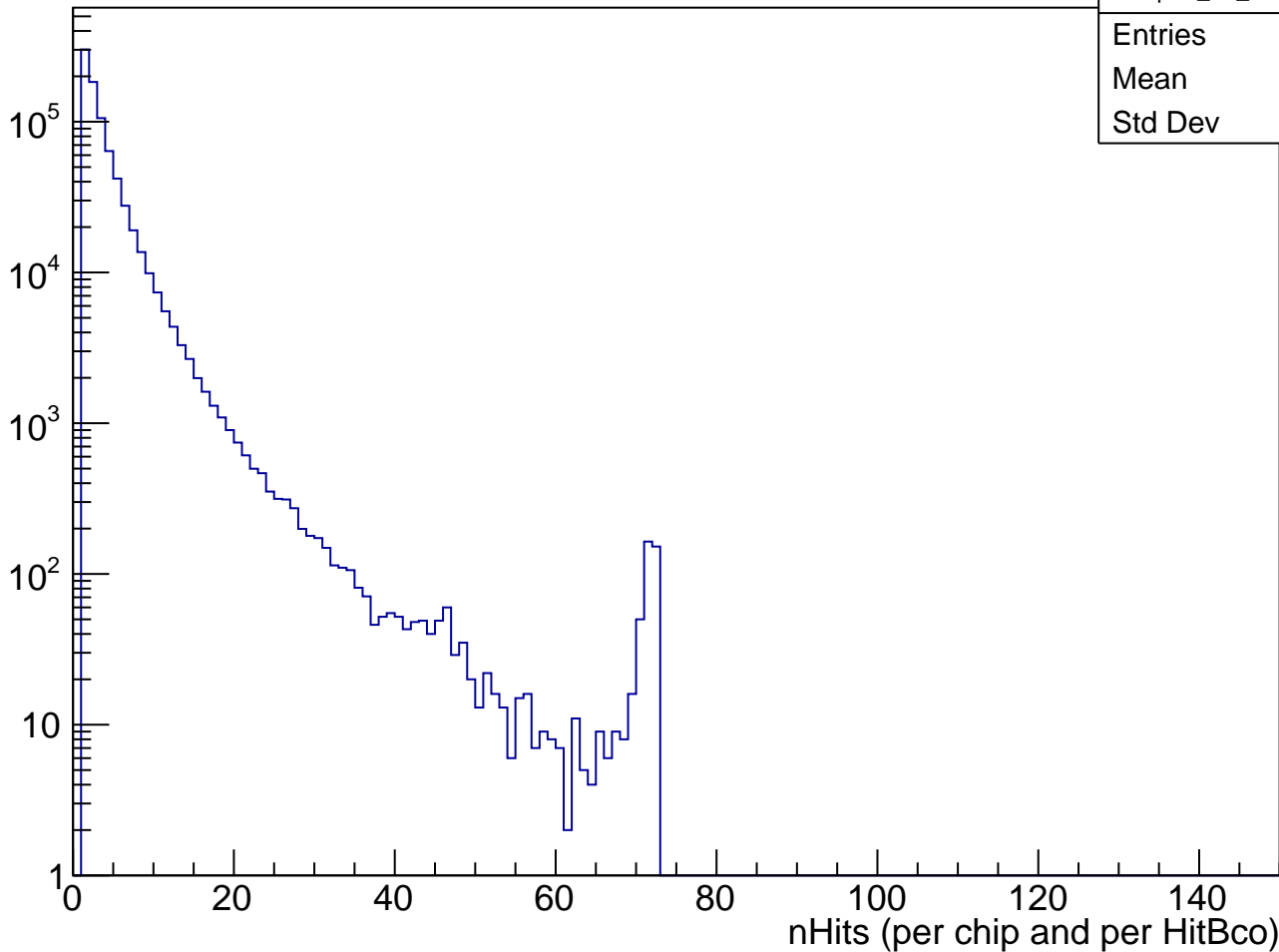


nChipHit_F7_Fch3_Chip23	
Entries	795028
Mean	3.144
Std Dev	3.799

nHits (per chip and per HitBco)

nChipHit_F7_Fch3_Chip24

Entries



nChipHit_F7_Fch3_Chip24

Entries 802610

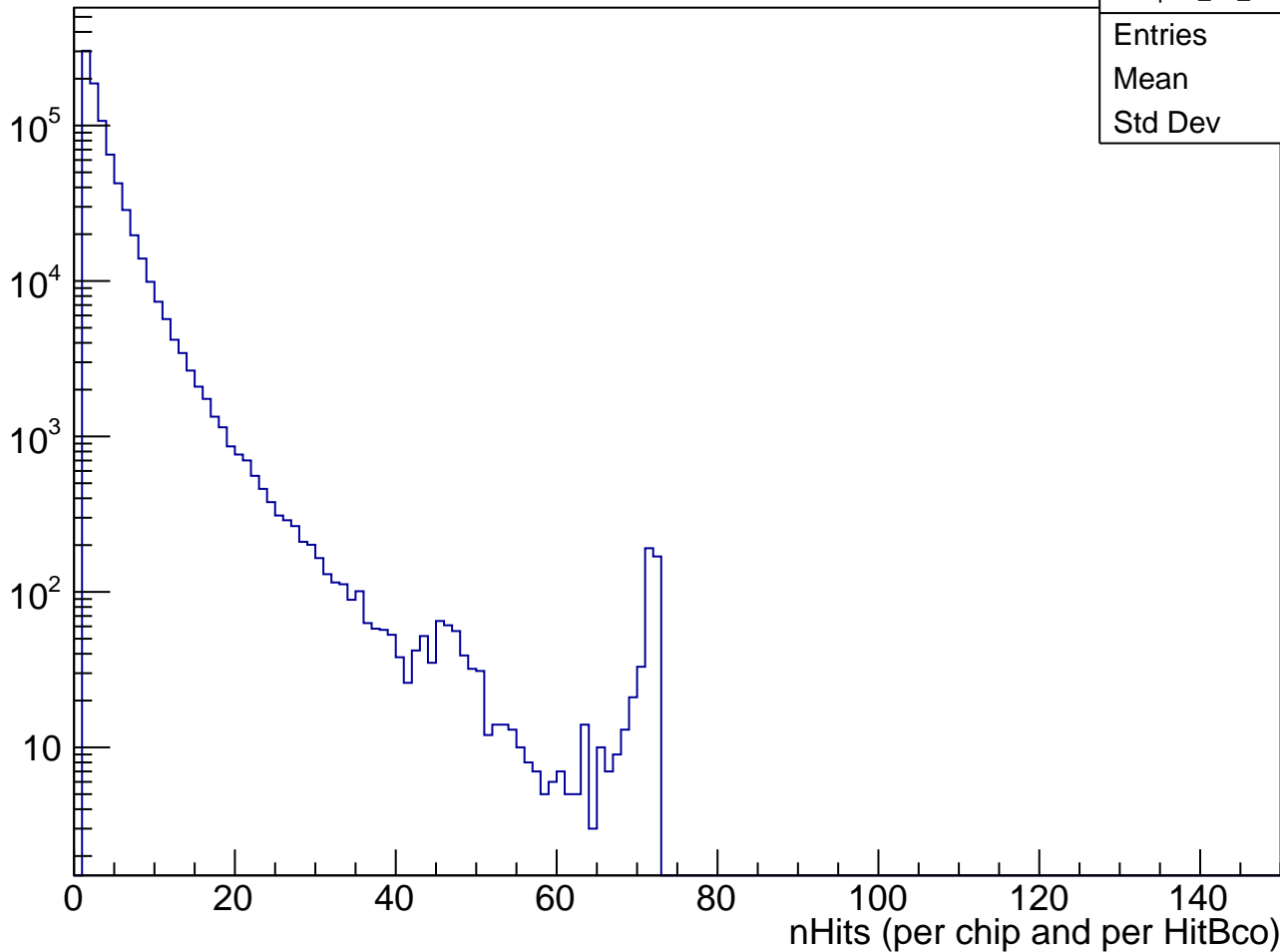
Mean 3.128

Std Dev 3.779

nHits (per chip and per HitBco)

nChipHit_F7_Fch3_Chip25

Entries

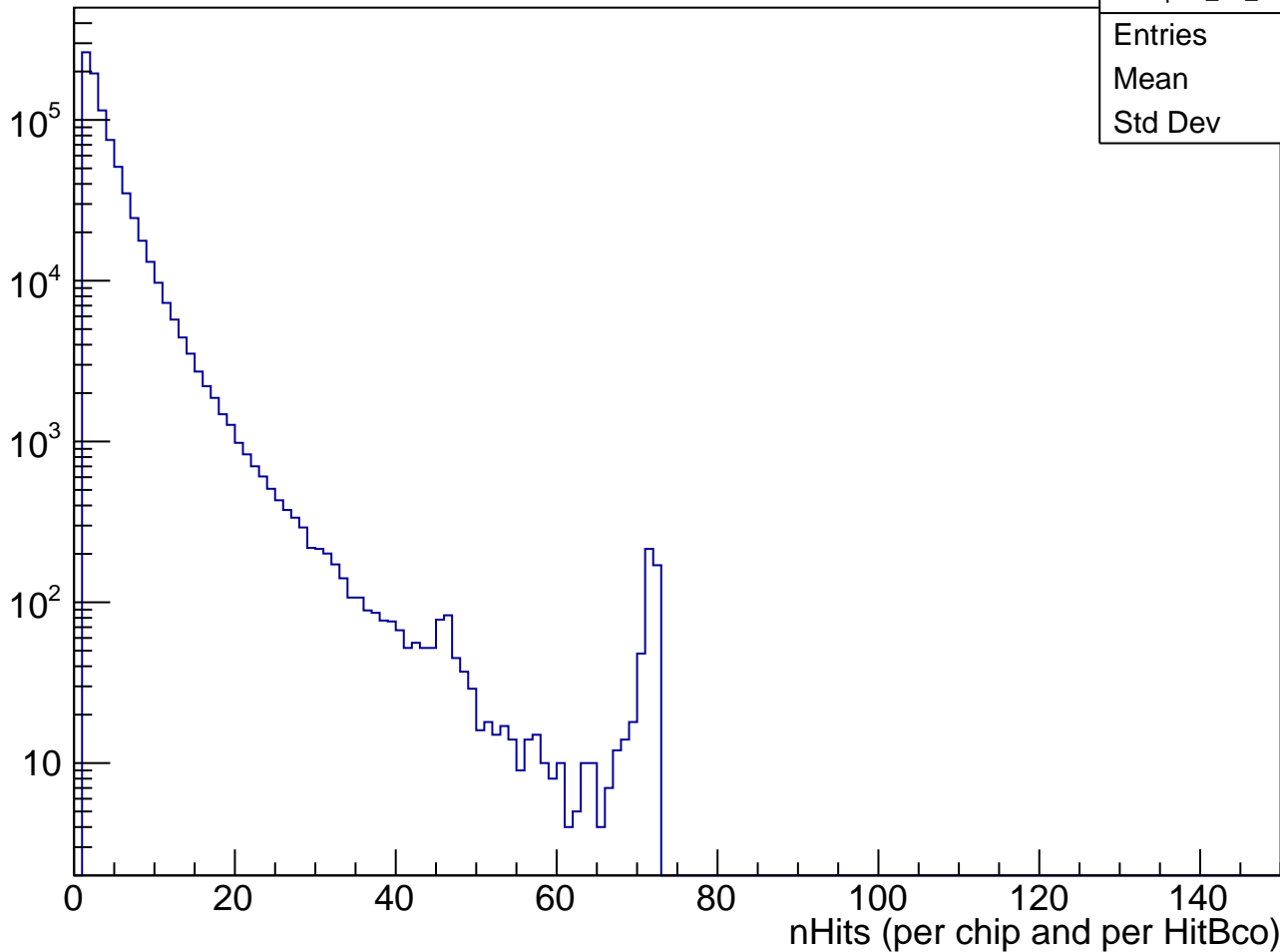


nChipHit_F7_Fch3_Chip25	
Entries	812412
Mean	3.145
Std Dev	3.819

nHits (per chip and per HitBco)

nChipHit_F7_Fch3_Chip3

Entries



nChipHit_F7_Fch3_Chip3

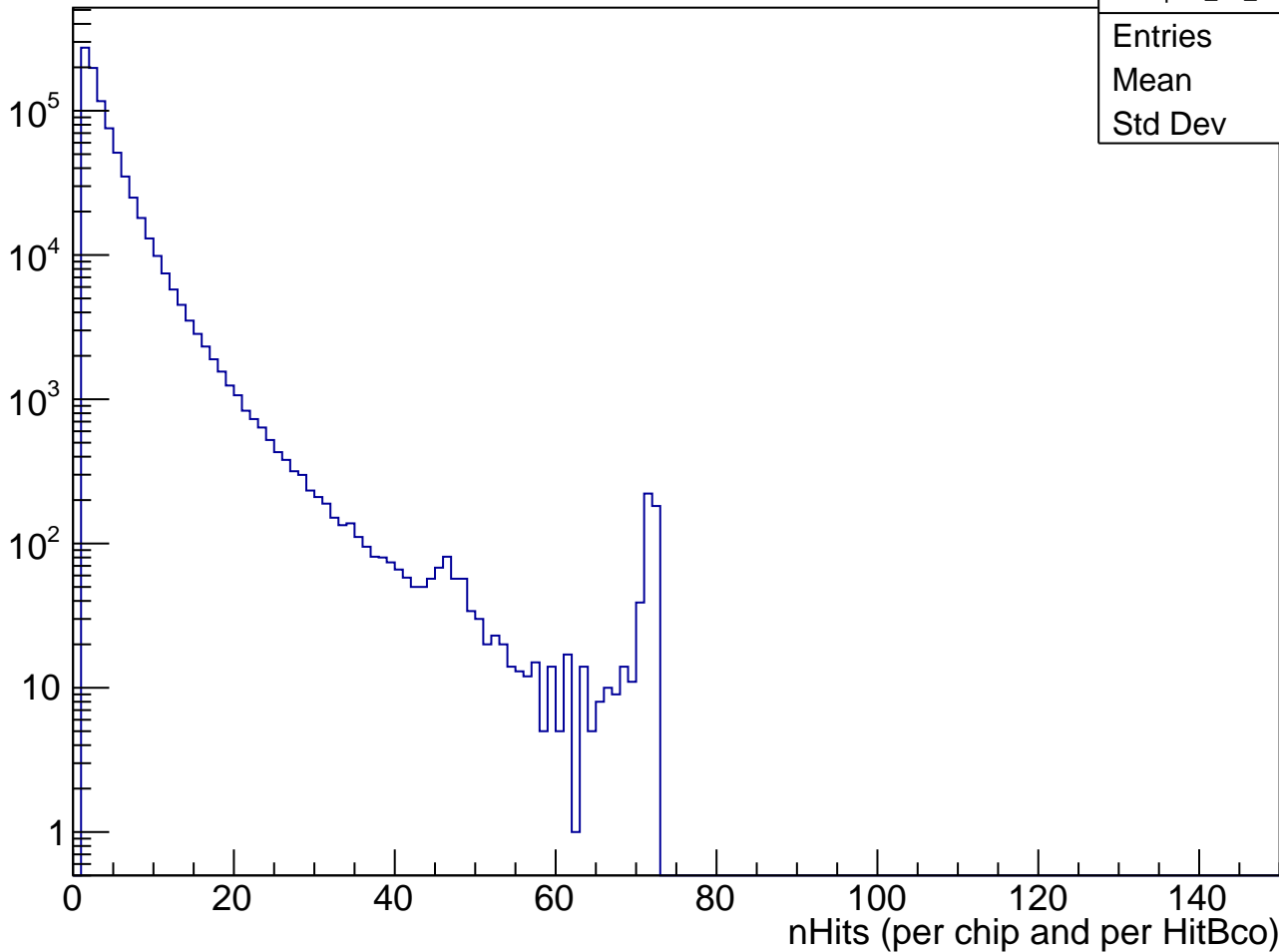
Entries 837312

Mean 3.51

Std Dev 4.098

nChipHit_F7_Fch3_Chip4

Entries



nChipHit_F7_Fch3_Chip4

Entries 855041

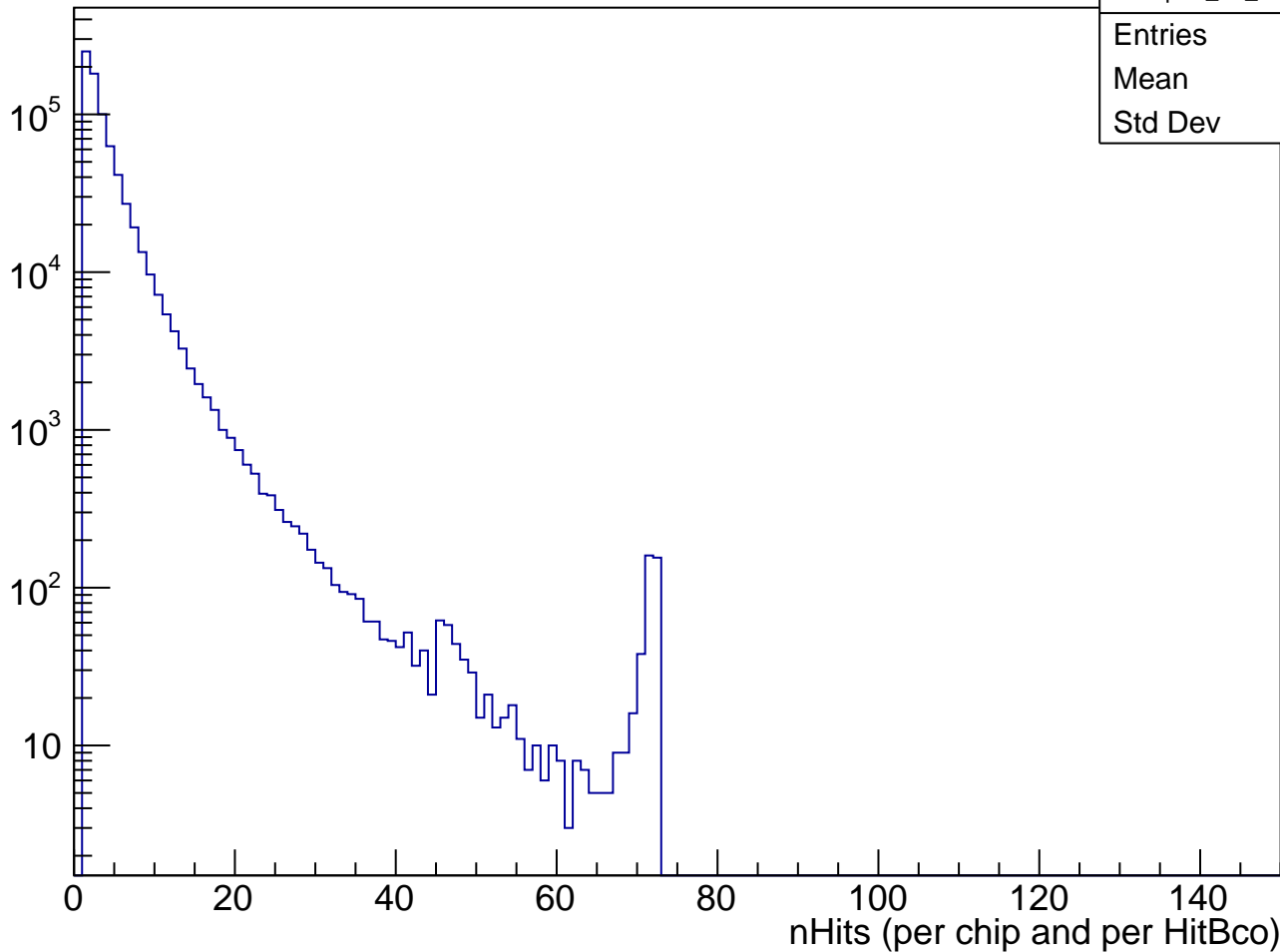
Mean 3.492

Std Dev 4.109

nHits (per chip and per HitBco)

nChipHit_F7_Fch3_Chip5

Entries



nChipHit_F7_Fch3_Chip5

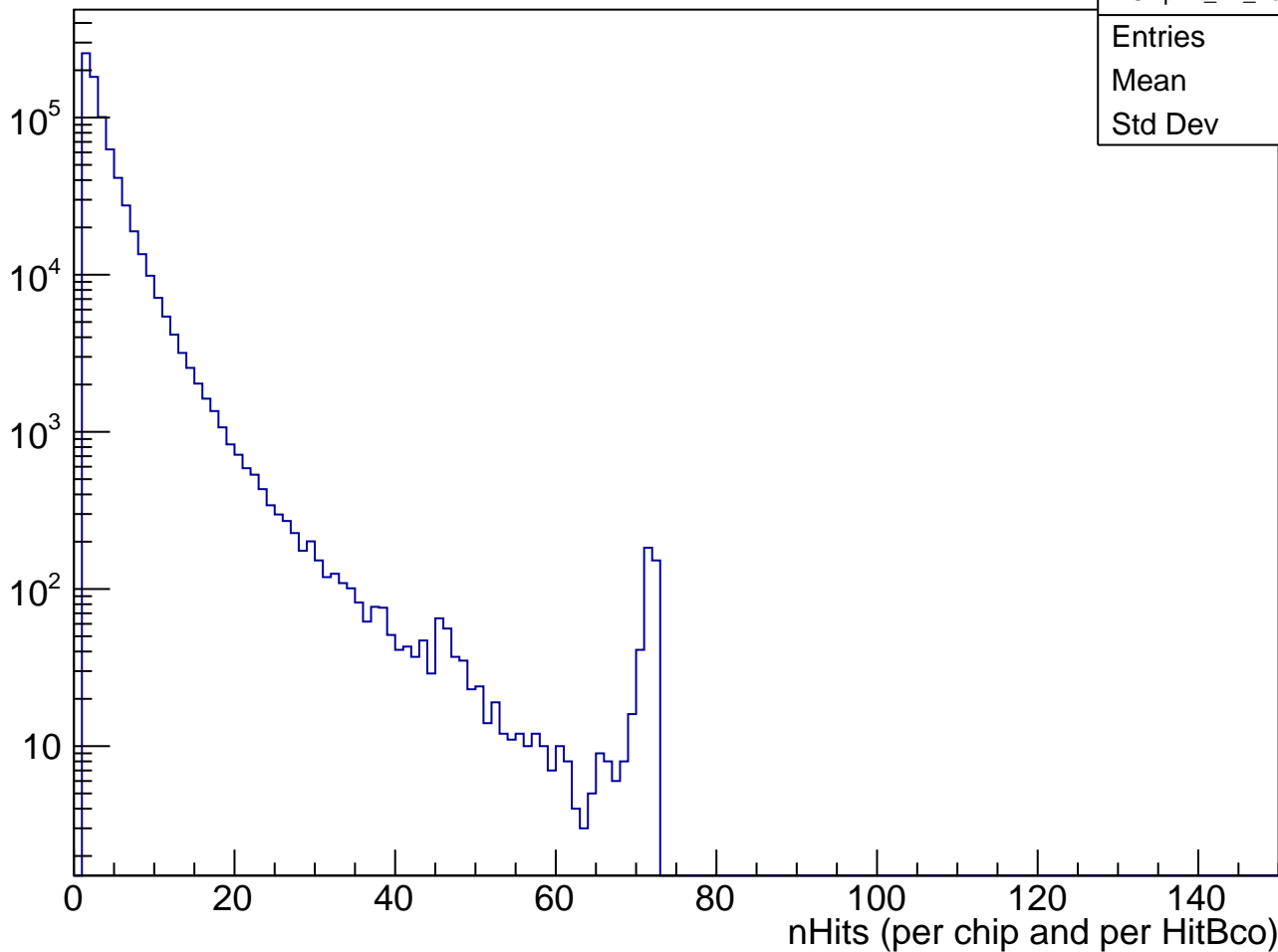
Entries 740525

Mean 3.252

Std Dev 3.842

nChipHit_F7_Fch3_Chip6

Entries



nChipHit_F7_Fch3_Chip6

Entries 747794

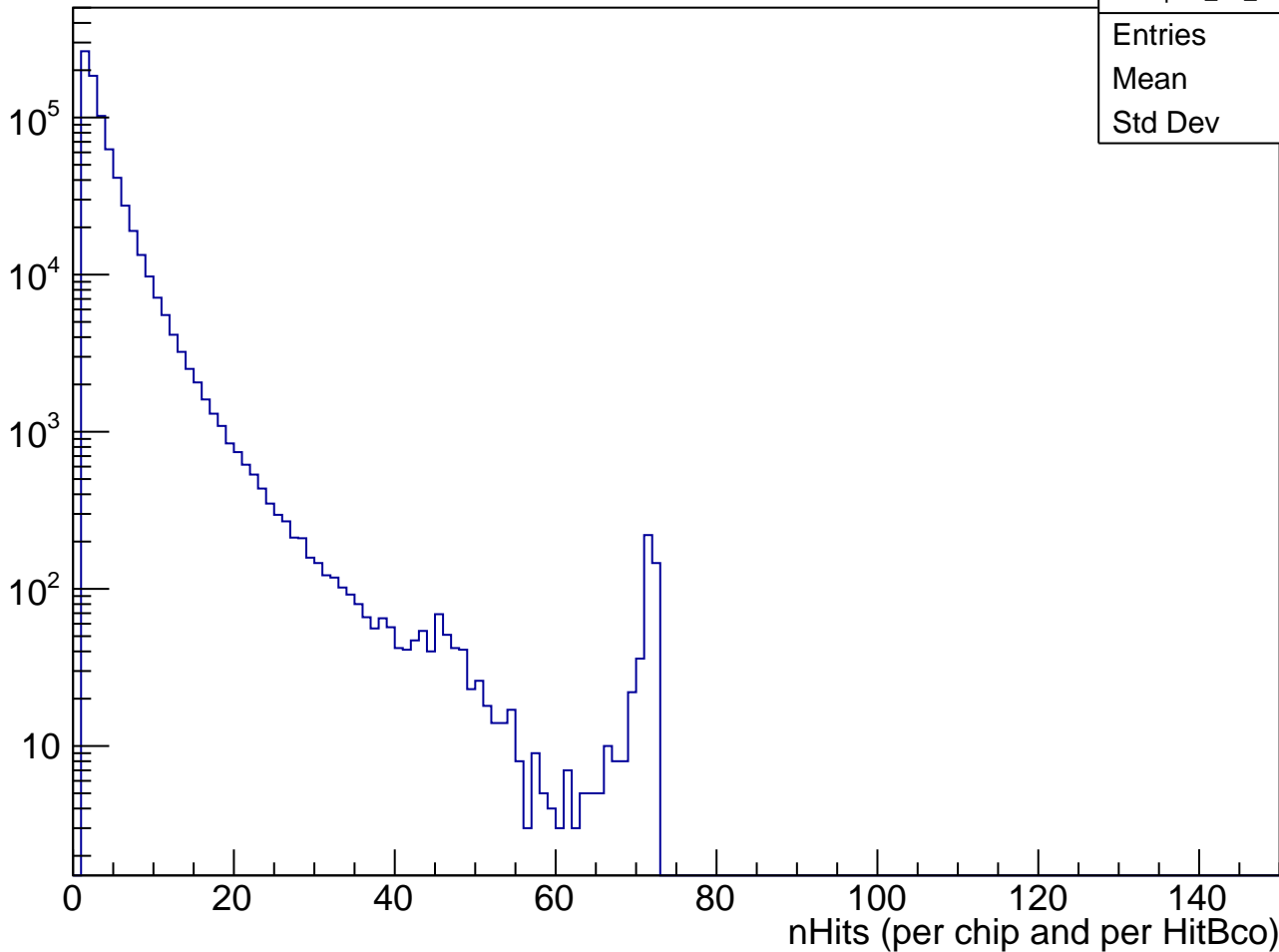
Mean 3.24

Std Dev 3.863

nHits (per chip and per HitBco)

nChipHit_F7_Fch3_Chip7

Entries



nChipHit_F7_Fch3_Chip7

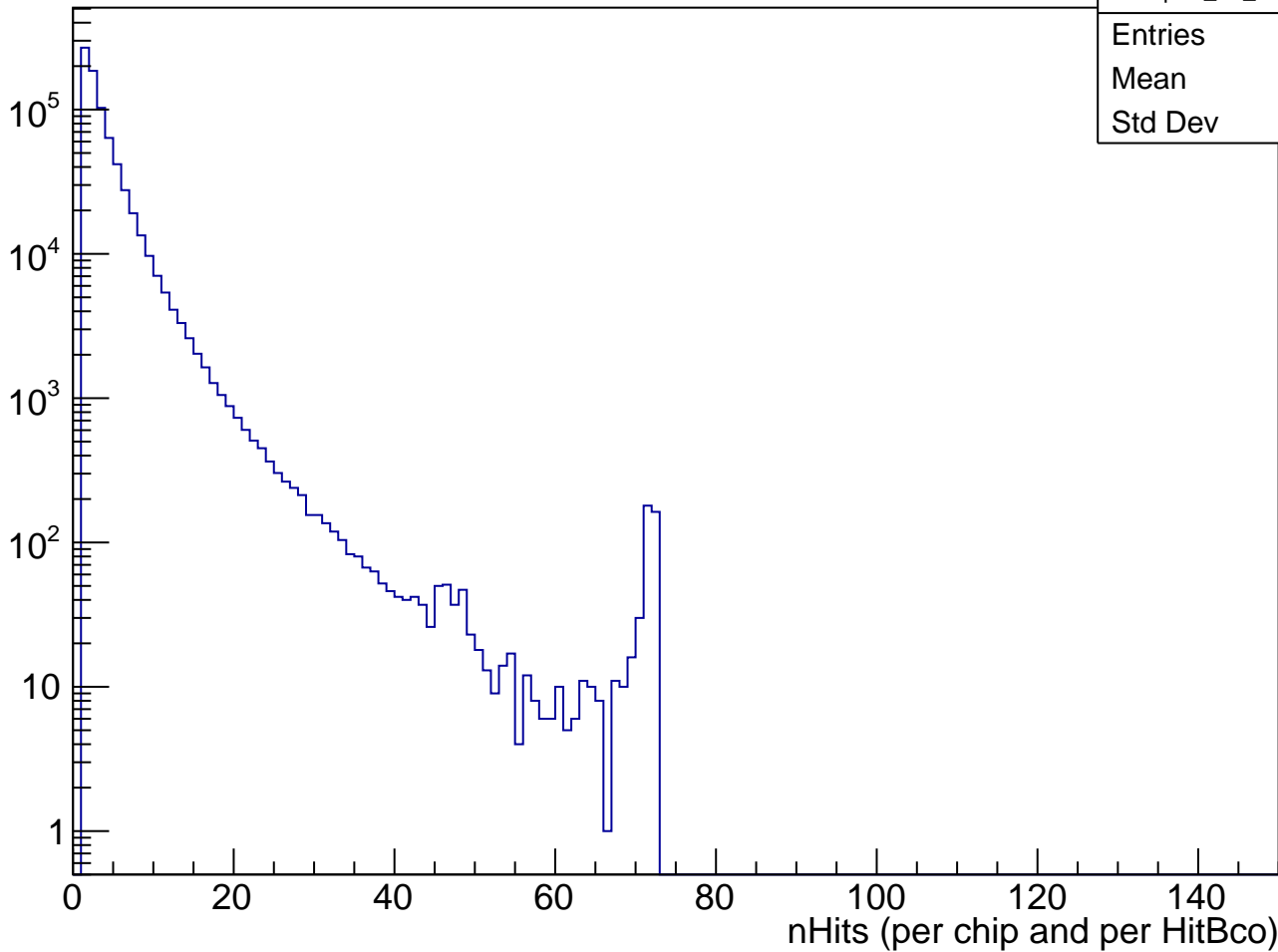
Entries 759454

Mean 3.214

Std Dev 3.856

nChipHit_F7_Fch3_Chip8

Entries



nChipHit_F7_Fch3_Chip8

Entries 767066

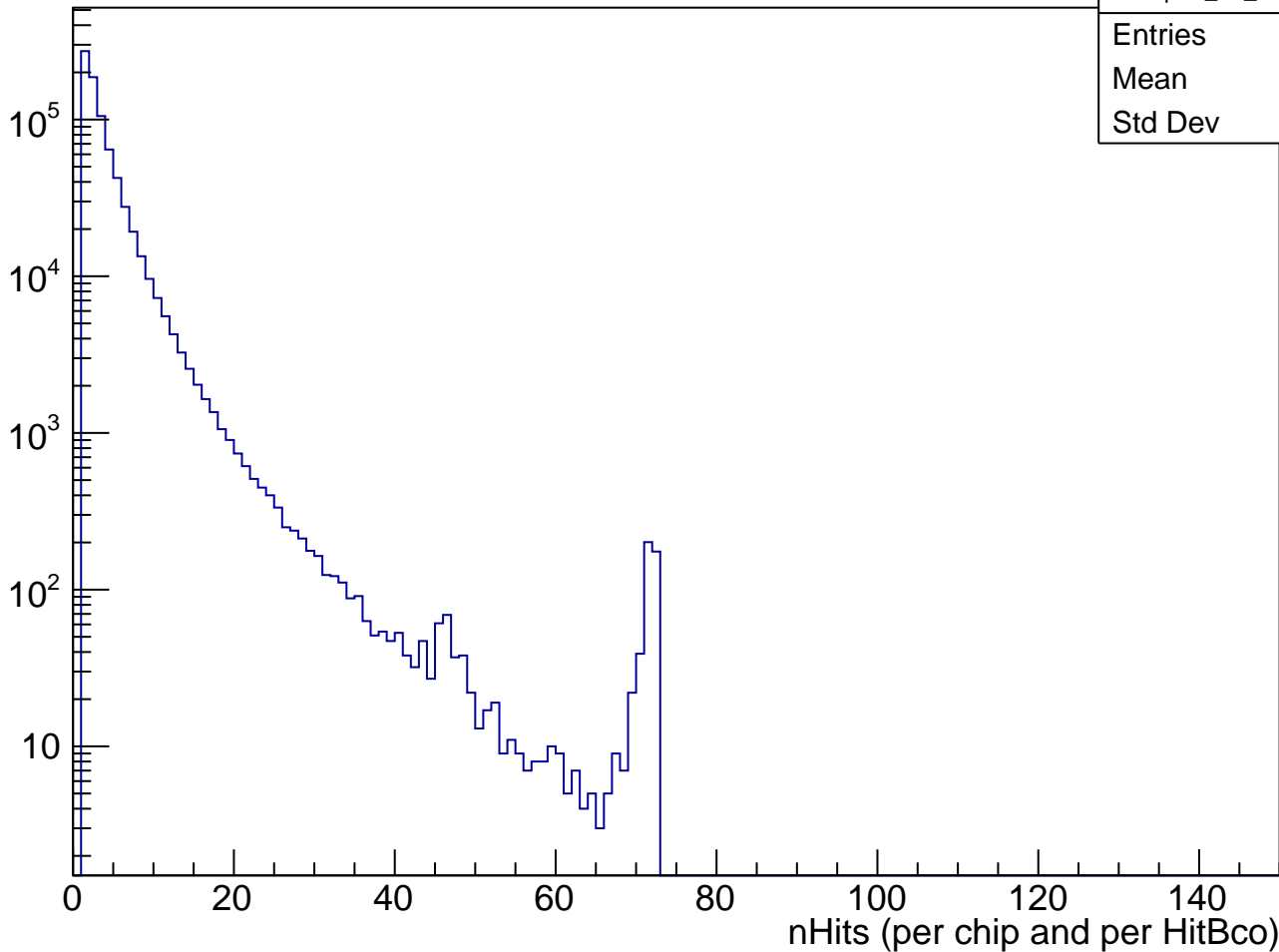
Mean 3.198

Std Dev 3.808

nHits (per chip and per HitBco)

nChipHit_F7_Fch3_Chip9

Entries

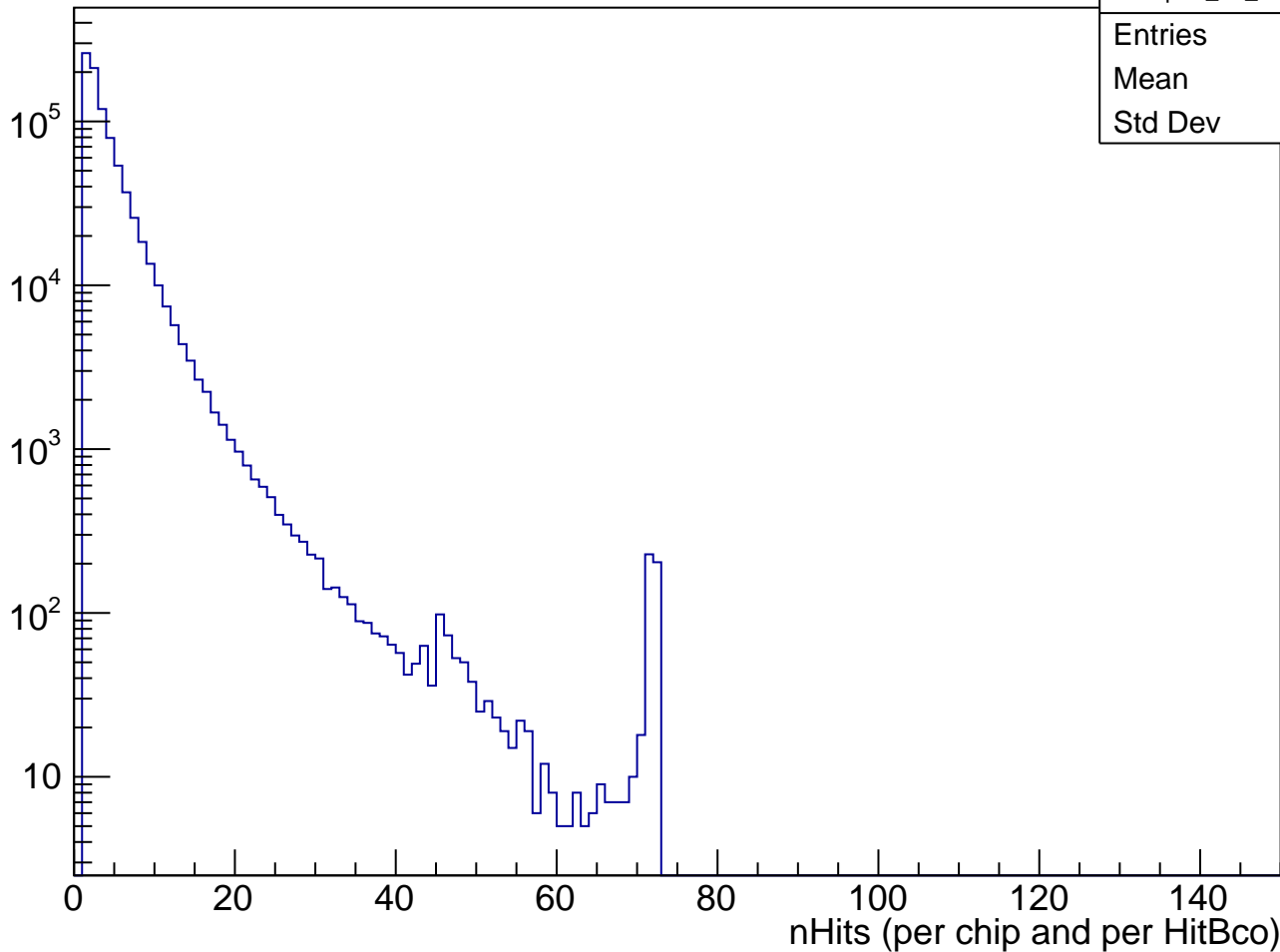


nChipHit_F7_Fch3_Chip9

Entries	777478
Mean	3.198
Std Dev	3.839

nChipHit_F7_Fch4_Chip0

Entries



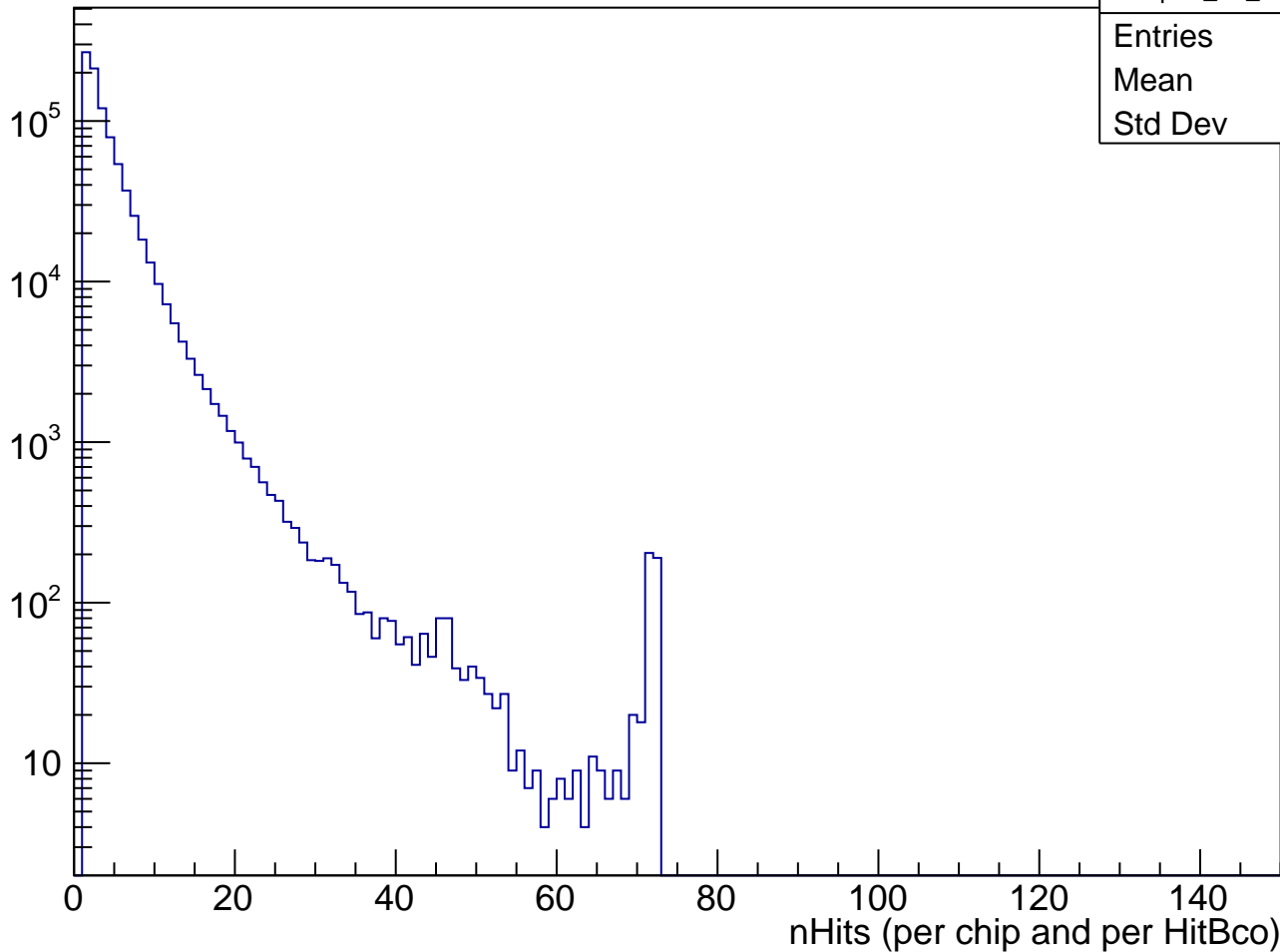
nChipHit_F7_Fch4_Chip0

Entries	867148
Mean	3.495
Std Dev	4.012

nHits (per chip and per HitBco)

nChipHit_F7_Fch4_Chip1

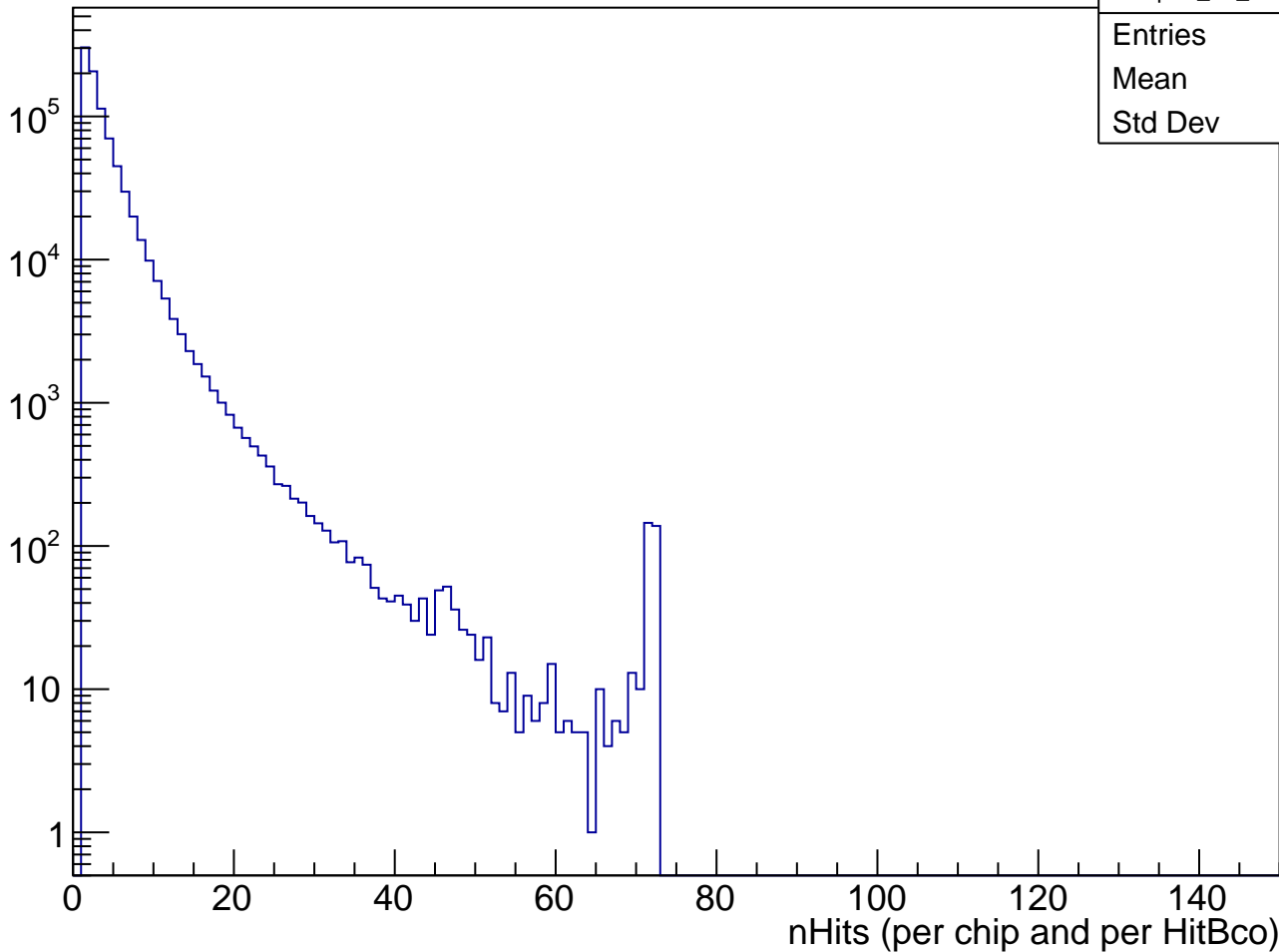
Entries



nChipHit_F7_Fch4_Chip1	
Entries	874311
Mean	3.456
Std Dev	3.96

nChipHit_F7_Fch4_Chip10

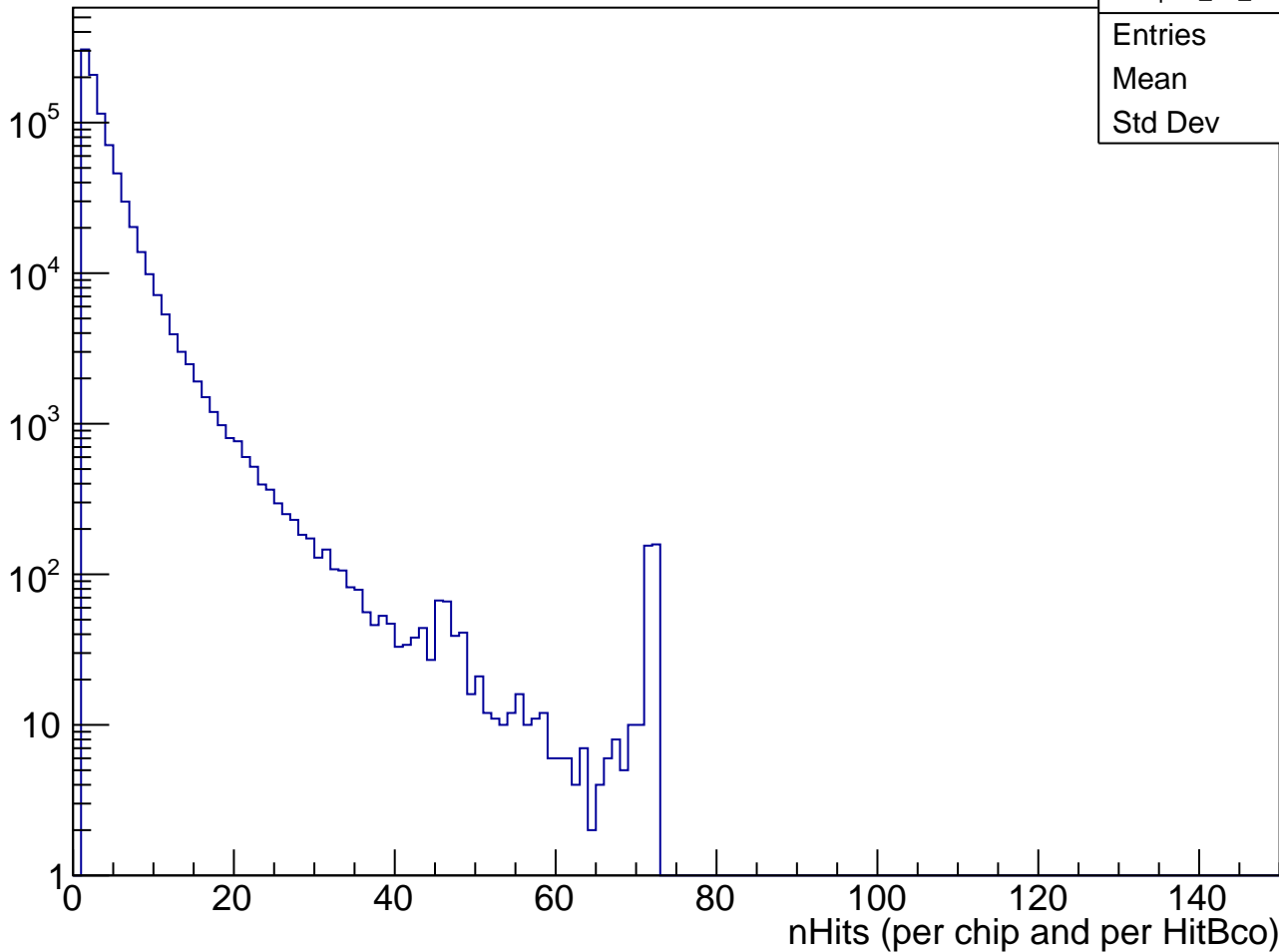
Entries



nChipHit_F7_Fch4_Chip10	
Entries	845086
Mean	3.068
Std Dev	3.536

nChipHit_F7_Fch4_Chip11

Entries

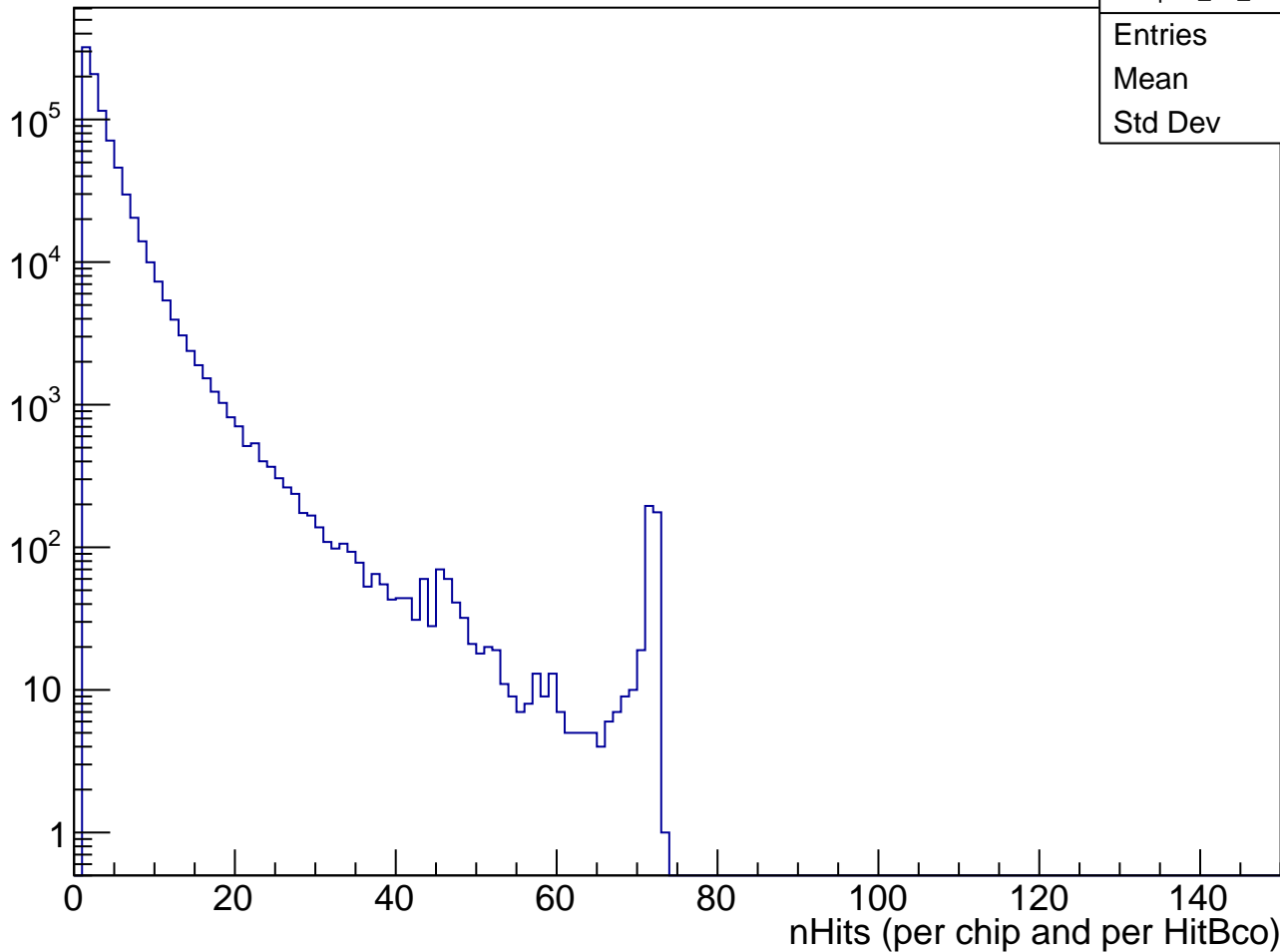


nChipHit_F7_Fch4_Chip11	
Entries	852859
Mean	3.076
Std Dev	3.572

nHits (per chip and per HitBco)

nChipHit_F7_Fch4_Chip12

Entries

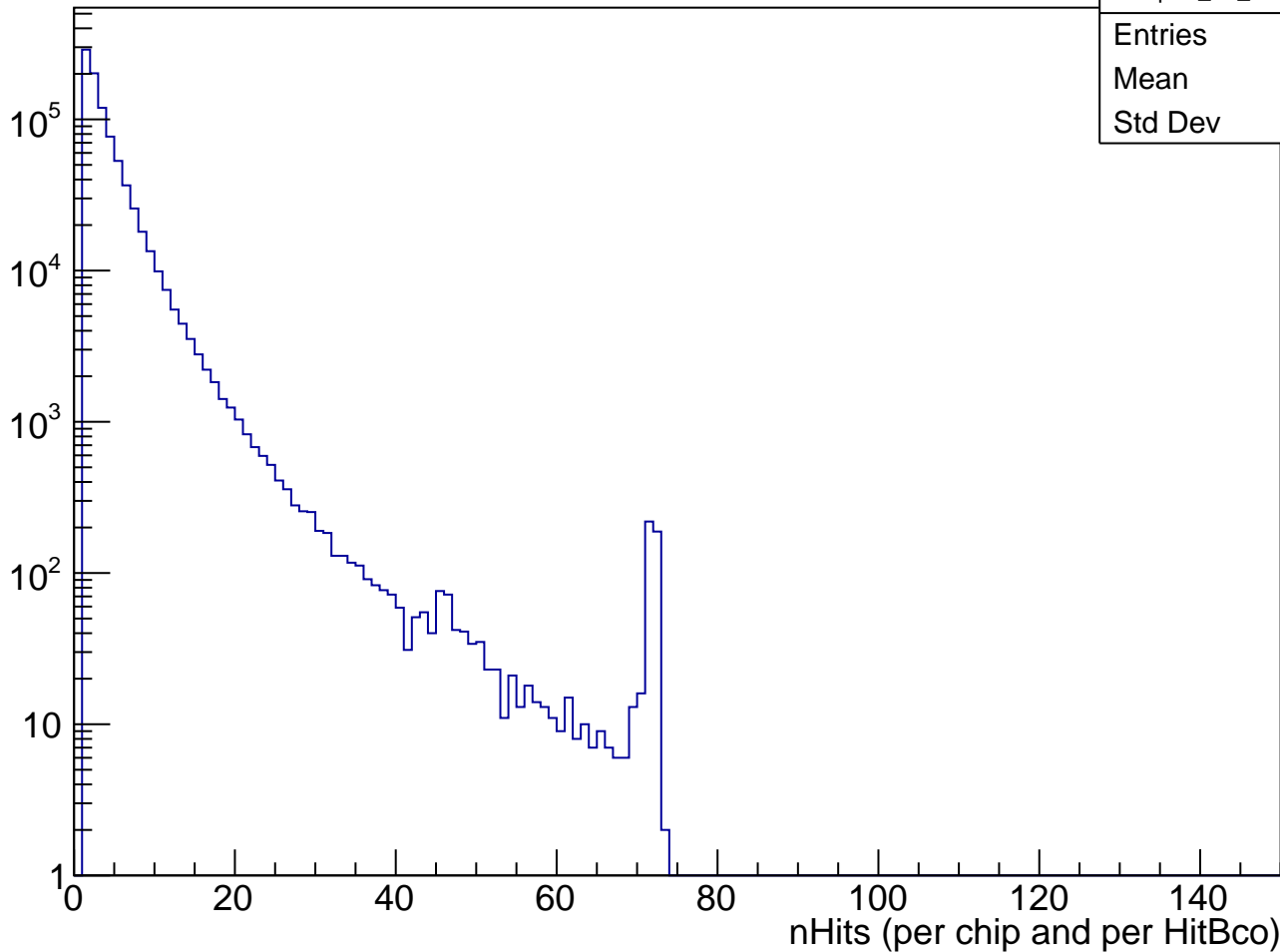


nChipHit_F7_Fch4_Chip12	
Entries	869513
Mean	3.05
Std Dev	3.611

nHits (per chip and per HitBco)

nChipHit_F7_Fch4_Chip13

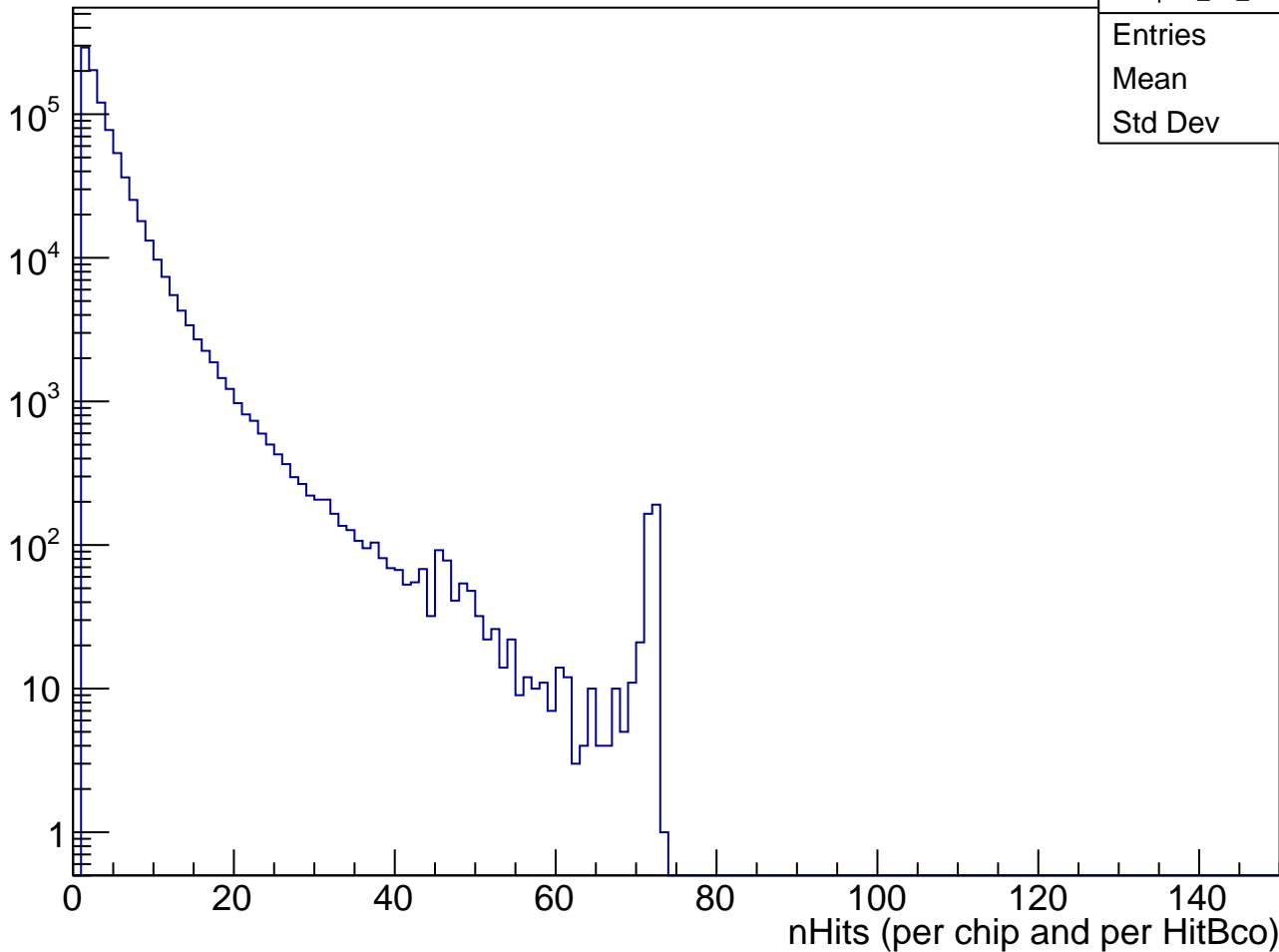
Entries



nChipHit_F7_Fch4_Chip13	
Entries	881856
Mean	3.435
Std Dev	4.005

nChipHit_F7_Fch4_Chip14

Entries

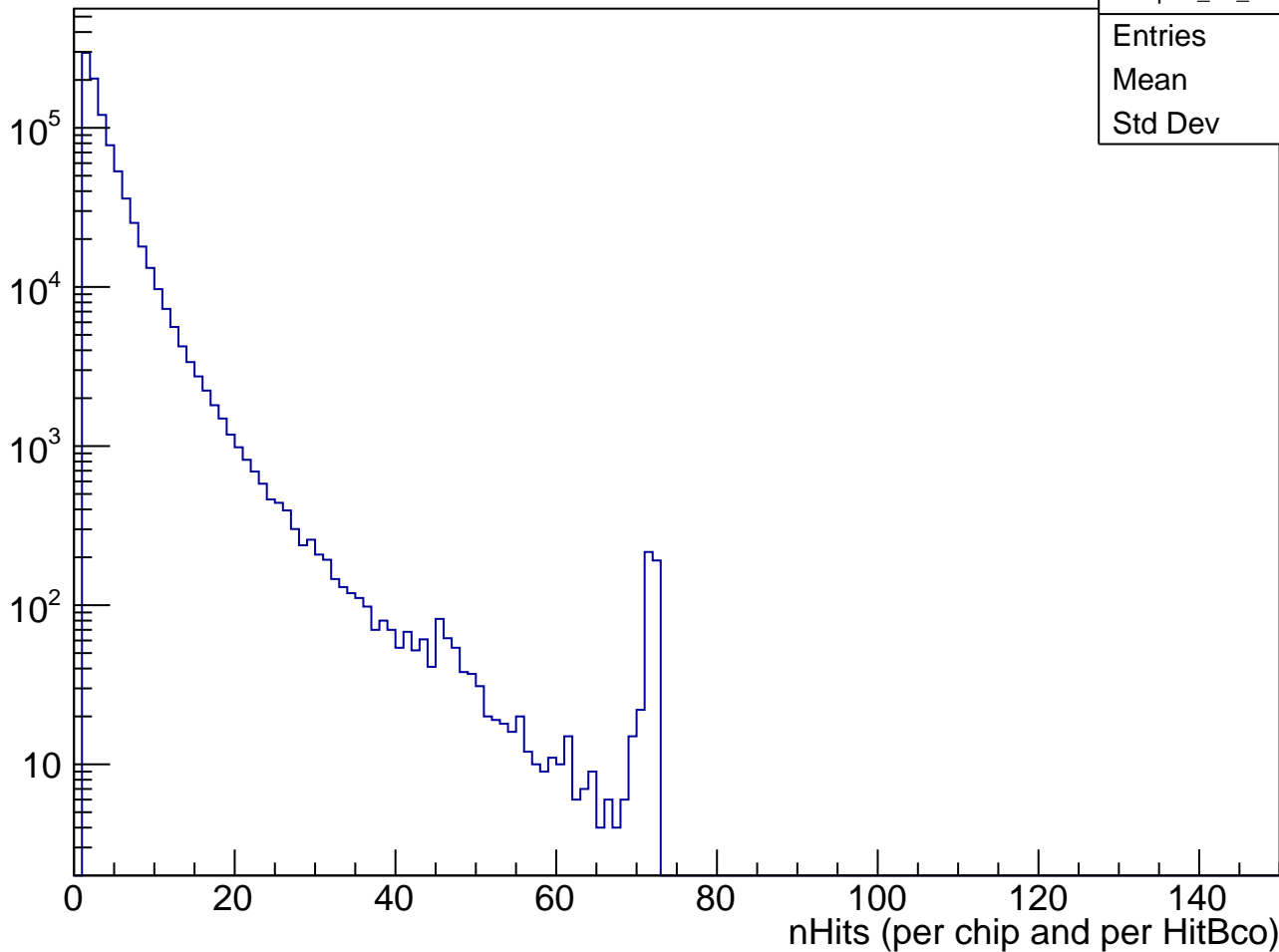


nChipHit_F7_Fch4_Chip14	
Entries	886200
Mean	3.42
Std Dev	3.979

nHits (per chip and per HitBco)

nChipHit_F7_Fch4_Chip15

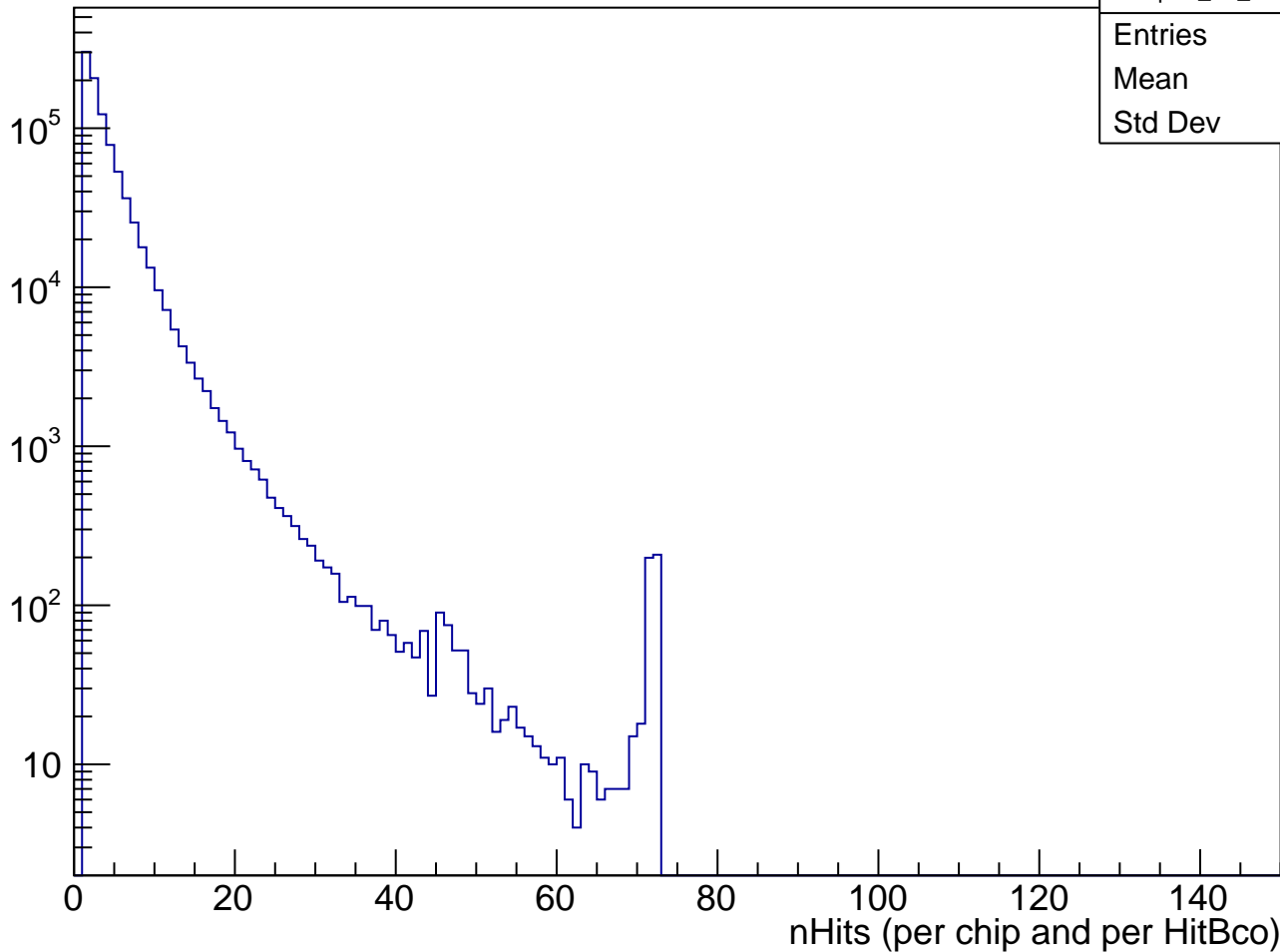
Entries



nChipHit_F7_Fch4_Chip15	
Entries	891405
Mean	3.401
Std Dev	3.985

nChipHit_F7_Fch4_Chip16

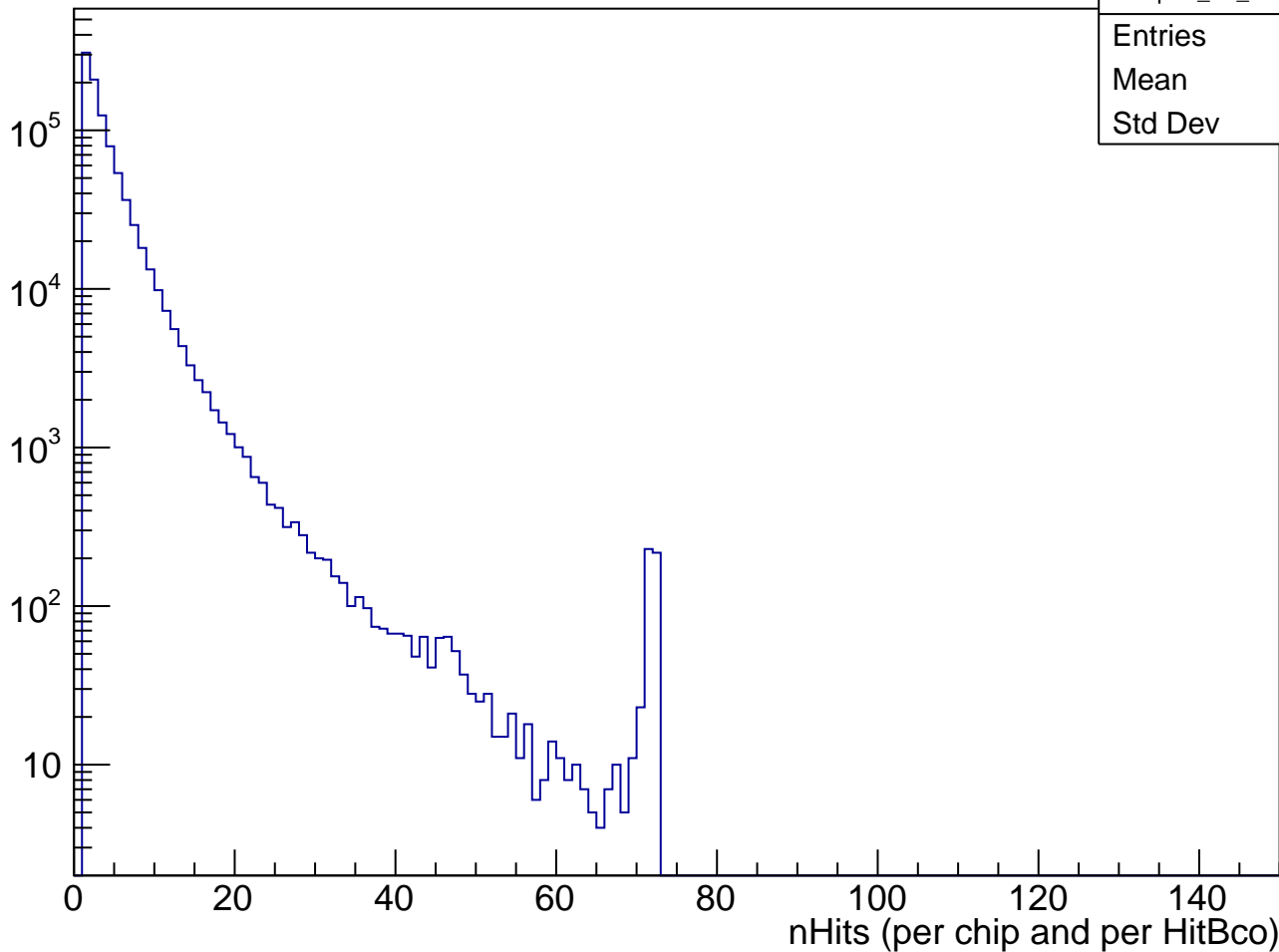
Entries



nChipHit_F7_Fch4_Chip16	
Entries	902730
Mean	3.374
Std Dev	3.955

nChipHit_F7_Fch4_Chip17

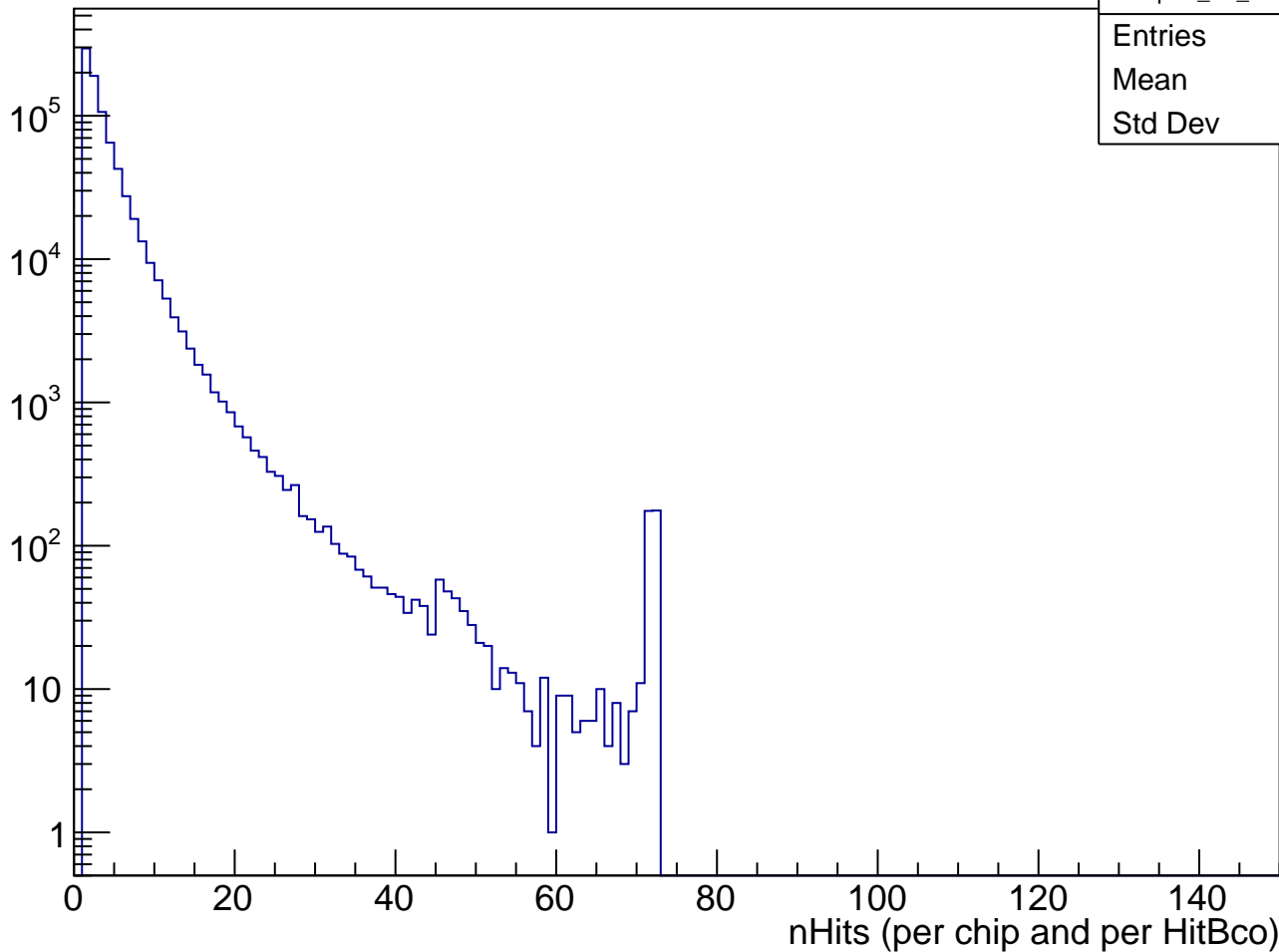
Entries



nChipHit_F7_Fch4_Chip17	
Entries	915039
Mean	3.362
Std Dev	3.953

nChipHit_F7_Fch4_Chip18

Entries

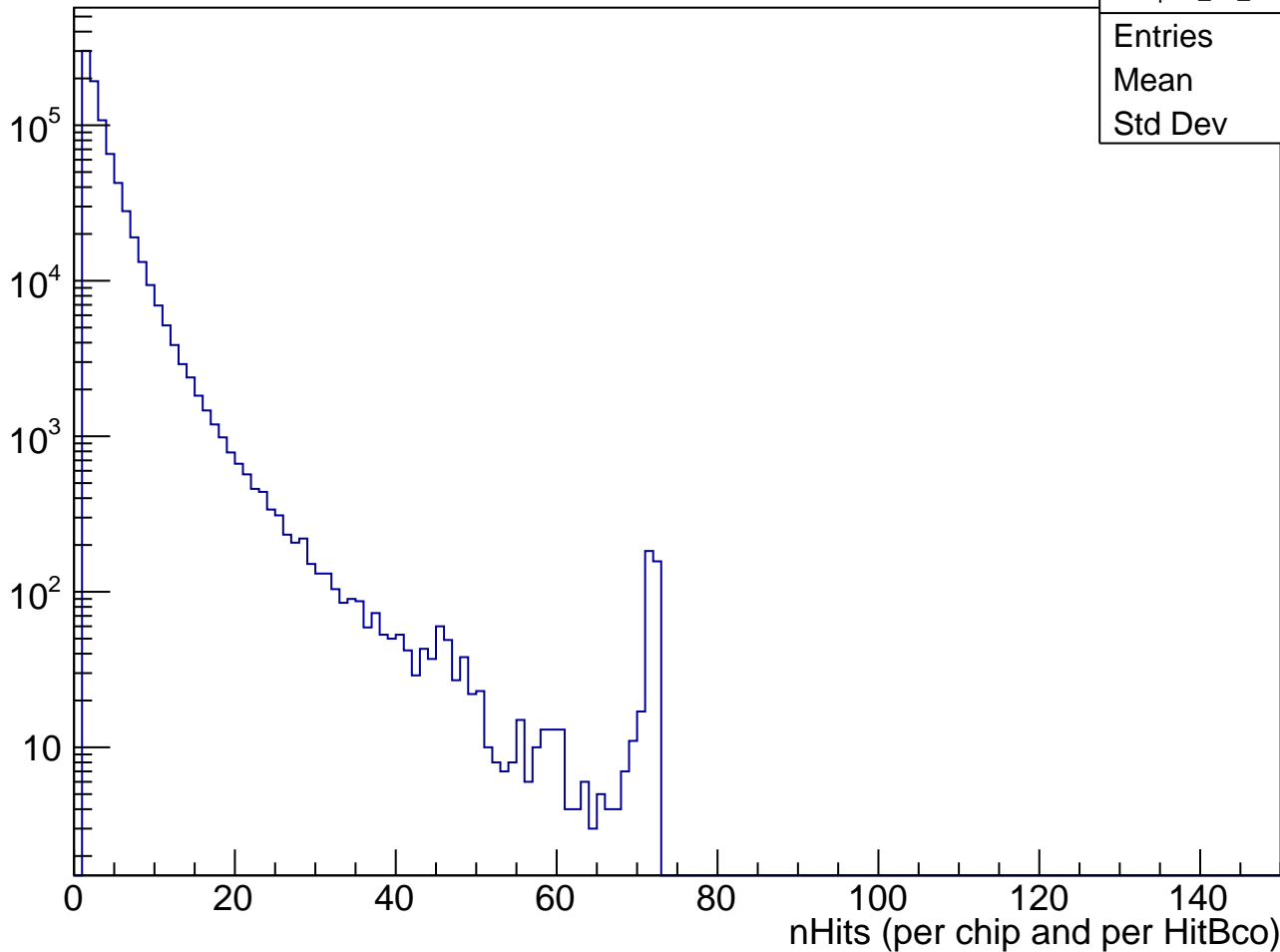


nChipHit_F7_Fch4_Chip18	
Entries	801604
Mean	3.09
Std Dev	3.671

nHits (per chip and per HitBco)

nChipHit_F7_Fch4_Chip19

Entries

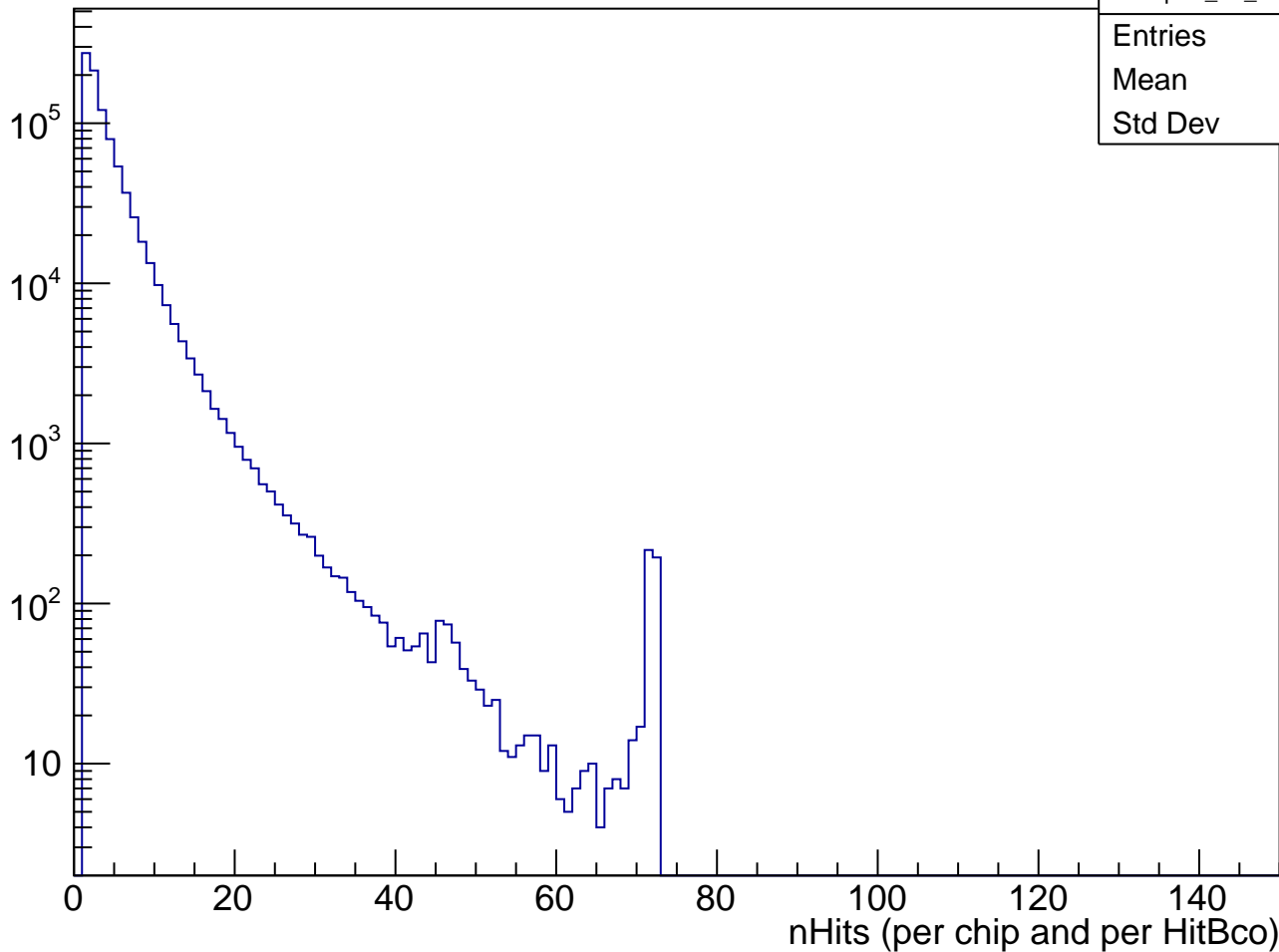


nChipHit_F7_Fch4_Chip19	
Entries	811131
Mean	3.066
Std Dev	3.645

nHits (per chip and per HitBco)

nChipHit_F7_Fch4_Chip2

Entries



nChipHit_F7_Fch4_Chip2

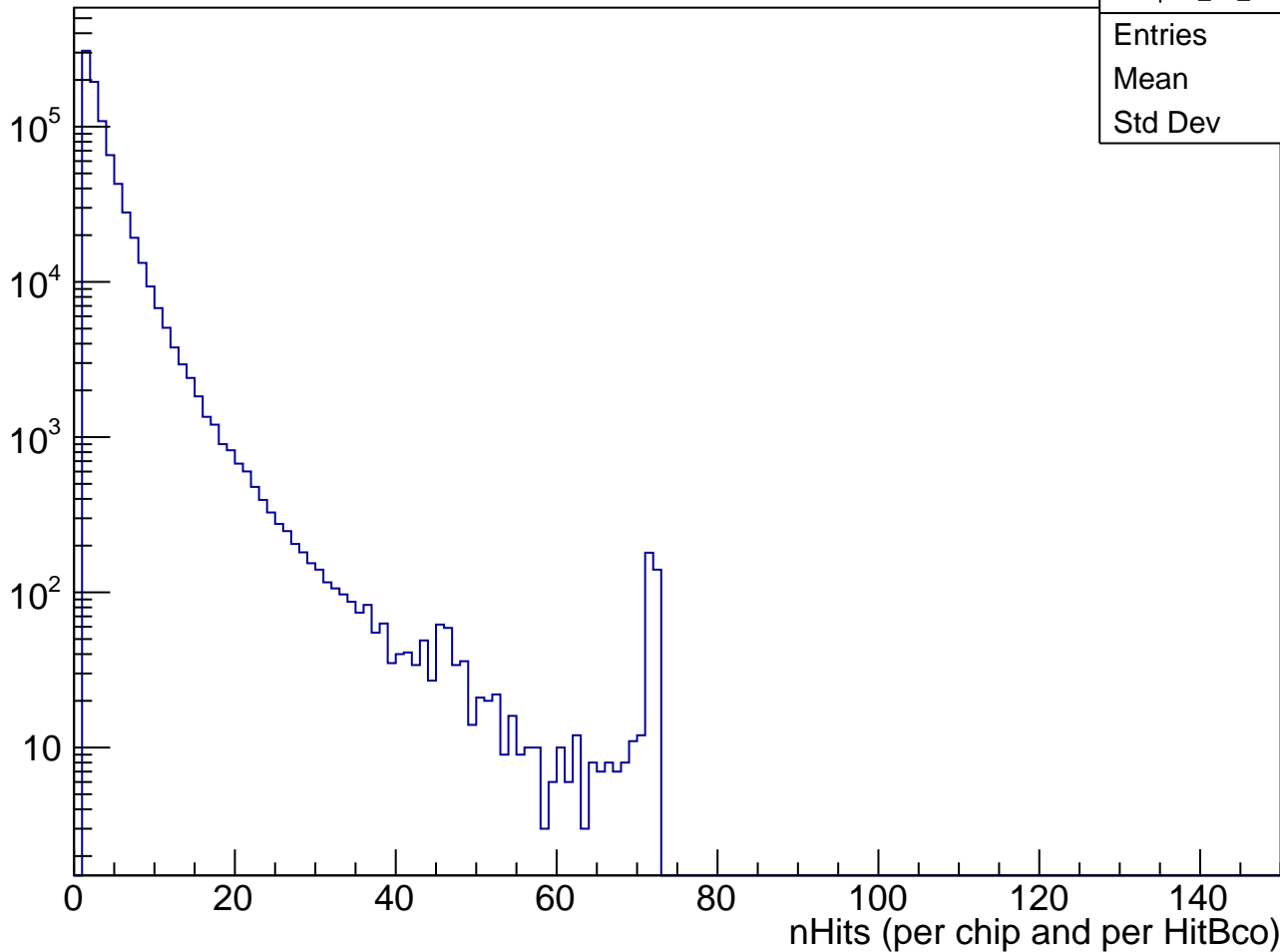
Entries 883215

Mean 3.448

Std Dev 3.976

nChipHit_F7_Fch4_Chip20

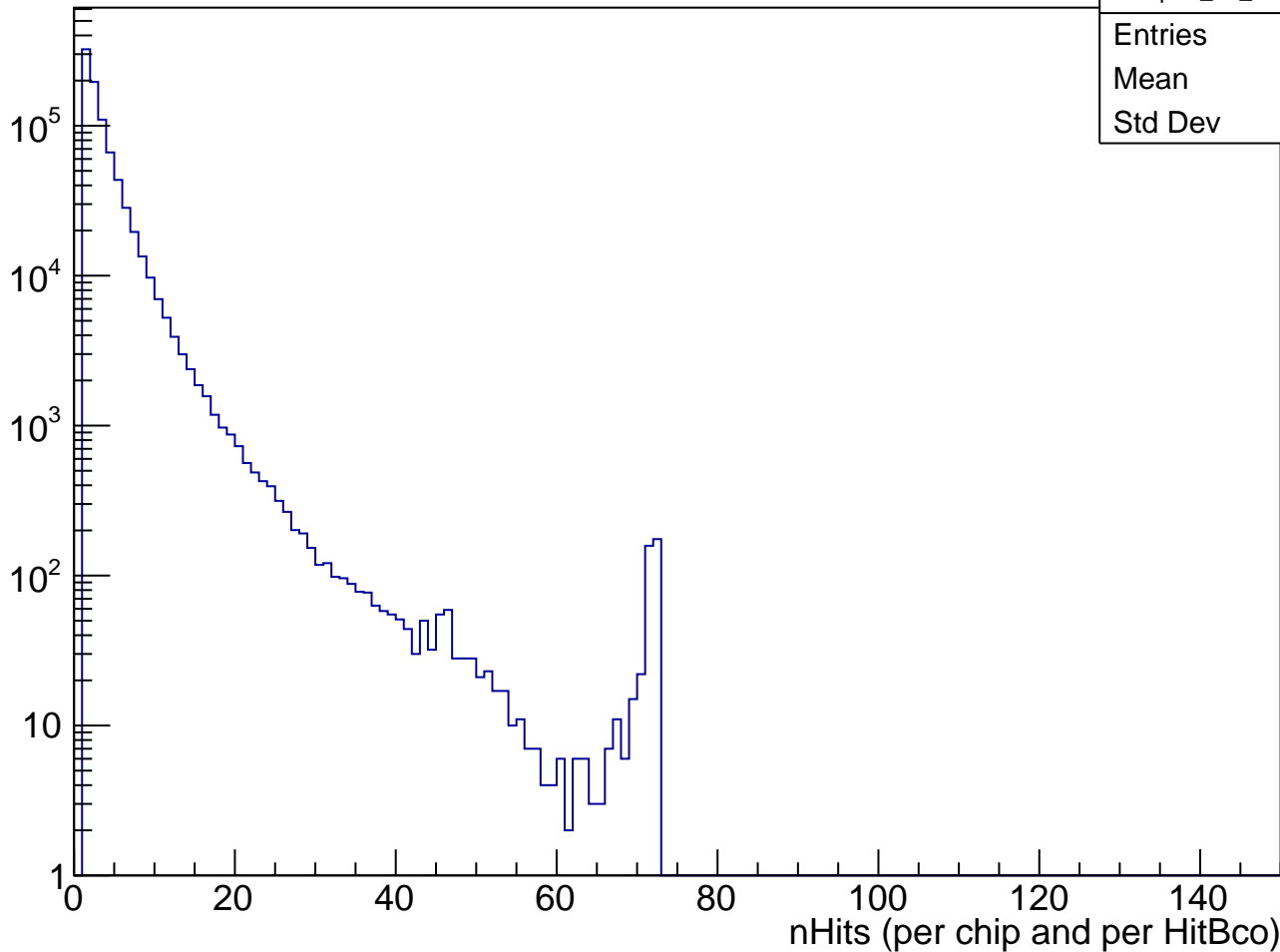
Entries



nChipHit_F7_Fch4_Chip20	
Entries	822104
Mean	3.039
Std Dev	3.608

nChipHit_F7_Fch4_Chip21

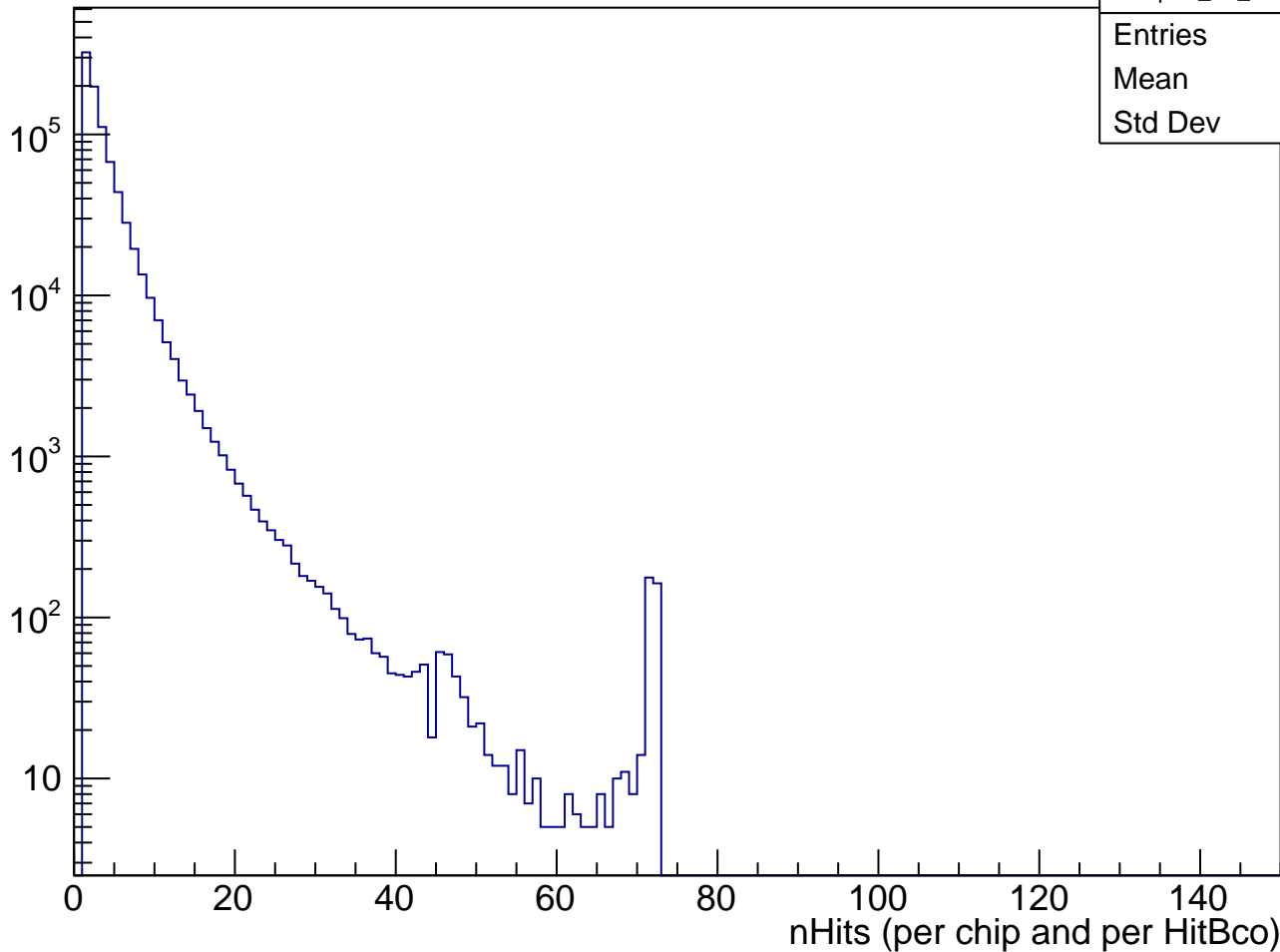
Entries



nChipHit_F7_Fch4_Chip21	
Entries	843596
Mean	3.025
Std Dev	3.617

nChipHit_F7_Fch4_Chip22

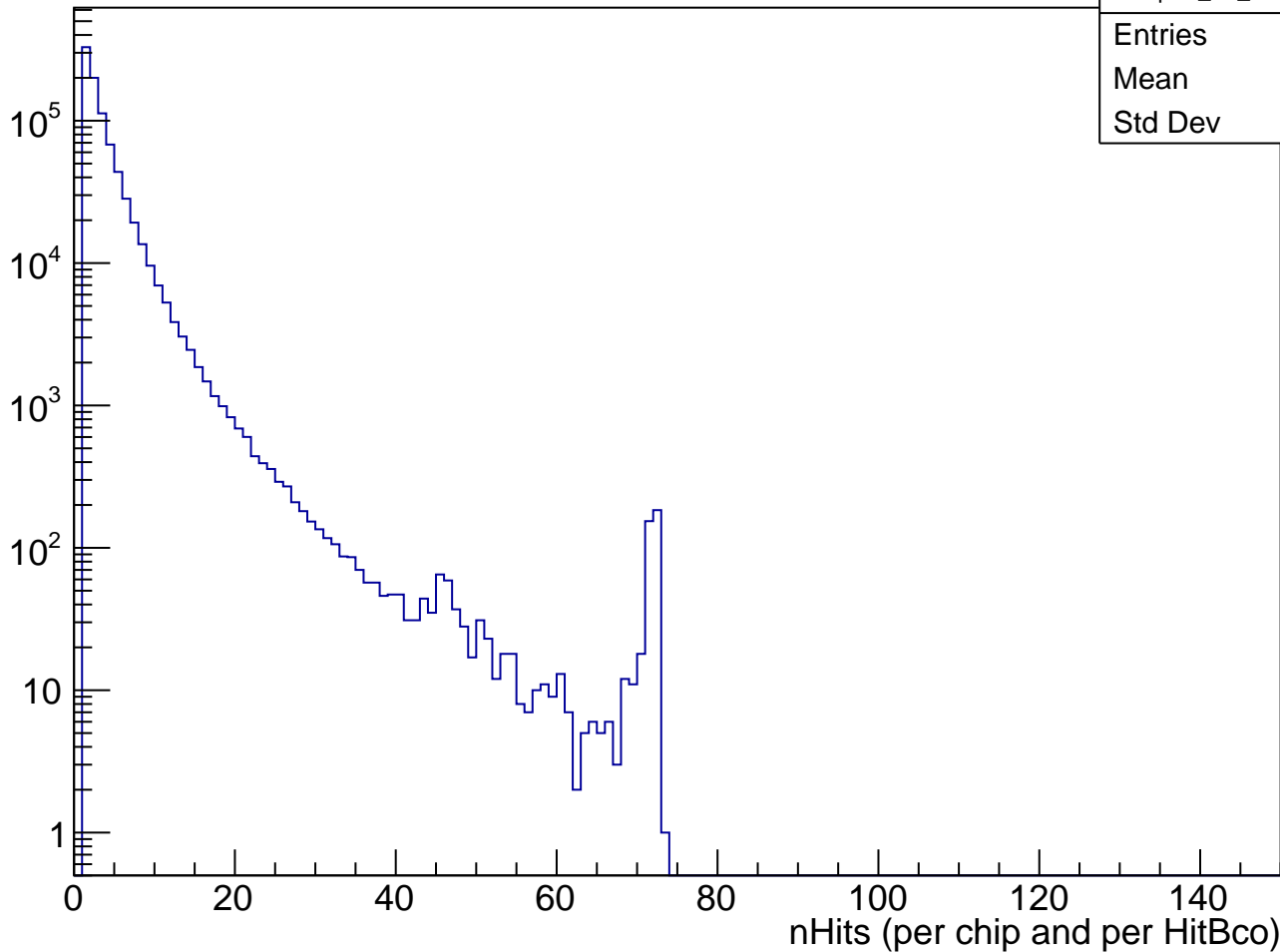
Entries



nChipHit_F7_Fch4_Chip22	
Entries	848262
Mean	3.025
Std Dev	3.61

nChipHit_F7_Fch4_Chip23

Entries

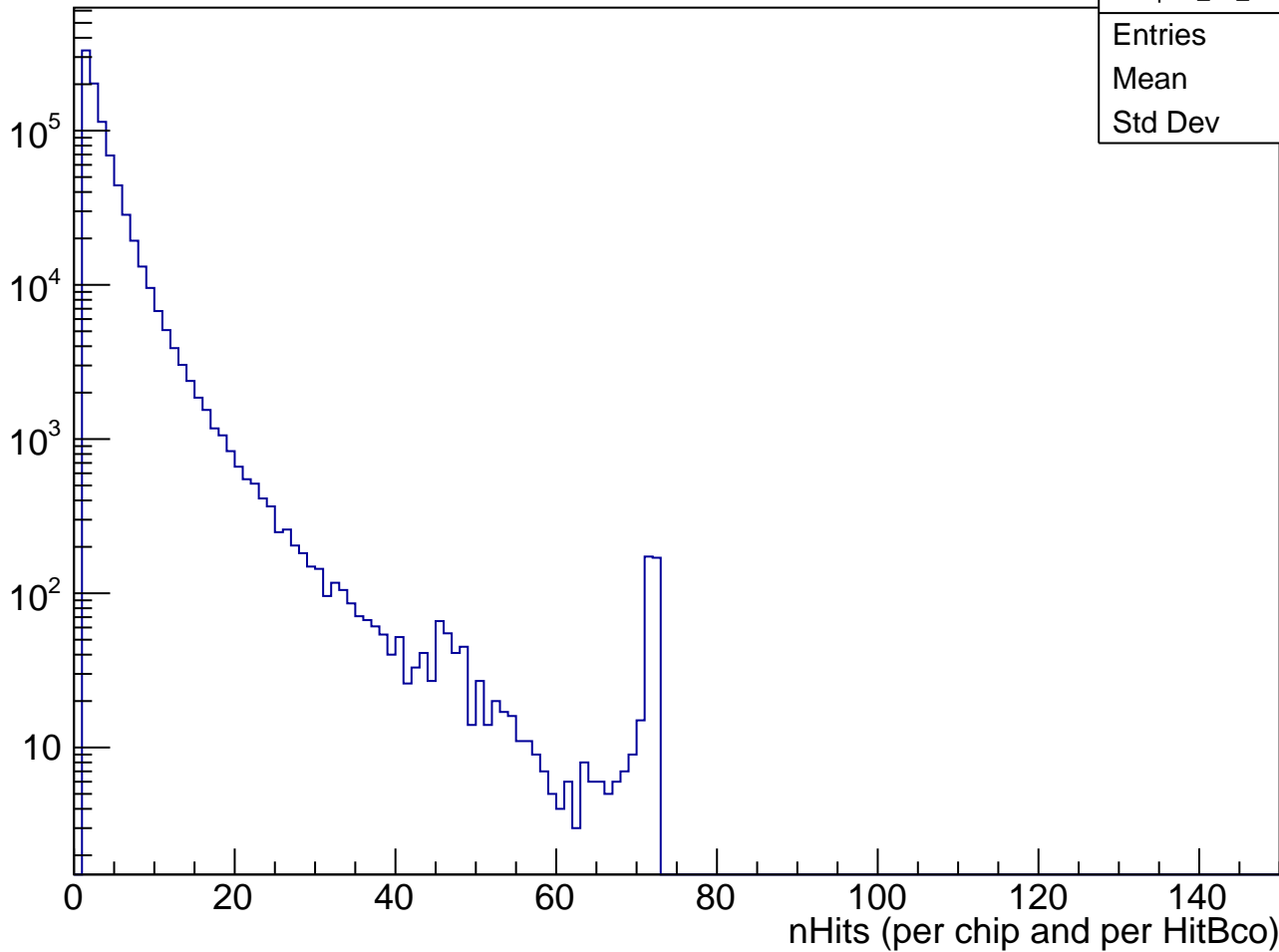


nChipHit_F7_Fch4_Chip23	
Entries	856852
Mean	3.005
Std Dev	3.585

nHits (per chip and per HitBco)

nChipHit_F7_Fch4_Chip24

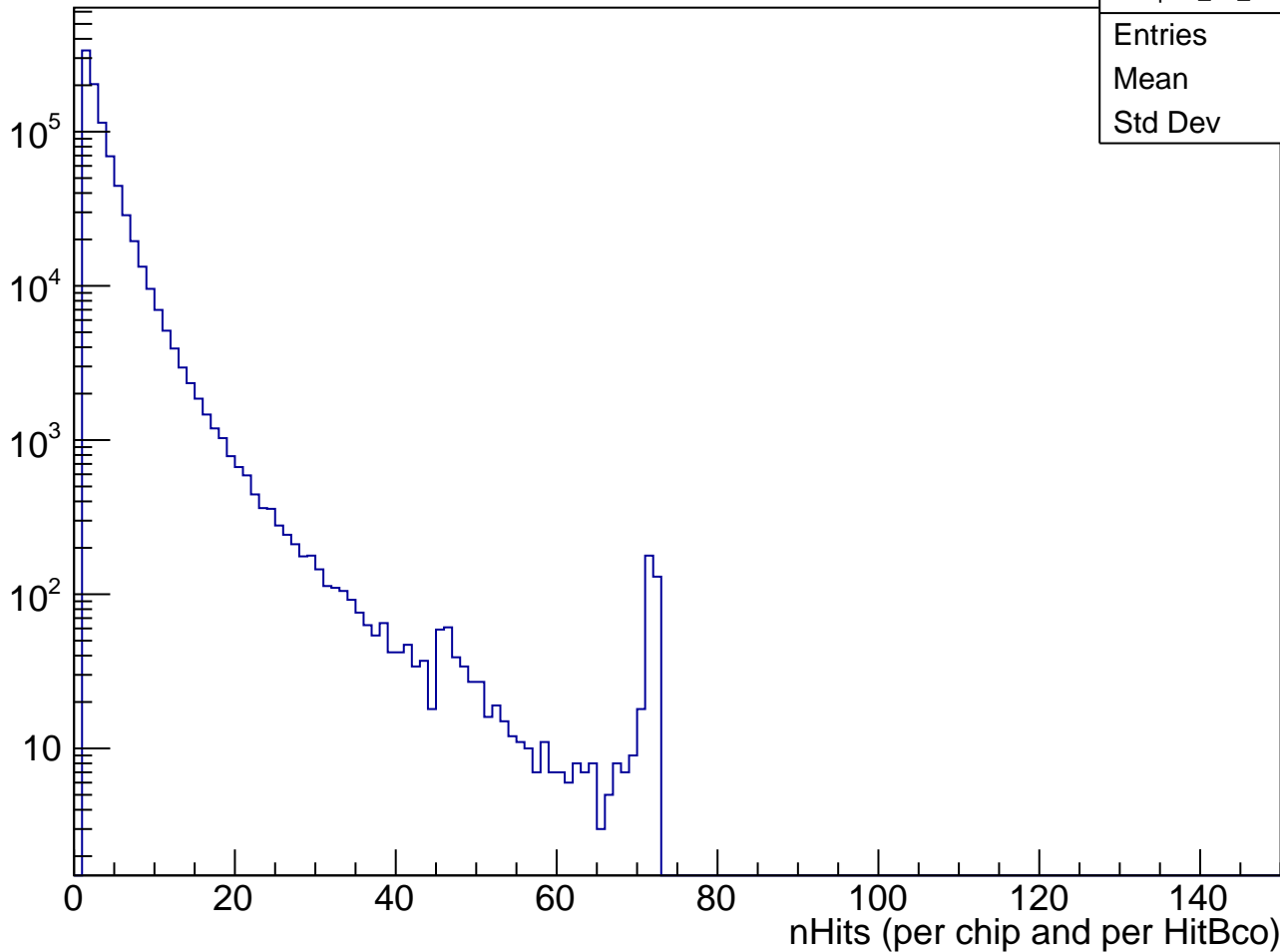
Entries



nChipHit_F7_Fch4_Chip24	
Entries	864156
Mean	2.994
Std Dev	3.566

nChipHit_F7_Fch4_Chip25

Entries

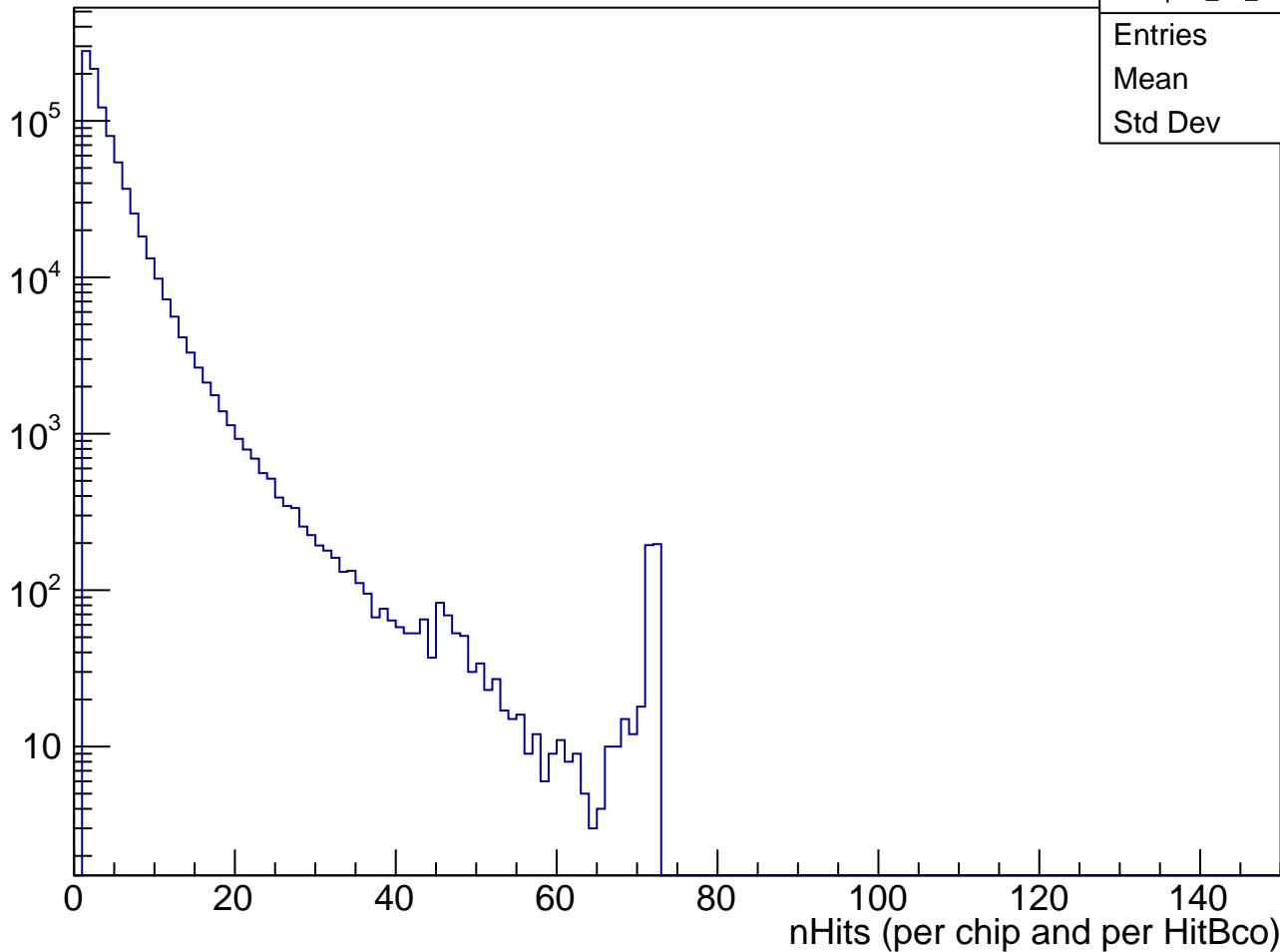


nChipHit_F7_Fch4_Chip25	
Entries	871952
Mean	2.981
Std Dev	3.532

nHits (per chip and per HitBco)

nChipHit_F7_Fch4_Chip3

Entries



nChipHit_F7_Fch4_Chip3

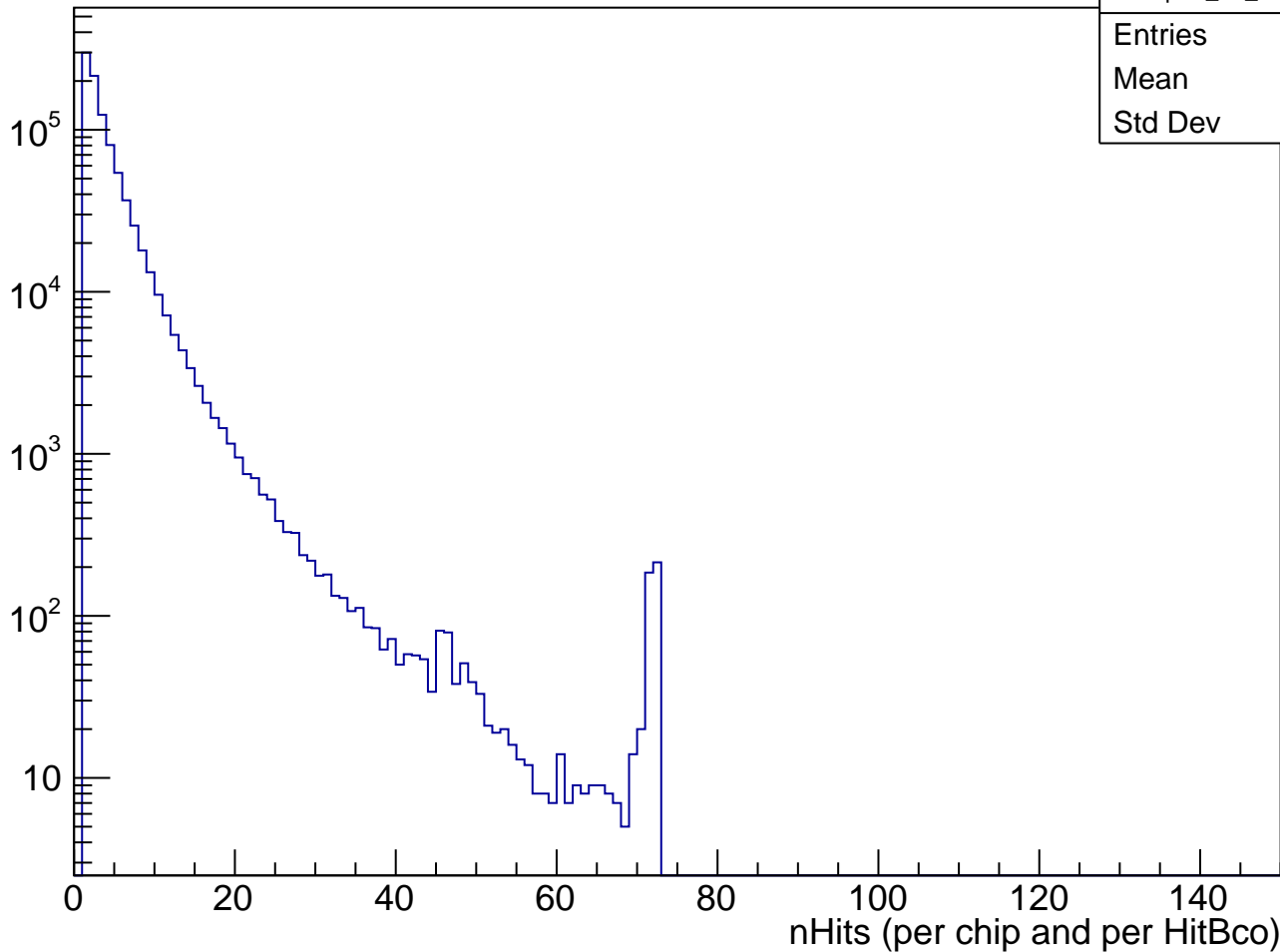
Entries 891533

Mean 3.425

Std Dev 3.948

nChipHit_F7_Fch4_Chip4

Entries



nChipHit_F7_Fch4_Chip4

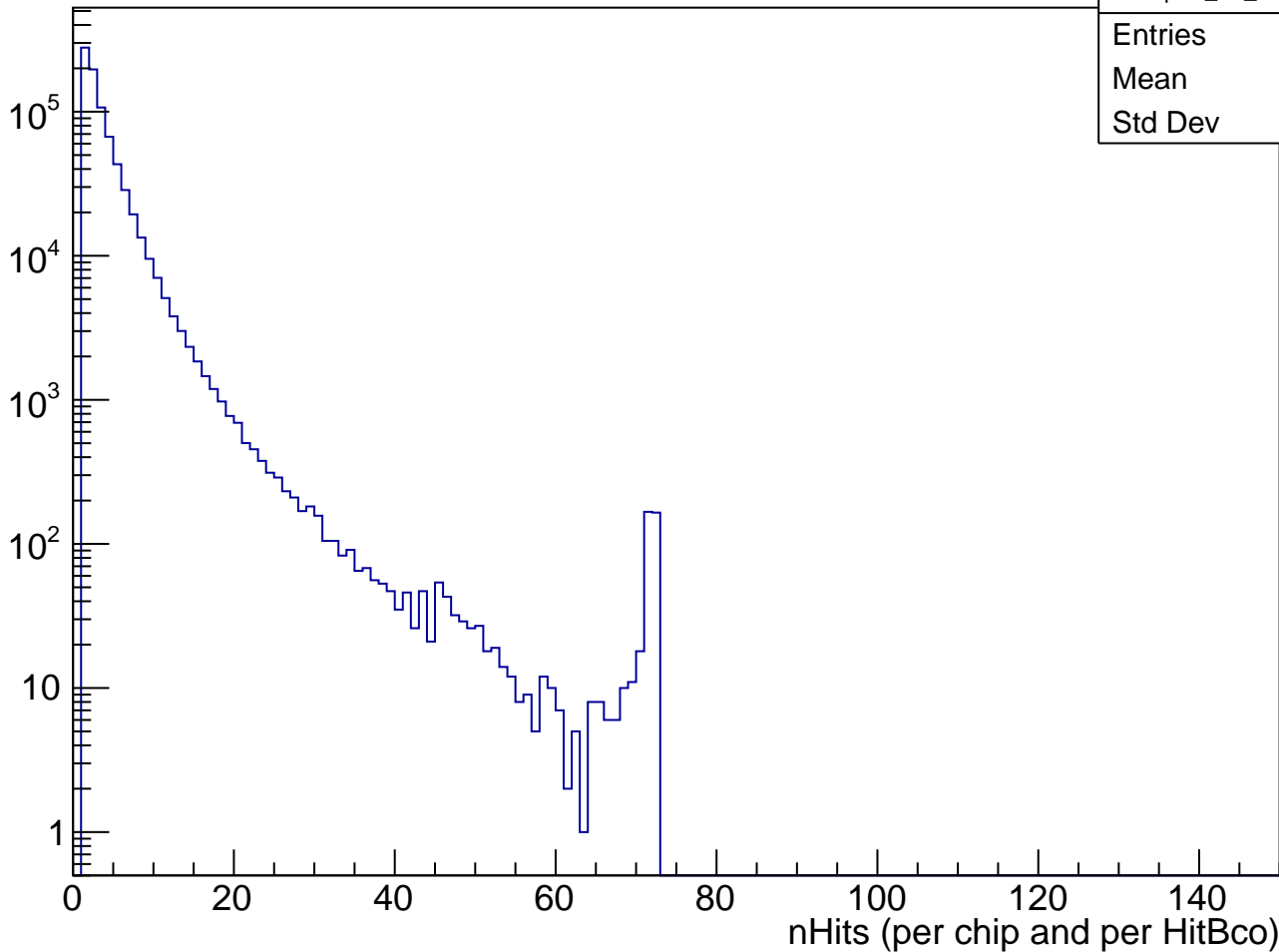
Entries 912968

Mean 3.364

Std Dev 3.904

nChipHit_F7_Fch4_Chip5

Entries



nChipHit_F7_Fch4_Chip5

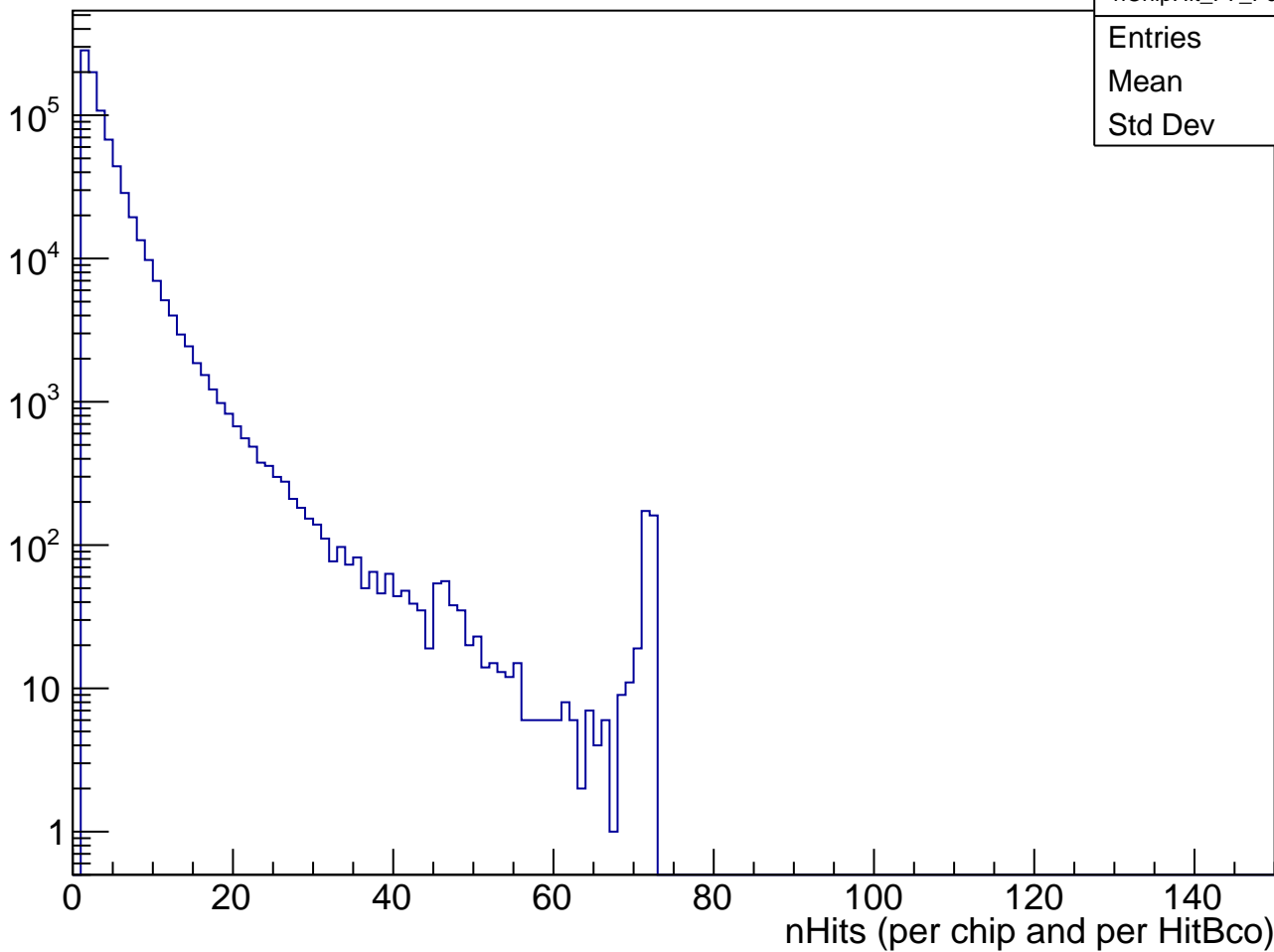
Entries 795564

Mean 3.121

Std Dev 3.643

nChipHit_F7_Fch4_Chip6

Entries



nChipHit_F7_Fch4_Chip6

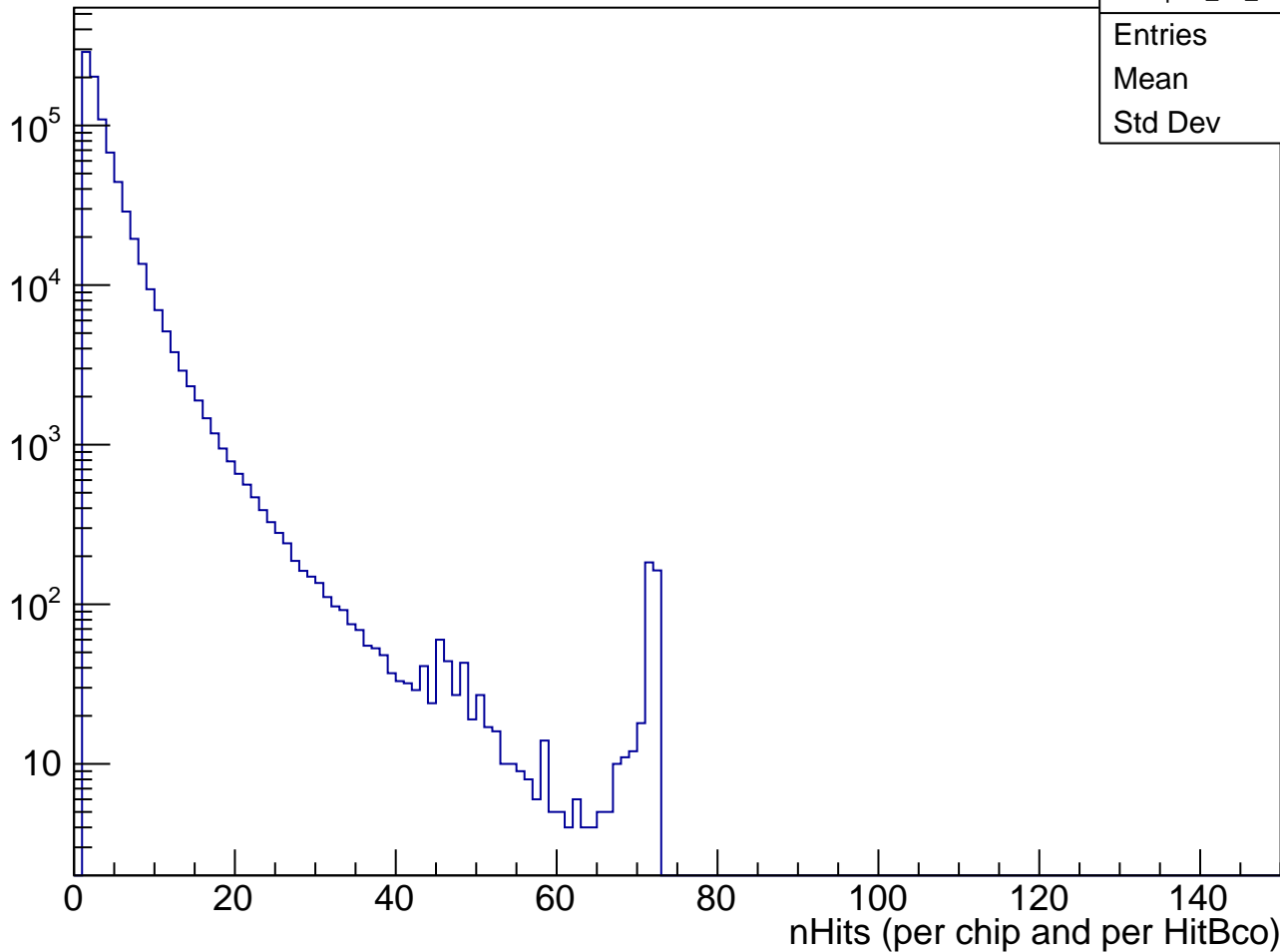
Entries 806925

Mean 3.116

Std Dev 3.64

nChipHit_F7_Fch4_Chip7

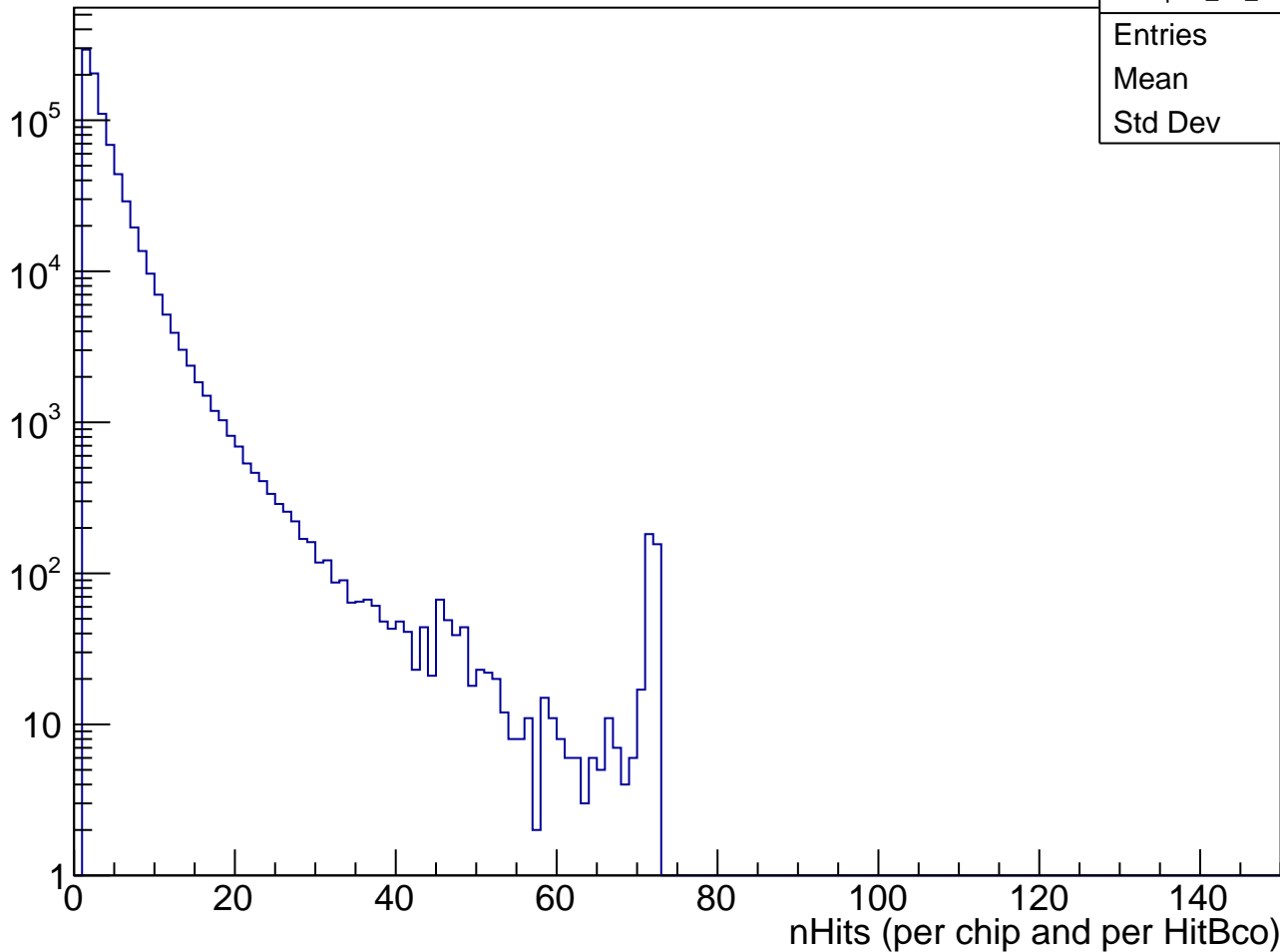
Entries



nChipHit_F7_Fch4_Chip7	
Entries	815760
Mean	3.087
Std Dev	3.6

nChipHit_F7_Fch4_Chip8

Entries



nChipHit_F7_Fch4_Chip8

Entries 825834

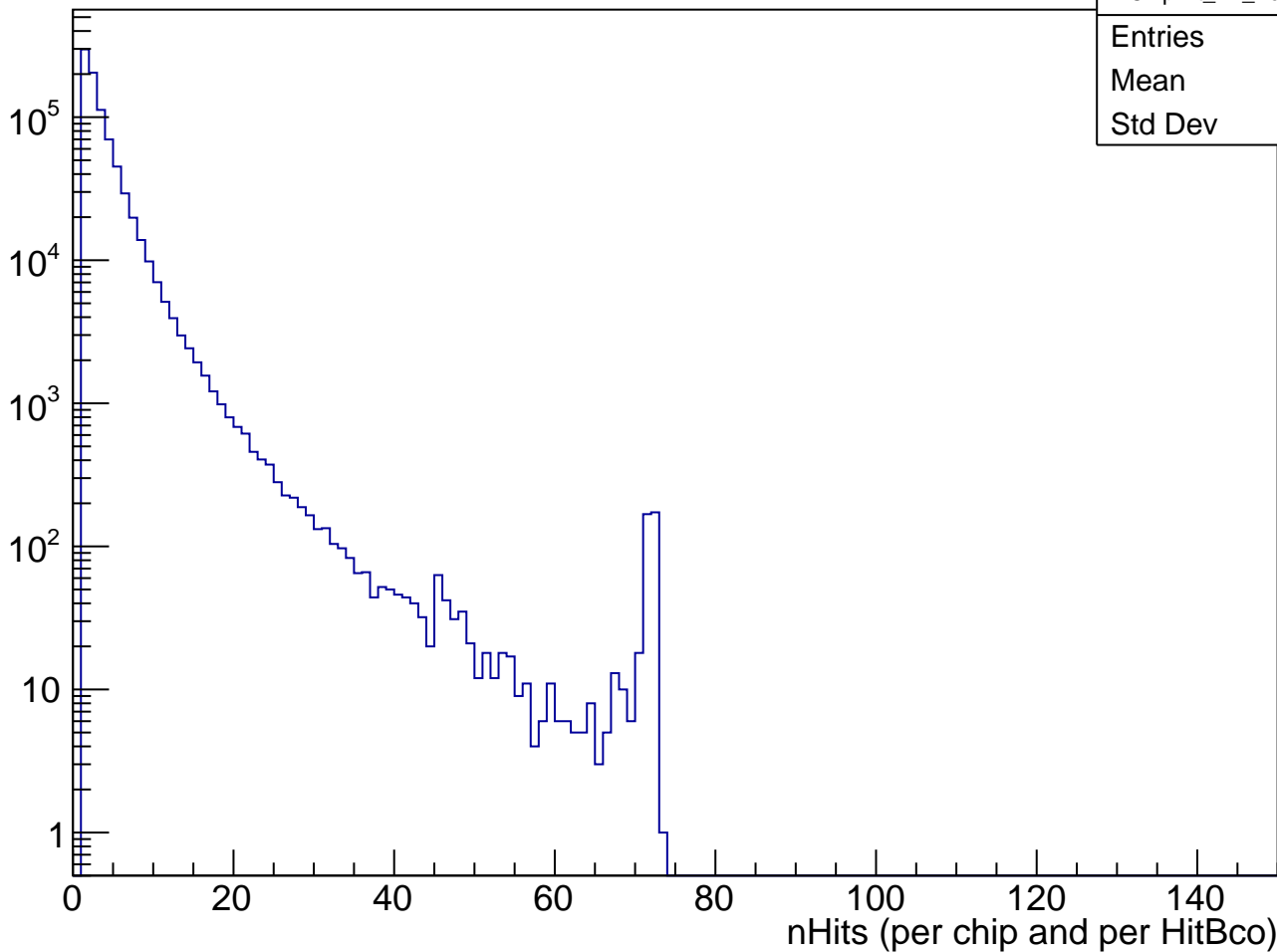
Mean 3.085

Std Dev 3.608

nHits (per chip and per HitBco)

nChipHit_F7_Fch4_Chip9

Entries



nChipHit_F7_Fch4_Chip9

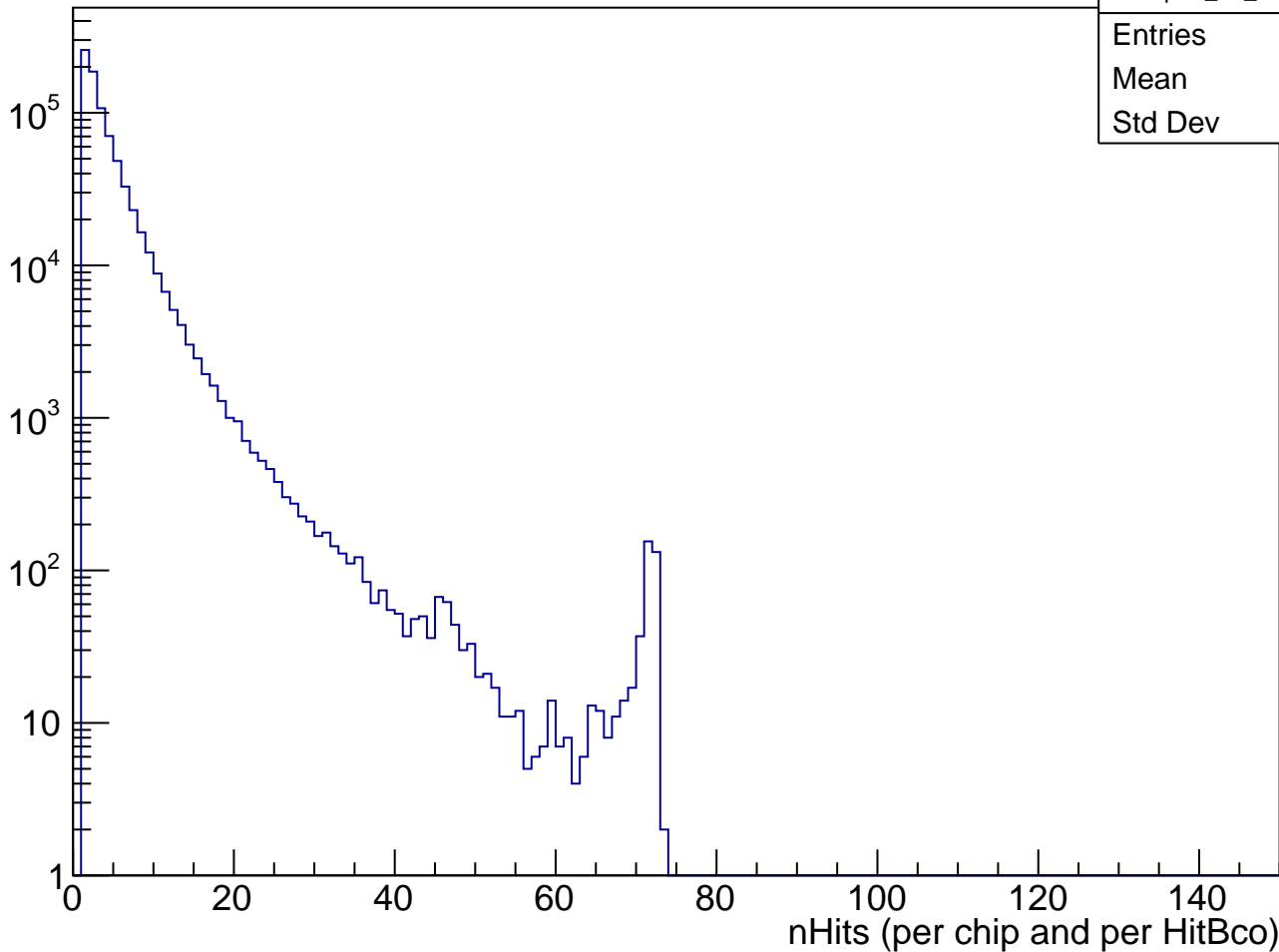
Entries 836106

Mean 3.088

Std Dev 3.605

nChipHit_F7_Fch5_Chip0

Entries

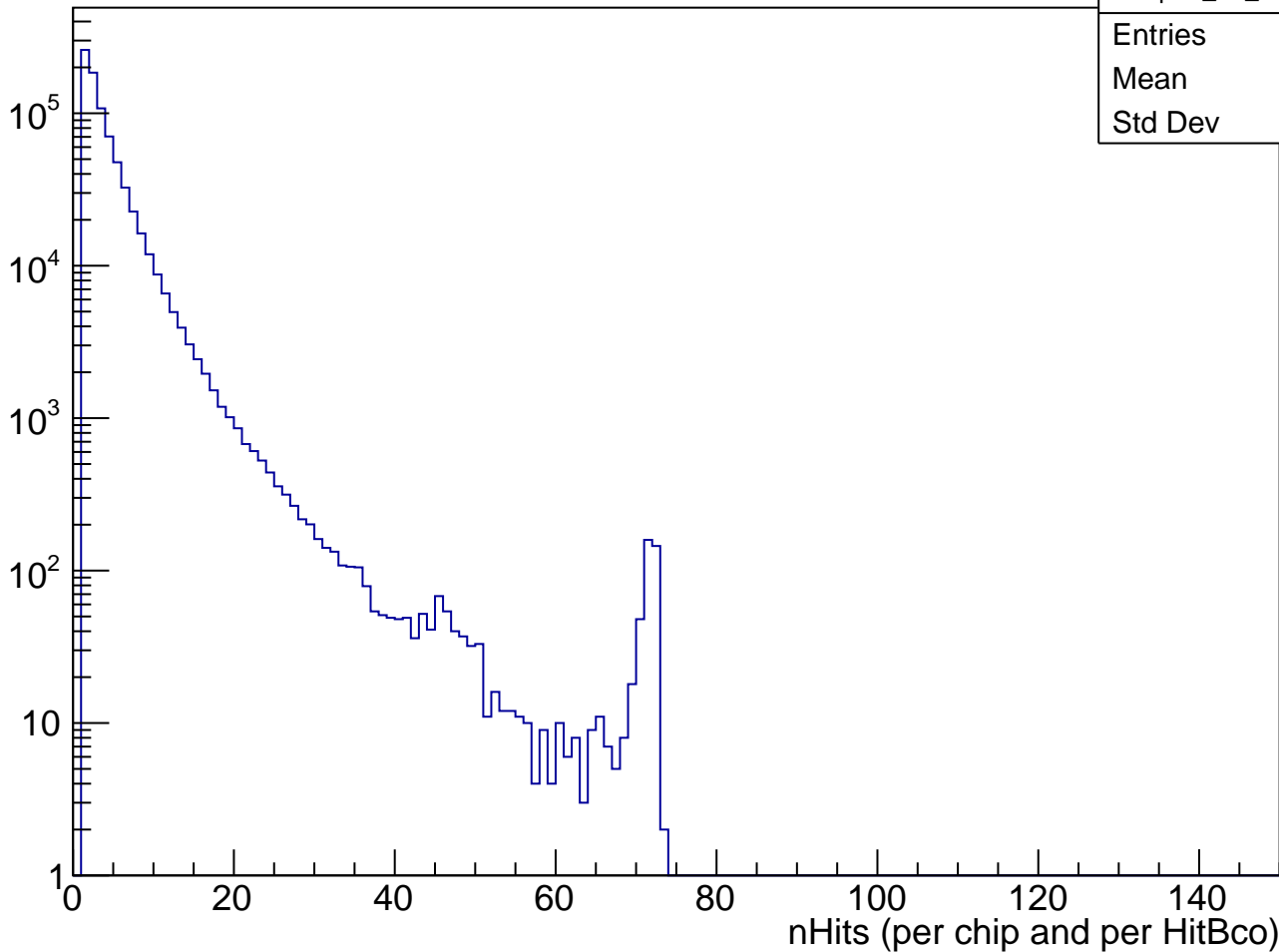


nChipHit_F7_Fch5_Chip0	
Entries	797652
Mean	3.425
Std Dev	3.947

nHits (per chip and per HitBco)

nChipHit_F7_Fch5_Chip1

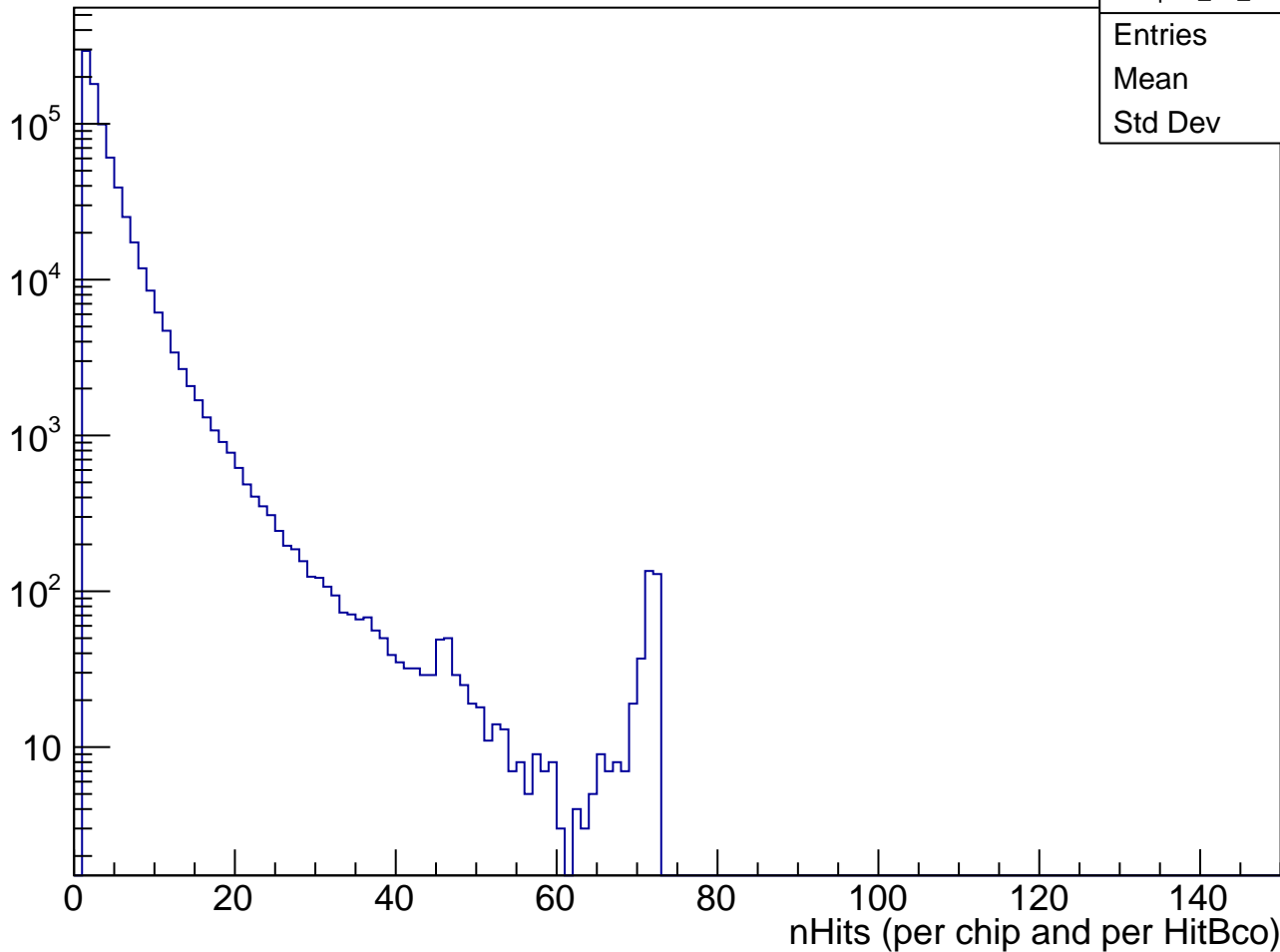
Entries



nChipHit_F7_Fch5_Chip1	
Entries	796086
Mean	3.399
Std Dev	3.917

nChipHit_F7_Fch5_Chip10

Entries

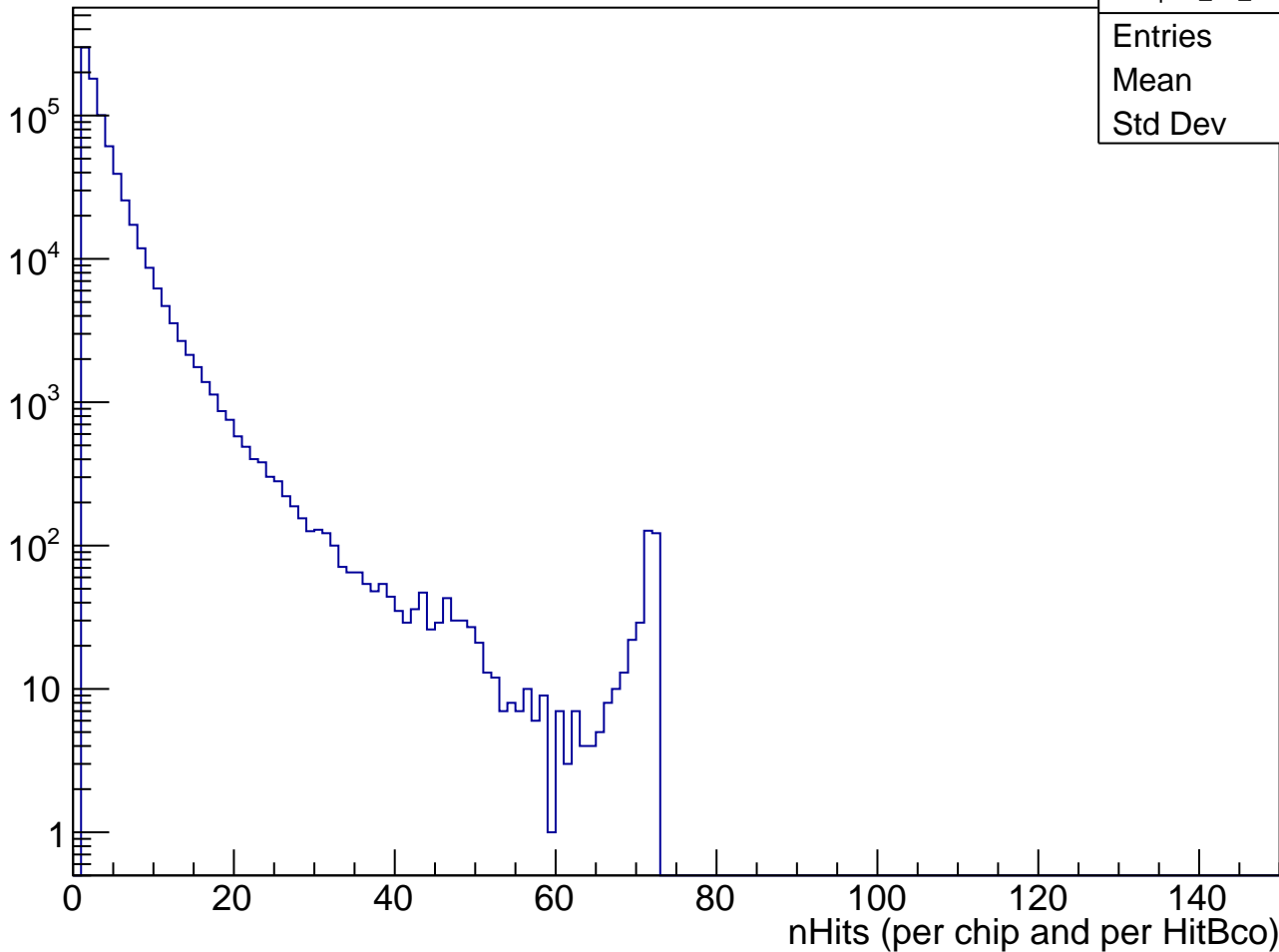


nChipHit_F7_Fch5_Chip10	
Entries	765438
Mean	2.991
Std Dev	3.543

nHits (per chip and per HitBco)

nChipHit_F7_Fch5_Chip11

Entries

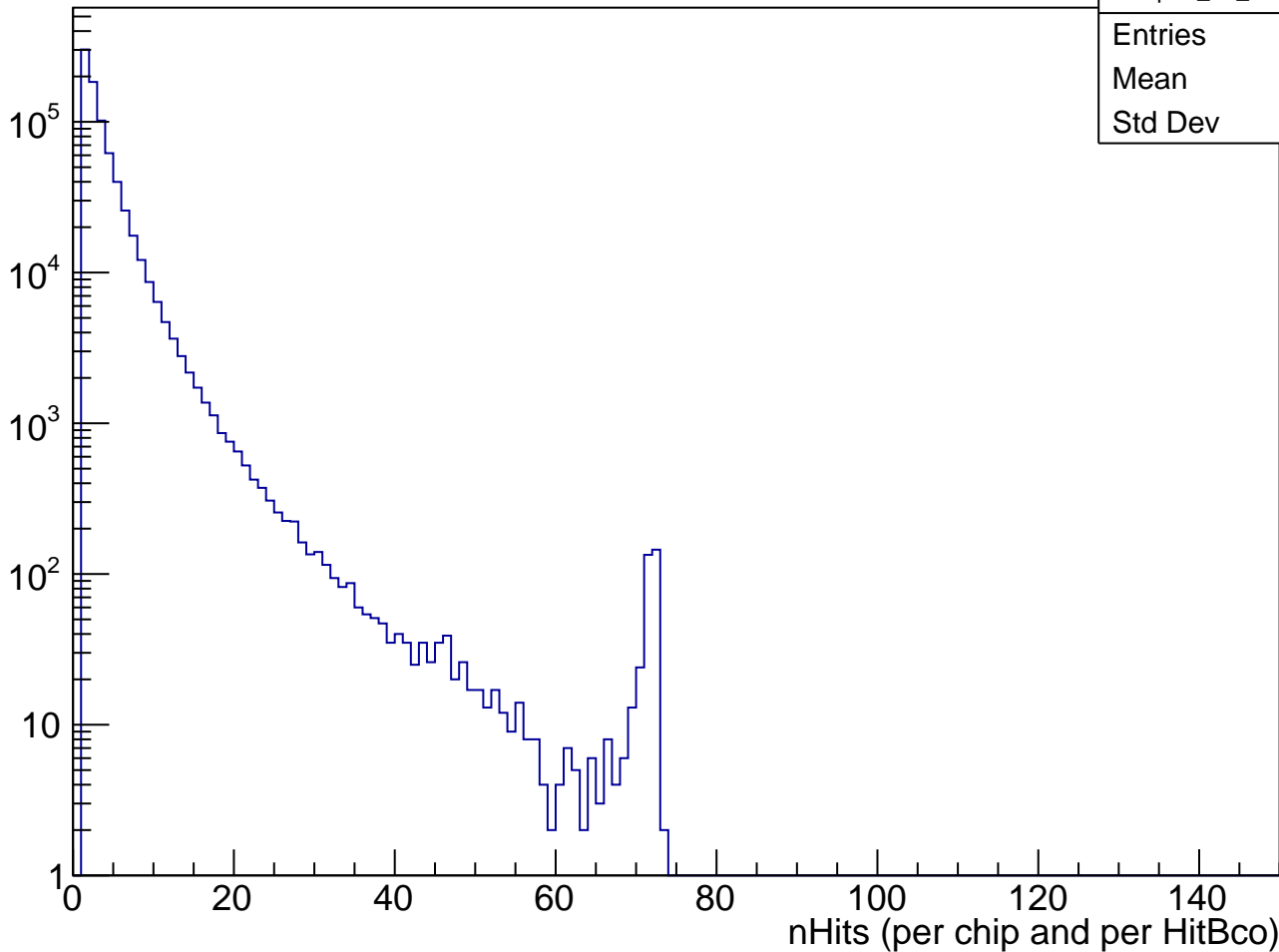


nChipHit_F7_Fch5_Chip11	
Entries	773151
Mean	2.988
Std Dev	3.535

nHits (per chip and per HitBco)

nChipHit_F7_Fch5_Chip12

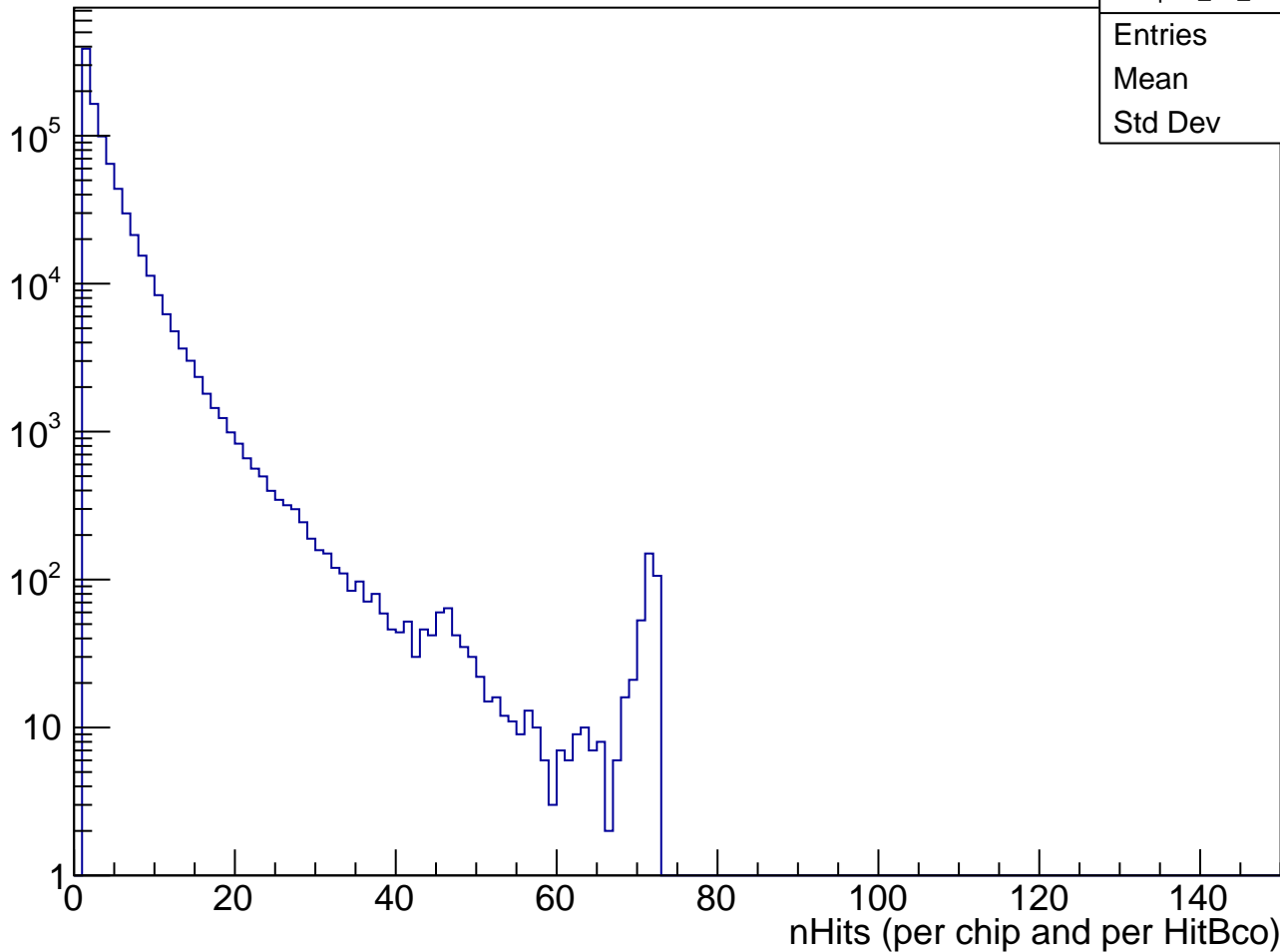
Entries



nChipHit_F7_Fch5_Chip12	
Entries	784203
Mean	2.989
Std Dev	3.53

nChipHit_F7_Fch5_Chip13

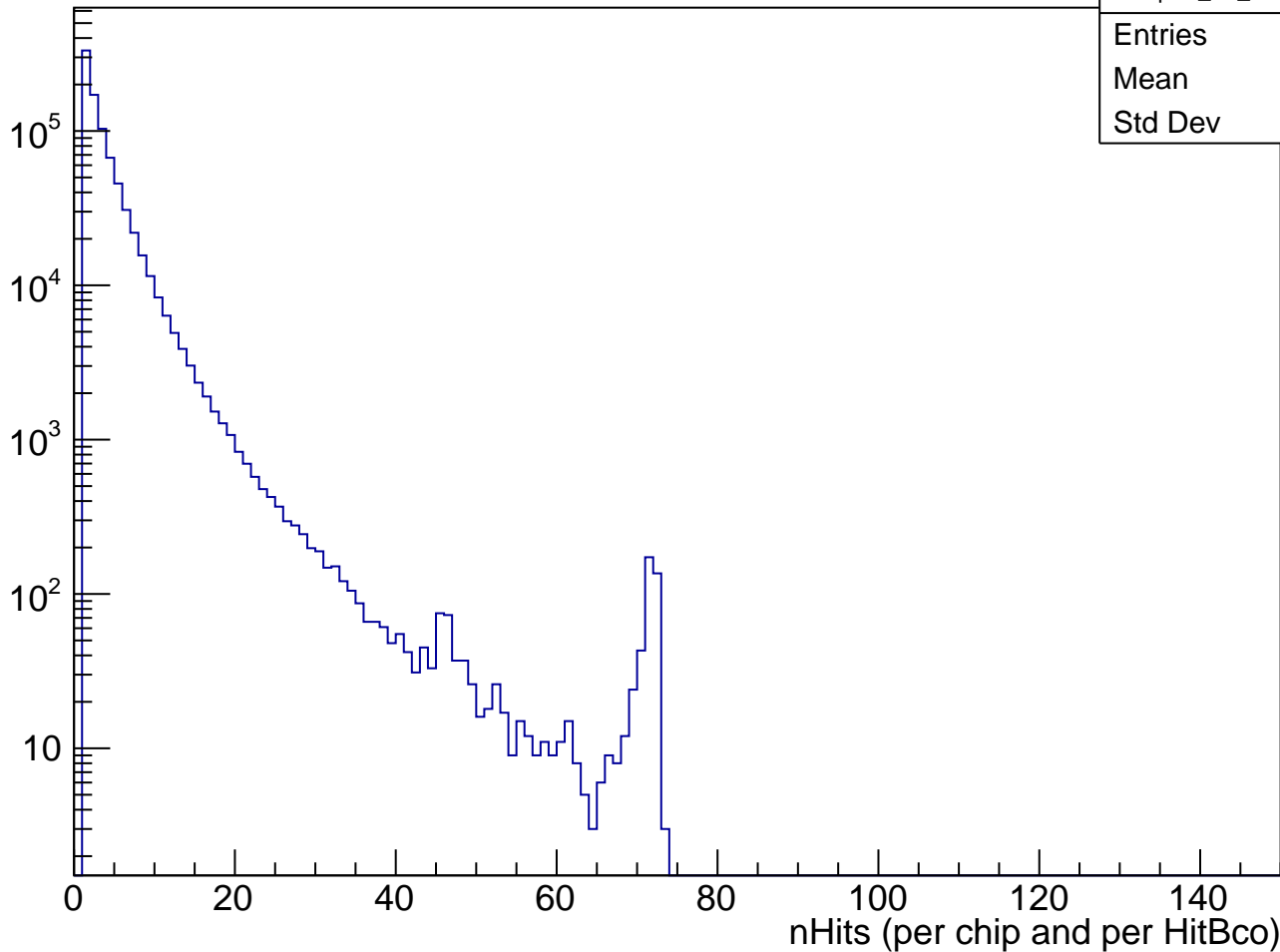
Entries



nChipHit_F7_Fch5_Chip13	
Entries	876809
Mean	3.033
Std Dev	3.749

nChipHit_F7_Fch5_Chip14

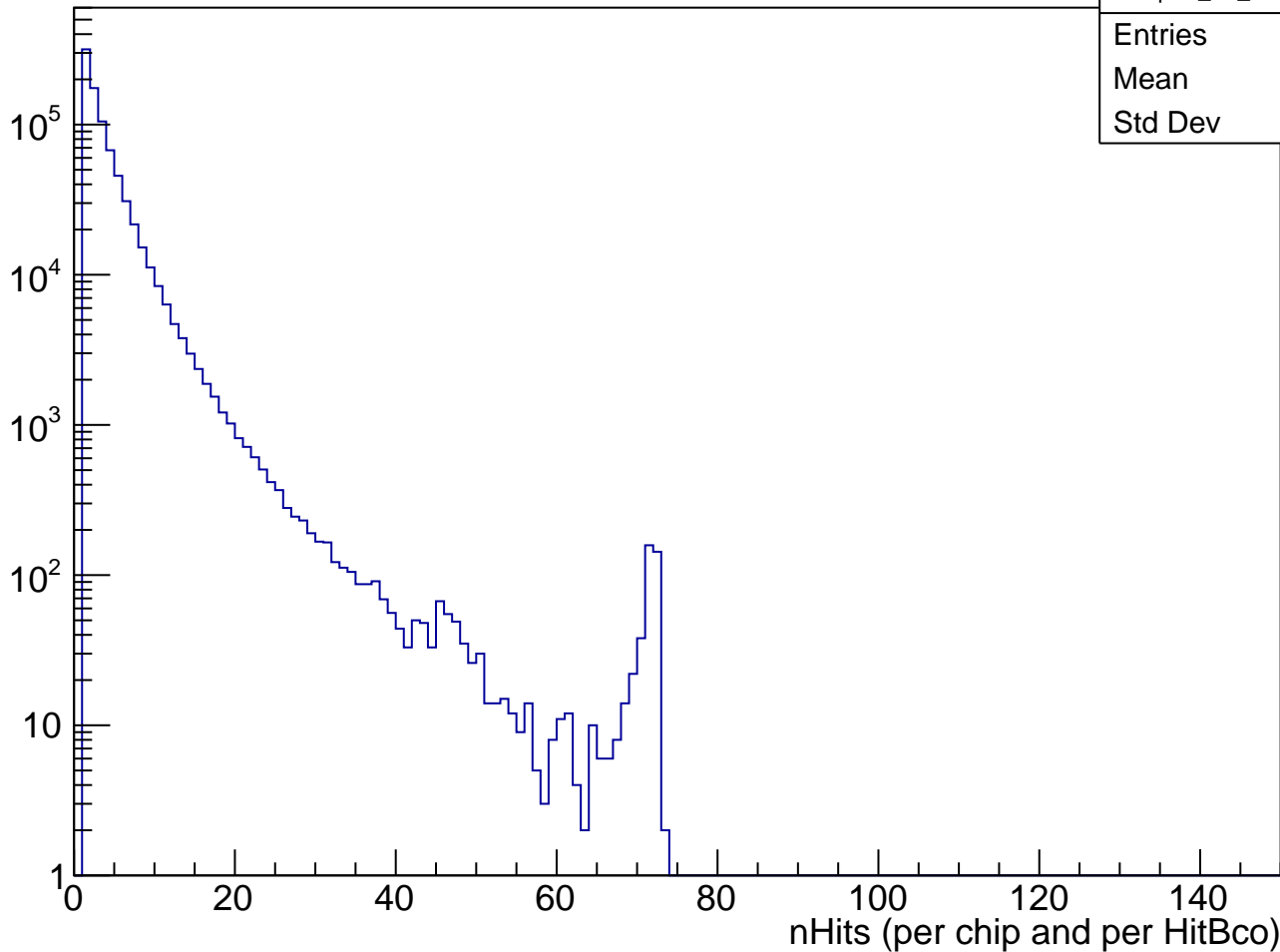
Entries



nChipHit_F7_Fch5_Chip14	
Entries	840779
Mean	3.193
Std Dev	3.875

nChipHit_F7_Fch5_Chip15

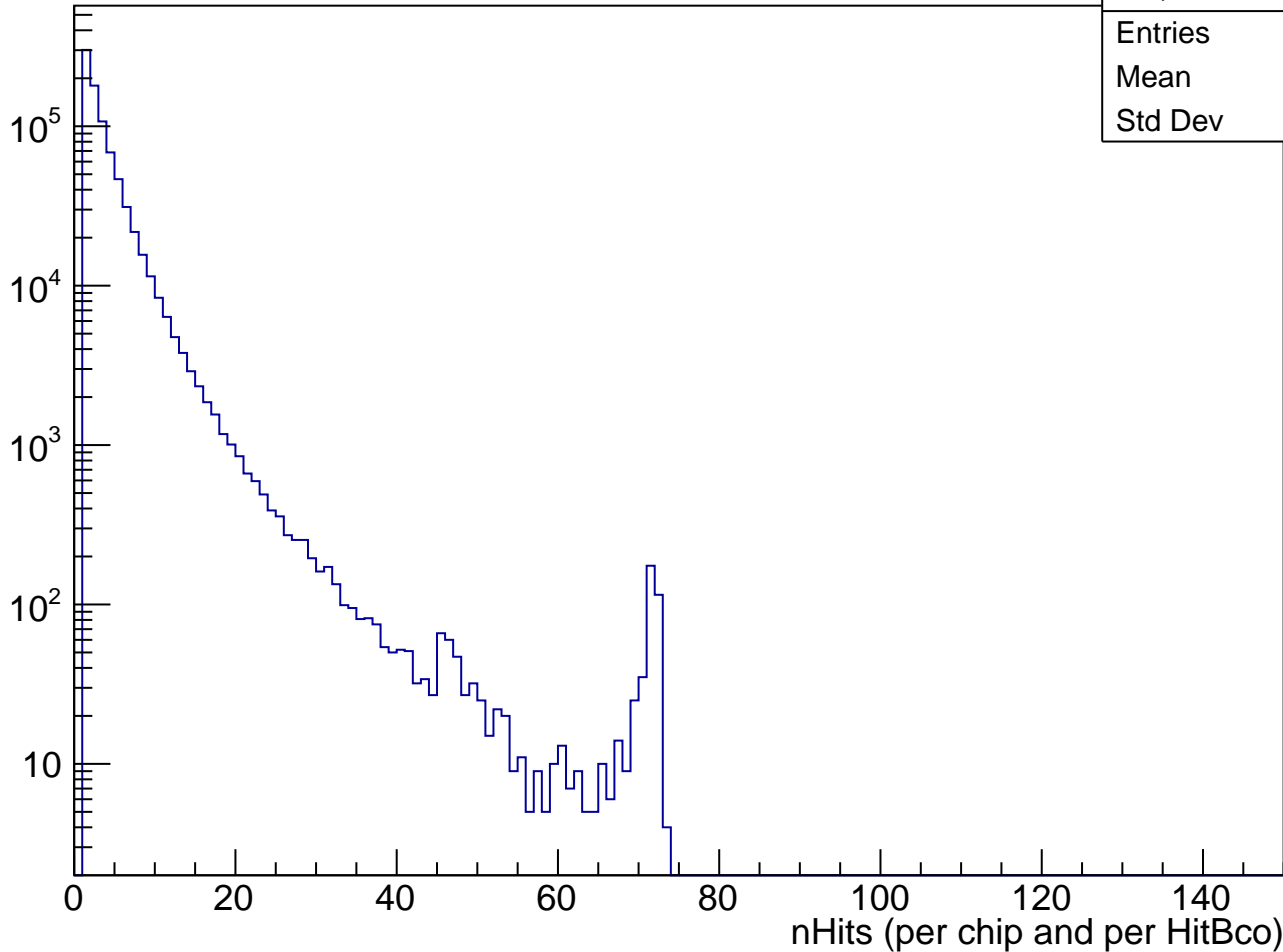
Entries



nChipHit_F7_Fch5_Chip15	
Entries	829536
Mean	3.214
Std Dev	3.856

nChipHit_F7_Fch5_Chip16

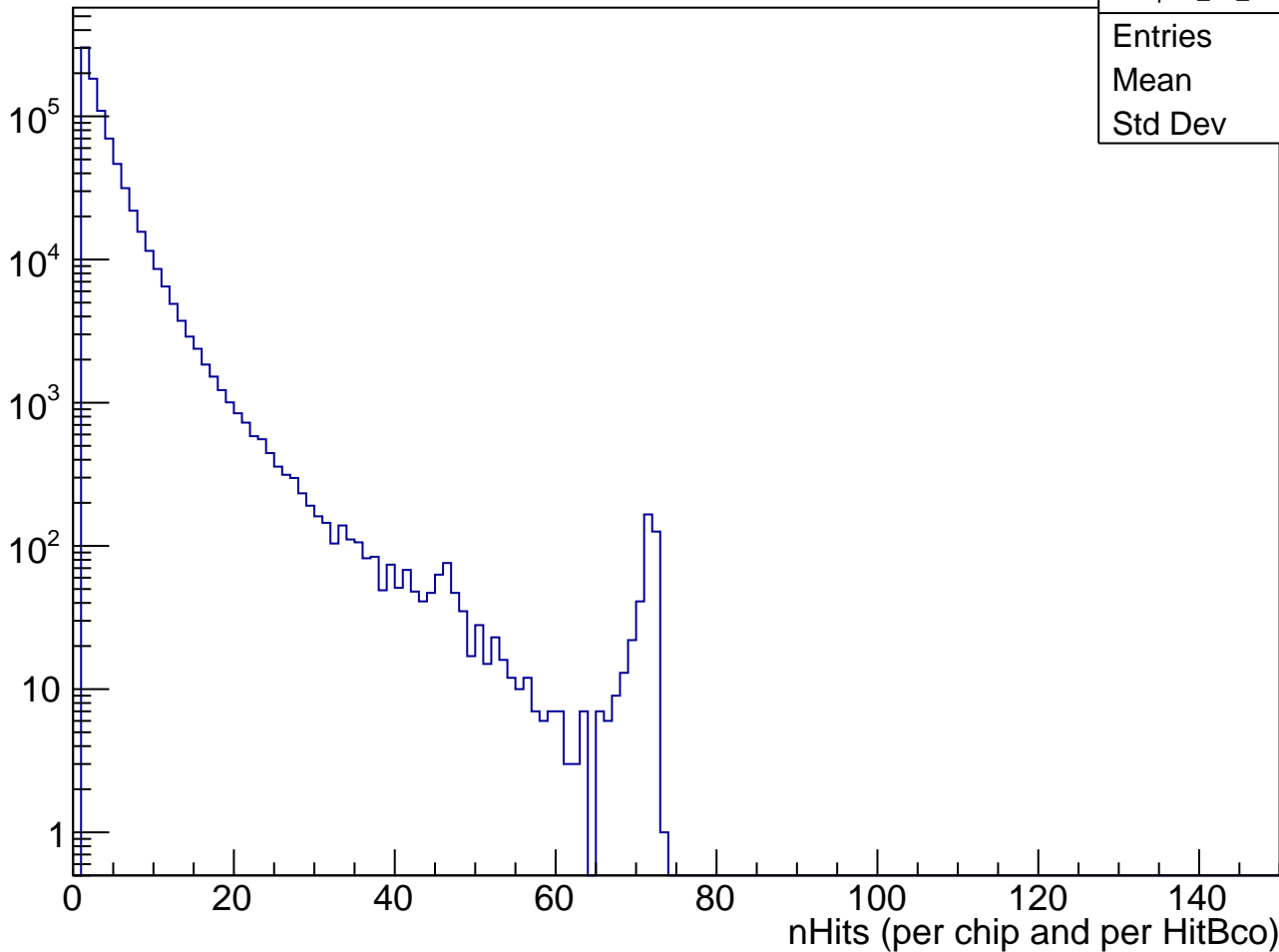
Entries



nChipHit_F7_Fch5_Chip16	
Entries	823987
Mean	3.251
Std Dev	3.842

nChipHit_F7_Fch5_Chip17

Entries

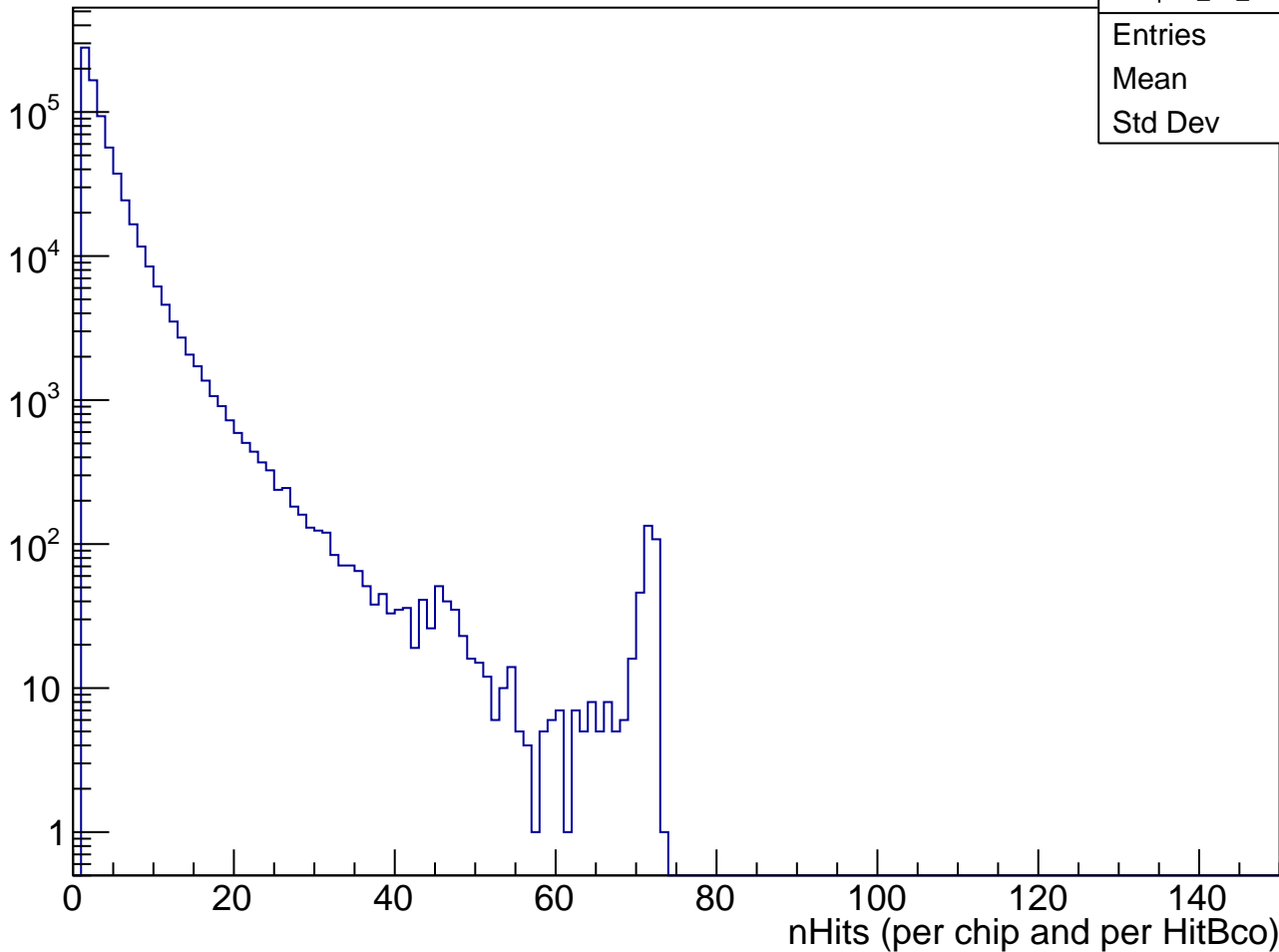


nChipHit_F7_Fch5_Chip17	
Entries	833621
Mean	3.259
Std Dev	3.859

nHits (per chip and per HitBco)

nChipHit_F7_Fch5_Chip18

Entries

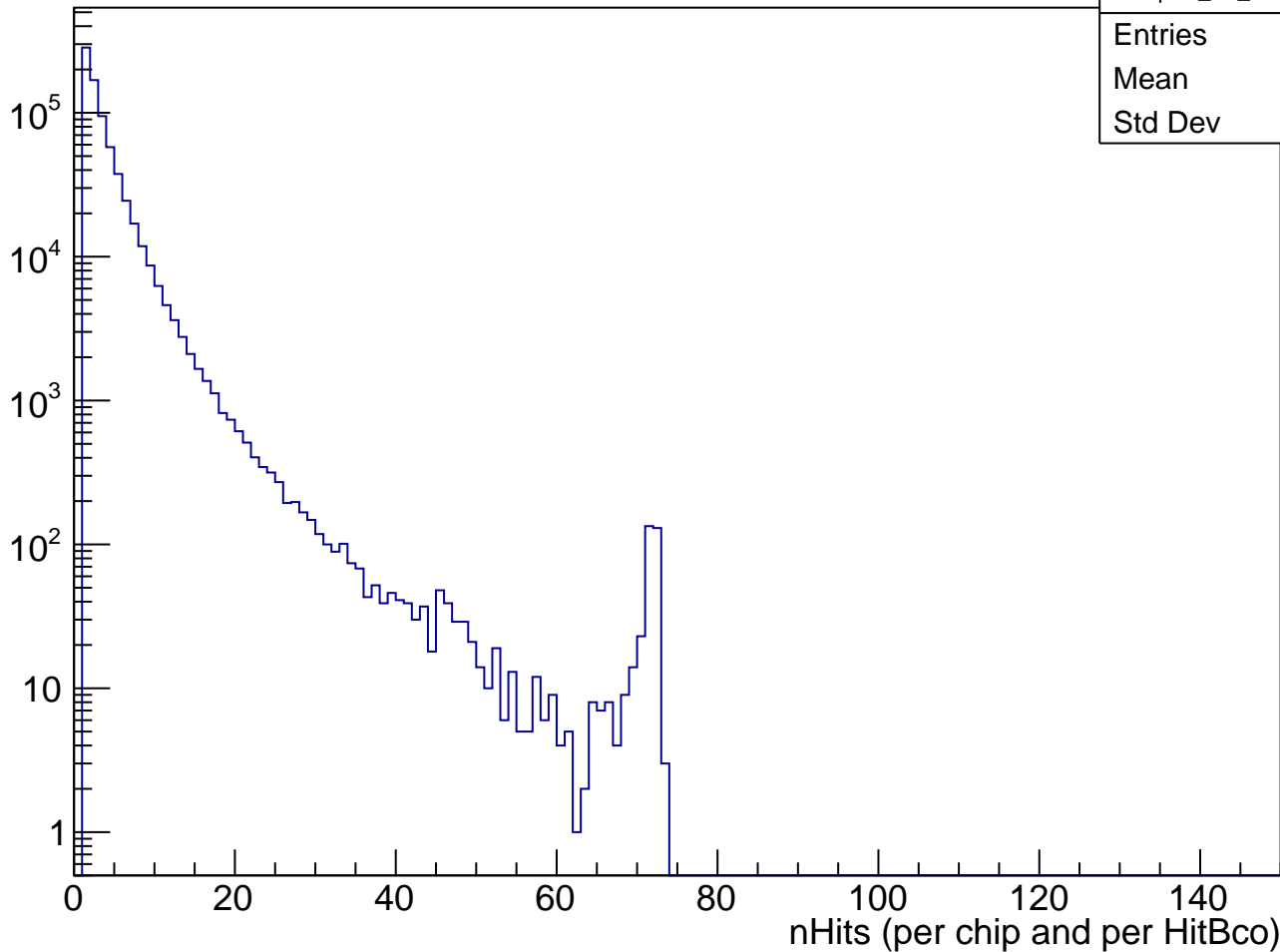


nChipHit_F7_Fch5_Chip18	
Entries	724360
Mean	3.027
Std Dev	3.598

nHits (per chip and per HitBco)

nChipHit_F7_Fch5_Chip19

Entries

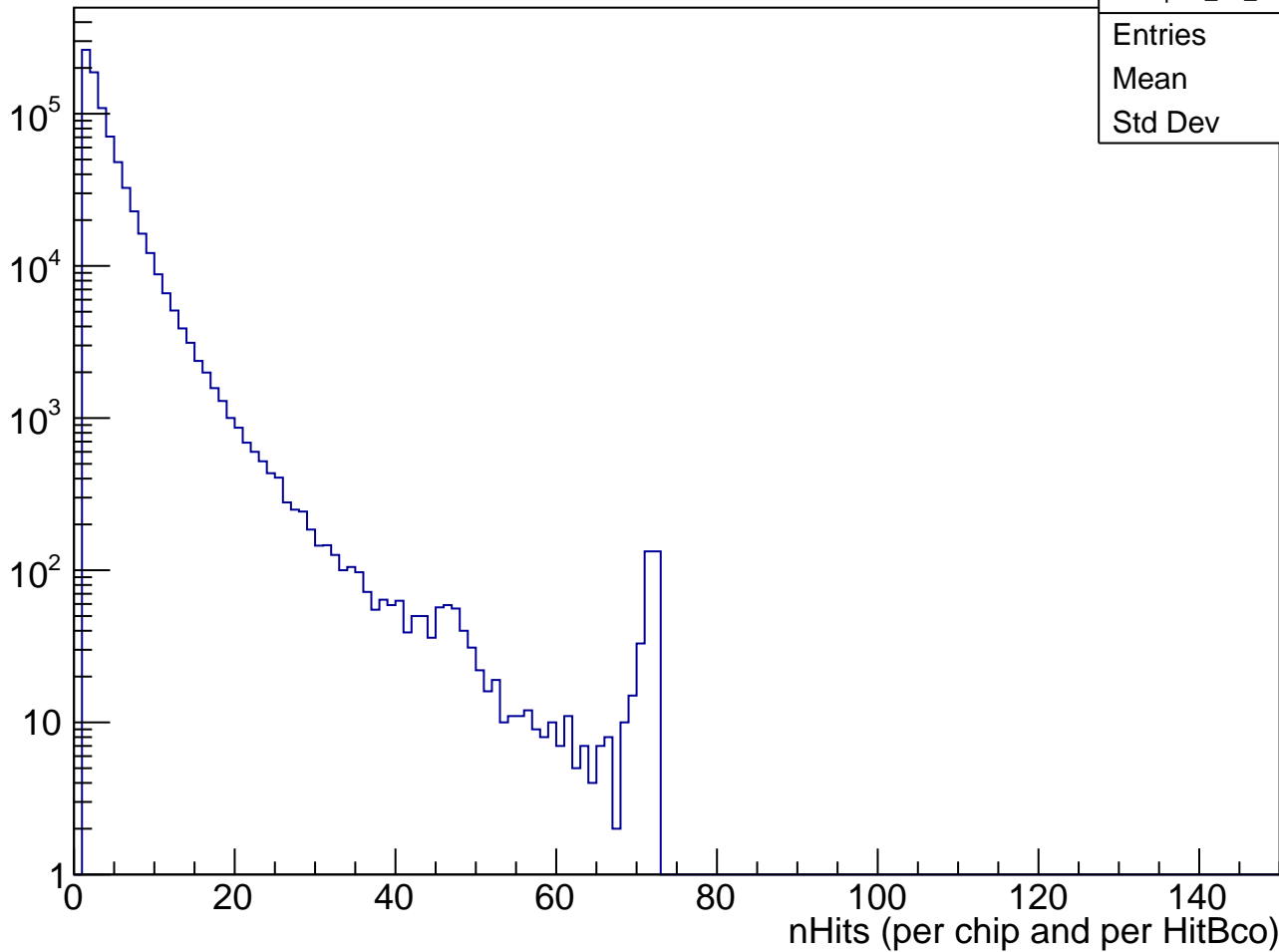


nChipHit_F7_Fch5_Chip19	
Entries	734626
Mean	3.026
Std Dev	3.601

nHits (per chip and per HitBco)

nChipHit_F7_Fch5_Chip2

Entries



nChipHit_F7_Fch5_Chip2

Entries 803885

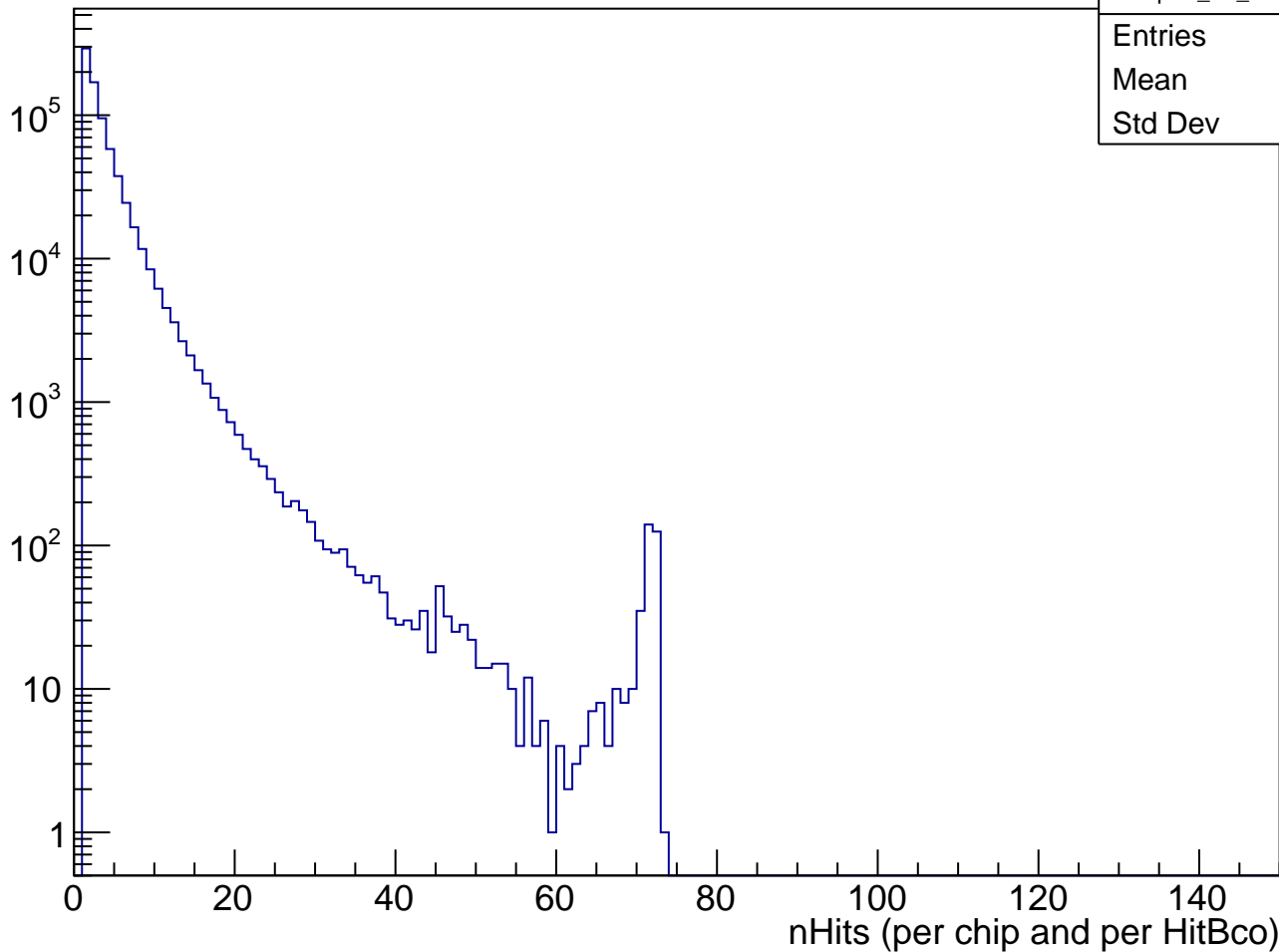
Mean 3.39

Std Dev 3.873

nHits (per chip and per HitBco)

nChipHit_F7_Fch5_Chip20

Entries

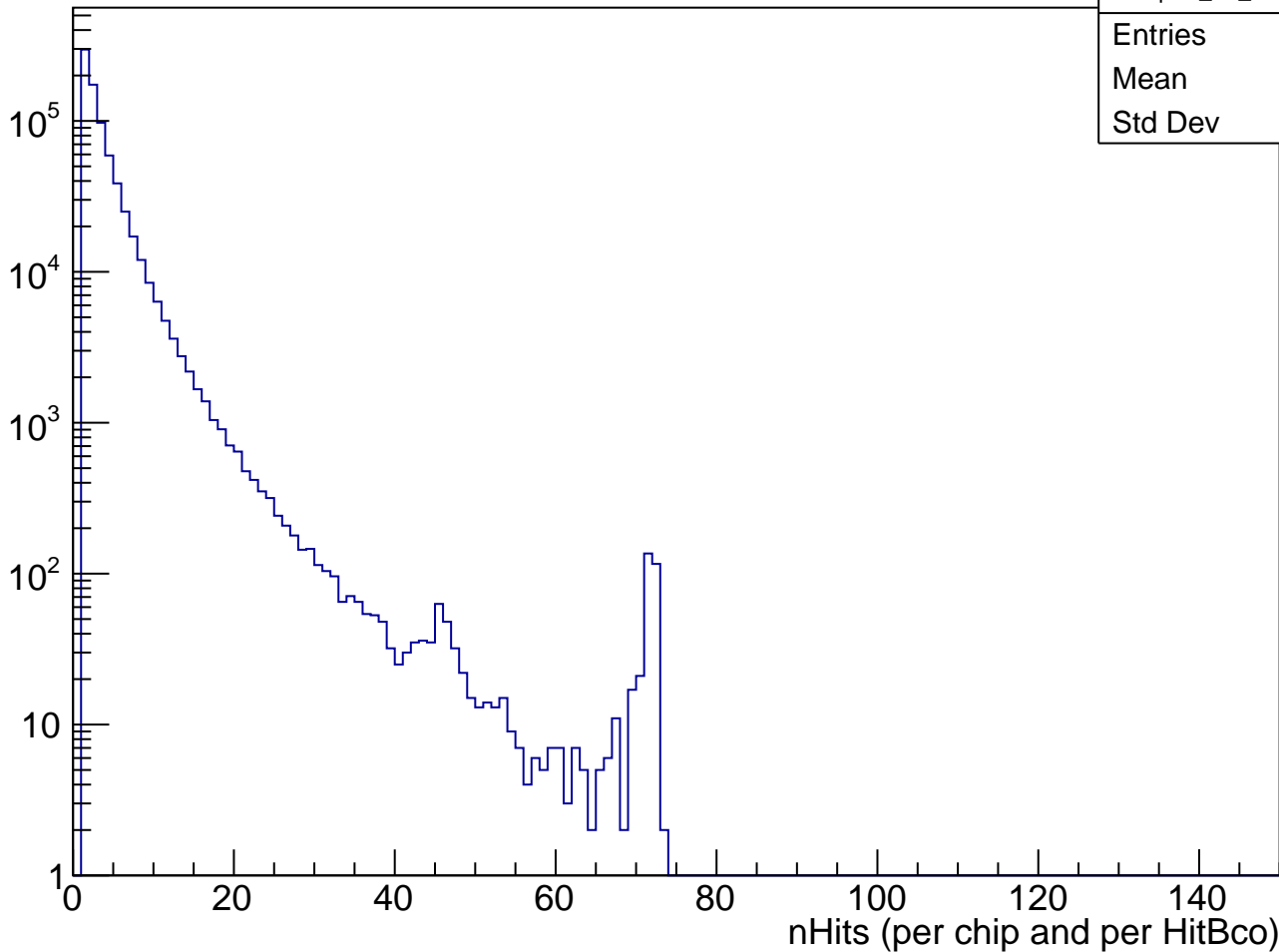


nChipHit_F7_Fch5_Chip20	
Entries	743135
Mean	2.989
Std Dev	3.563

nHits (per chip and per HitBco)

nChipHit_F7_Fch5_Chip21

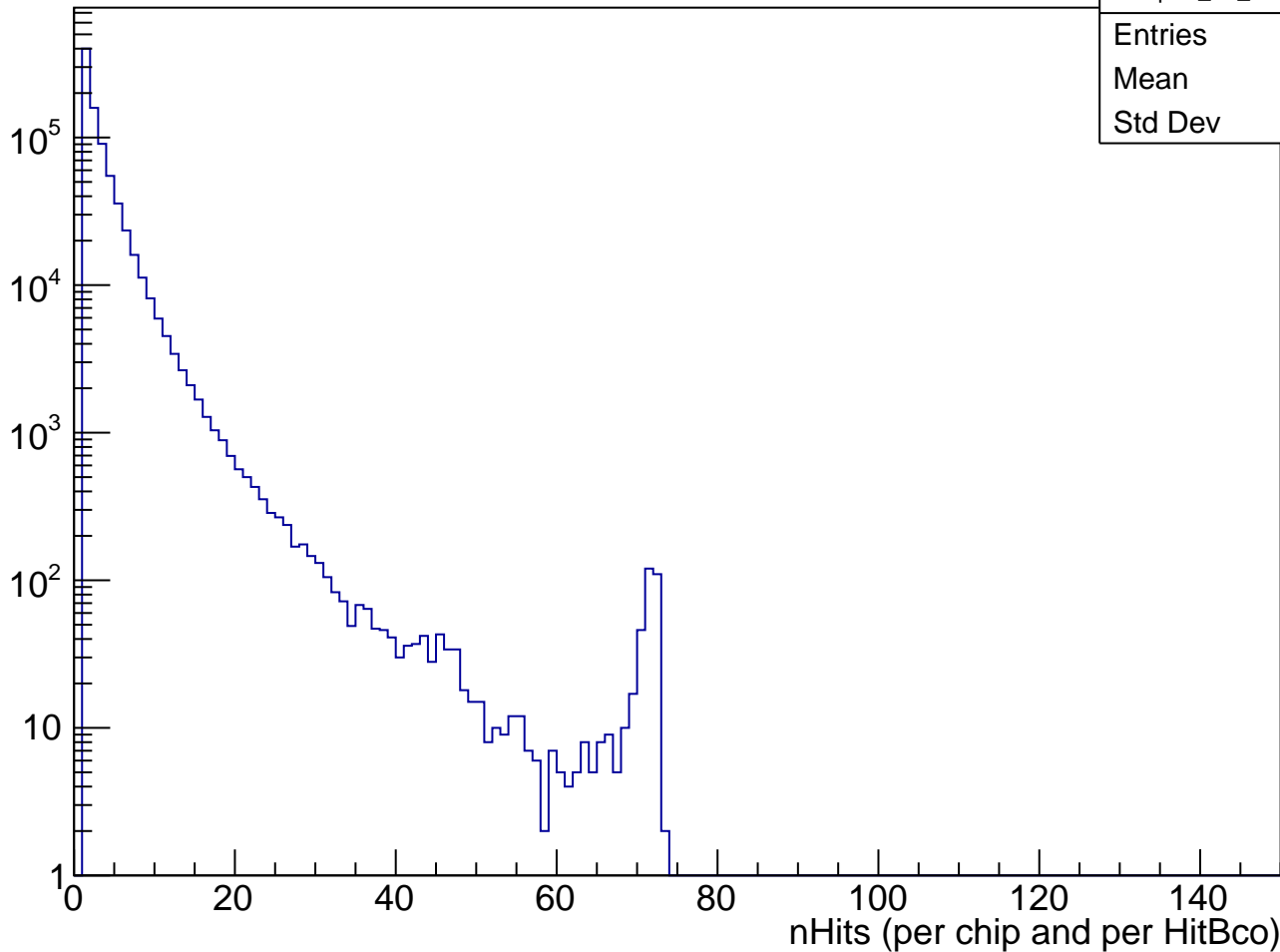
Entries



nChipHit_F7_Fch5_Chip21	
Entries	758528
Mean	2.99
Std Dev	3.539

nChipHit_F7_Fch5_Chip22

Entries

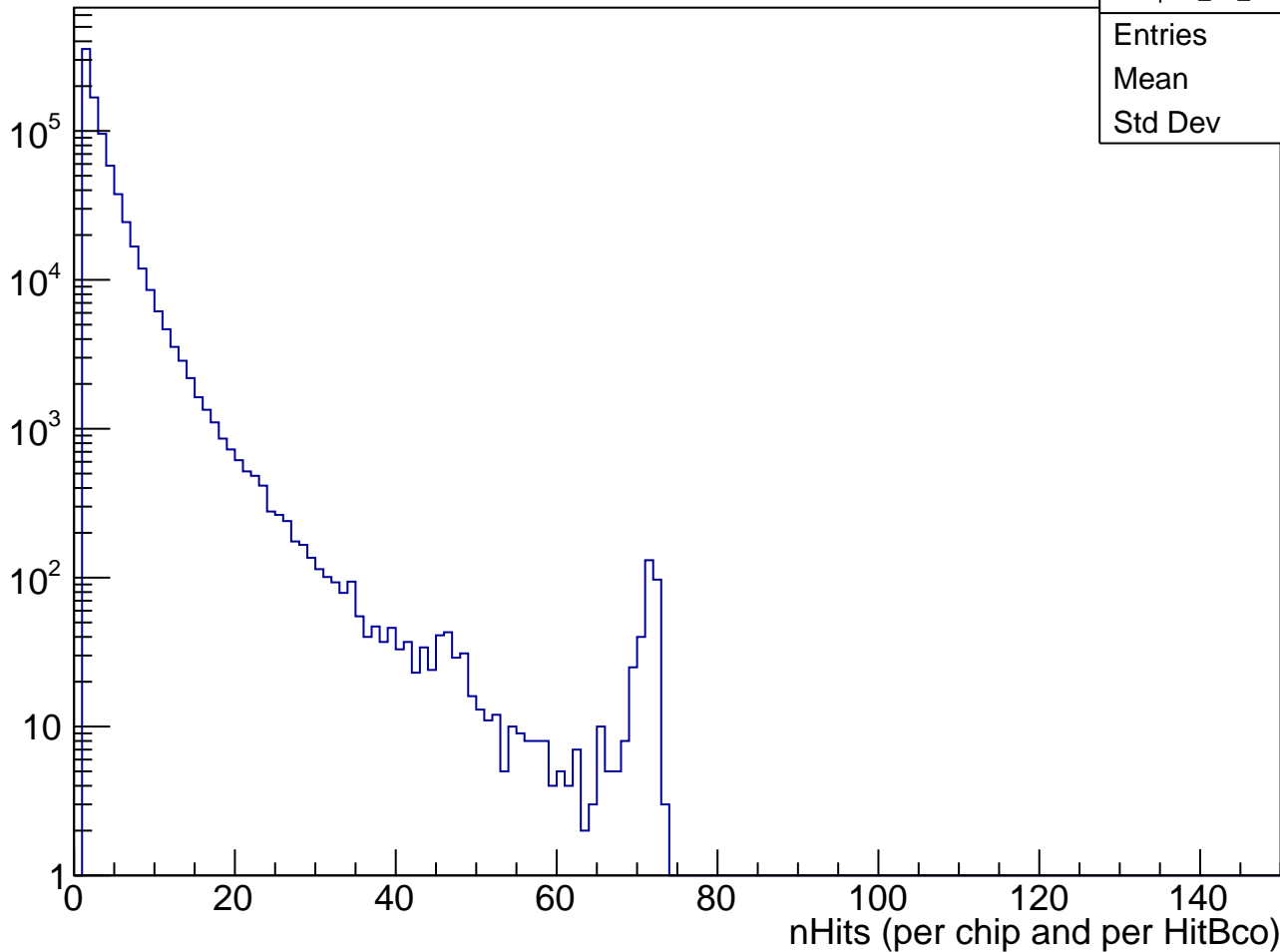


nChipHit_F7_Fch5_Chip22	
Entries	827969
Mean	2.718
Std Dev	3.418

nHits (per chip and per HitBco)

nChipHit_F7_Fch5_Chip23

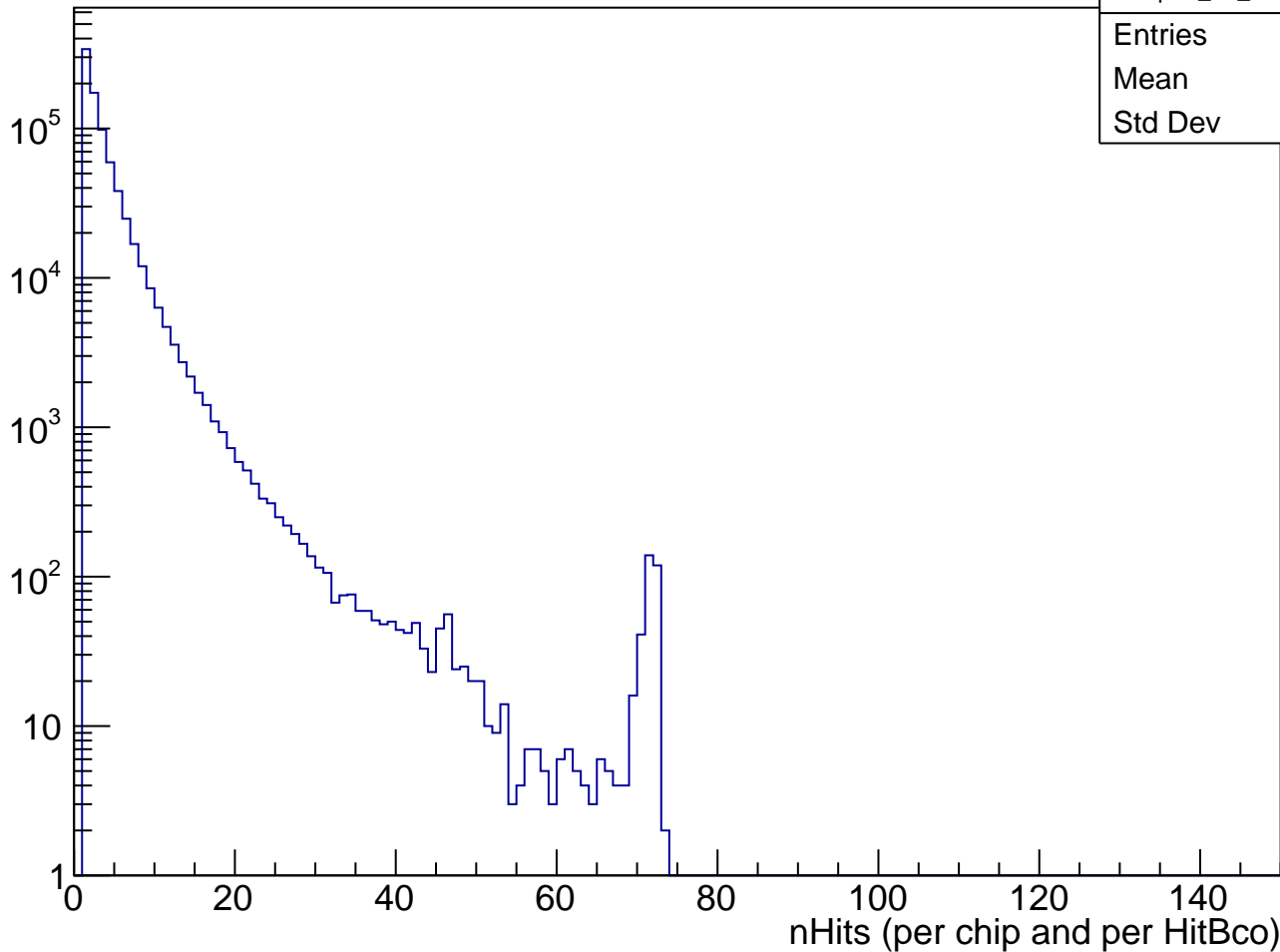
Entries



nChipHit_F7_Fch5_Chip23	
Entries	806297
Mean	2.847
Std Dev	3.469

nChipHit_F7_Fch5_Chip24

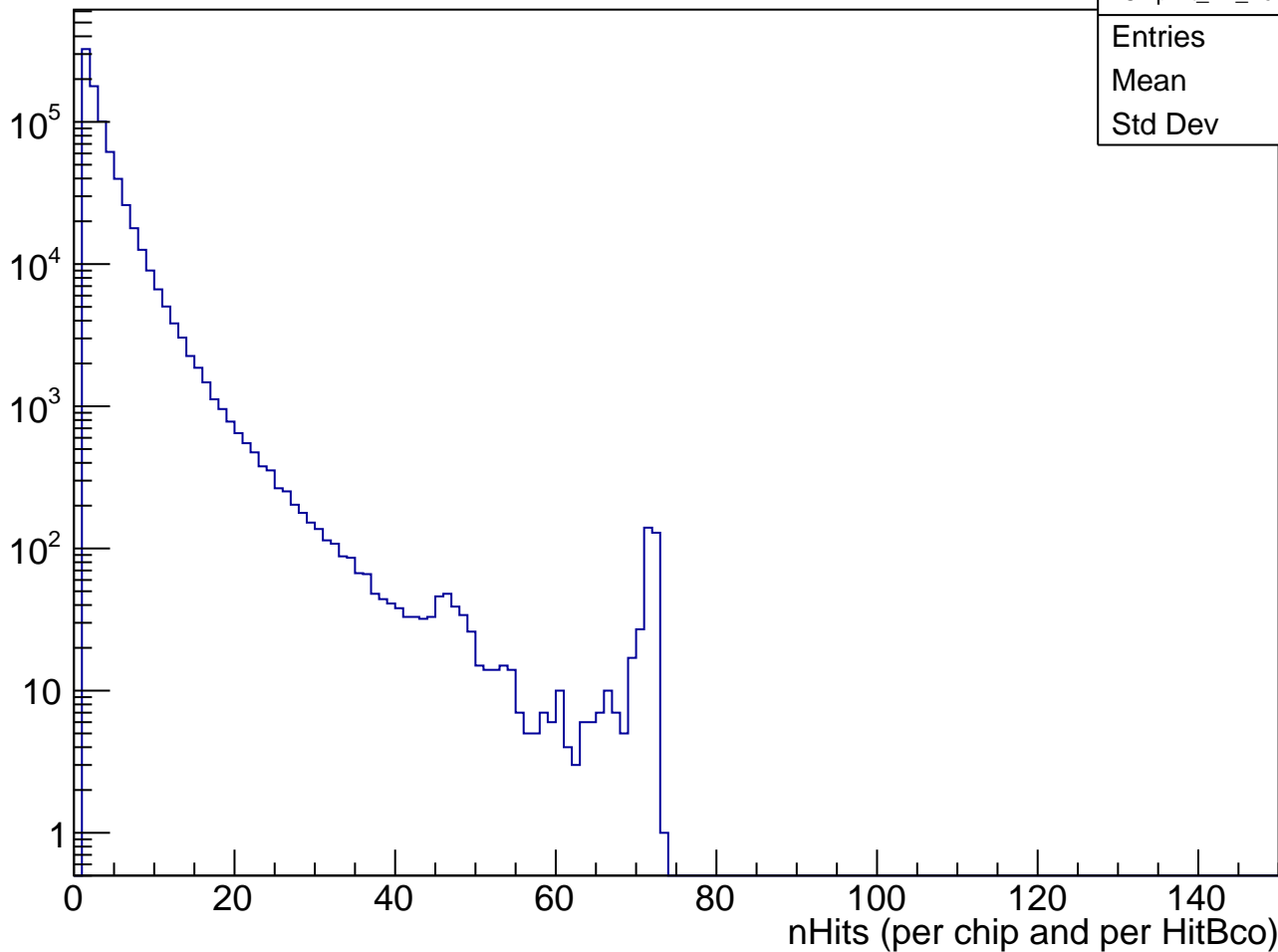
Entries



nChipHit_F7_Fch5_Chip24	
Entries	801028
Mean	2.886
Std Dev	3.495

nChipHit_F7_Fch5_Chip25

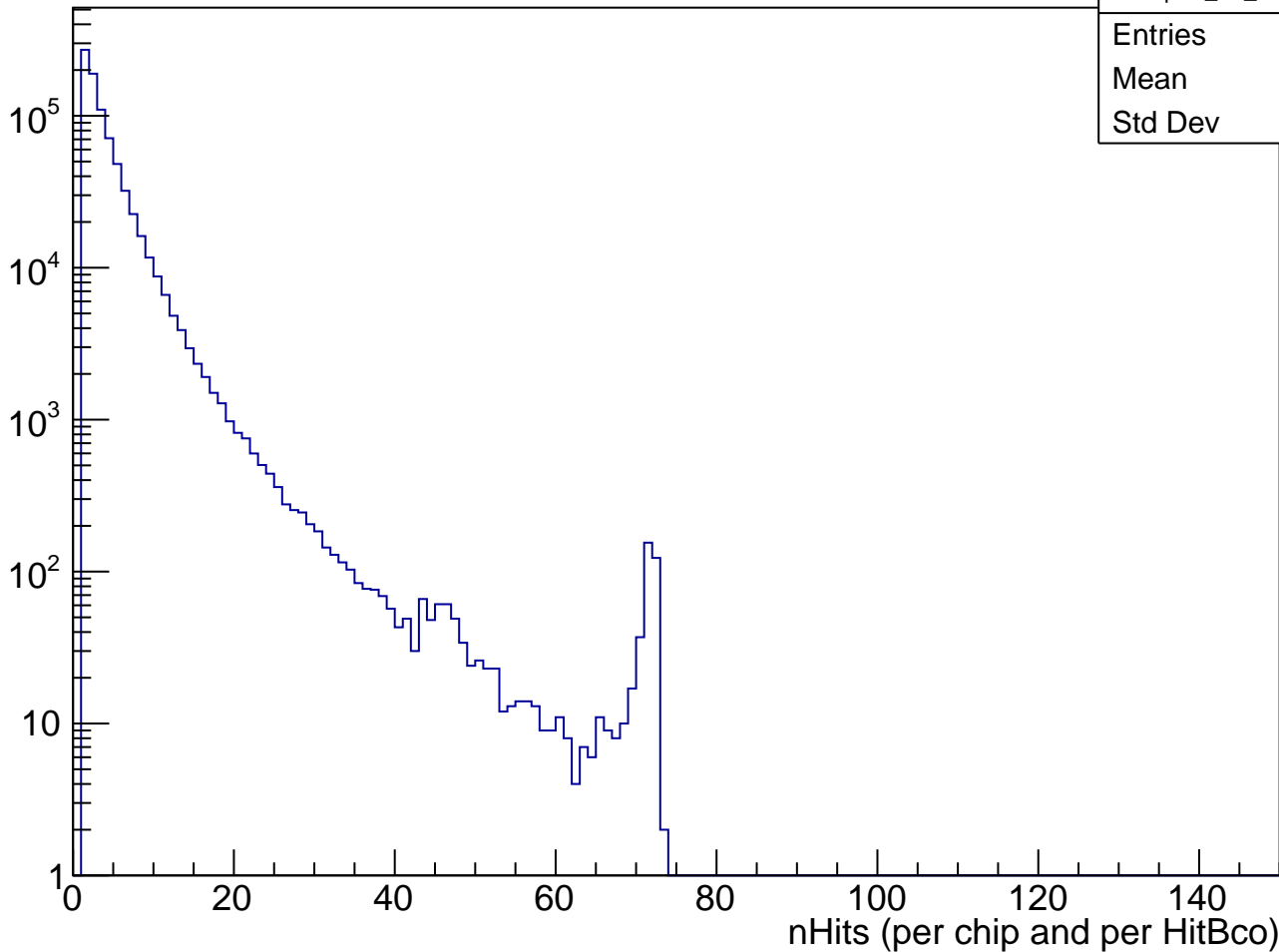
Entries



nChipHit_F7_Fch5_Chip25	
Entries	803199
Mean	2.973
Std Dev	3.58

nChipHit_F7_Fch5_Chip3

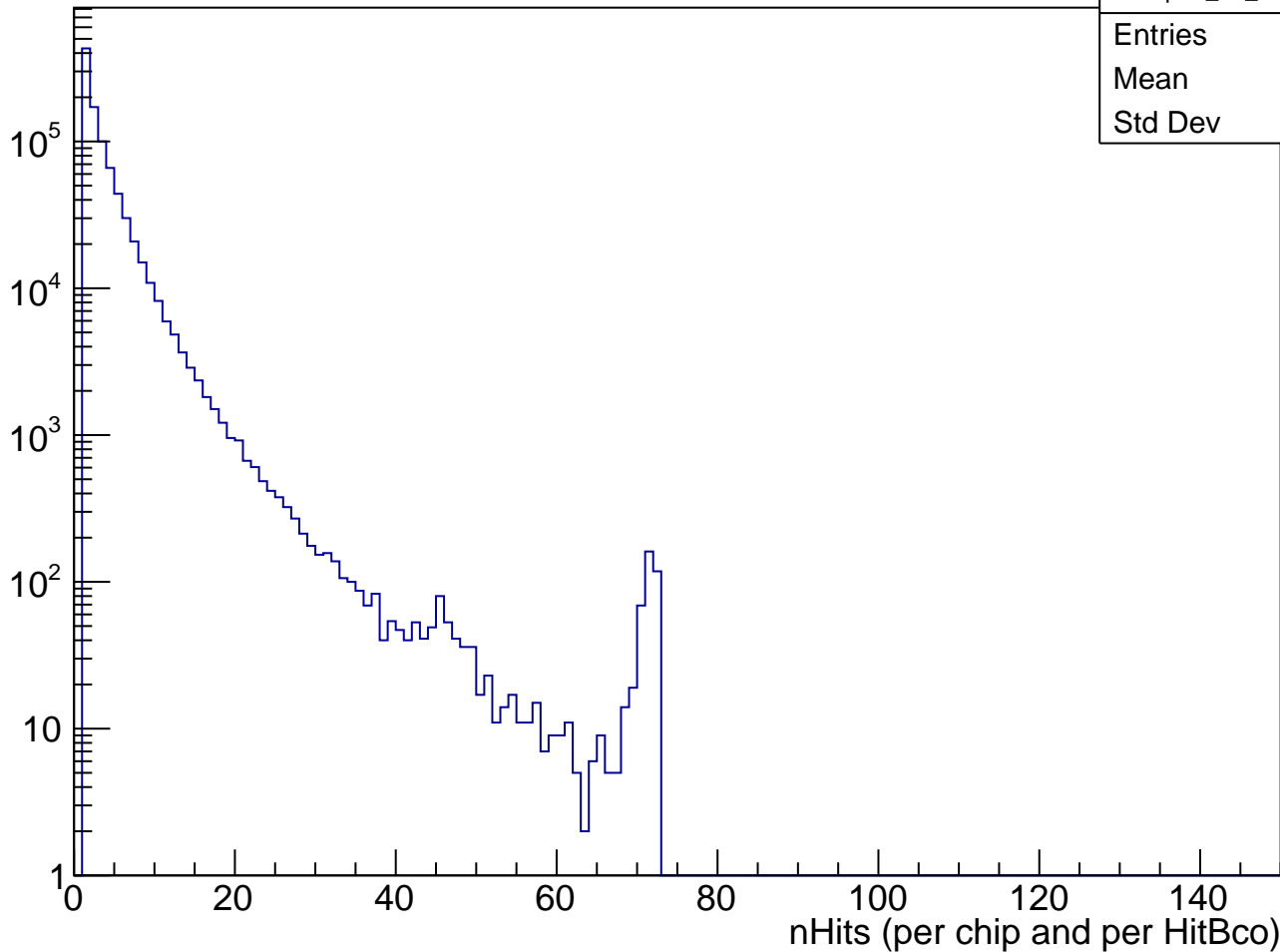
Entries



nChipHit_F7_Fch5_Chip3	
Entries	814178
Mean	3.351
Std Dev	3.878

nChipHit_F7_Fch5_Chip4

Entries



nChipHit_F7_Fch5_Chip4

Entries 928705

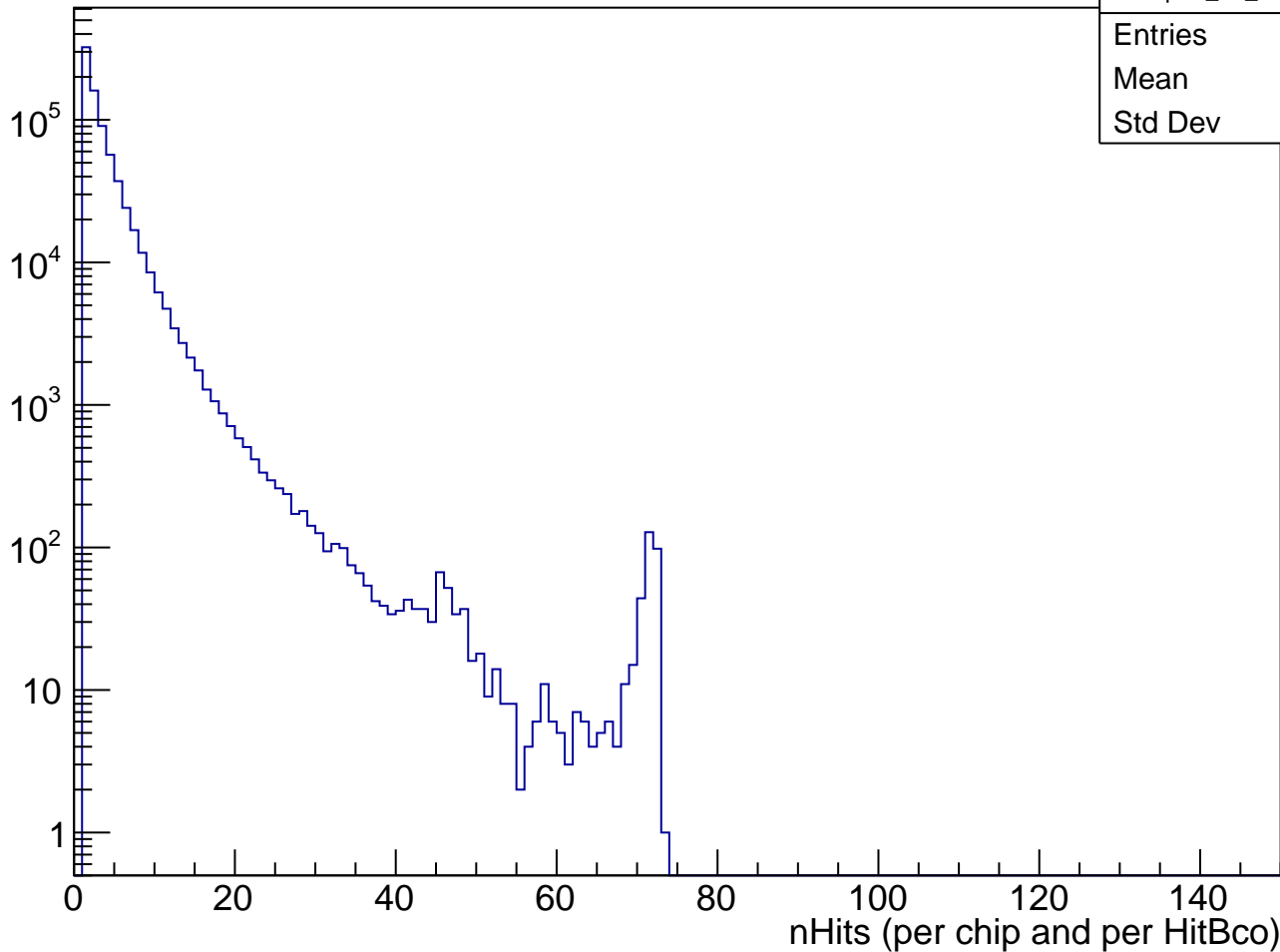
Mean 2.929

Std Dev 3.701

nHits (per chip and per HitBco)

nChipHit_F7_Fch5_Chip5

Entries



nChipHit_F7_Fch5_Chip5

Entries 759030

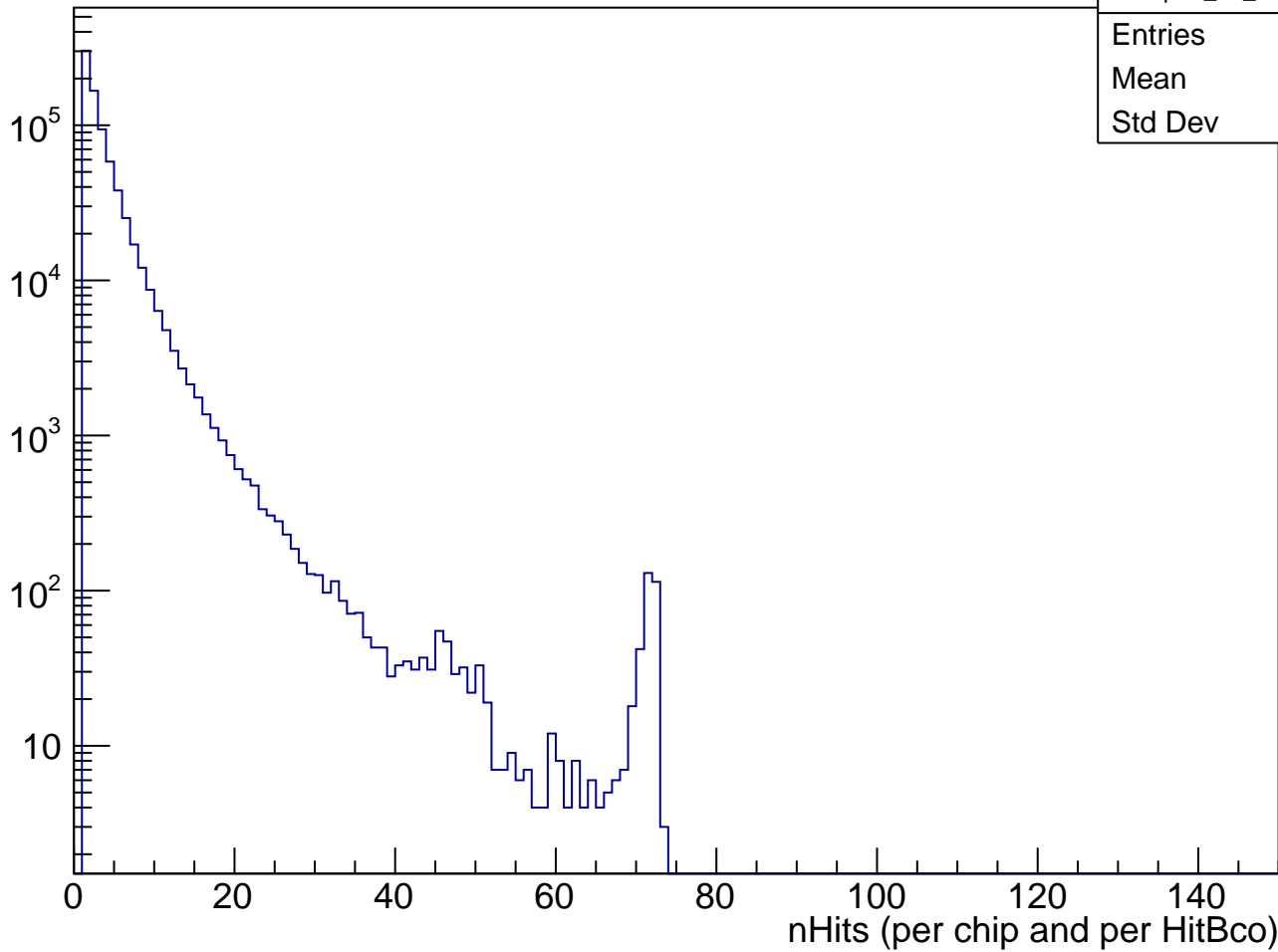
Mean 2.923

Std Dev 3.557

nHits (per chip and per HitBco)

nChipHit_F7_Fch5_Chip6

Entries



nChipHit_F7_Fch5_Chip6

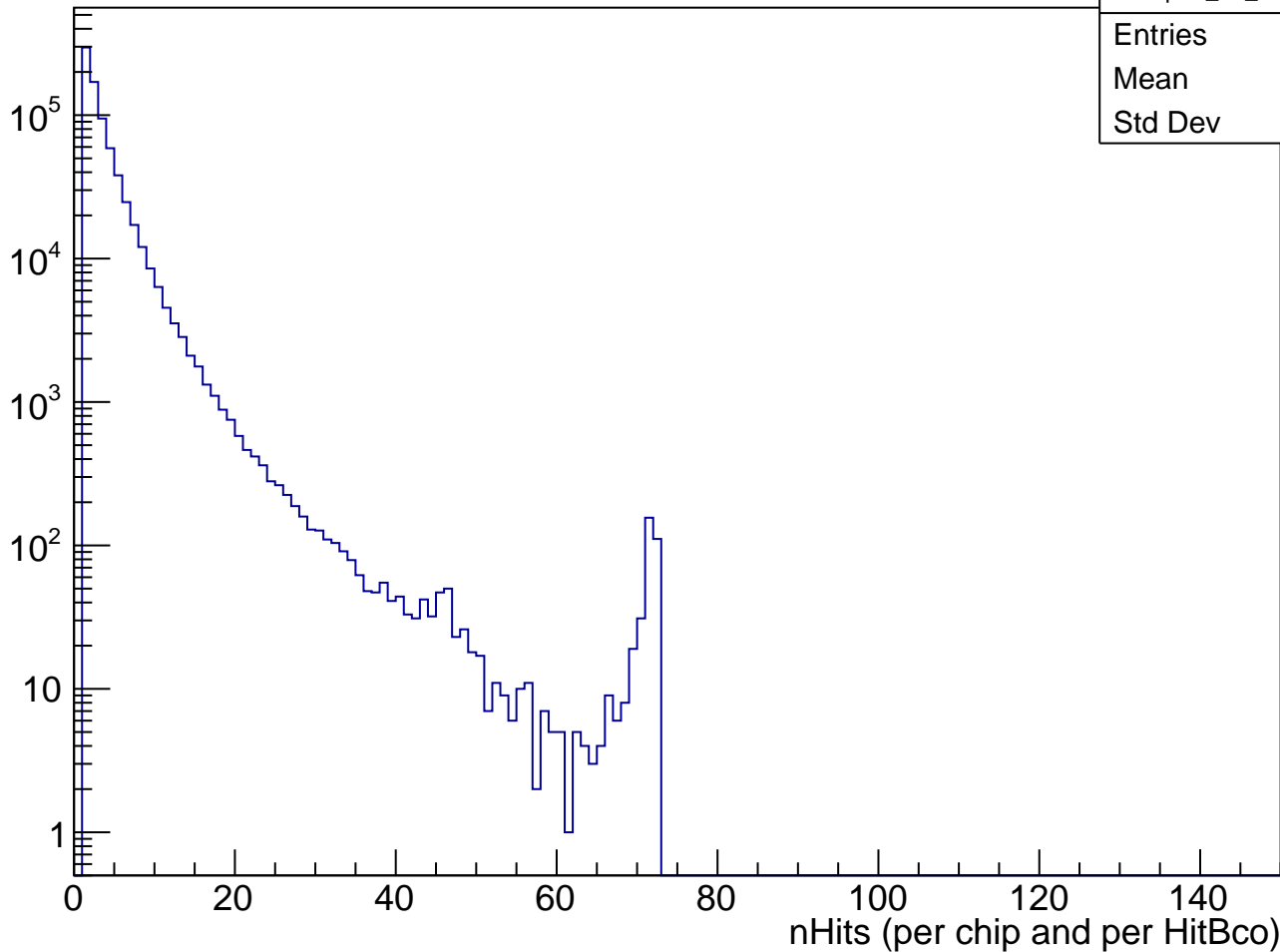
Entries 752725

Mean 2.994

Std Dev 3.597

nChipHit_F7_Fch5_Chip7

Entries

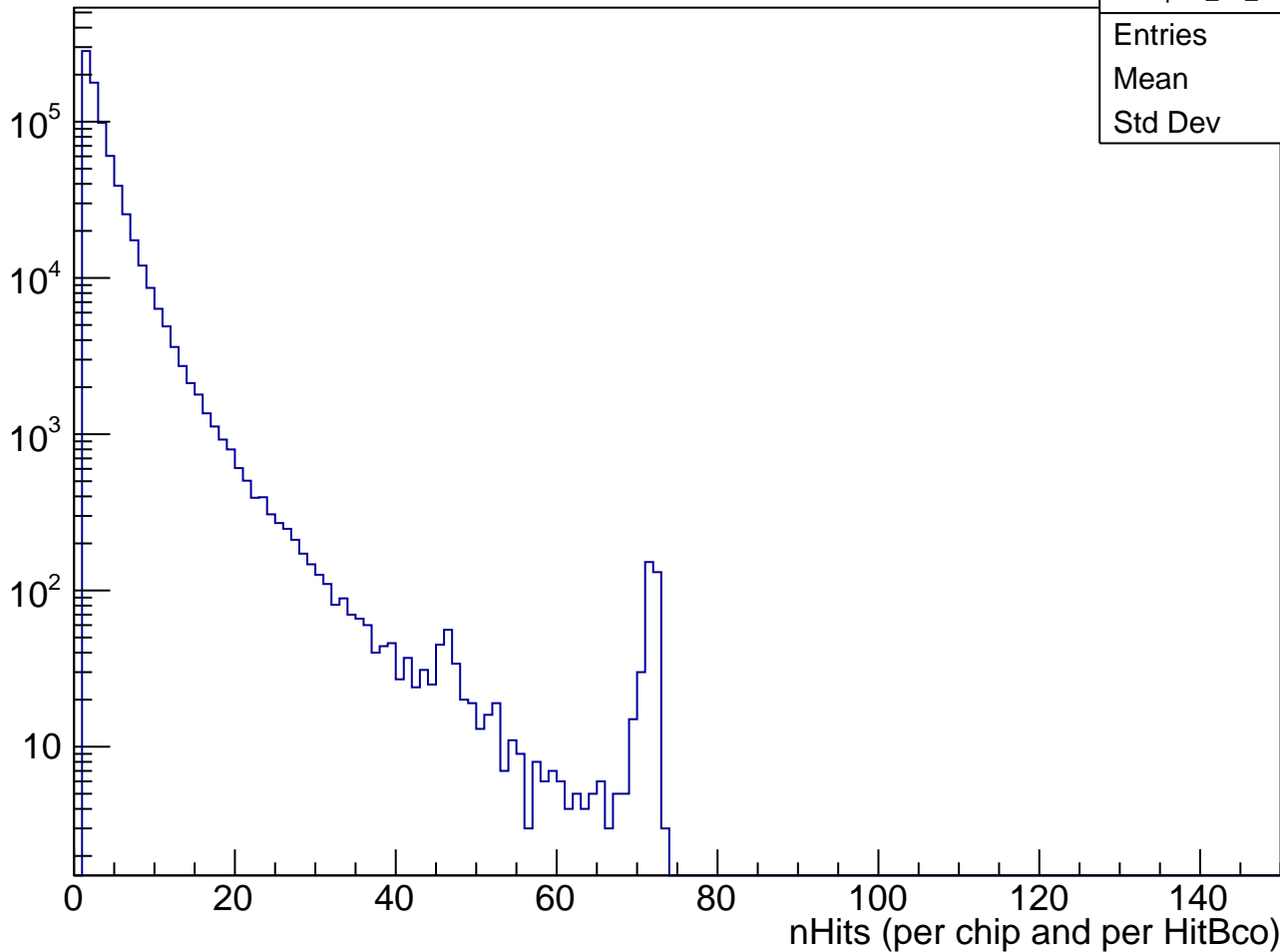


nChipHit_F7_Fch5_Chip7

Entries	750718
Mean	2.996
Std Dev	3.581

nChipHit_F7_Fch5_Chip8

Entries



nChipHit_F7_Fch5_Chip8

Entries 752734

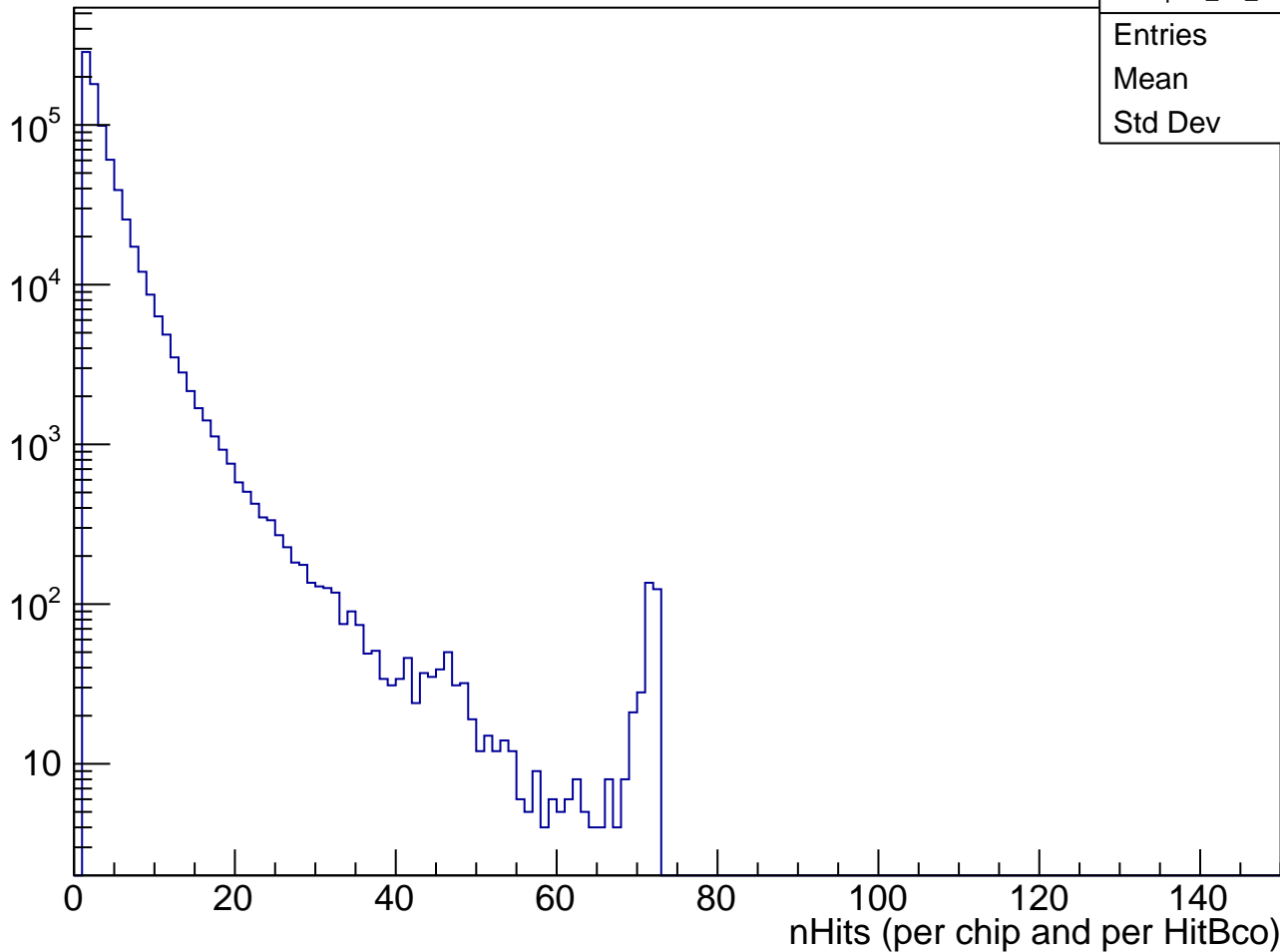
Mean 3.042

Std Dev 3.602

nHits (per chip and per HitBco)

nChipHit_F7_Fch5_Chip9

Entries

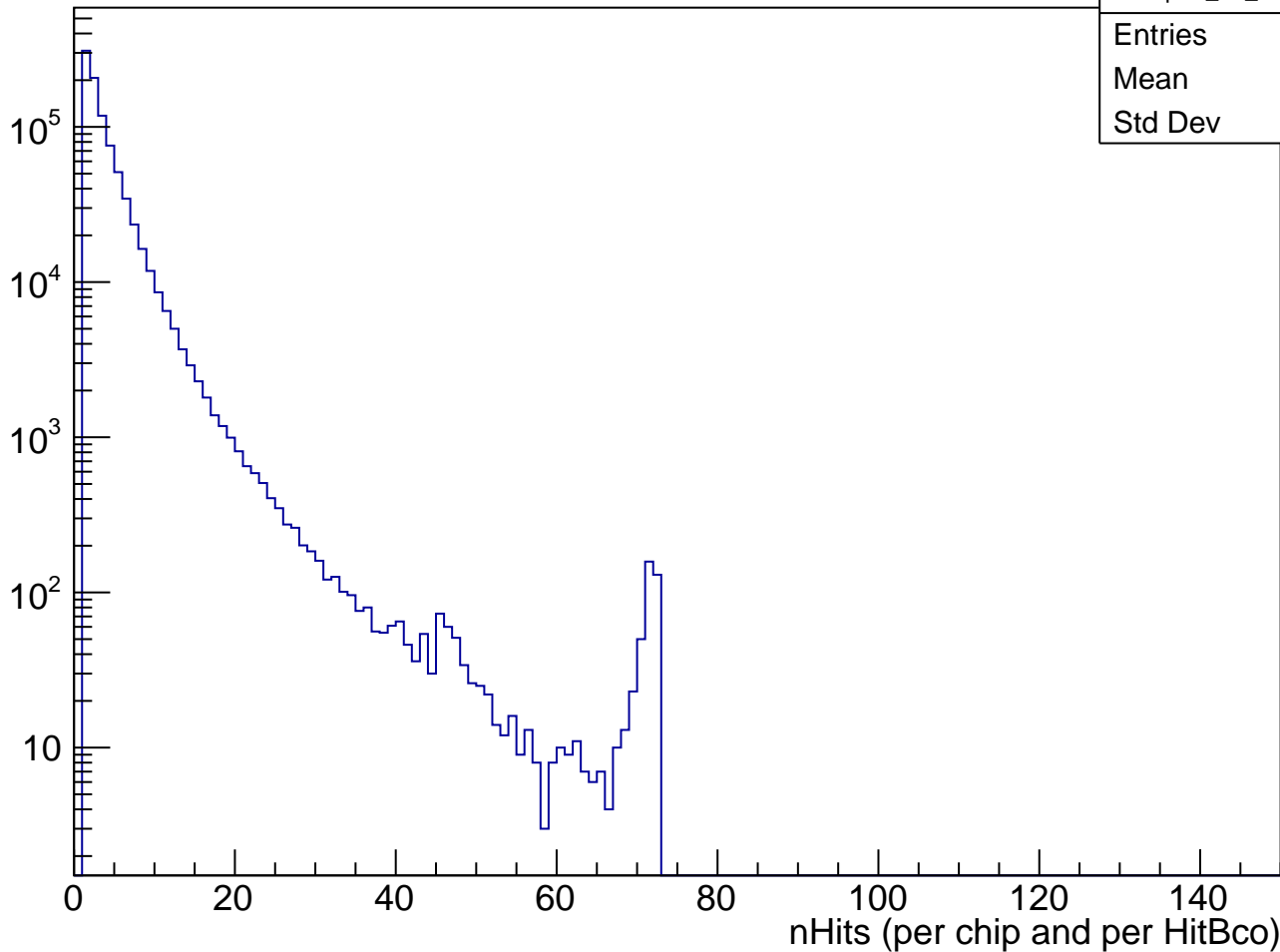


nChipHit_F7_Fch5_Chip9	
Entries	758992
Mean	3.029
Std Dev	3.58

nHits (per chip and per HitBco)

nChipHit_F7_Fch6_Chip0

Entries



nChipHit_F7_Fch6_Chip0

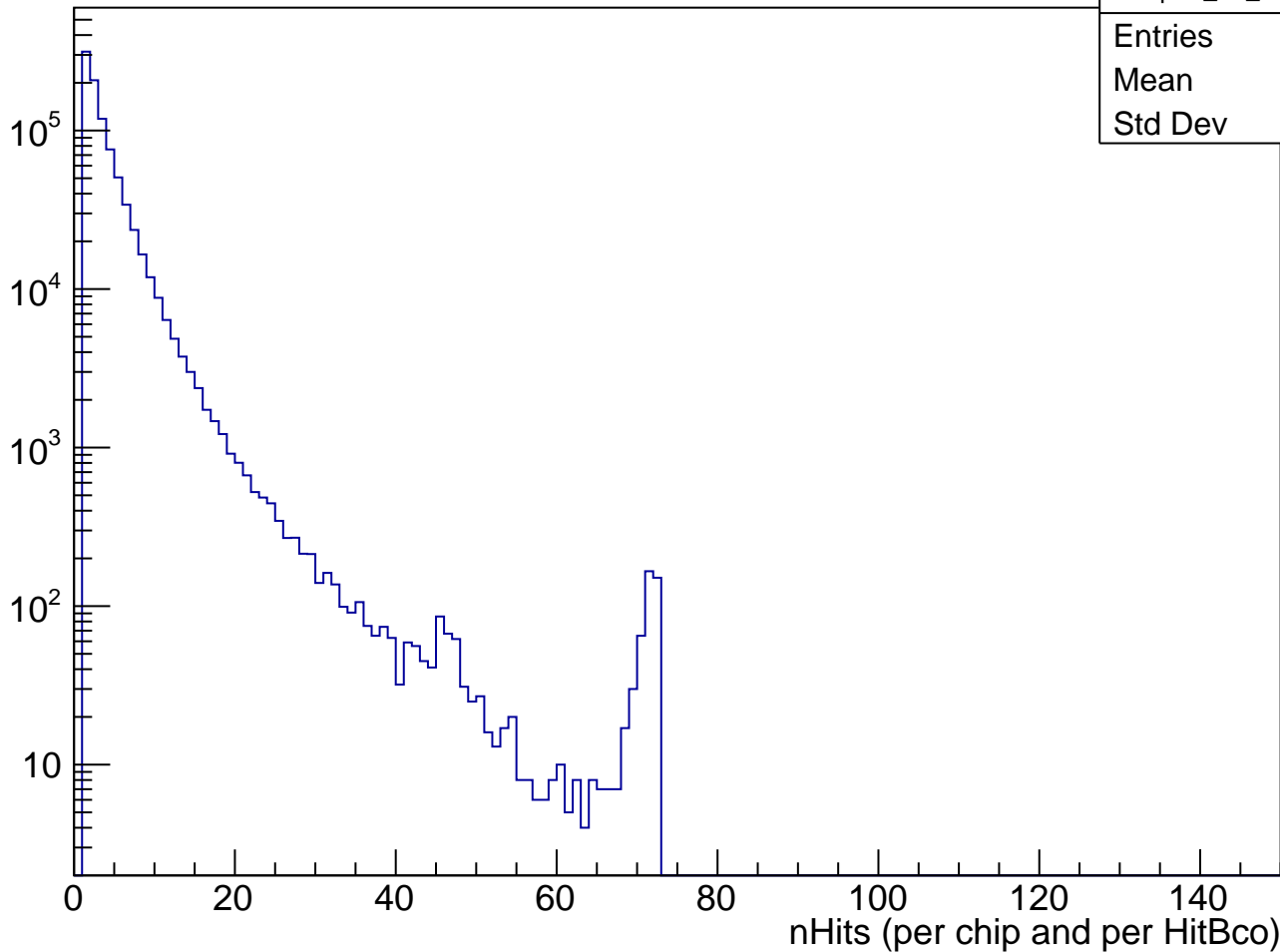
Entries 888315

Mean 3.226

Std Dev 3.724

nChipHit_F7_Fch6_Chip1

Entries

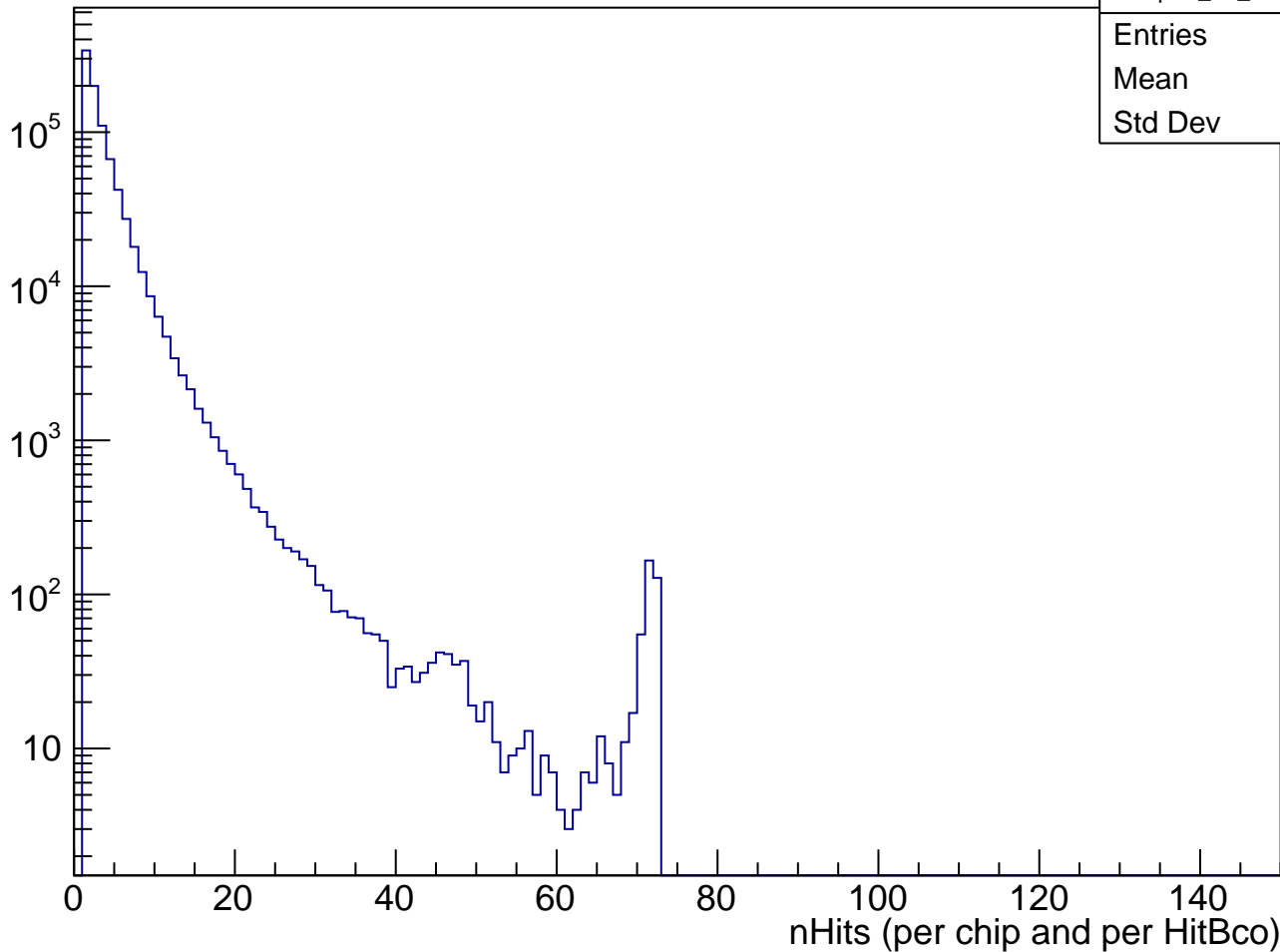


nChipHit_F7_Fch6_Chip1	
Entries	894111
Mean	3.223
Std Dev	3.774

nHits (per chip and per HitBco)

nChipHit_F7_Fch6_Chip10

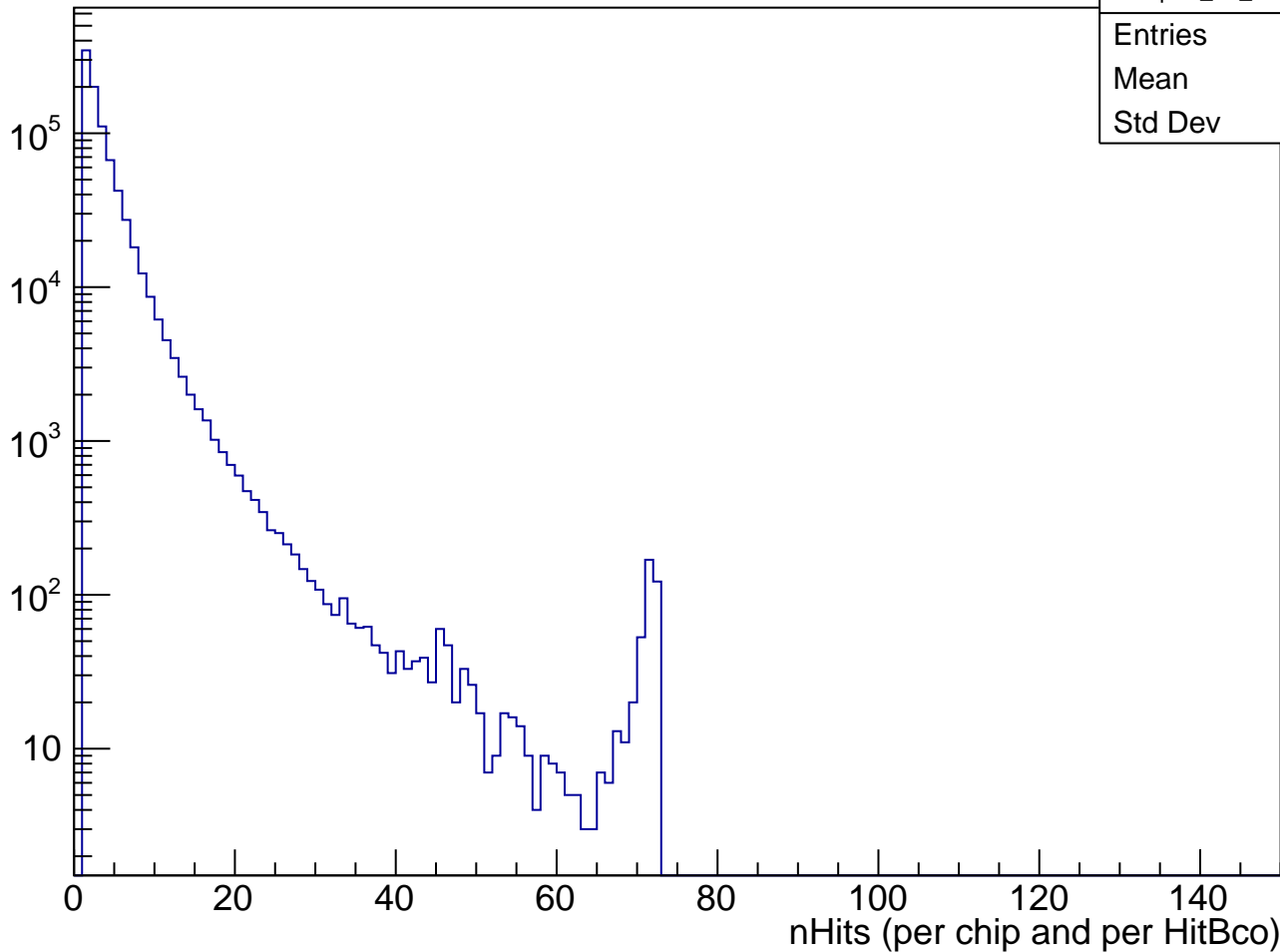
Entries



nChipHit_F7_Fch6_Chip10	
Entries	853717
Mean	2.893
Std Dev	3.435

nChipHit_F7_Fch6_Chip11

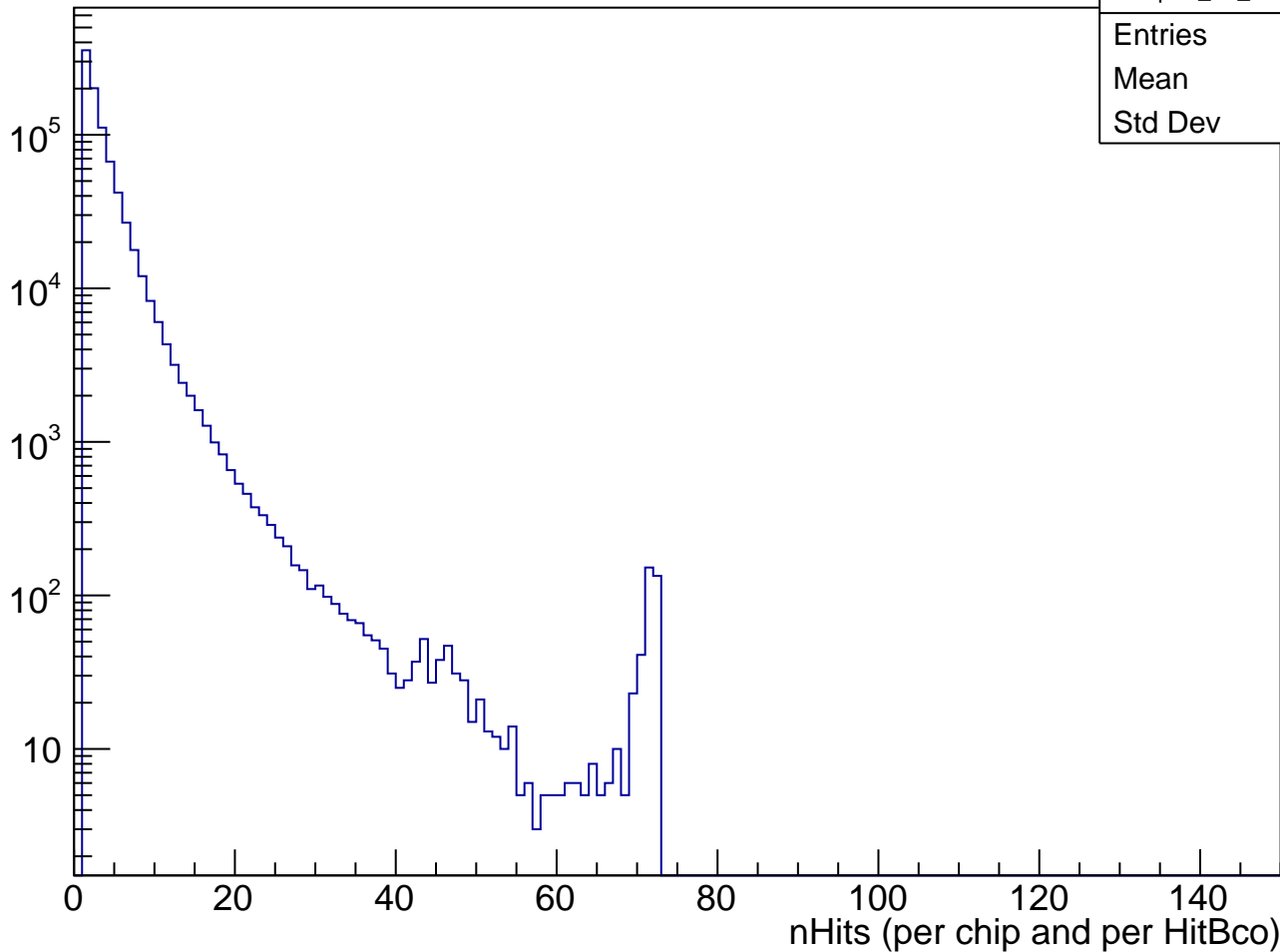
Entries



nChipHit_F7_Fch6_Chip11	
Entries	861613
Mean	2.874
Std Dev	3.419

nChipHit_F7_Fch6_Chip12

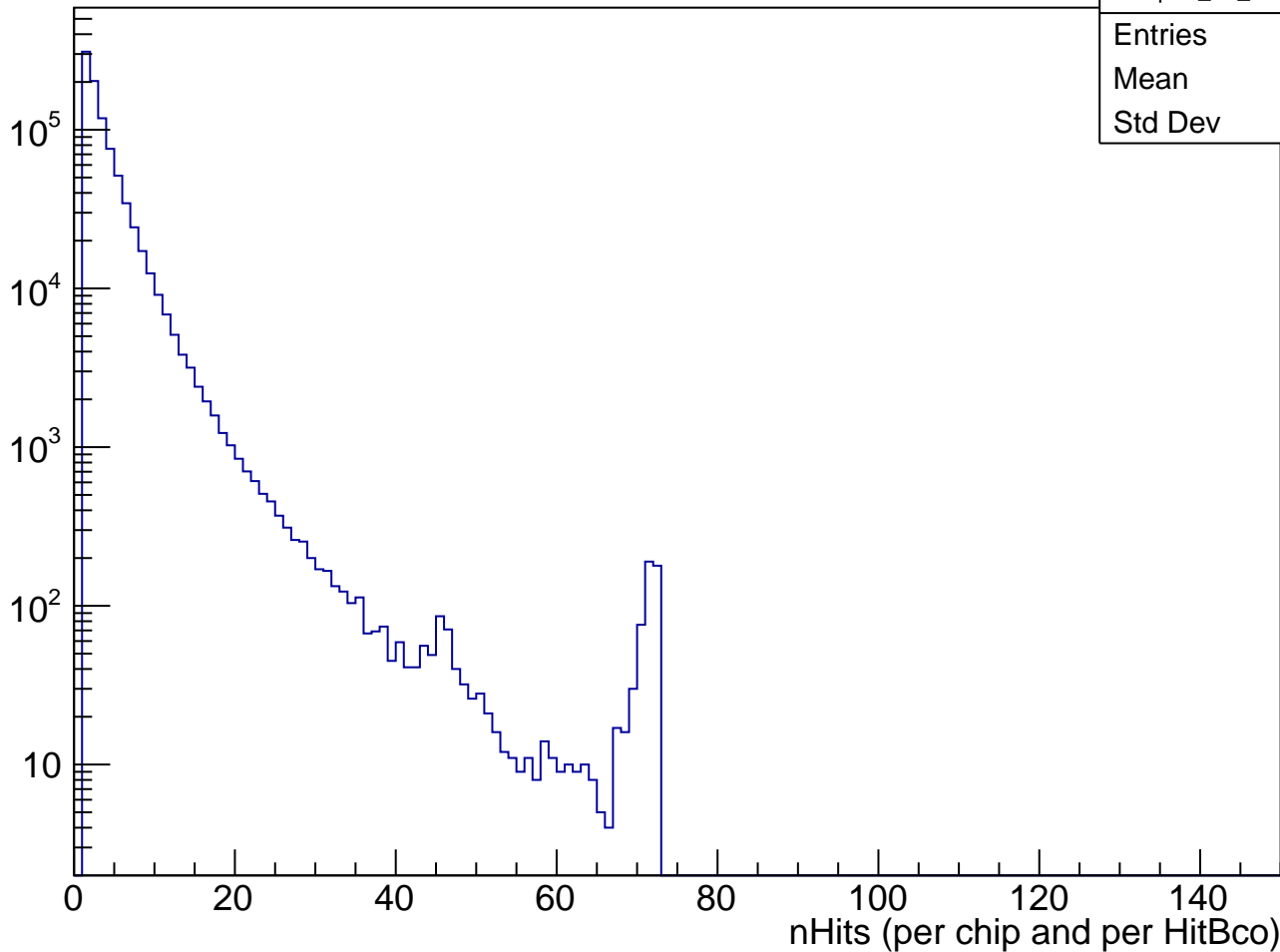
Entries



nChipHit_F7_Fch6_Chip12	
Entries	869299
Mean	2.826
Std Dev	3.346

nChipHit_F7_Fch6_Chip13

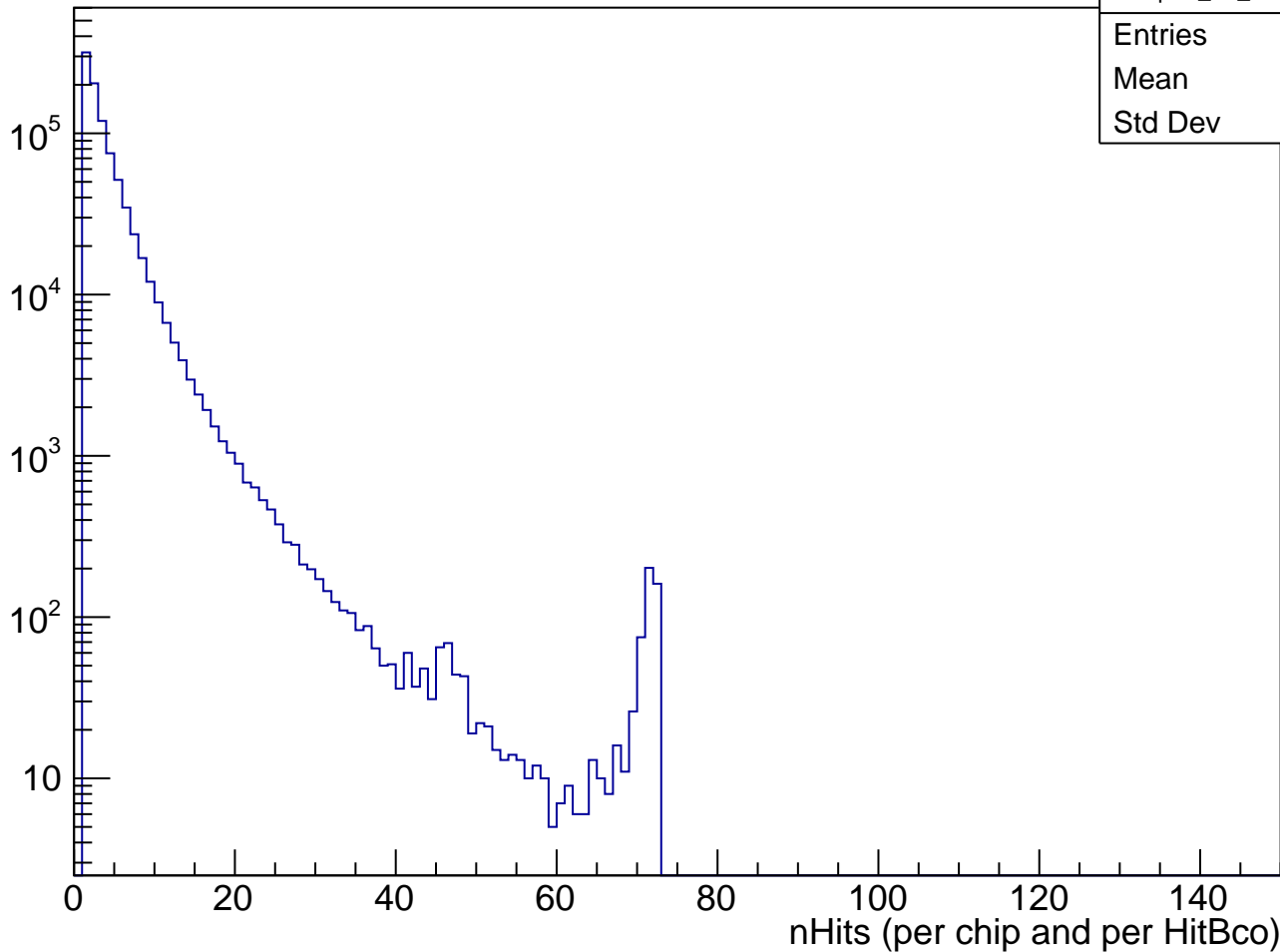
Entries



nChipHit_F7_Fch6_Chip13	
Entries	890024
Mean	3.286
Std Dev	3.887

nChipHit_F7_Fch6_Chip14

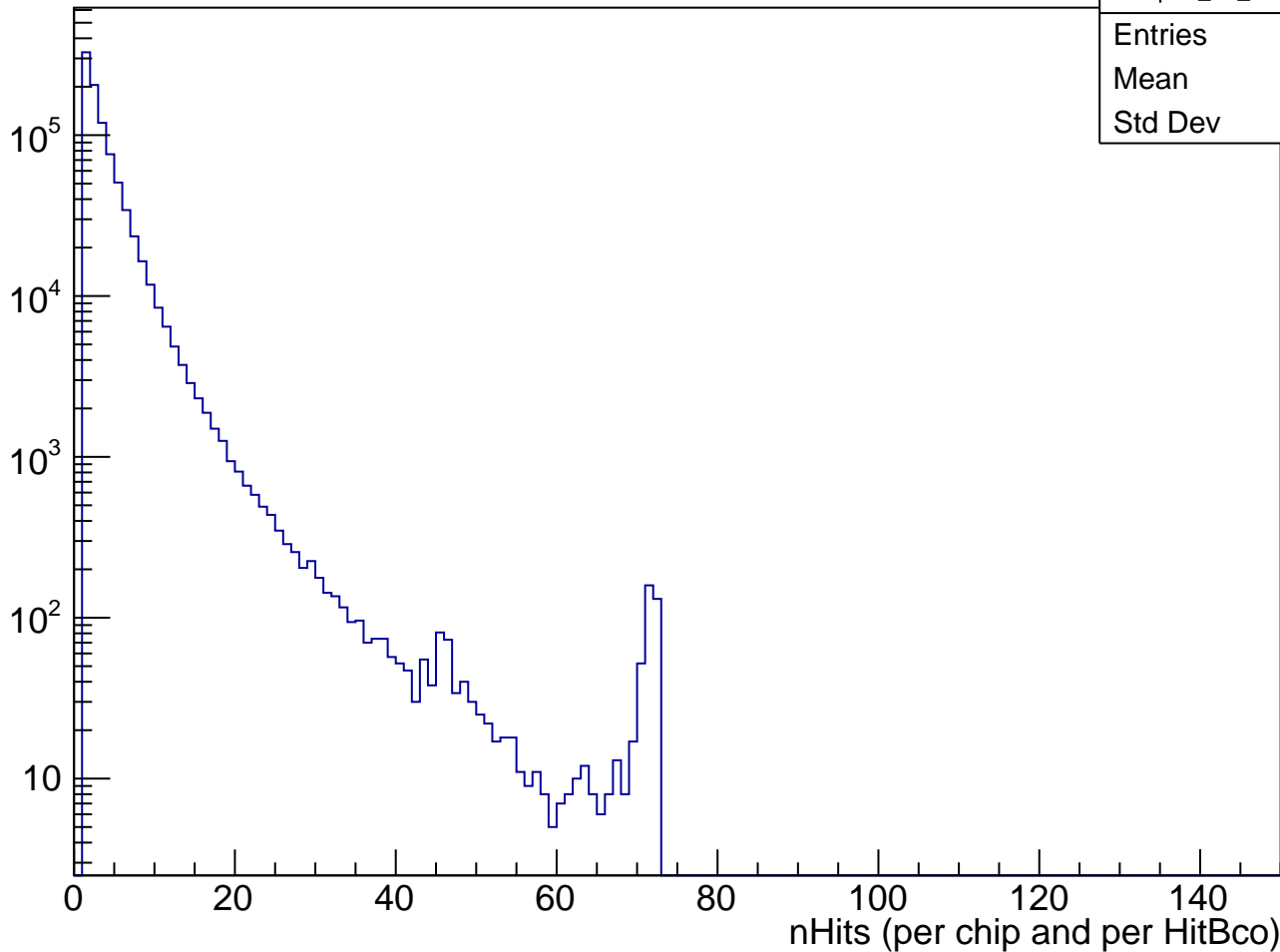
Entries



nChipHit_F7_Fch6_Chip14	
Entries	897396
Mean	3.247
Std Dev	3.835

nChipHit_F7_Fch6_Chip15

Entries

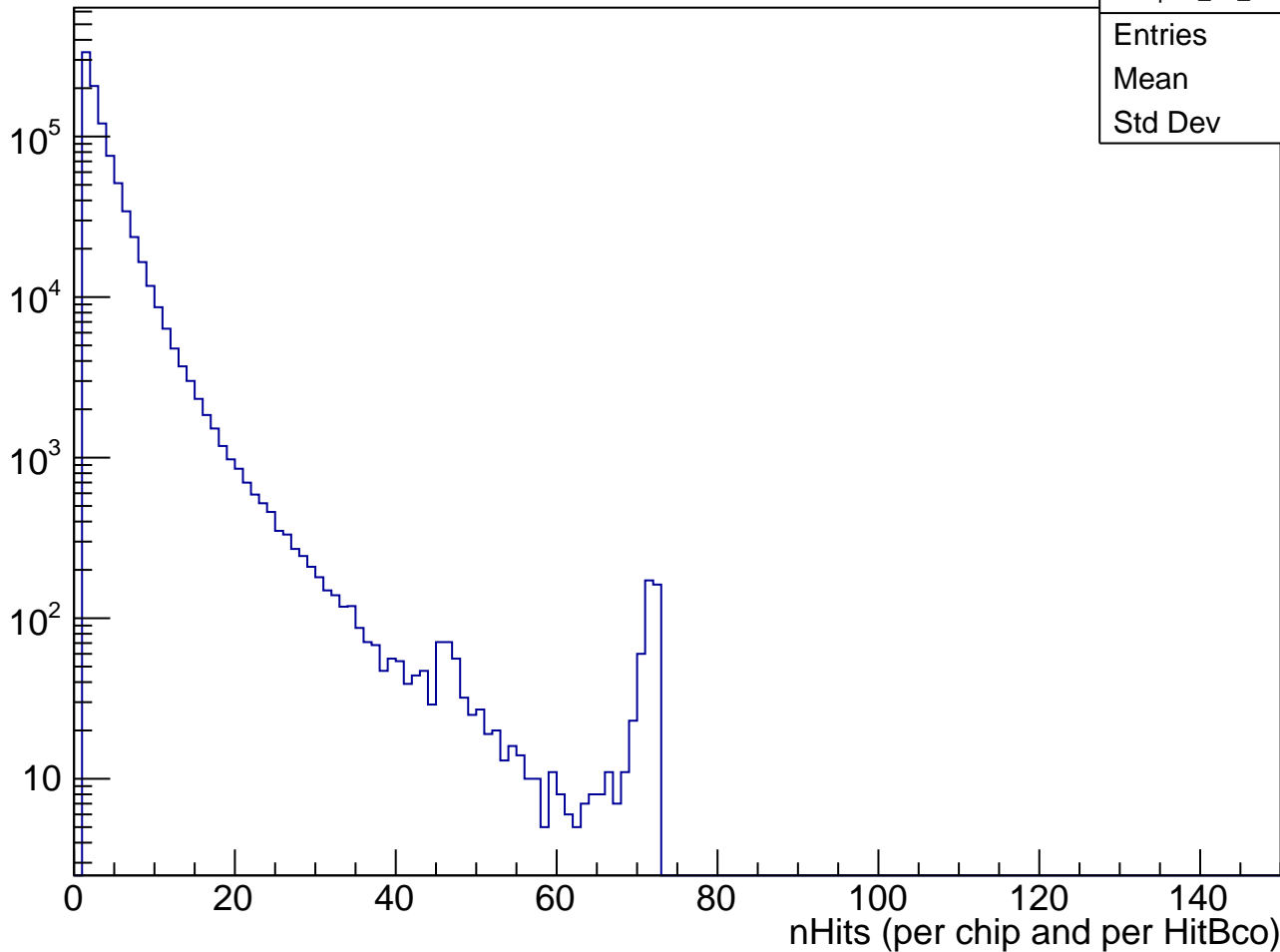


nChipHit_F7_Fch6_Chip15	
Entries	905244
Mean	3.191
Std Dev	3.733

nHits (per chip and per HitBco)

nChipHit_F7_Fch6_Chip16

Entries

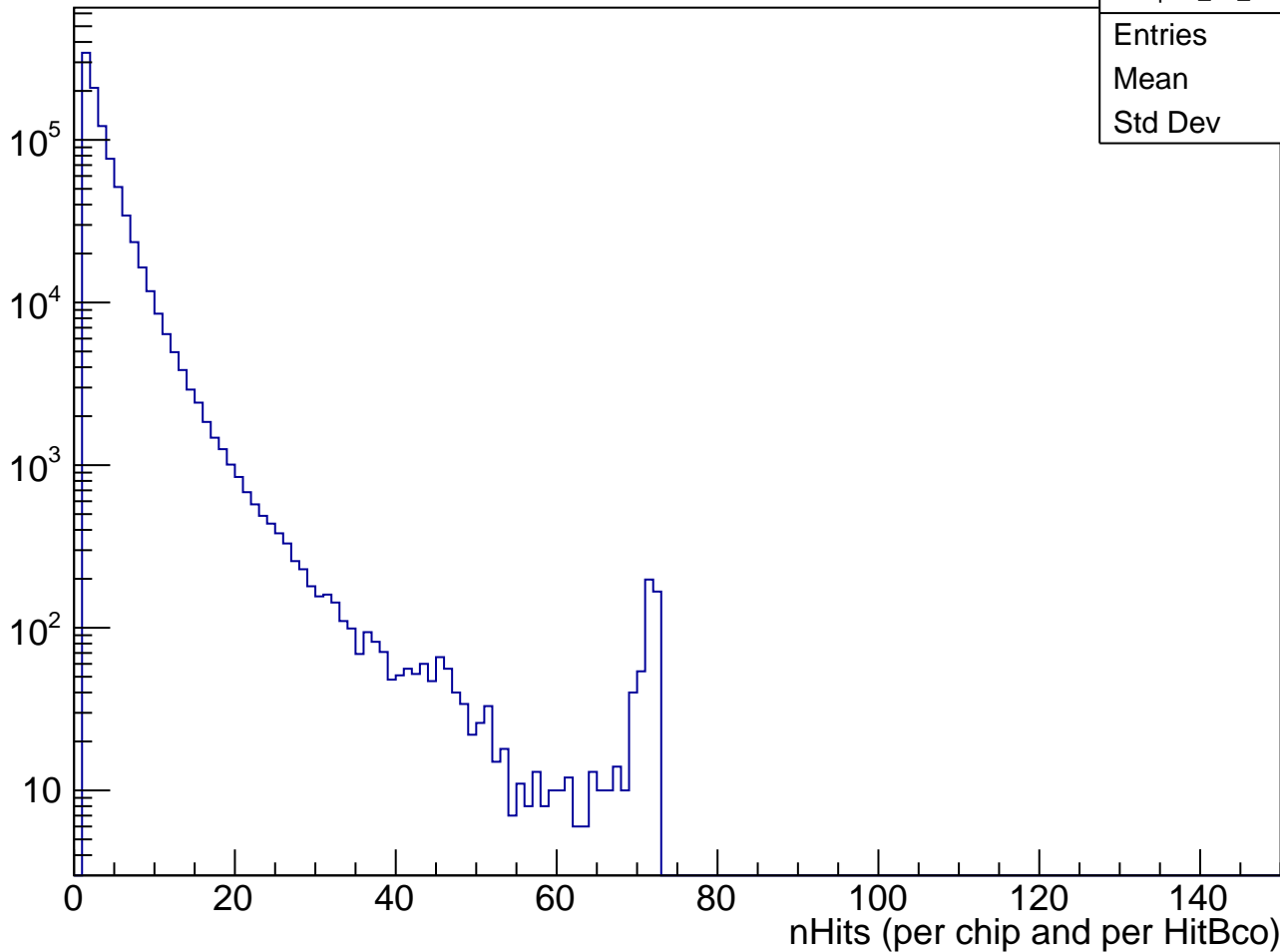


nChipHit_F7_Fch6_Chip16	
Entries	916104
Mean	3.181
Std Dev	3.761

nHits (per chip and per HitBco)

nChipHit_F7_Fch6_Chip17

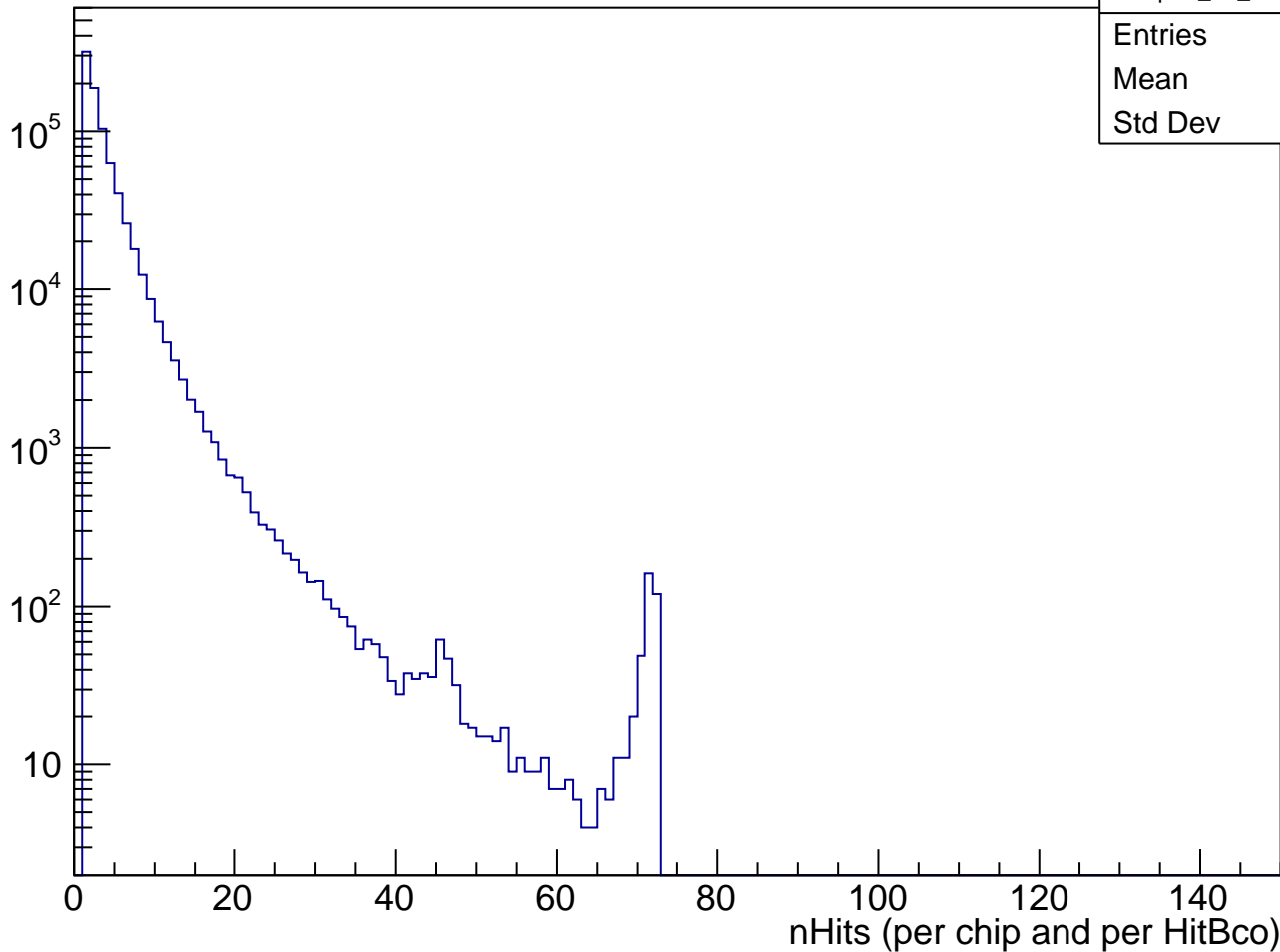
Entries



nChipHit_F7_Fch6_Chip17	
Entries	928962
Mean	3.164
Std Dev	3.781

nChipHit_F7_Fch6_Chip18

Entries

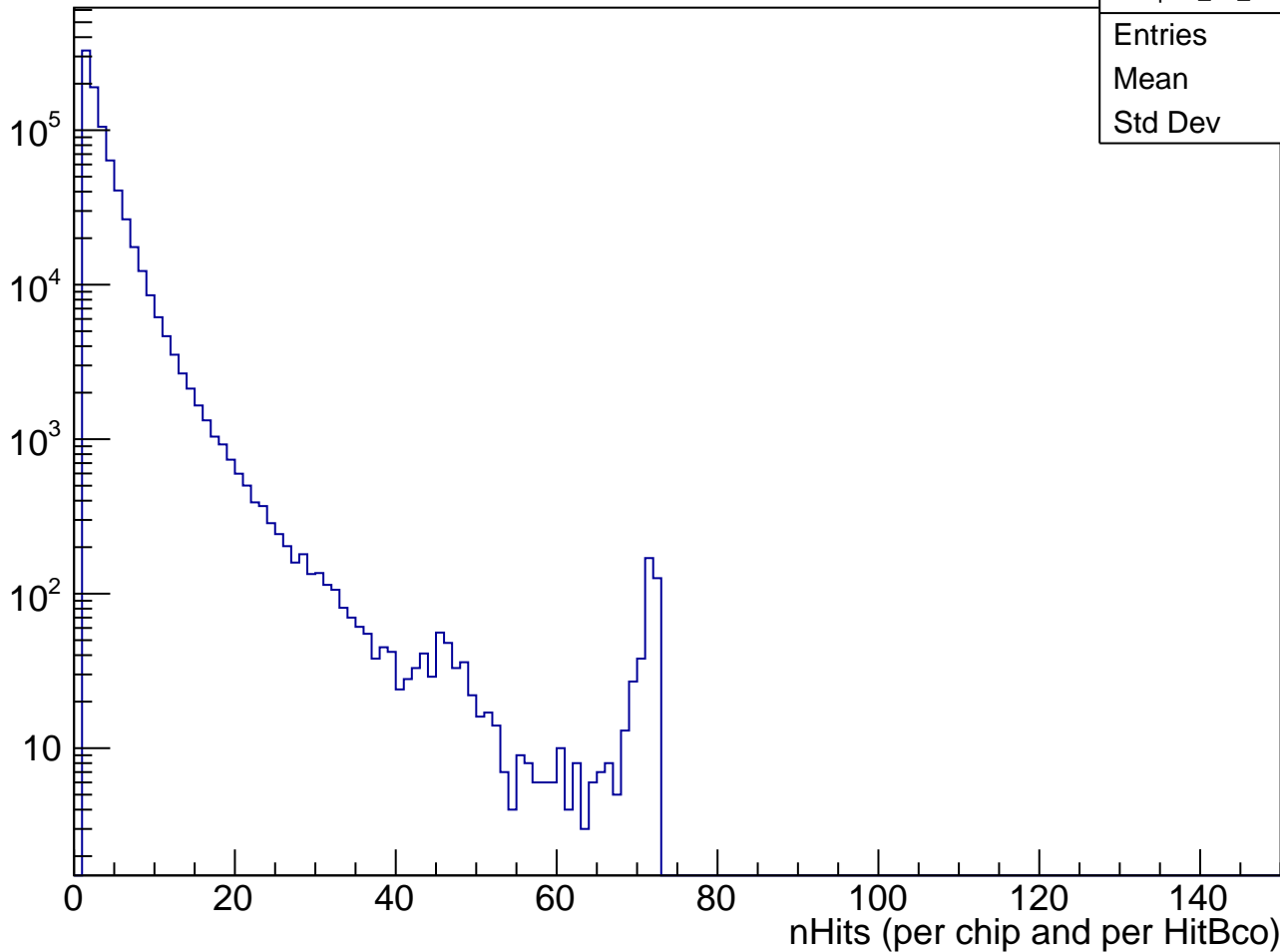


nChipHit_F7_Fch6_Chip18	
Entries	807298
Mean	2.95
Std Dev	3.531

nHits (per chip and per HitBco)

nChipHit_F7_Fch6_Chip19

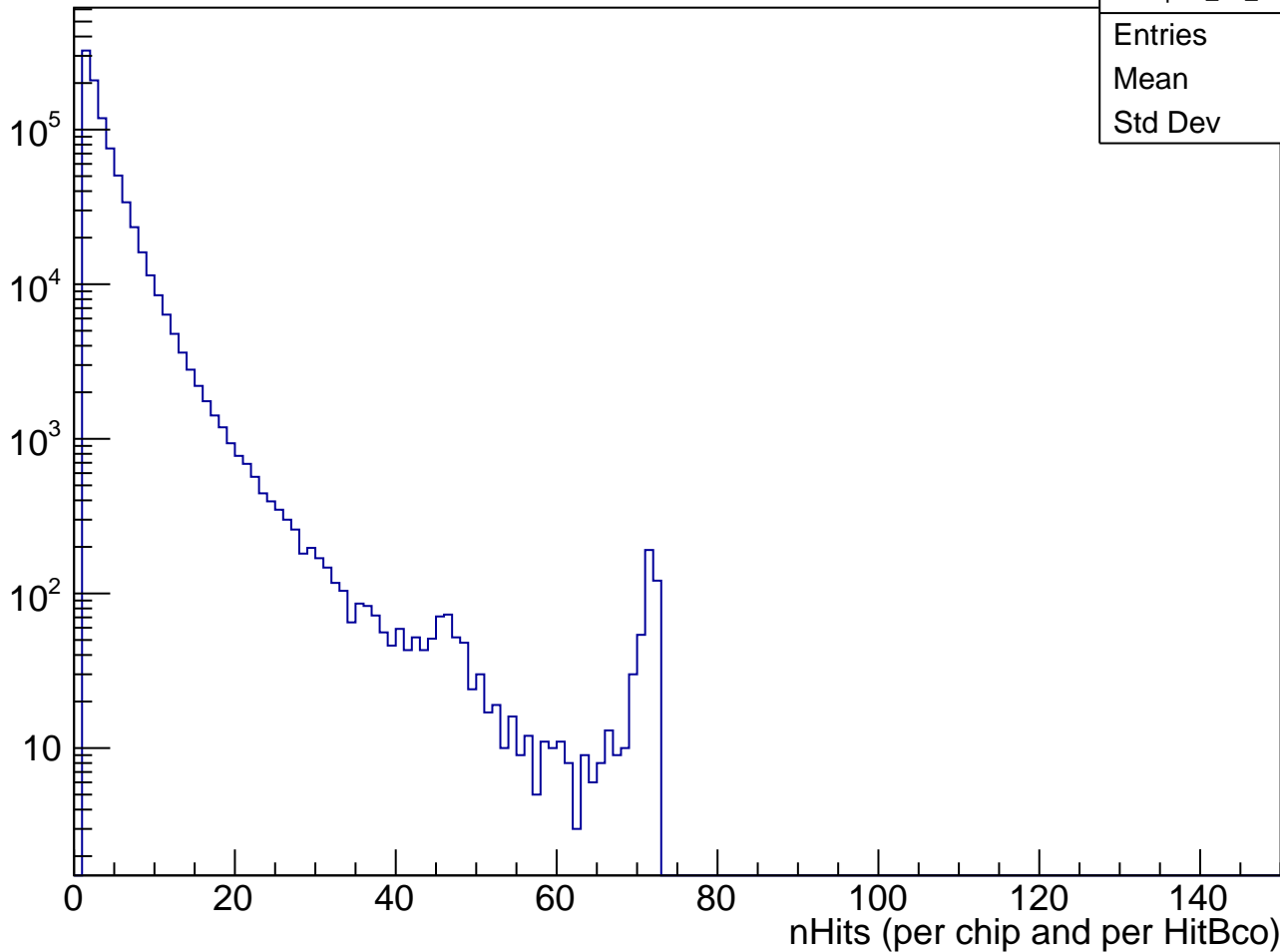
Entries



nChipHit_F7_Fch6_Chip19	
Entries	821437
Mean	2.919
Std Dev	3.498

nChipHit_F7_Fch6_Chip2

Entries



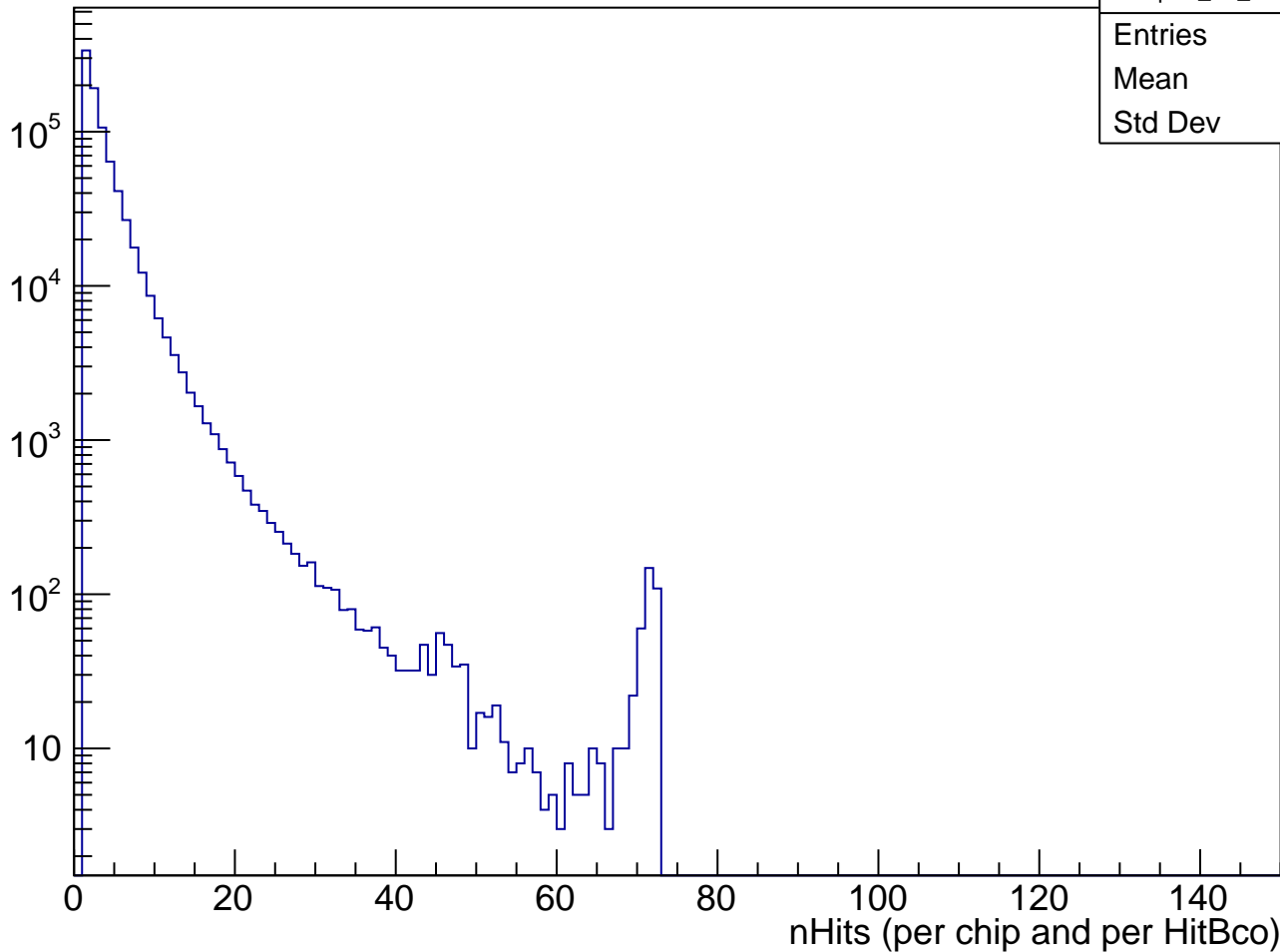
nChipHit_F7_Fch6_Chip2

Entries	901747
Mean	3.175
Std Dev	3.727

nHits (per chip and per HitBco)

nChipHit_F7_Fch6_Chip20

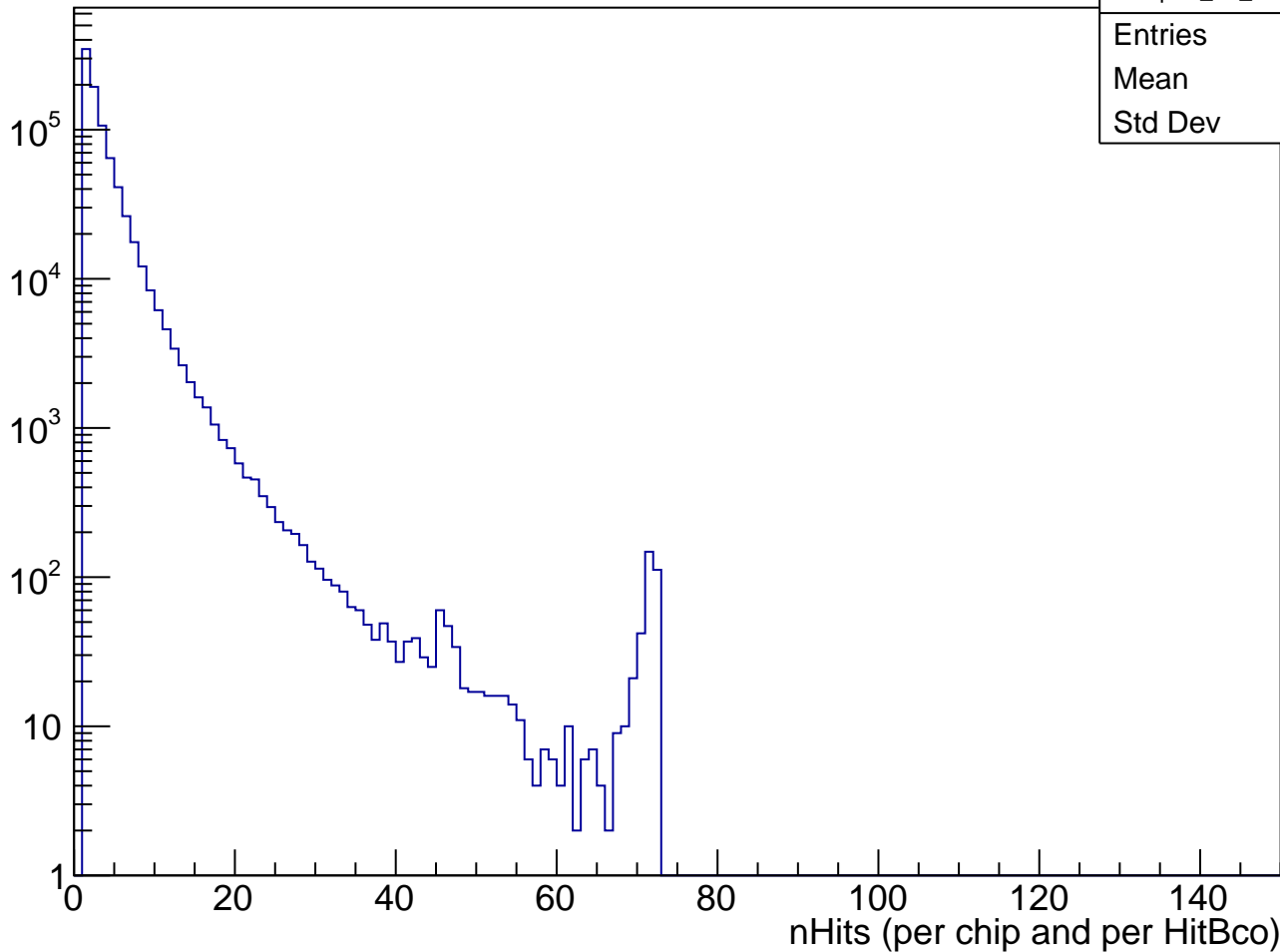
Entries



nChipHit_F7_Fch6_Chip20	
Entries	834244
Mean	2.898
Std Dev	3.463

nChipHit_F7_Fch6_Chip21

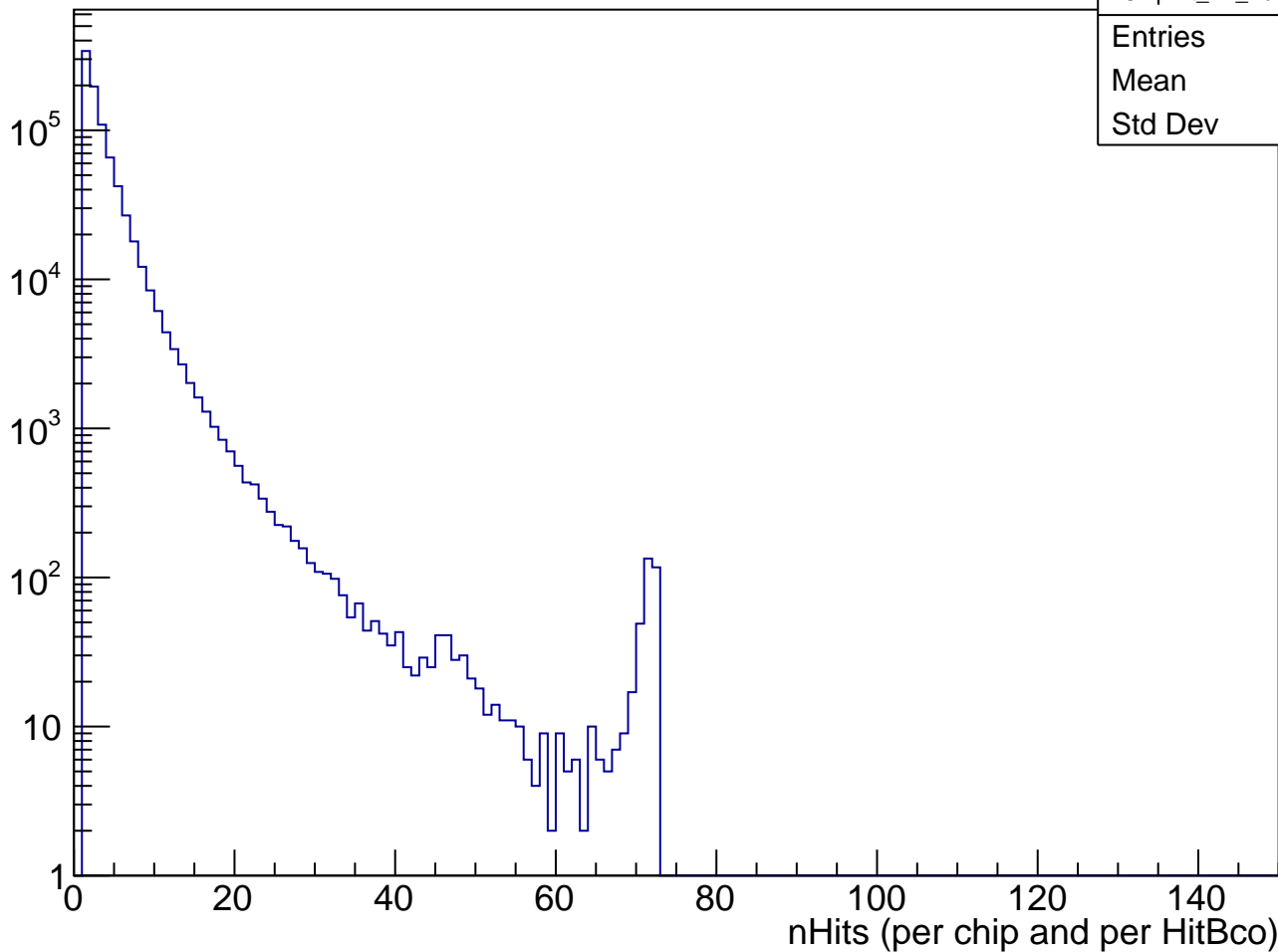
Entries



nChipHit_F7_Fch6_Chip21	
Entries	846359
Mean	2.86
Std Dev	3.403

nChipHit_F7_Fch6_Chip22

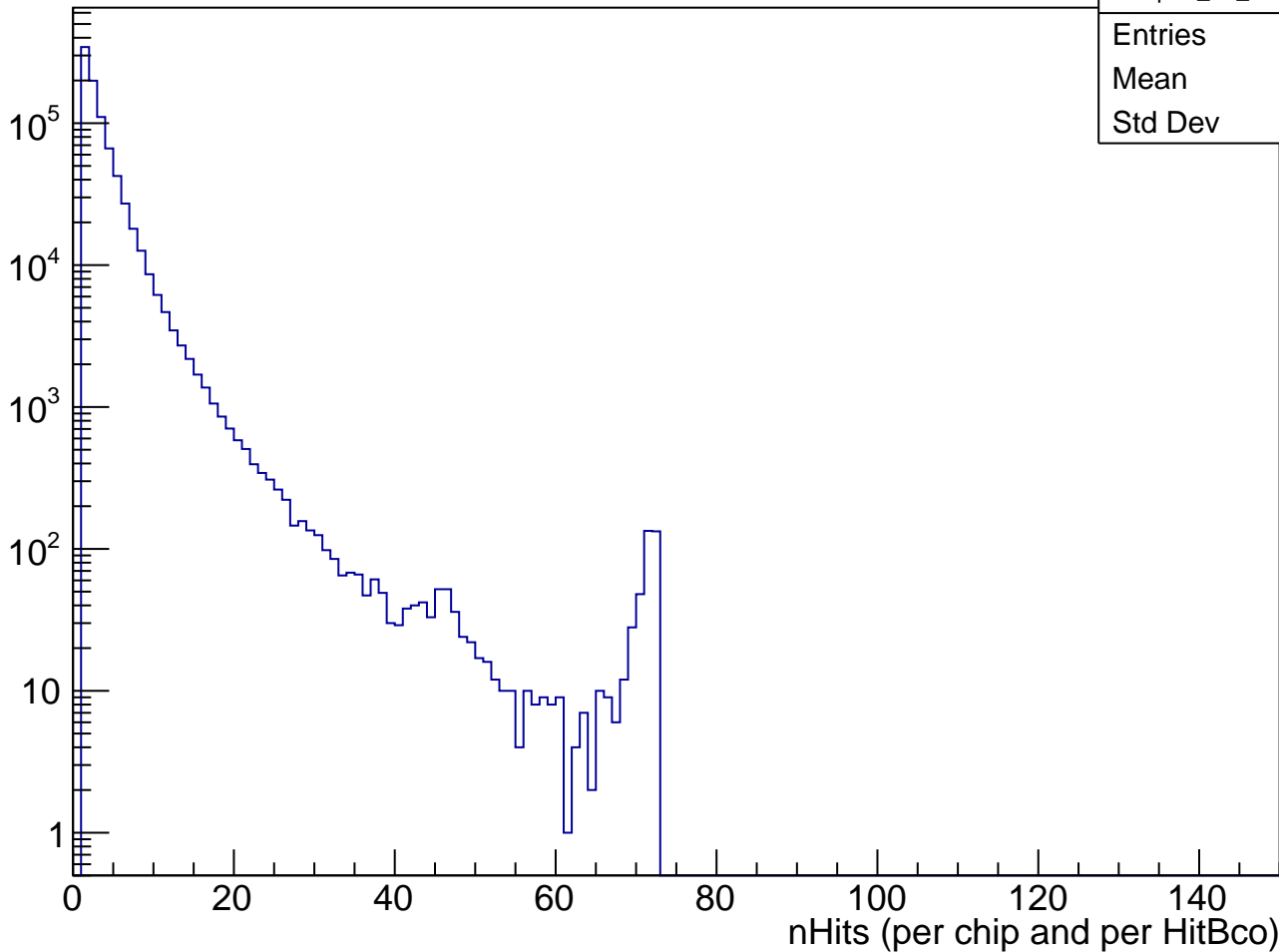
Entries



nChipHit_F7_Fch6_Chip22	
Entries	847838
Mean	2.872
Std Dev	3.37

nChipHit_F7_Fch6_Chip23

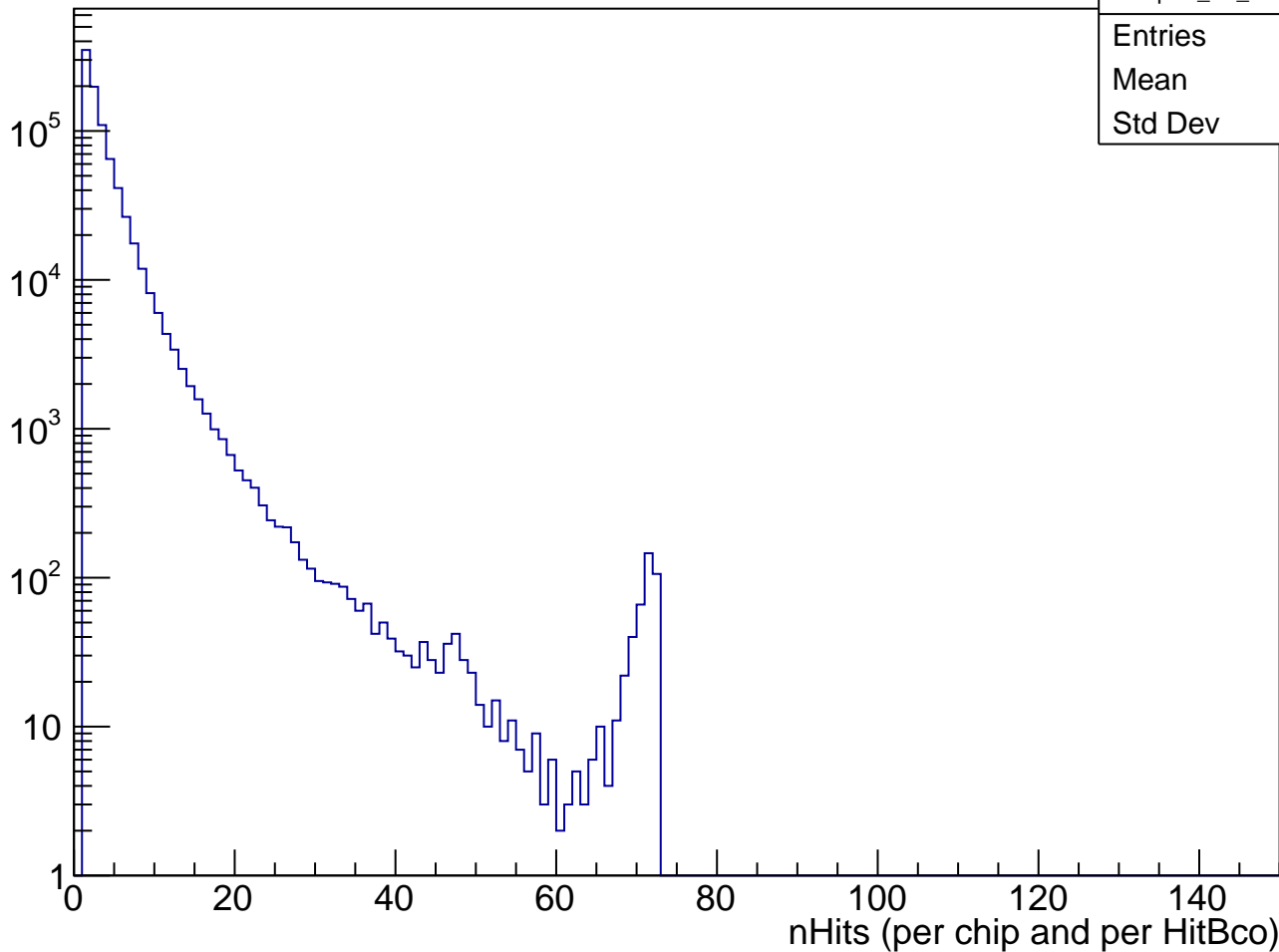
Entries



nChipHit_F7_Fch6_Chip23	
Entries	858759
Mean	2.886
Std Dev	3.424

nChipHit_F7_Fch6_Chip24

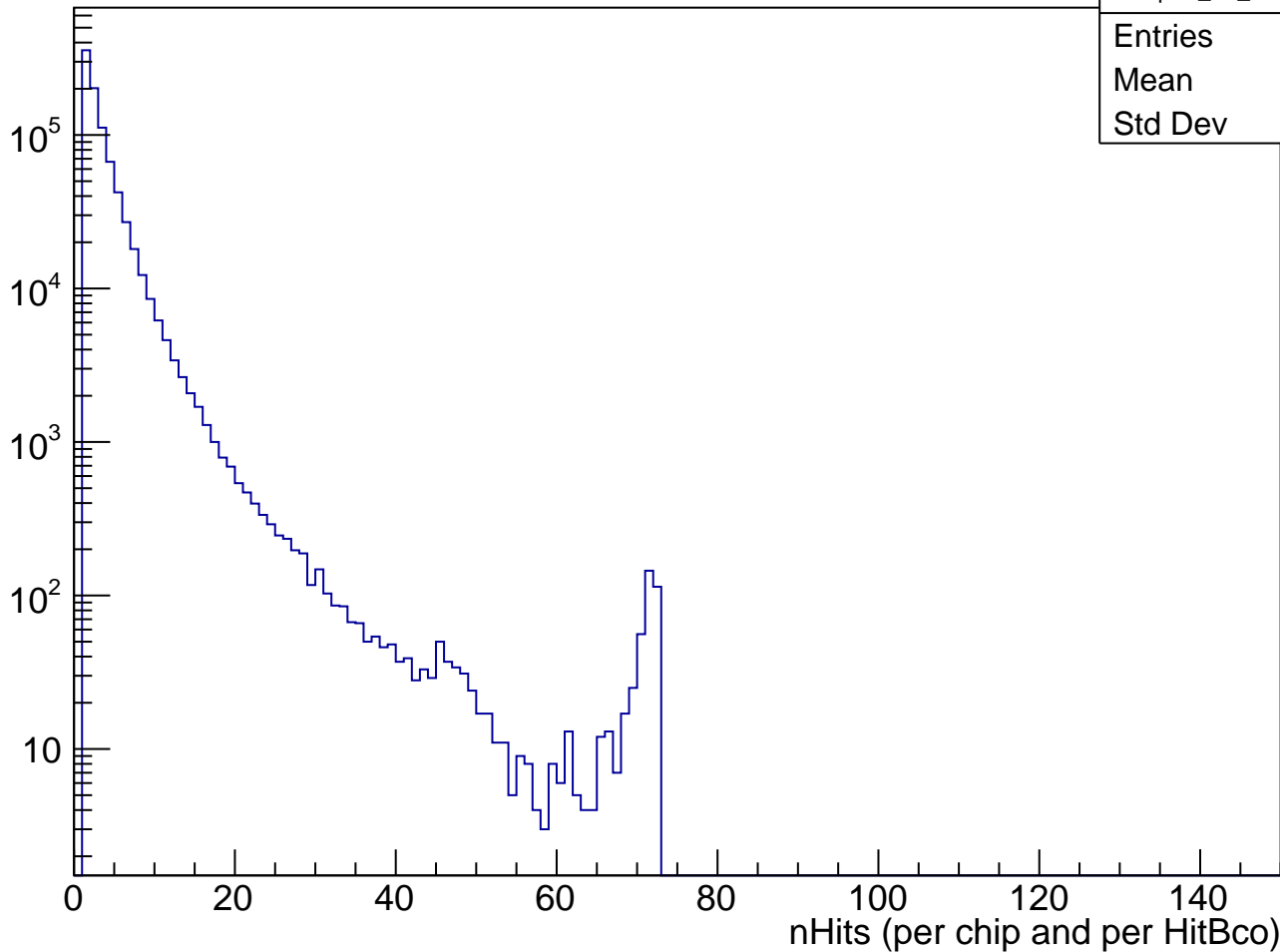
Entries



nChipHit_F7_Fch6_Chip24	
Entries	855754
Mean	2.833
Std Dev	3.37

nChipHit_F7_Fch6_Chip25

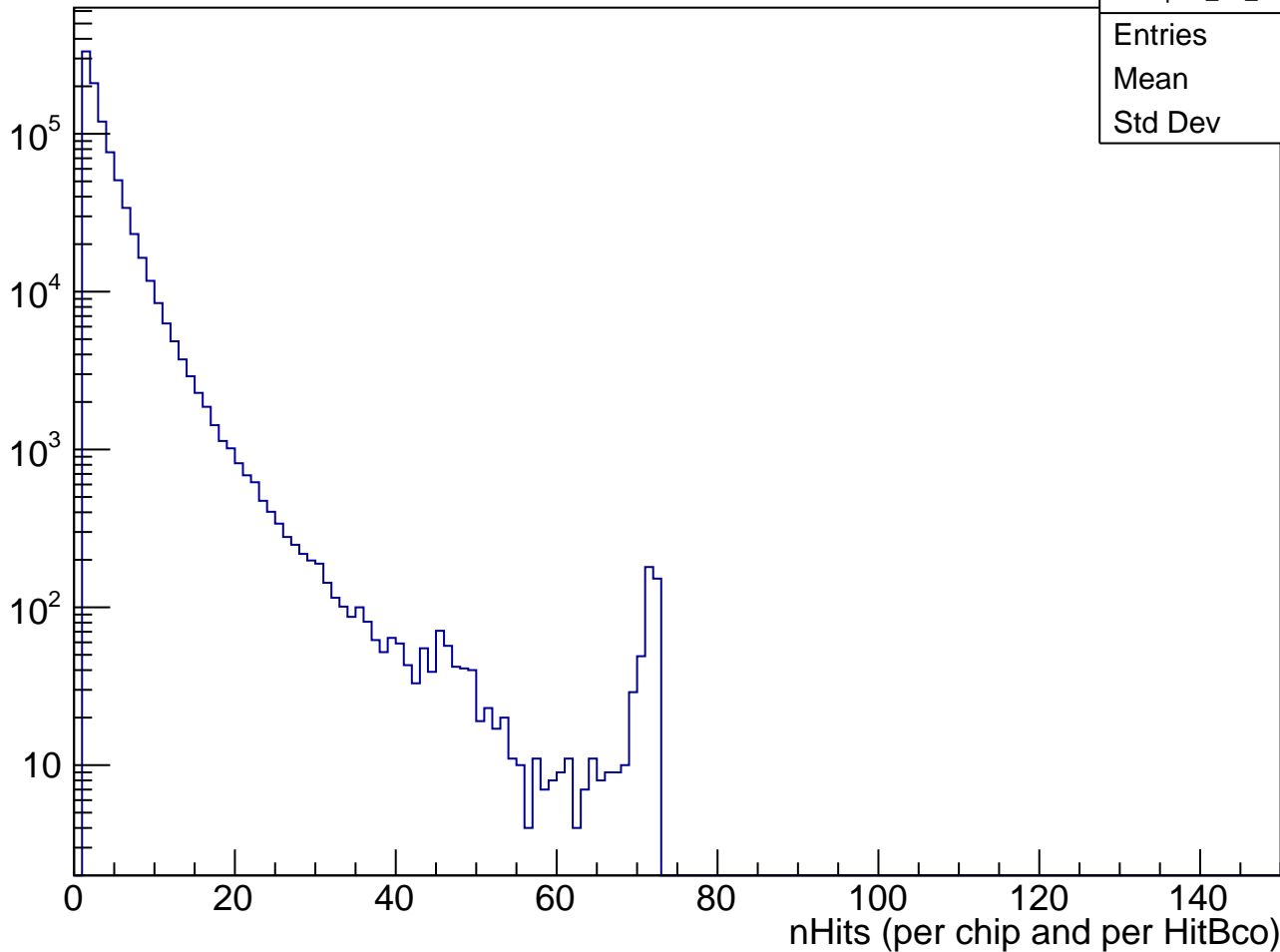
Entries



nChipHit_F7_Fch6_Chip25	
Entries	873941
Mean	2.849
Std Dev	3.394

nChipHit_F7_Fch6_Chip3

Entries

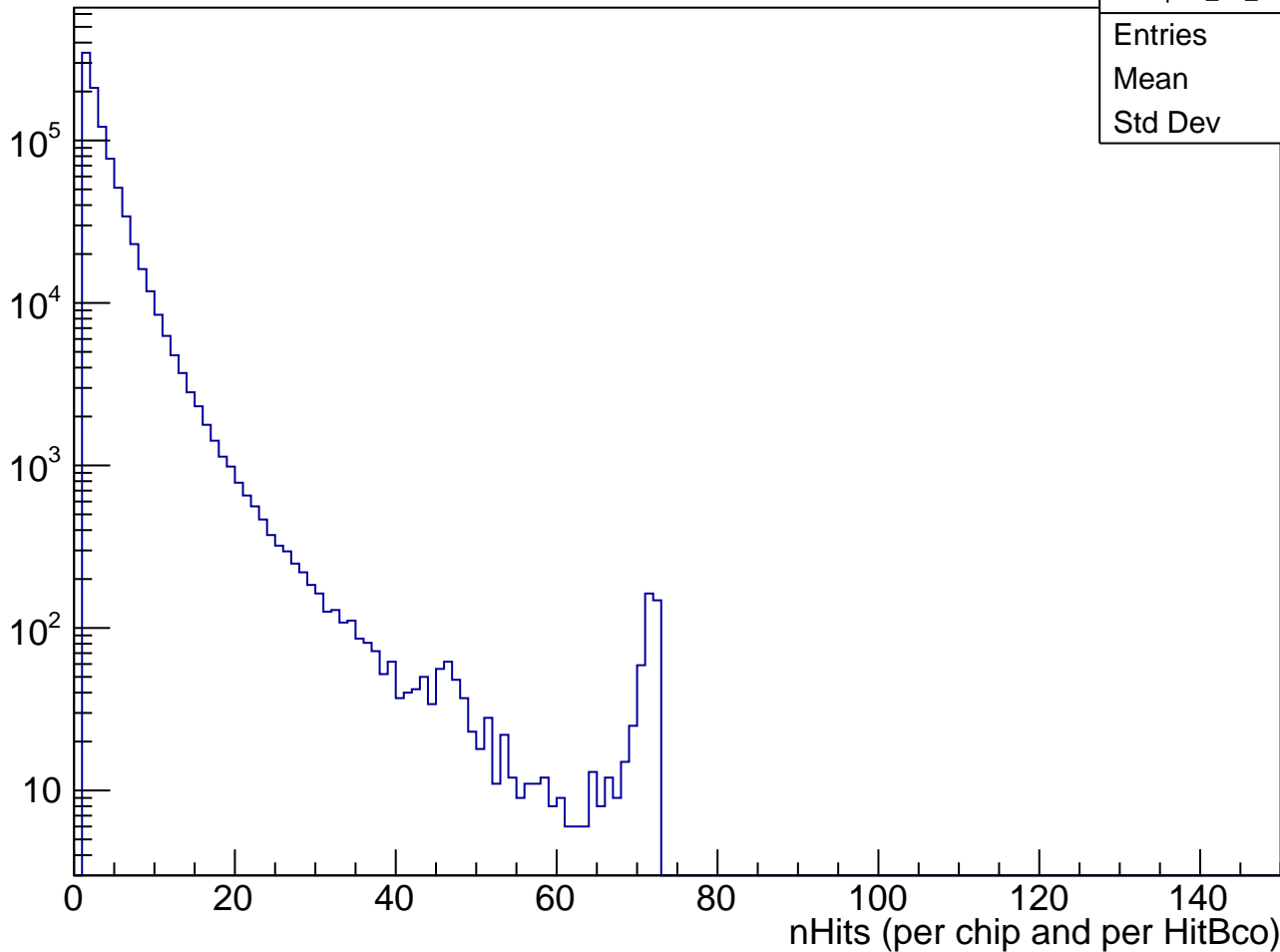


nChipHit_F7_Fch6_Chip3

Entries	914406
Mean	3.167
Std Dev	3.731

nChipHit_F7_Fch6_Chip4

Entries



nChipHit_F7_Fch6_Chip4

Entries 931667

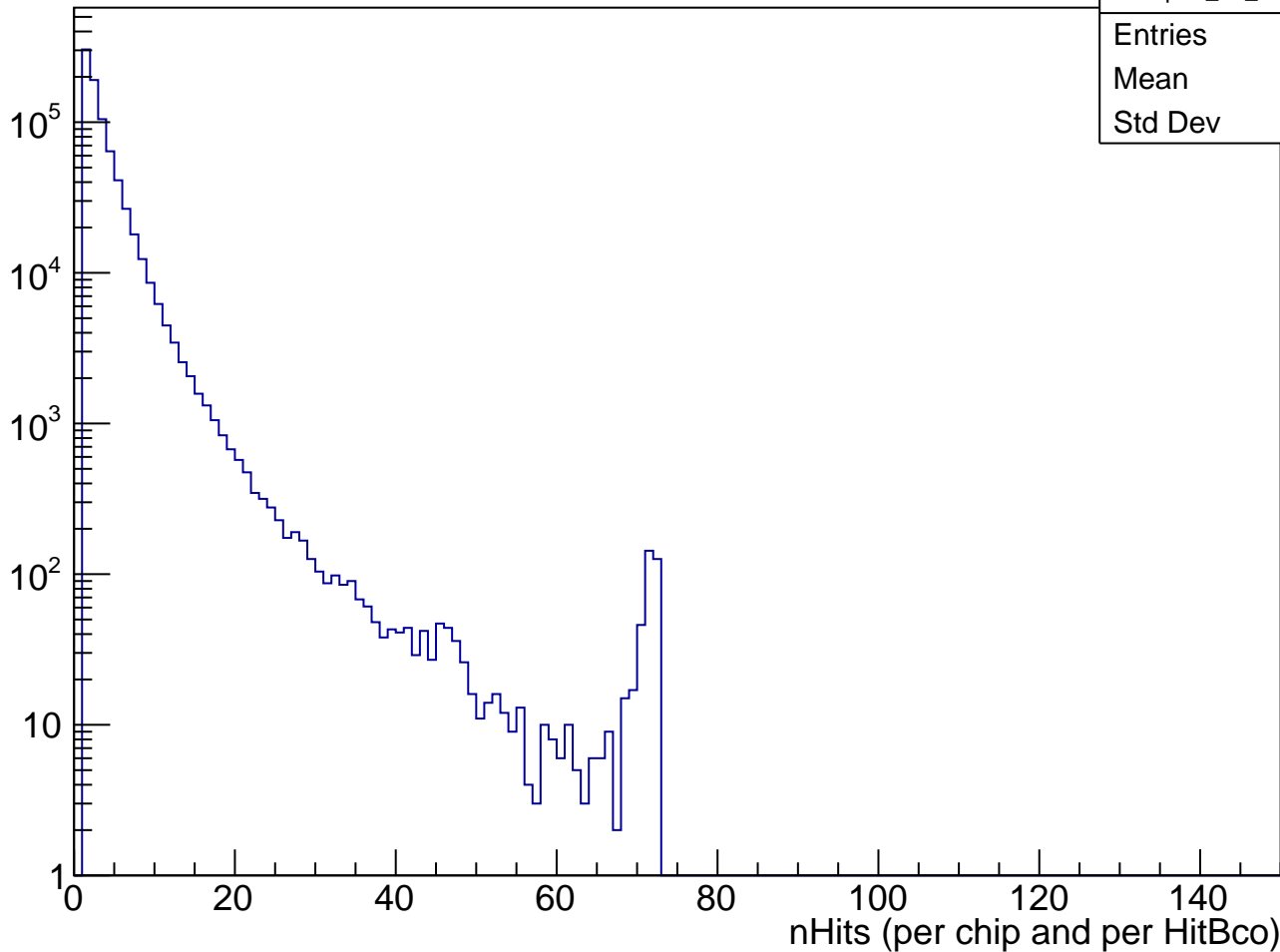
Mean 3.124

Std Dev 3.677

nHits (per chip and per HitBco)

nChipHit_F7_Fch6_Chip5

Entries



nChipHit_F7_Fch6_Chip5

Entries 798544

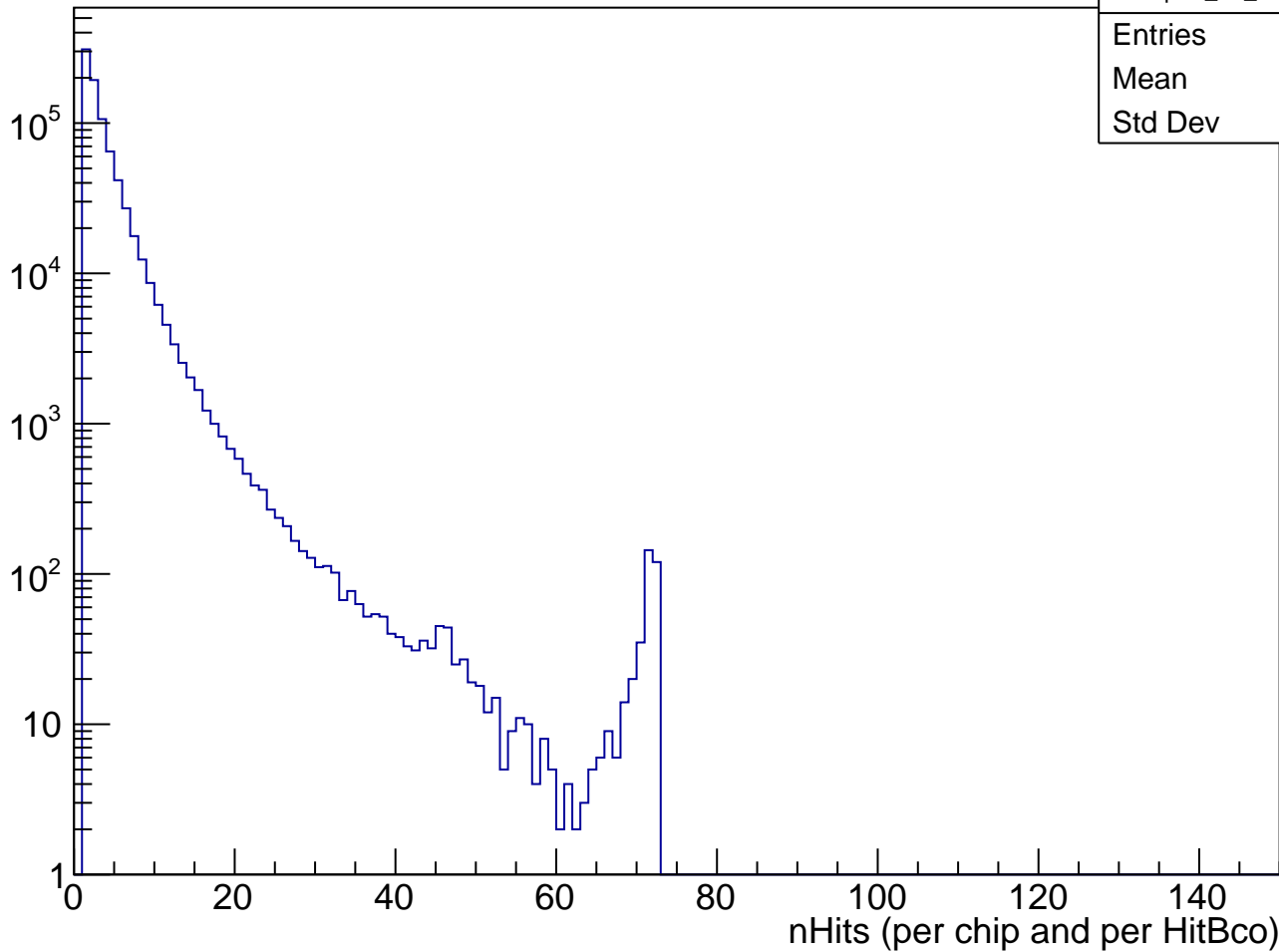
Mean 2.965

Std Dev 3.481

nHits (per chip and per HitBco)

nChipHit_F7_Fch6_Chip6

Entries



nChipHit_F7_Fch6_Chip6

Entries 809937

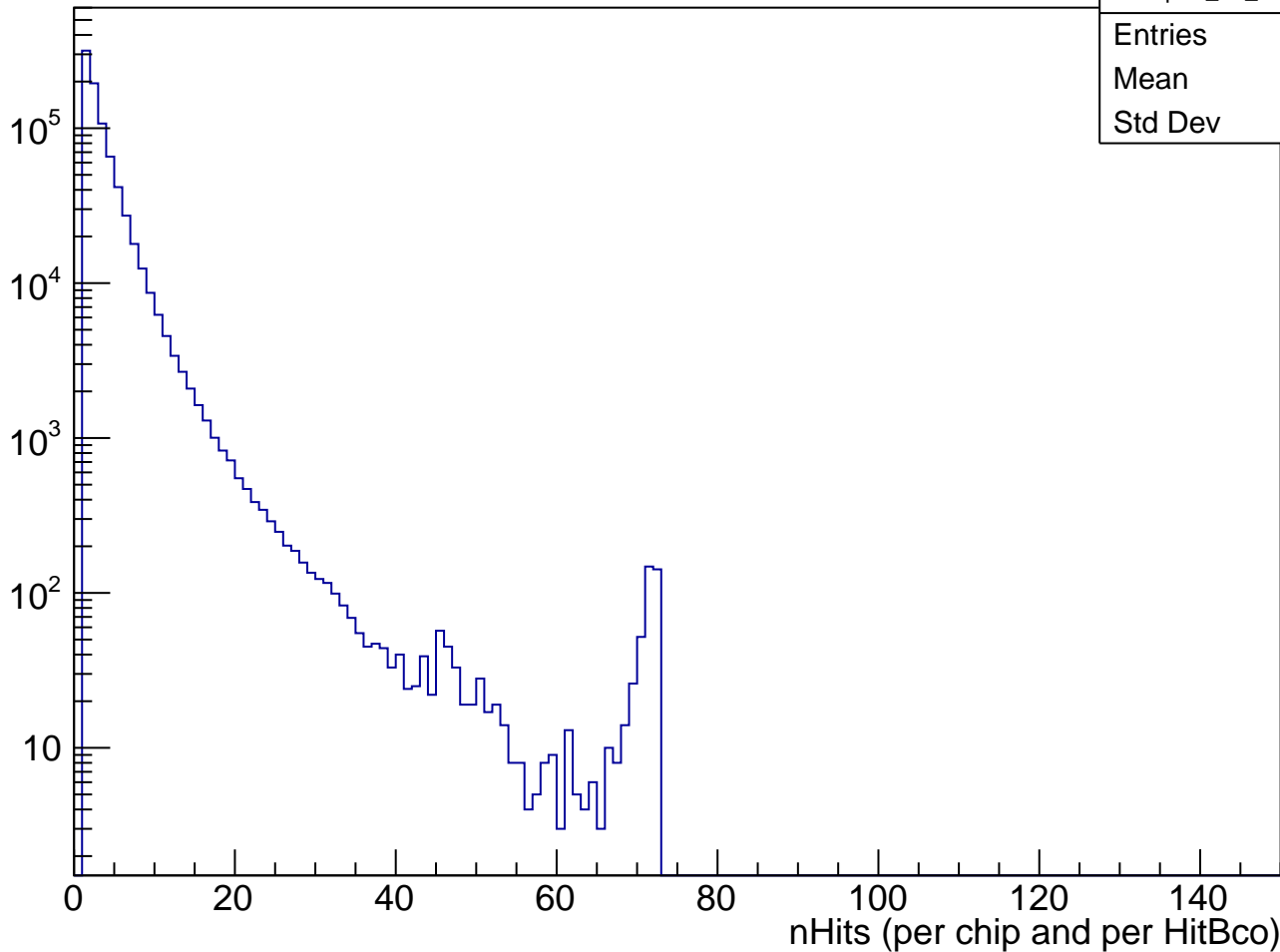
Mean 2.947

Std Dev 3.438

nHits (per chip and per HitBco)

nChipHit_F7_Fch6_Chip7

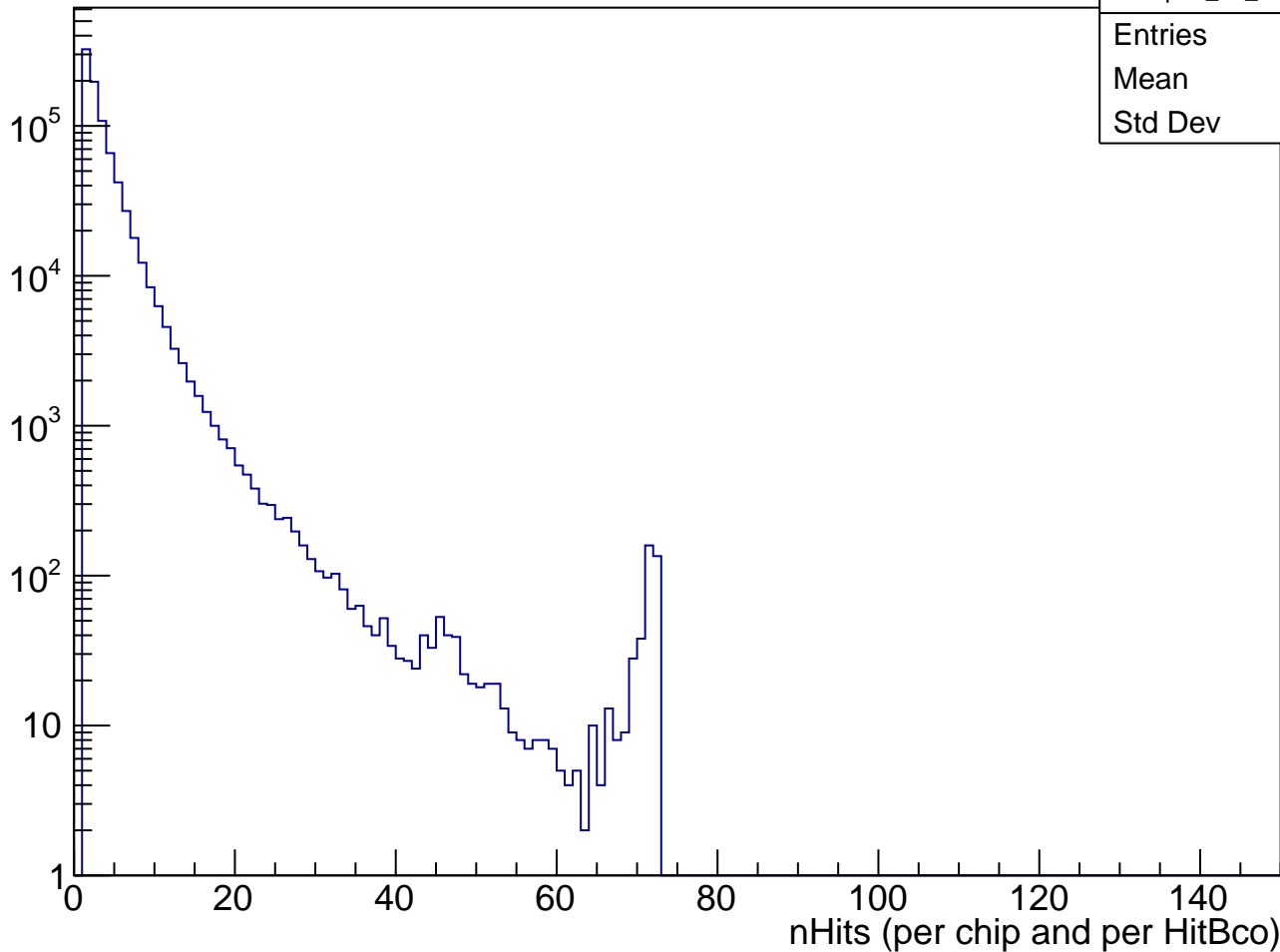
Entries



nChipHit_F7_Fch6_Chip7	
Entries	821419
Mean	2.942
Std Dev	3.484

nChipHit_F7_Fch6_Chip8

Entries



nChipHit_F7_Fch6_Chip8

Entries 831617

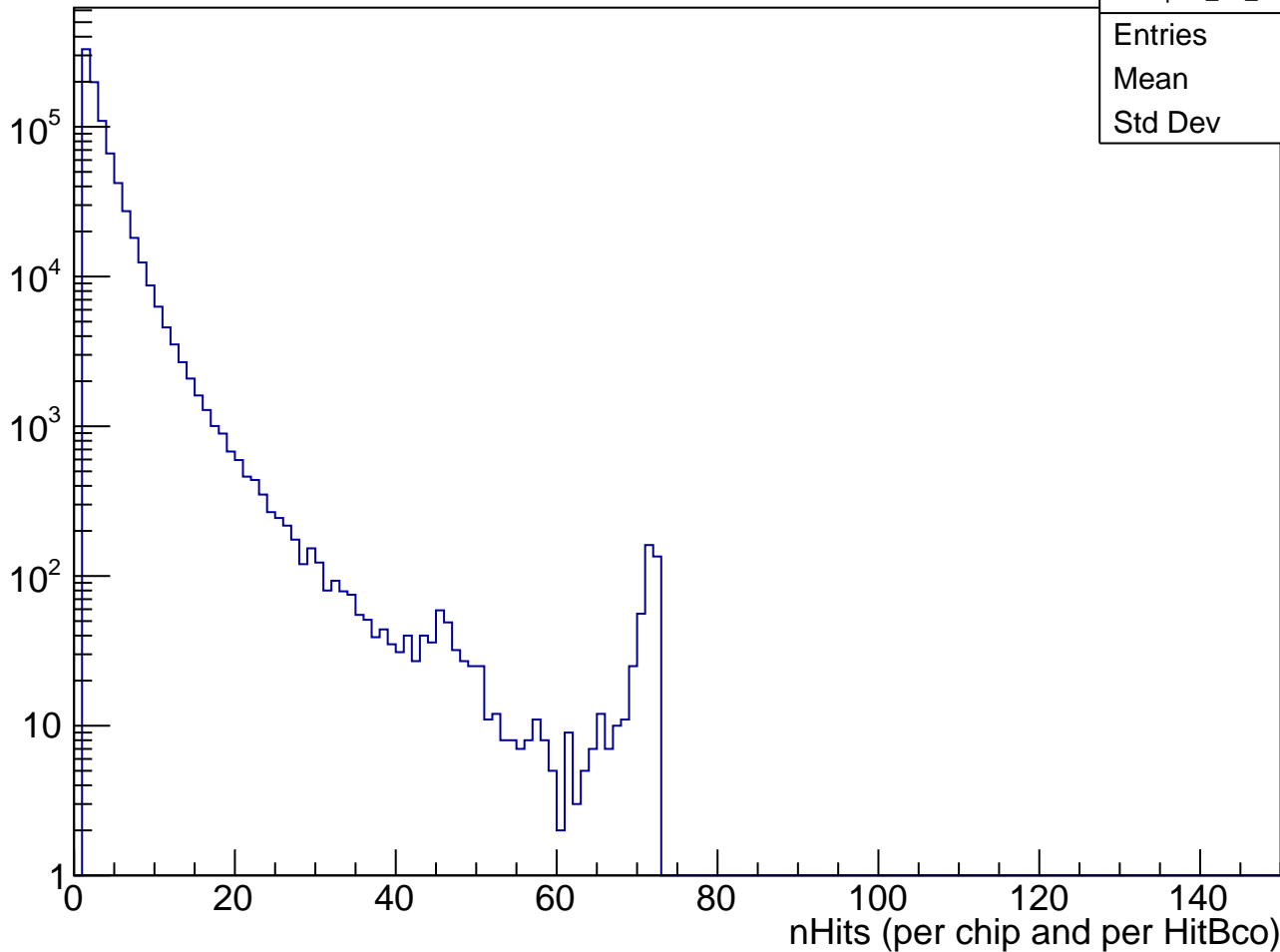
Mean 2.912

Std Dev 3.443

nHits (per chip and per HitBco)

nChipHit_F7_Fch6_Chip9

Entries



nChipHit_F7_Fch6_Chip9

Entries 842608

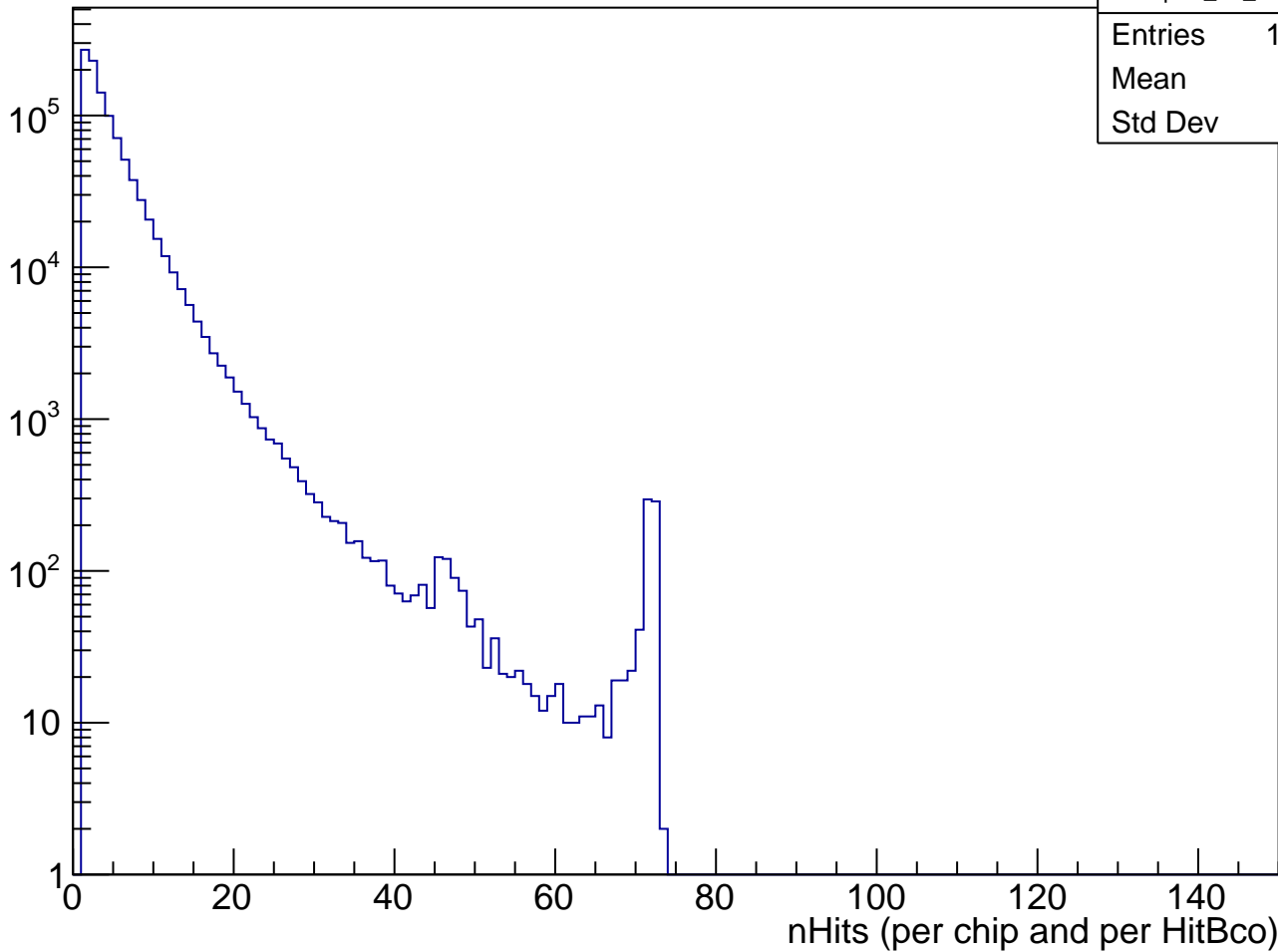
Mean 2.915

Std Dev 3.463

nHits (per chip and per HitBco)

nChipHit_F7_Fch7_Chip0

Entries



nChipHit_F7_Fch7_Chip0

Entries 1025036

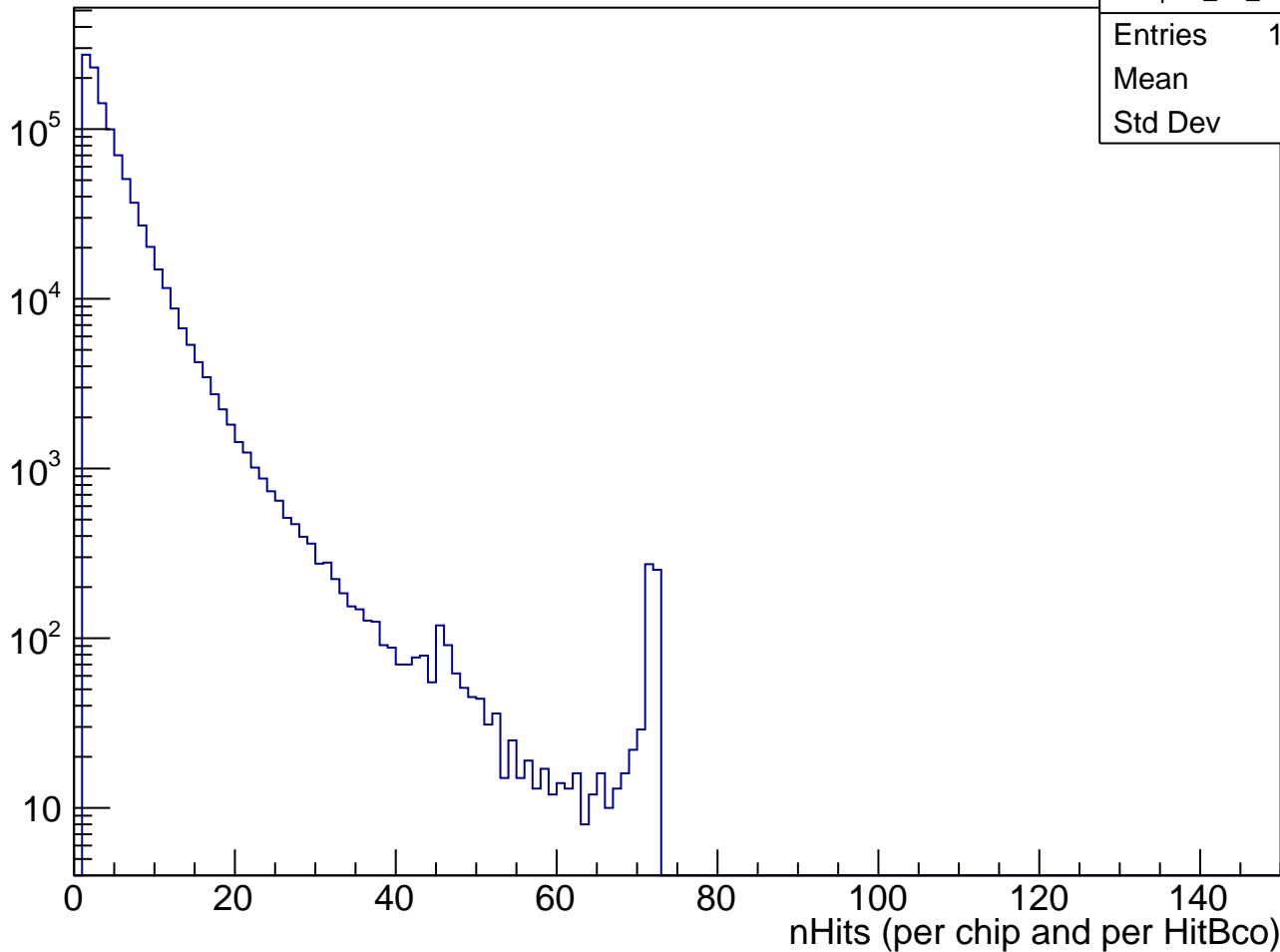
Mean 3.937

Std Dev 4.412

nHits (per chip and per HitBco)

nChipHit_F7_Fch7_Chip1

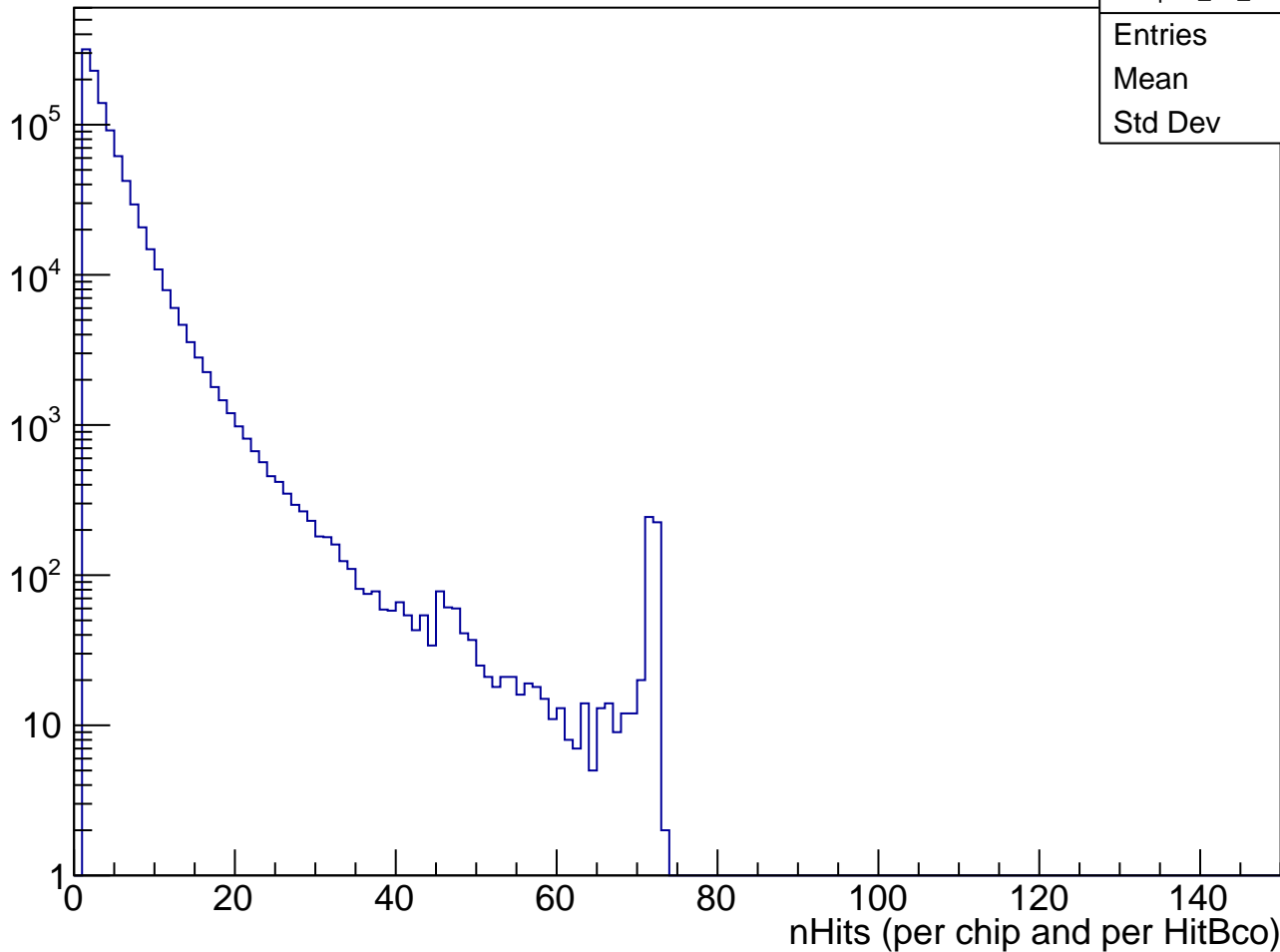
Entries



nChipHit_F7_Fch7_Chip1	
Entries	1024507
Mean	3.888
Std Dev	4.337

nChipHit_F7_Fch7_Chip10

Entries

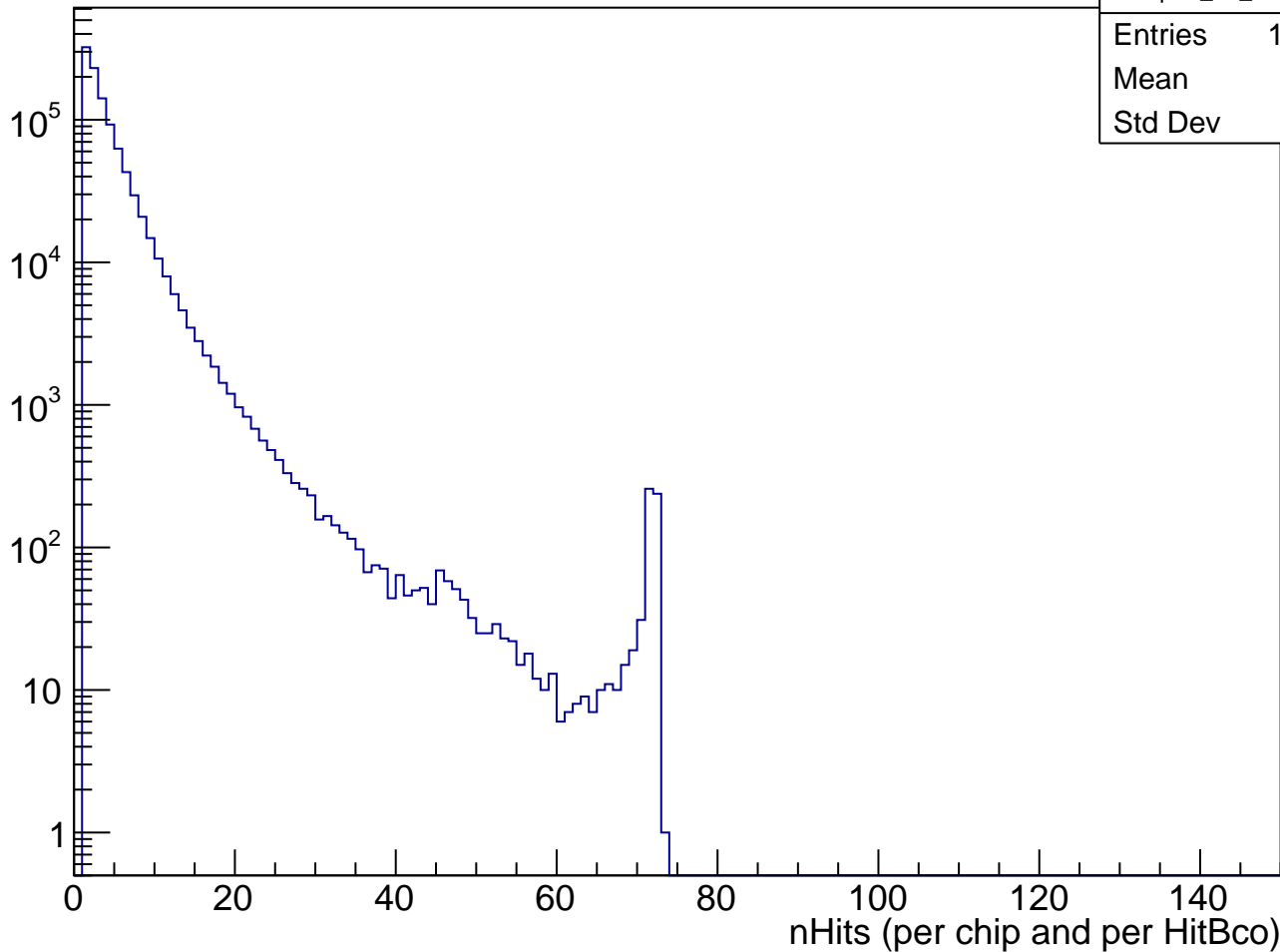


nChipHit_F7_Fch7_Chip10	
Entries	996345
Mean	3.404
Std Dev	3.878

nHits (per chip and per HitBco)

nChipHit_F7_Fch7_Chip11

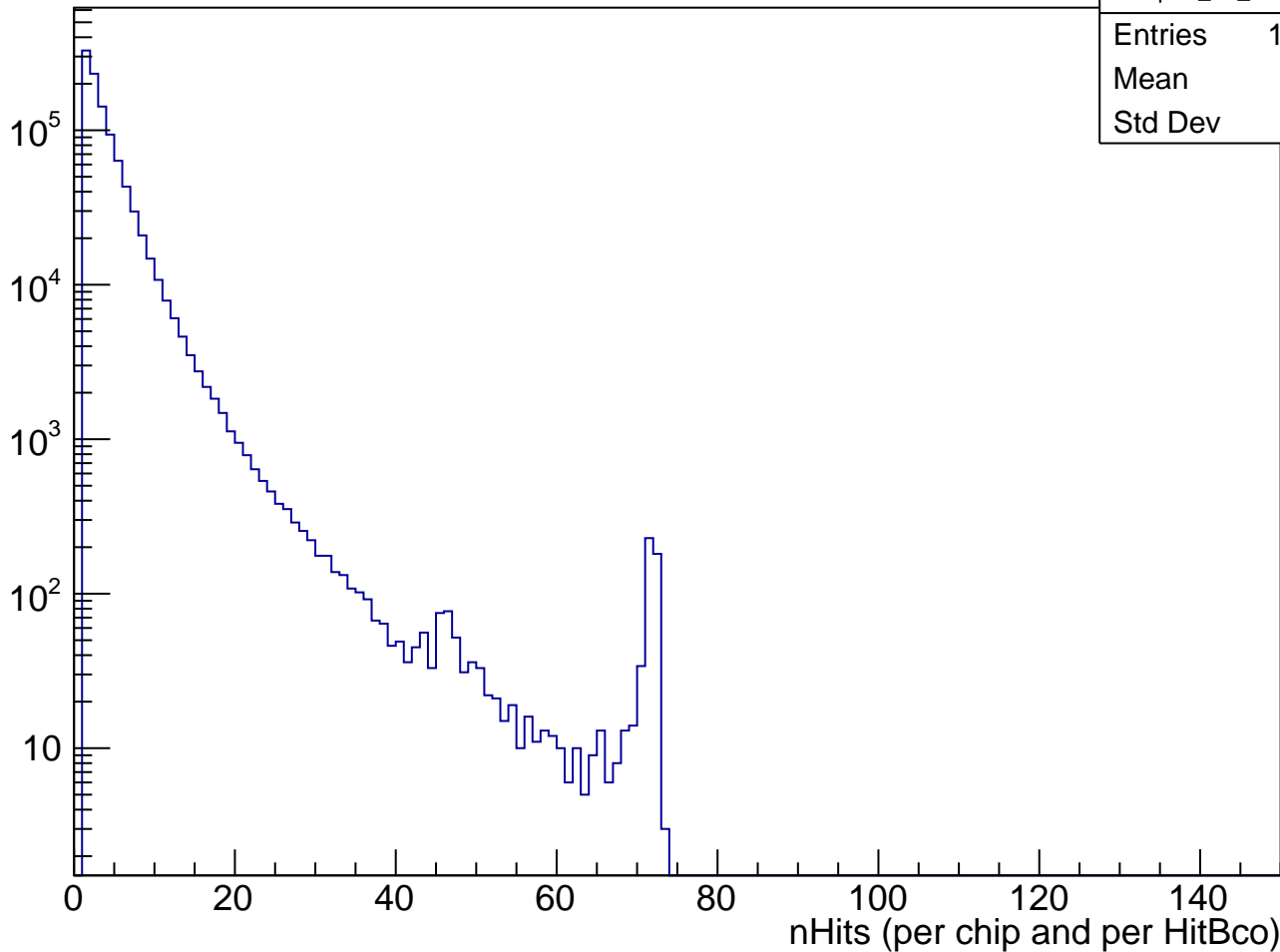
Entries



nChipHit_F7_Fch7_Chip11	
Entries	1008094
Mean	3.39
Std Dev	3.869

nChipHit_F7_Fch7_Chip12

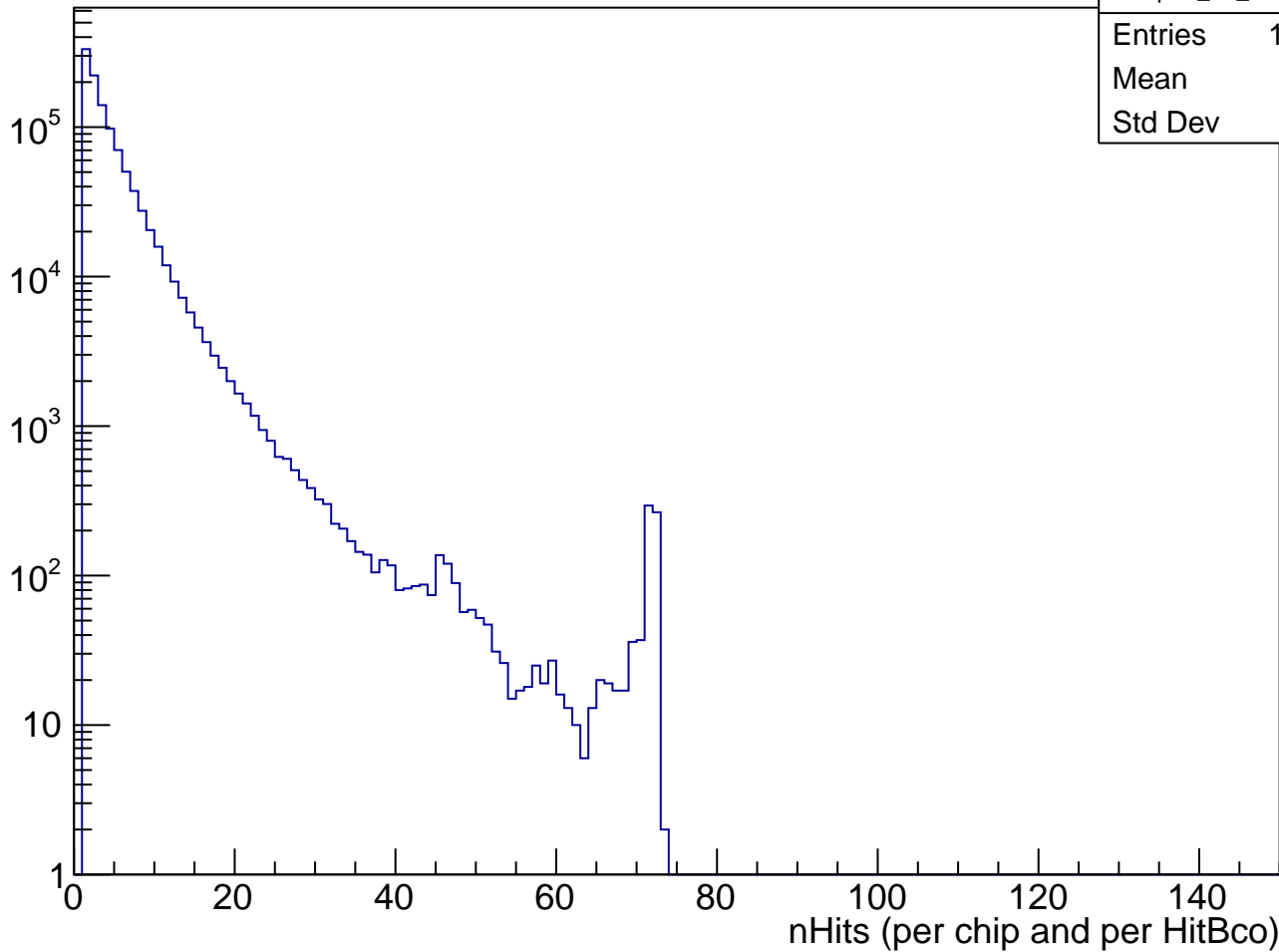
Entries



nChipHit_F7_Fch7_Chip12	
Entries	1017869
Mean	3.369
Std Dev	3.789

nChipHit_F7_Fch7_Chip13

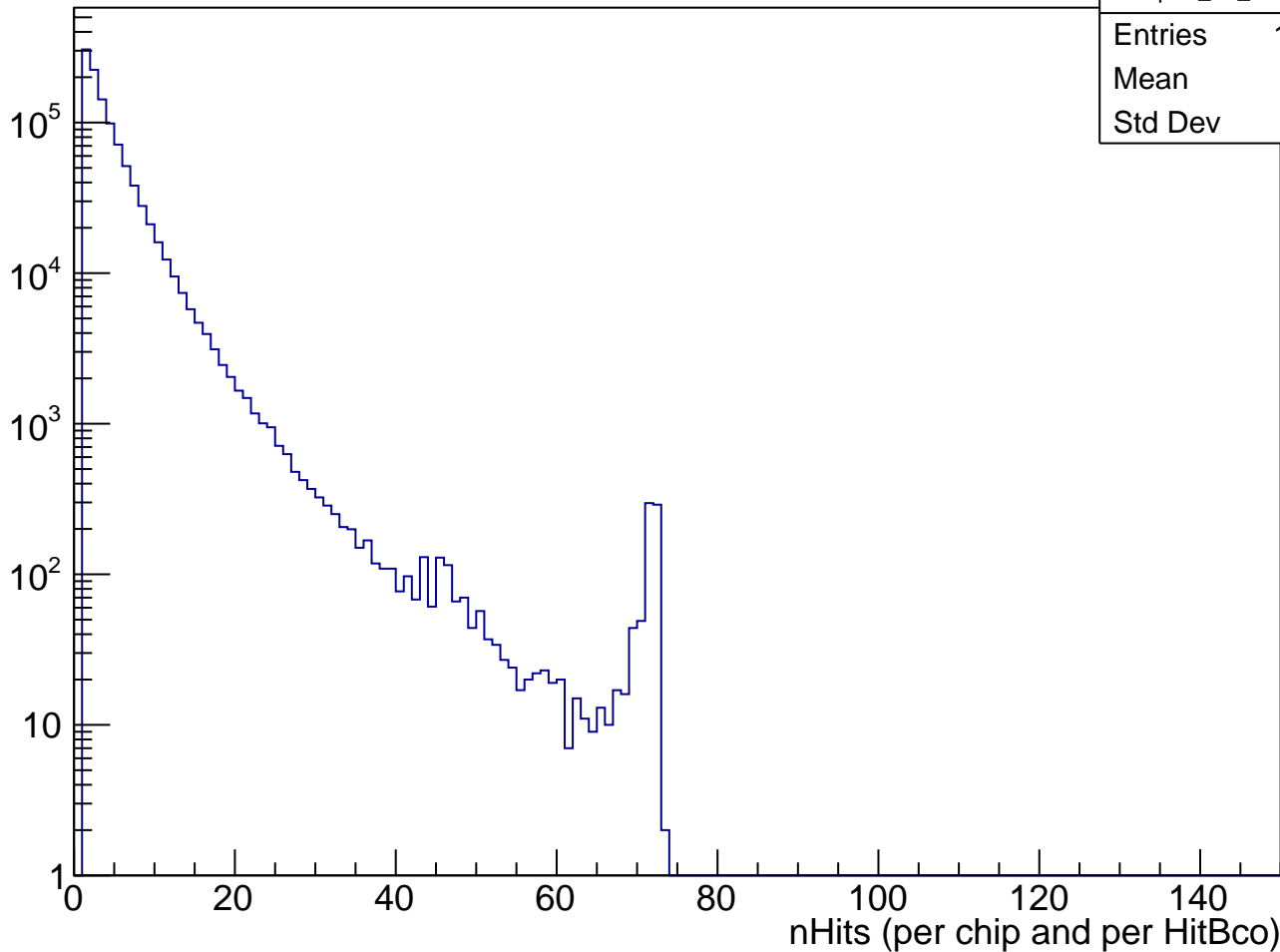
Entries



nChipHit_F7_Fch7_Chip13	
Entries	1076143
Mean	3.817
Std Dev	4.442

nChipHit_F7_Fch7_Chip14

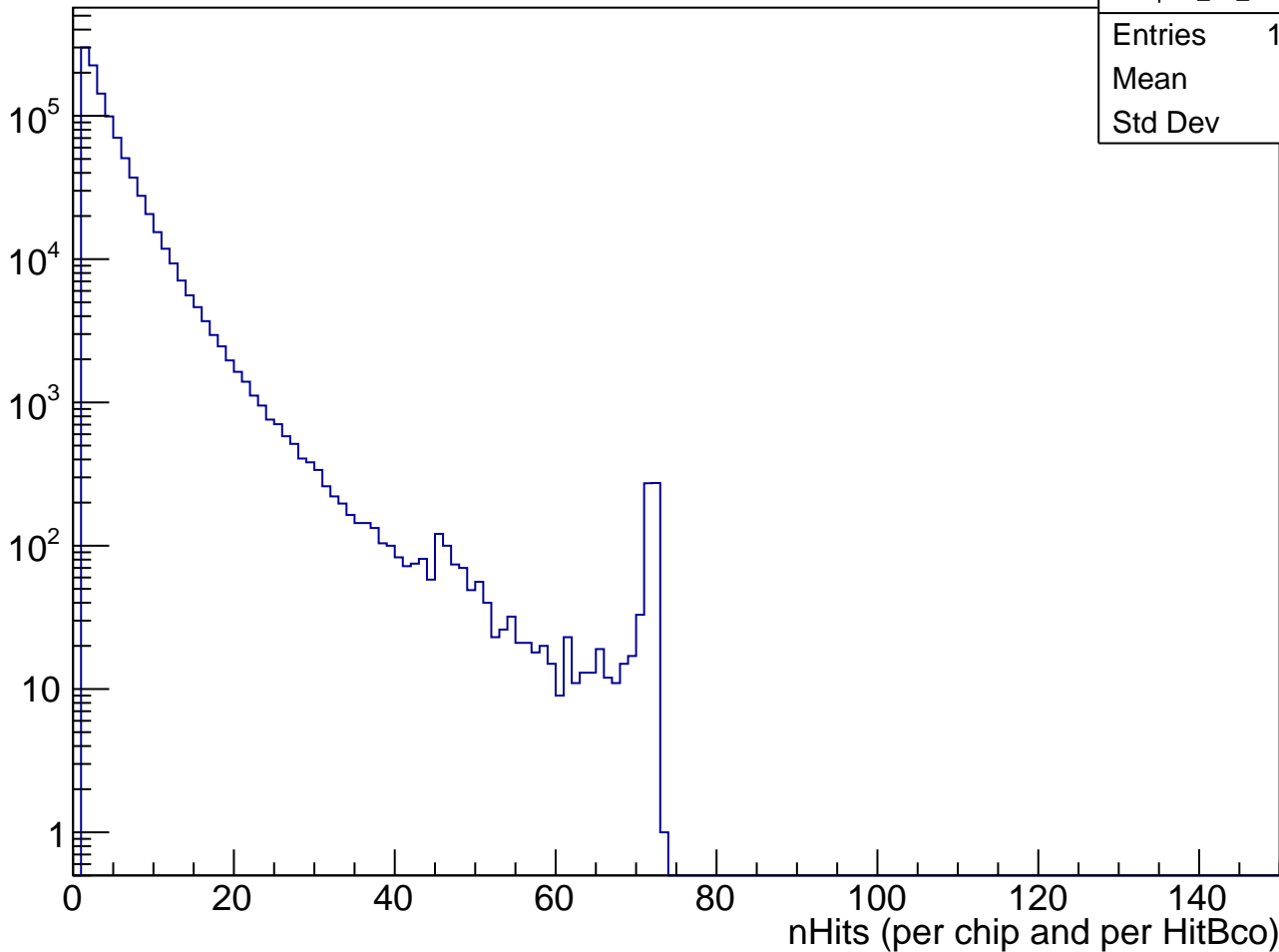
Entries



nChipHit_F7_Fch7_Chip14	
Entries	1060911
Mean	3.917
Std Dev	4.505

nChipHit_F7_Fch7_Chip15

Entries

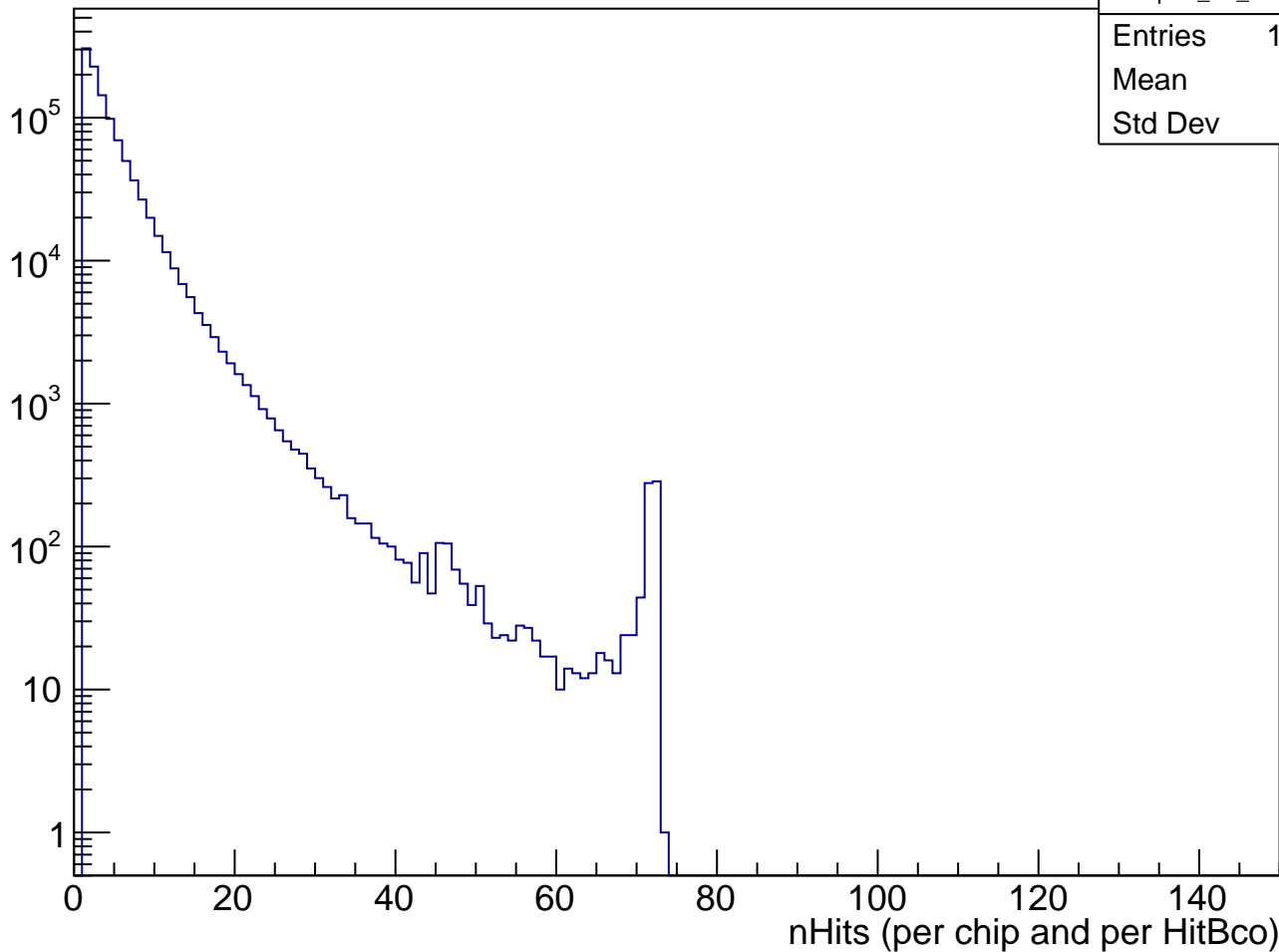


nChipHit_F7_Fch7_Chip15	
Entries	1050999
Mean	3.884
Std Dev	4.421

nHits (per chip and per HitBco)

nChipHit_F7_Fch7_Chip16

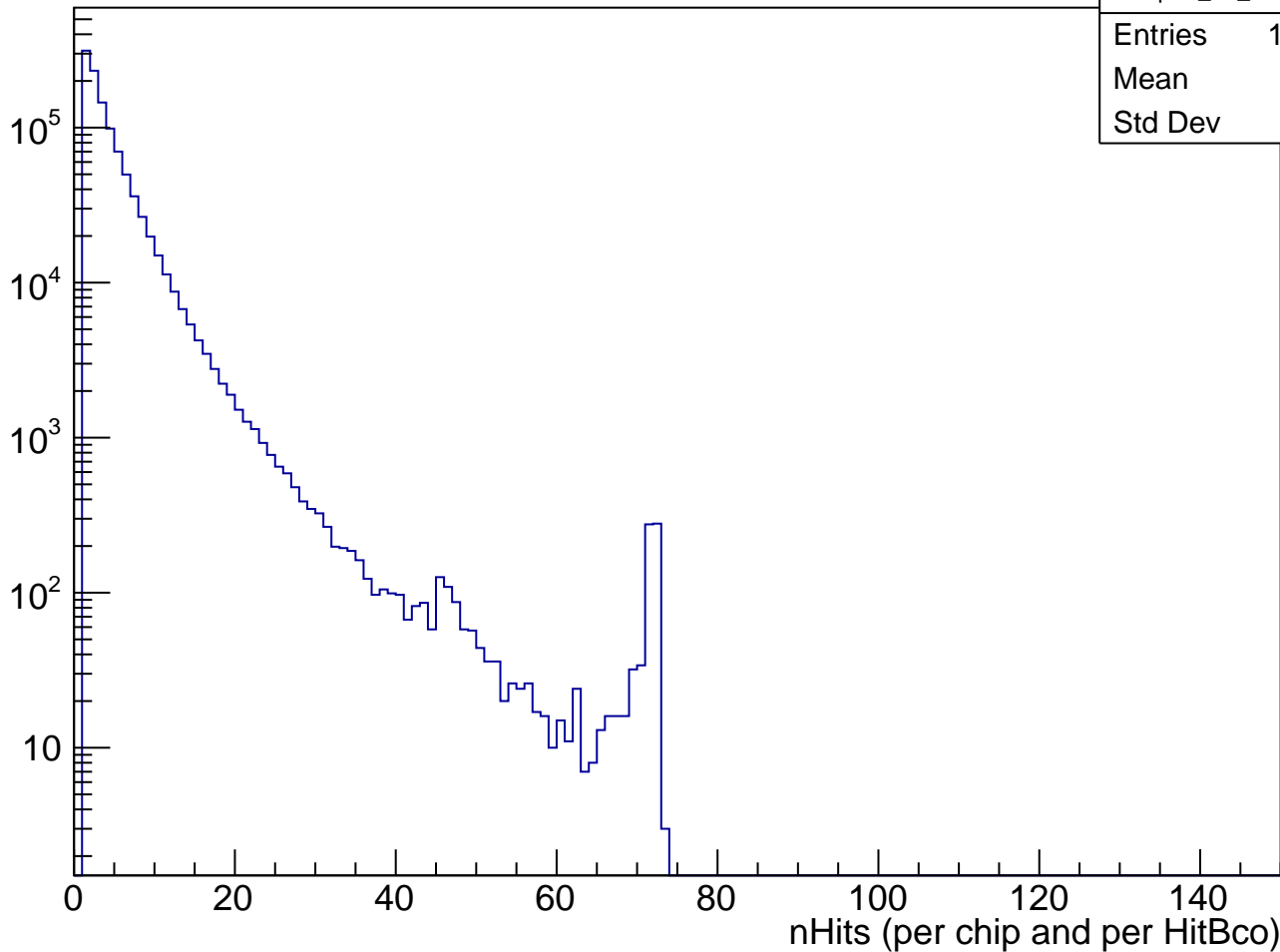
Entries



nChipHit_F7_Fch7_Chip16	
Entries	1051938
Mean	3.828
Std Dev	4.394

nChipHit_F7_Fch7_Chip17

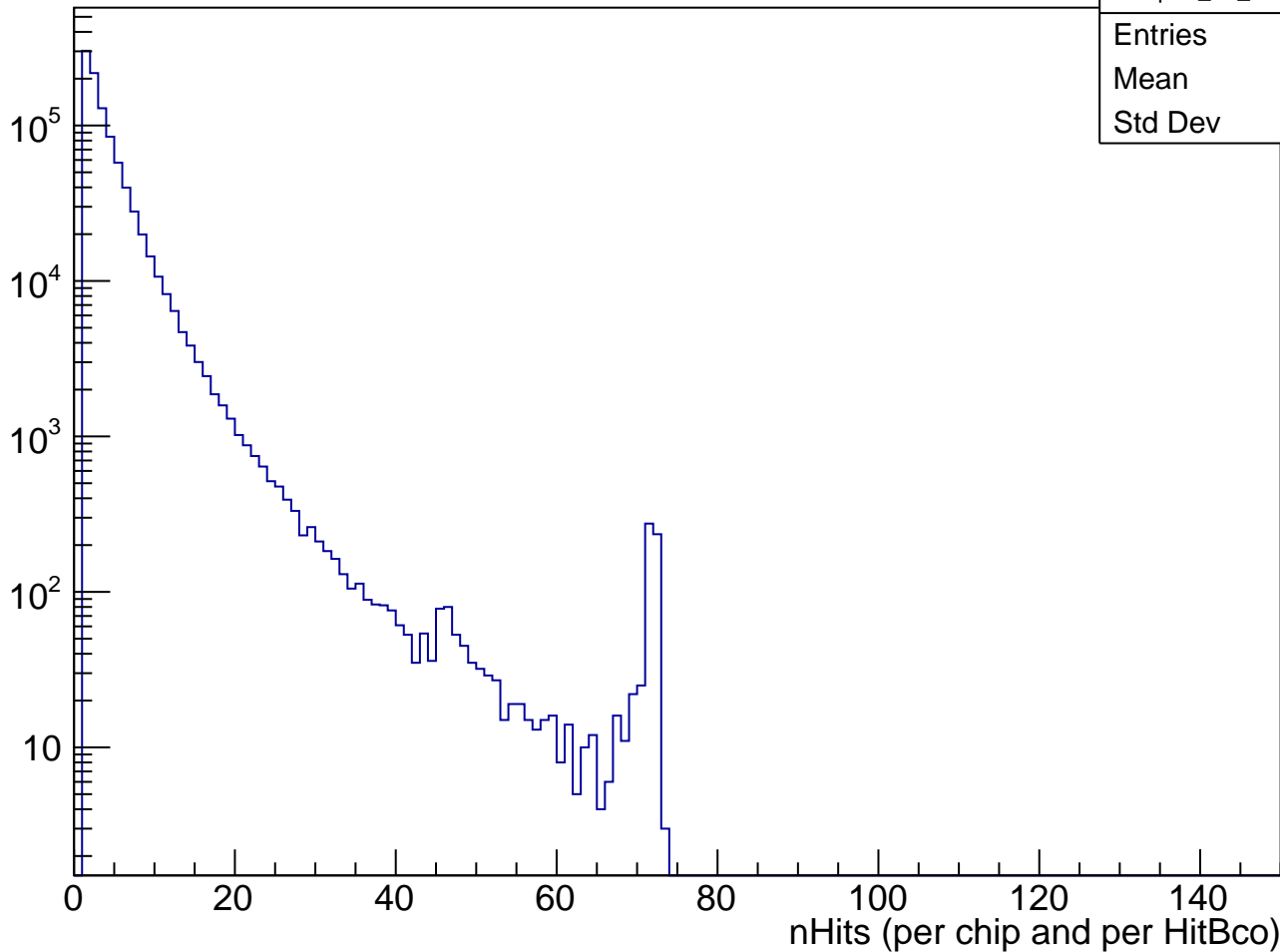
Entries



nChipHit_F7_Fch7_Chip17	
Entries	1065795
Mean	3.785
Std Dev	4.36

nChipHit_F7_Fch7_Chip18

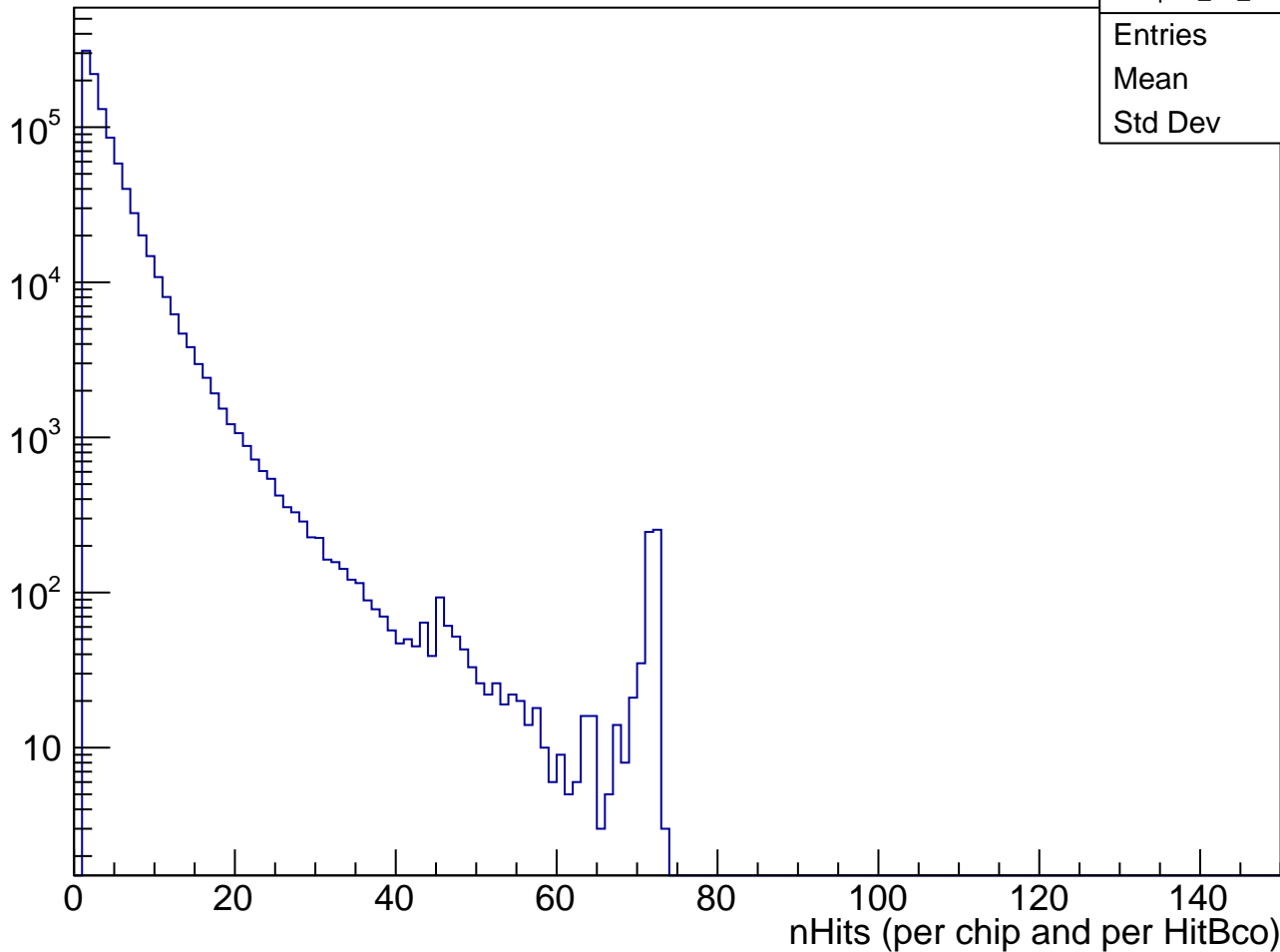
Entries



nChipHit_F7_Fch7_Chip18	
Entries	945290
Mean	3.471
Std Dev	4.058

nChipHit_F7_Fch7_Chip19

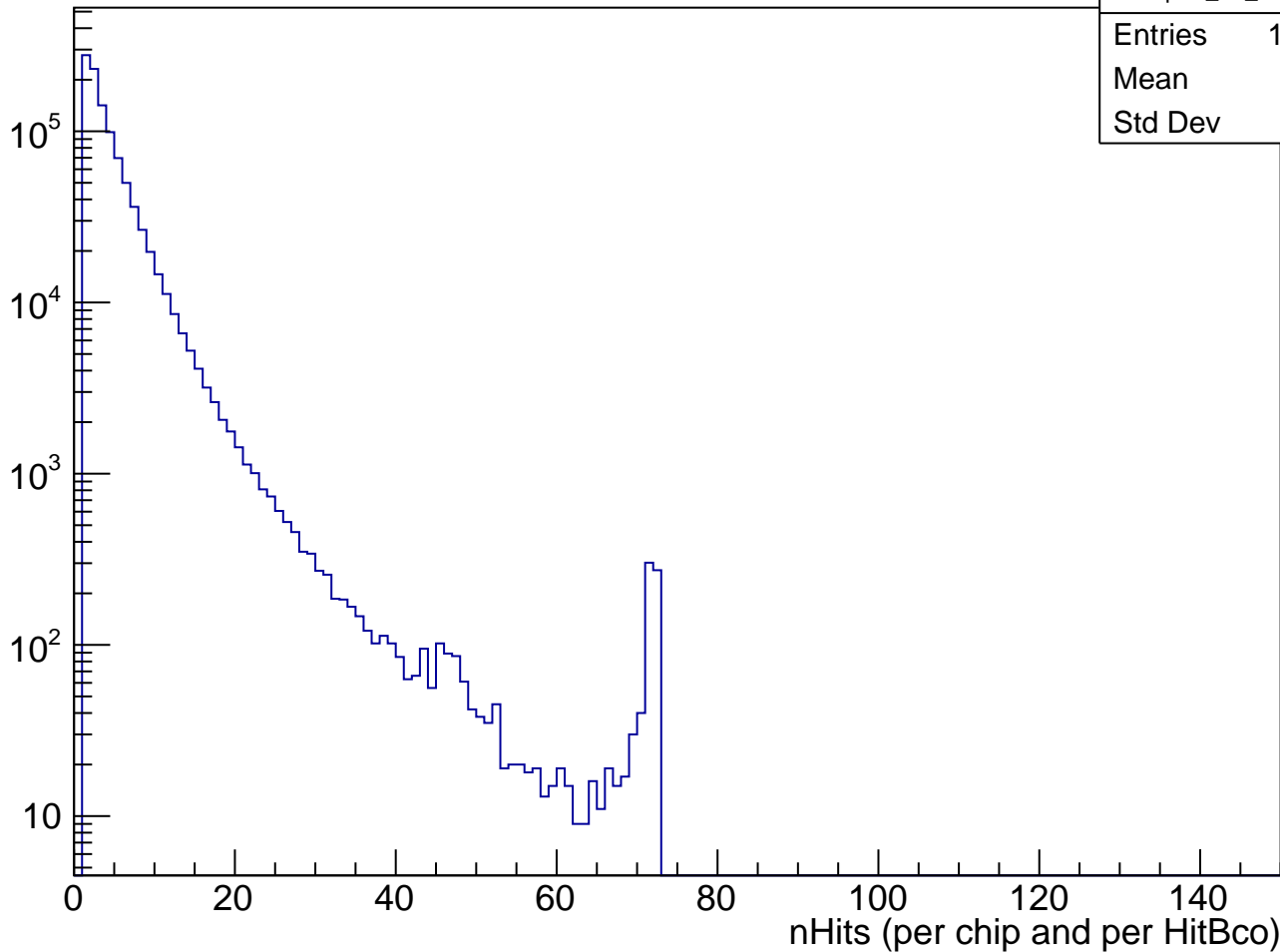
Entries



nChipHit_F7_Fch7_Chip19	
Entries	960382
Mean	3.44
Std Dev	4.012

nChipHit_F7_Fch7_Chip2

Entries



nChipHit_F7_Fch7_Chip2

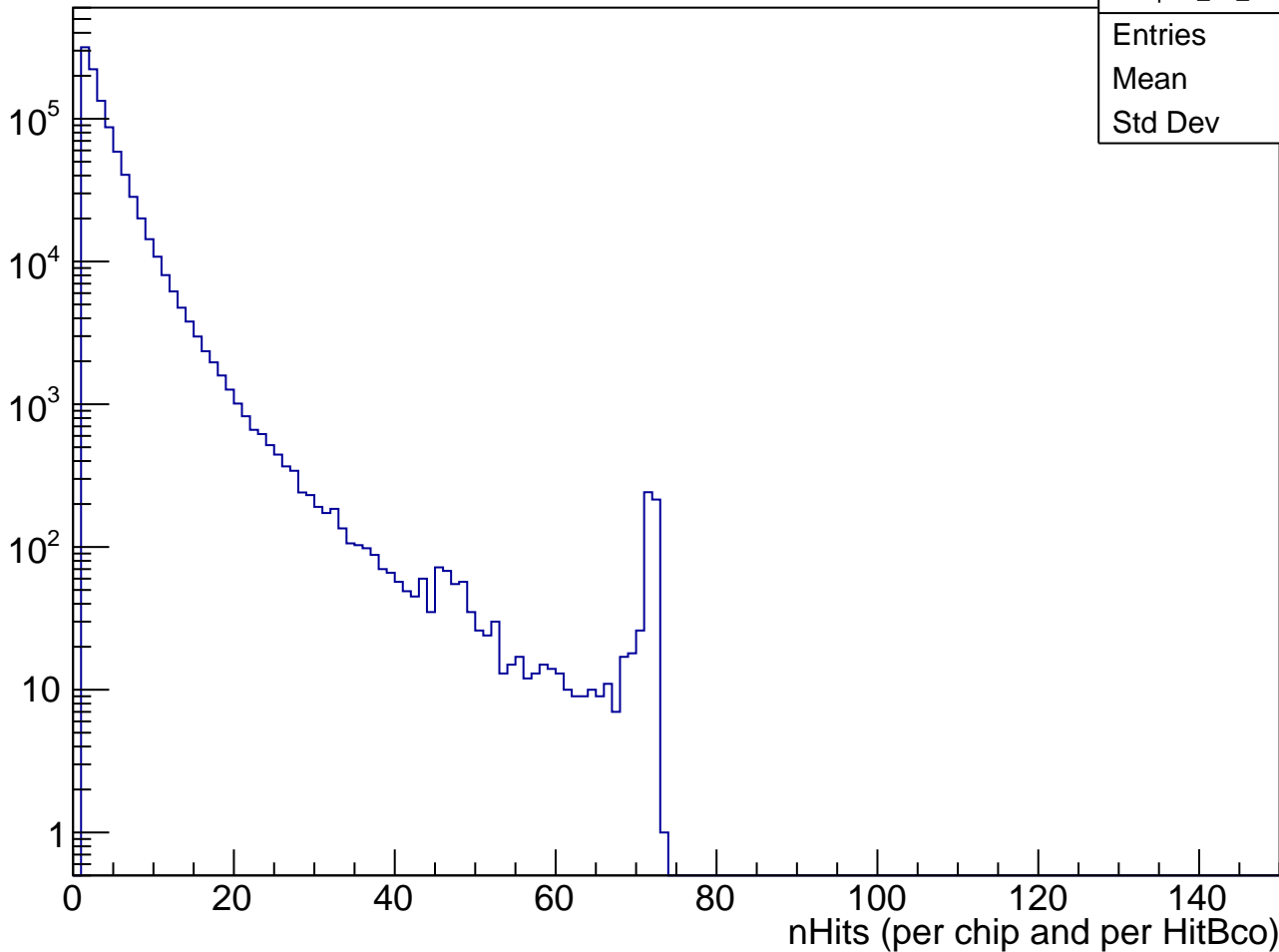
Entries 1023304

Mean 3.85

Std Dev 4.352

nChipHit_F7_Fch7_Chip20

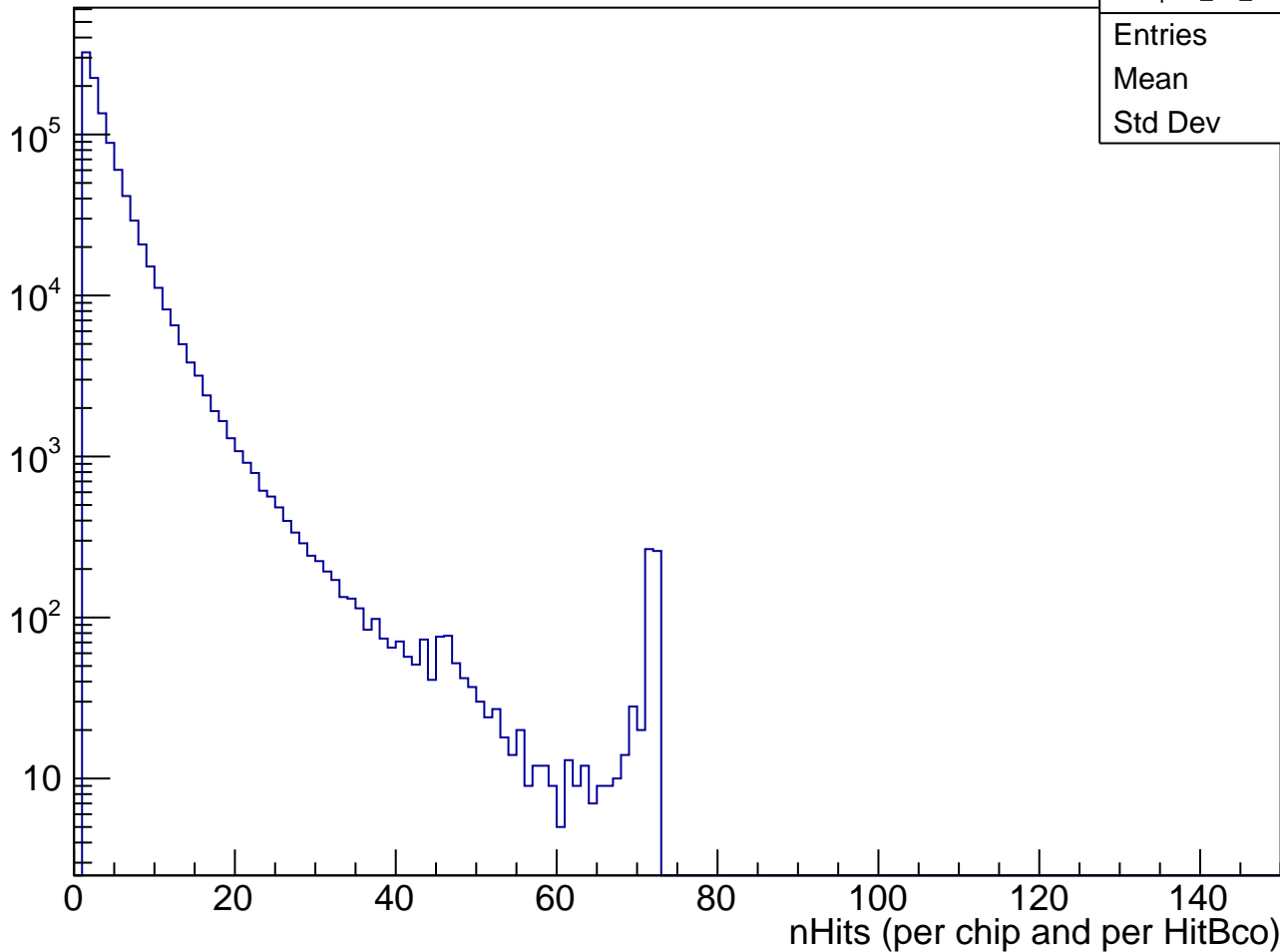
Entries



nChipHit_F7_Fch7_Chip20	
Entries	973475
Mean	3.418
Std Dev	3.959

nChipHit_F7_Fch7_Chip21

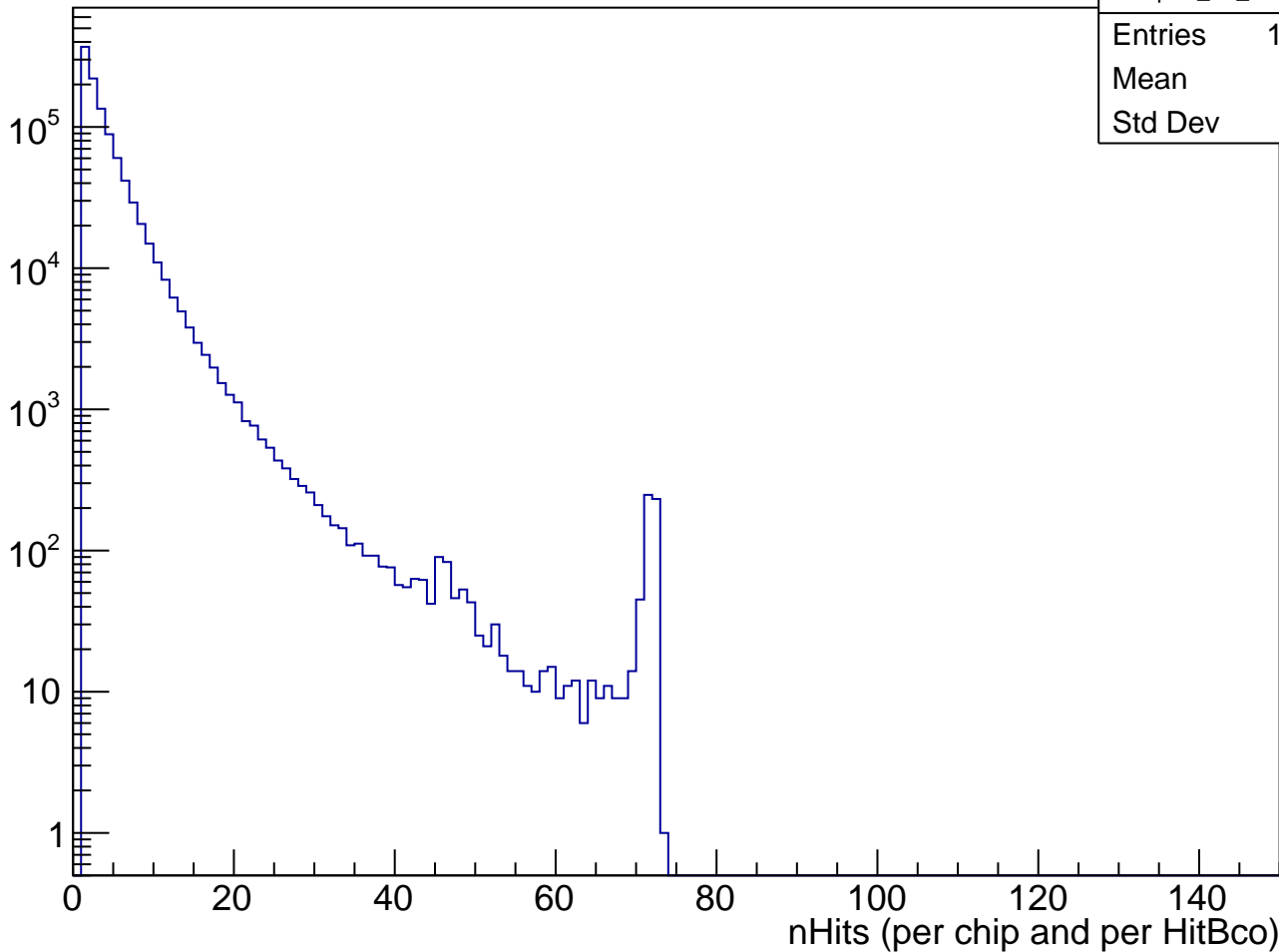
Entries



nChipHit_F7_Fch7_Chip21	
Entries	992633
Mean	3.444
Std Dev	4.023

nChipHit_F7_Fch7_Chip22

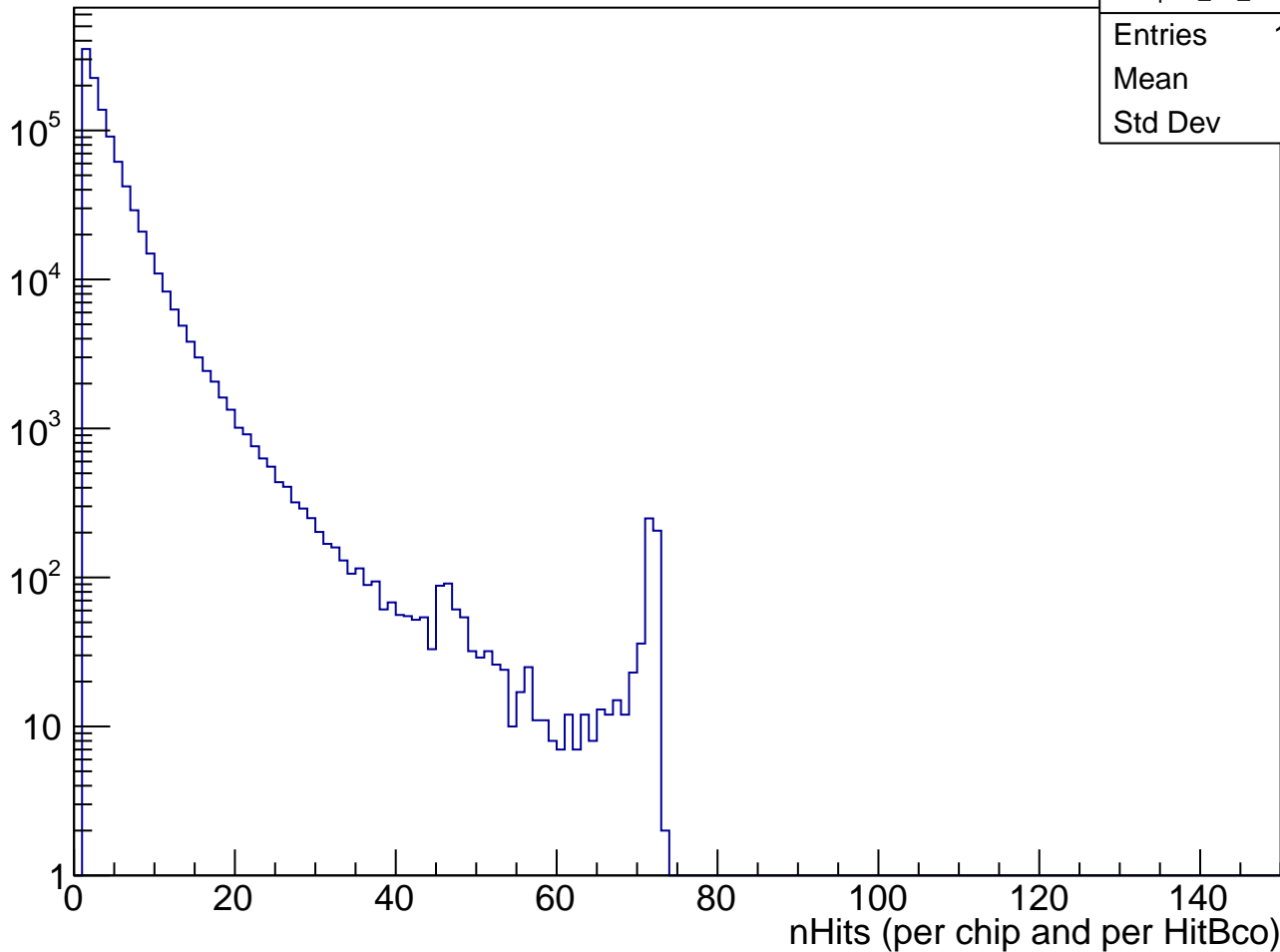
Entries



nChipHit_F7_Fch7_Chip22	
Entries	1032606
Mean	3.327
Std Dev	3.937

nChipHit_F7_Fch7_Chip23

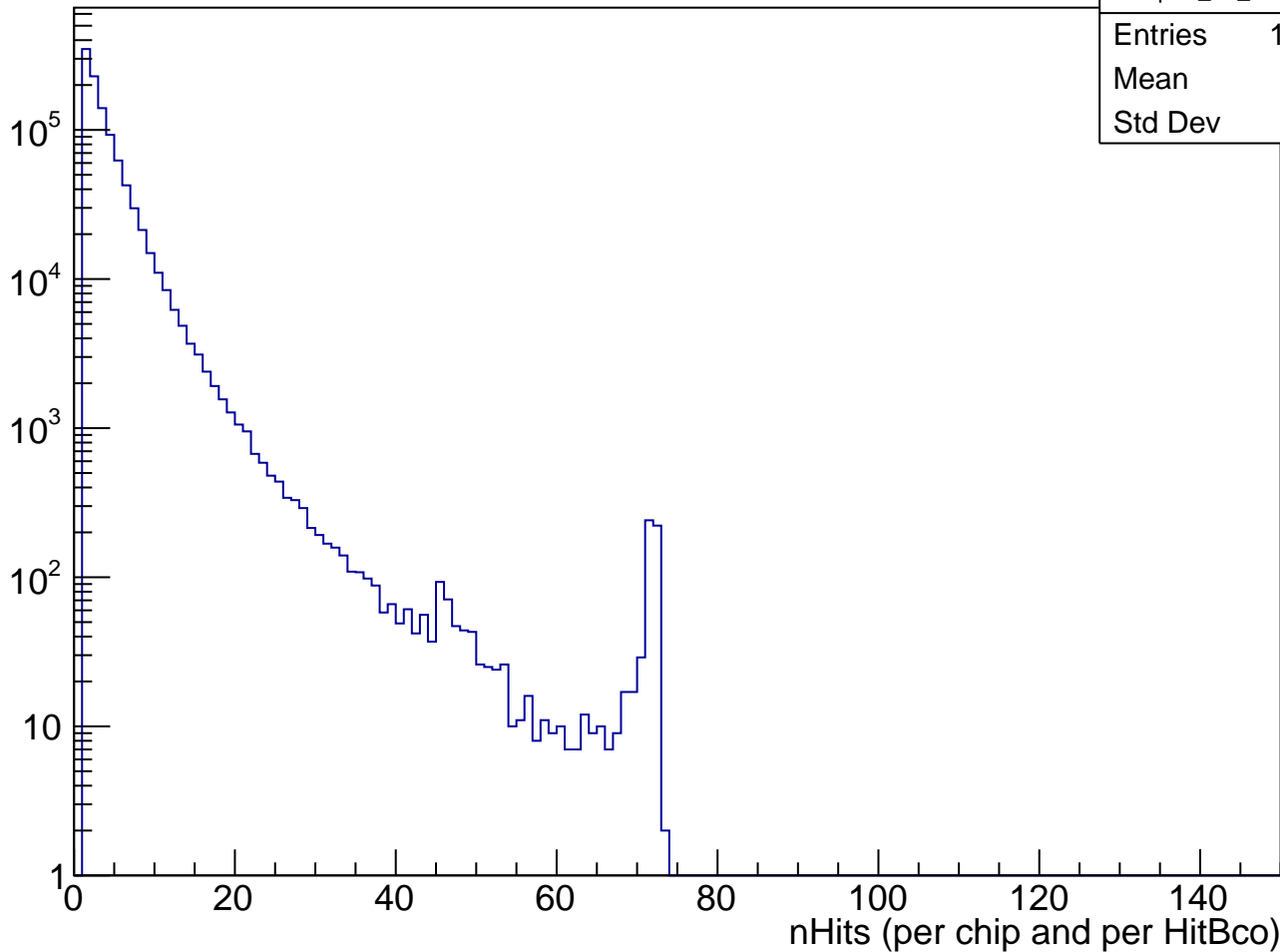
Entries



nChipHit_F7_Fch7_Chip23	
Entries	1027101
Mean	3.369
Std Dev	3.936

nChipHit_F7_Fch7_Chip24

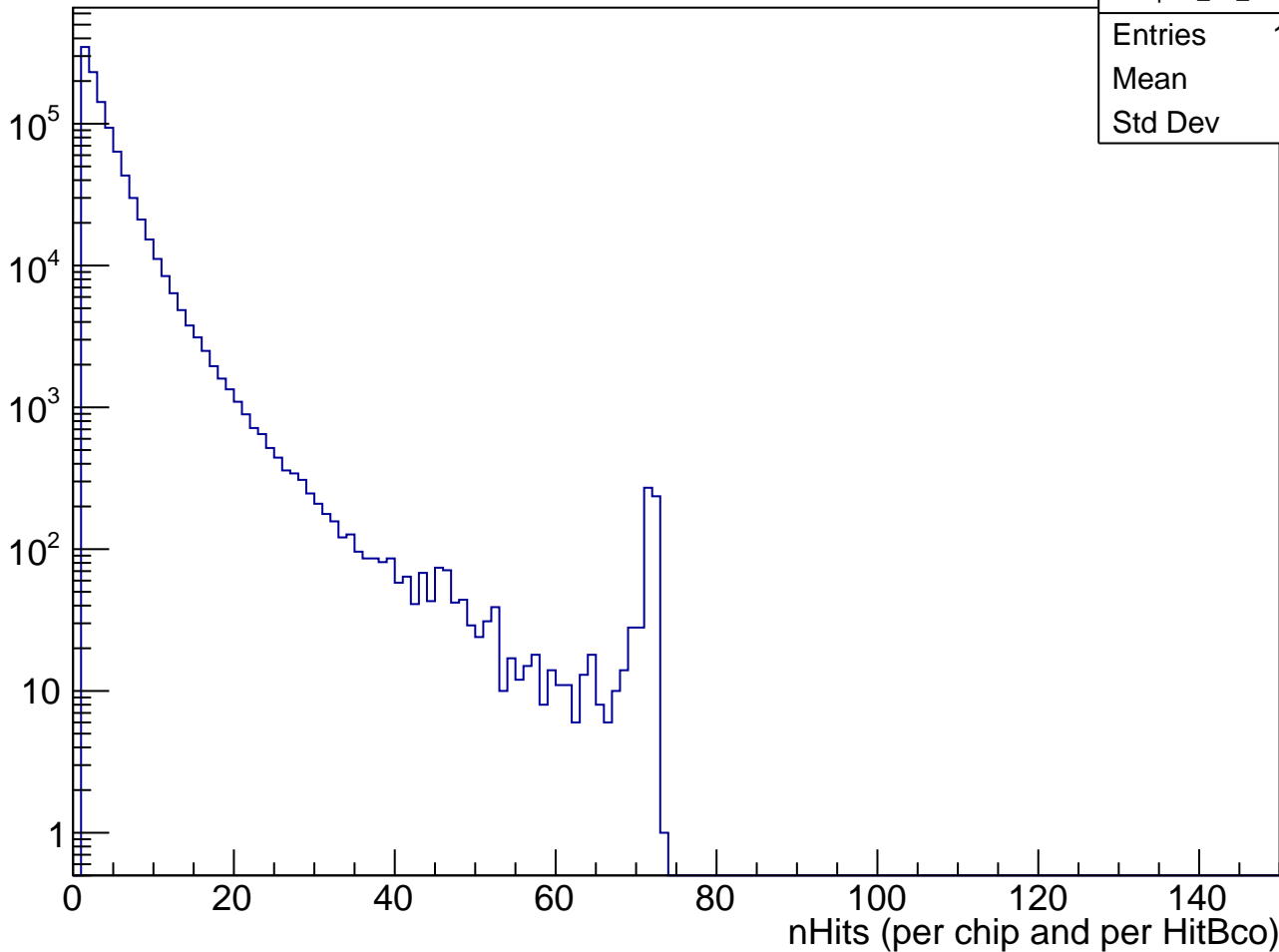
Entries



nChipHit_F7_Fch7_Chip24	
Entries	1033573
Mean	3.366
Std Dev	3.882

nChipHit_F7_Fch7_Chip25

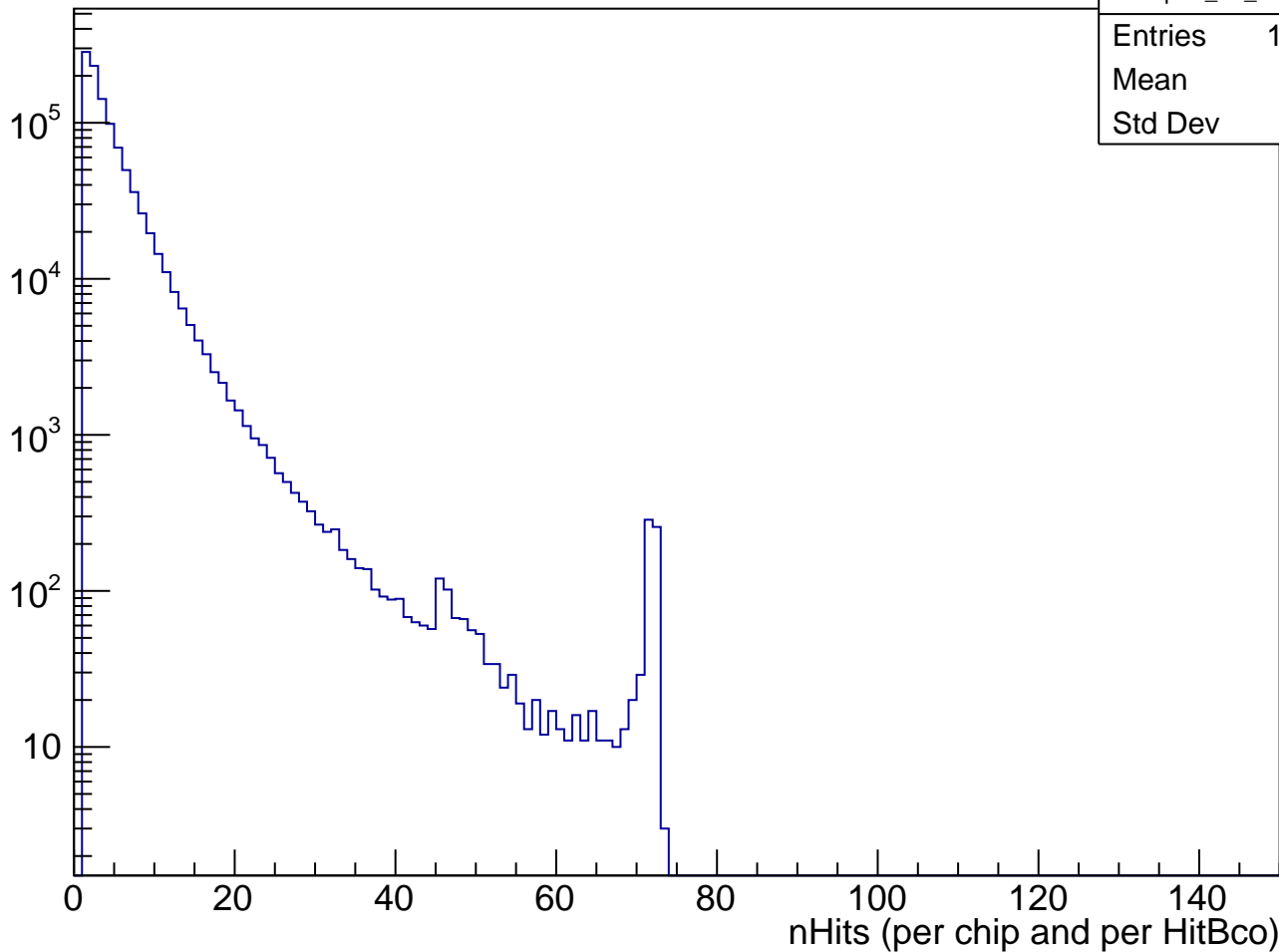
Entries



nChipHit_F7_Fch7_Chip25	
Entries	1041111
Mean	3.386
Std Dev	3.934

nChipHit_F7_Fch7_Chip3

Entries



nChipHit_F7_Fch7_Chip3

Entries 1027348

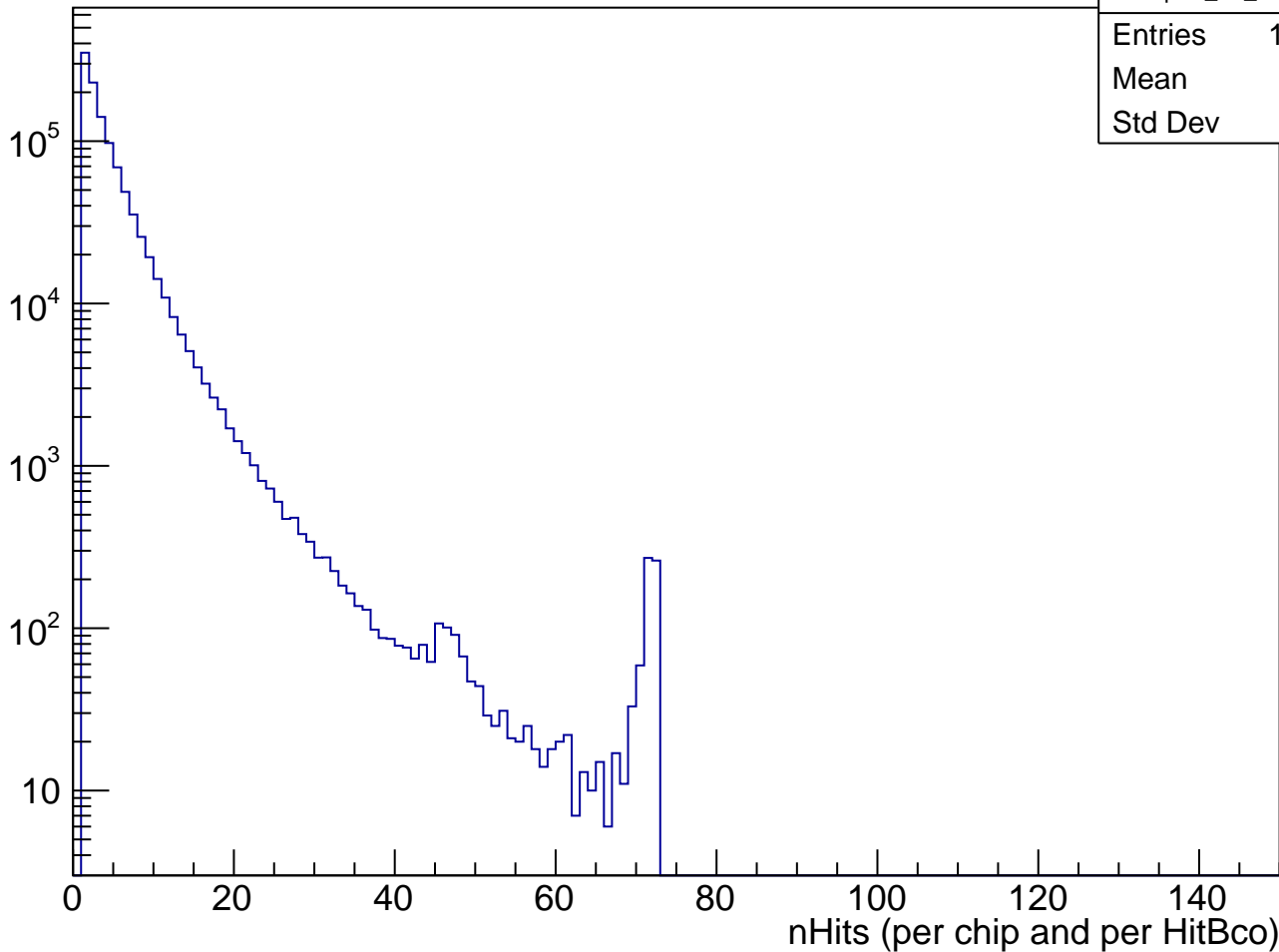
Mean 3.814

Std Dev 4.304

nHits (per chip and per HitBco)

nChipHit_F7_Fch7_Chip4

Entries



nChipHit_F7_Fch7_Chip4

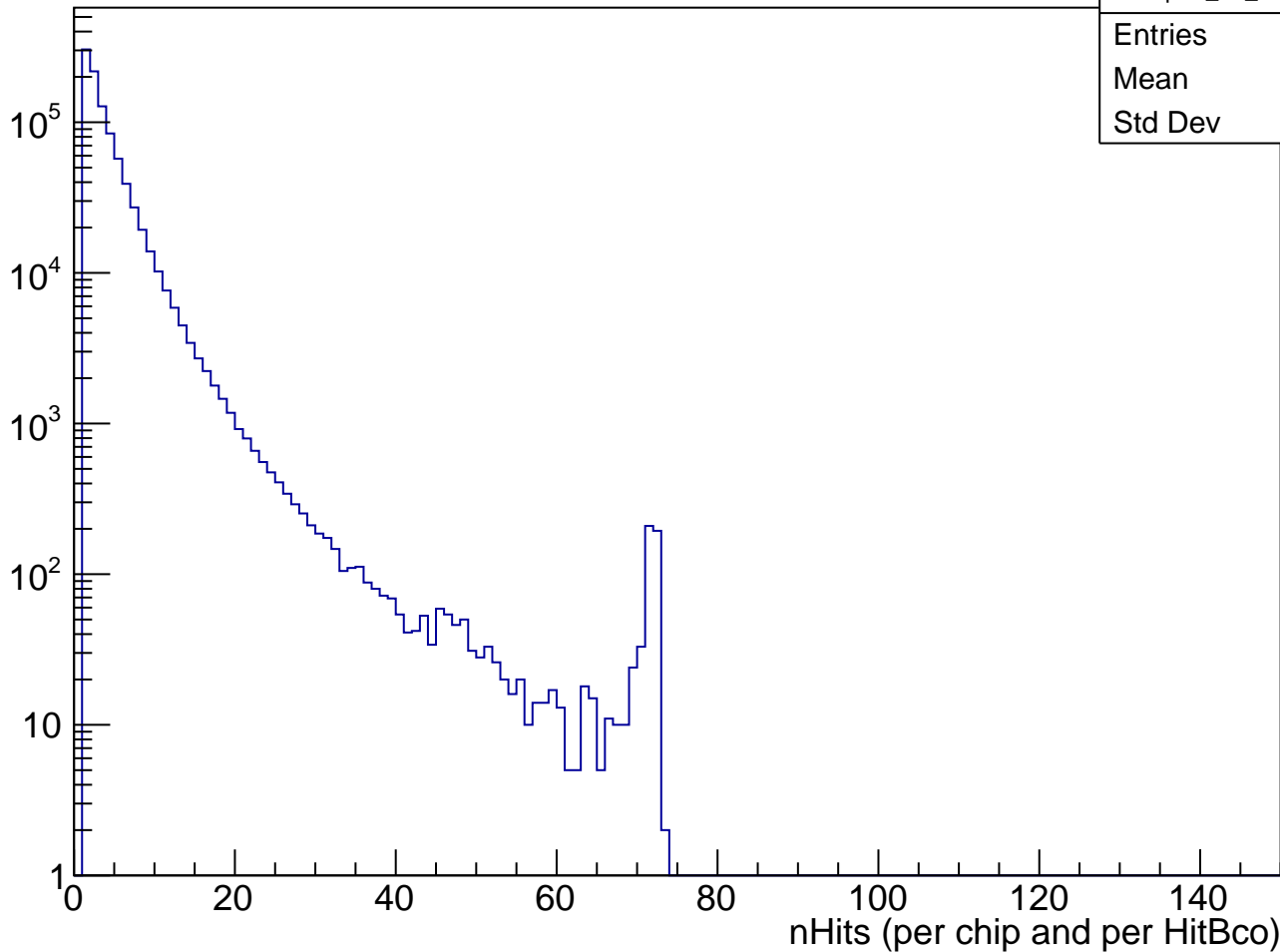
Entries 1086180

Mean 3.644

Std Dev 4.262

nChipHit_F7_Fch7_Chip5

Entries



nChipHit_F7_Fch7_Chip5

Entries 938774

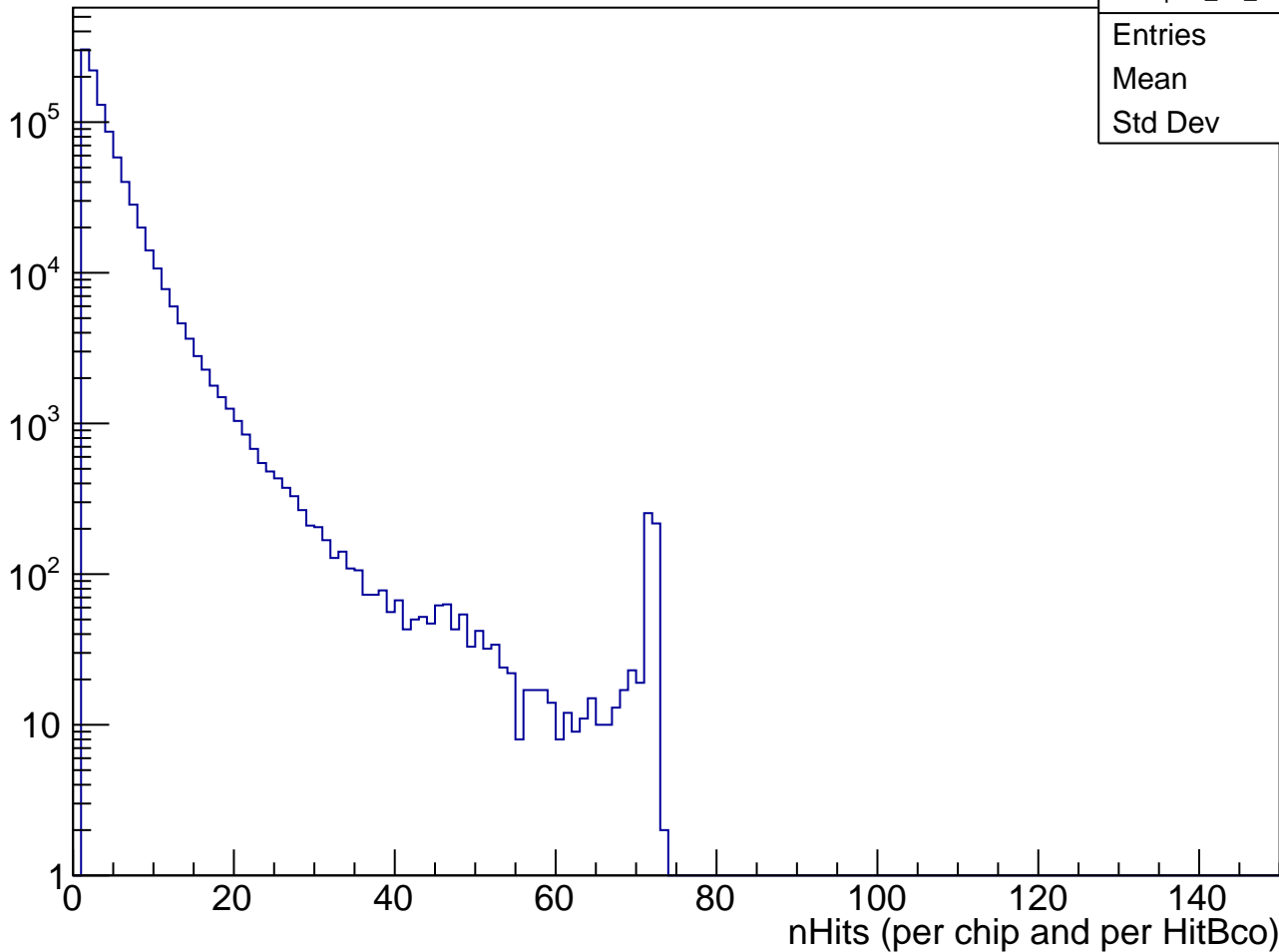
Mean 3.399

Std Dev 3.912

nHits (per chip and per HitBco)

nChipHit_F7_Fch7_Chip6

Entries



nChipHit_F7_Fch7_Chip6

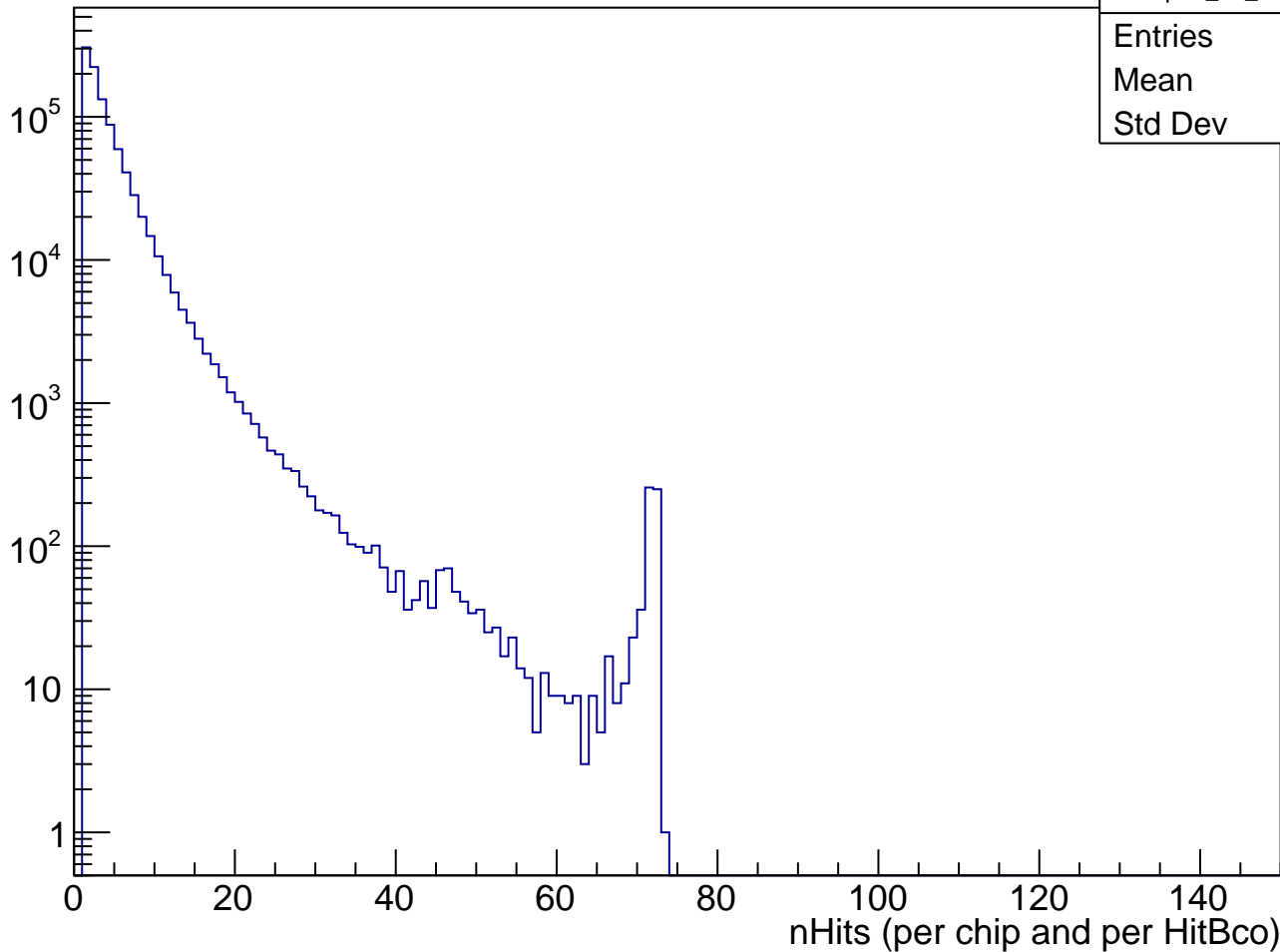
Entries 951652

Mean 3.436

Std Dev 3.98

nChipHit_F7_Fch7_Chip7

Entries

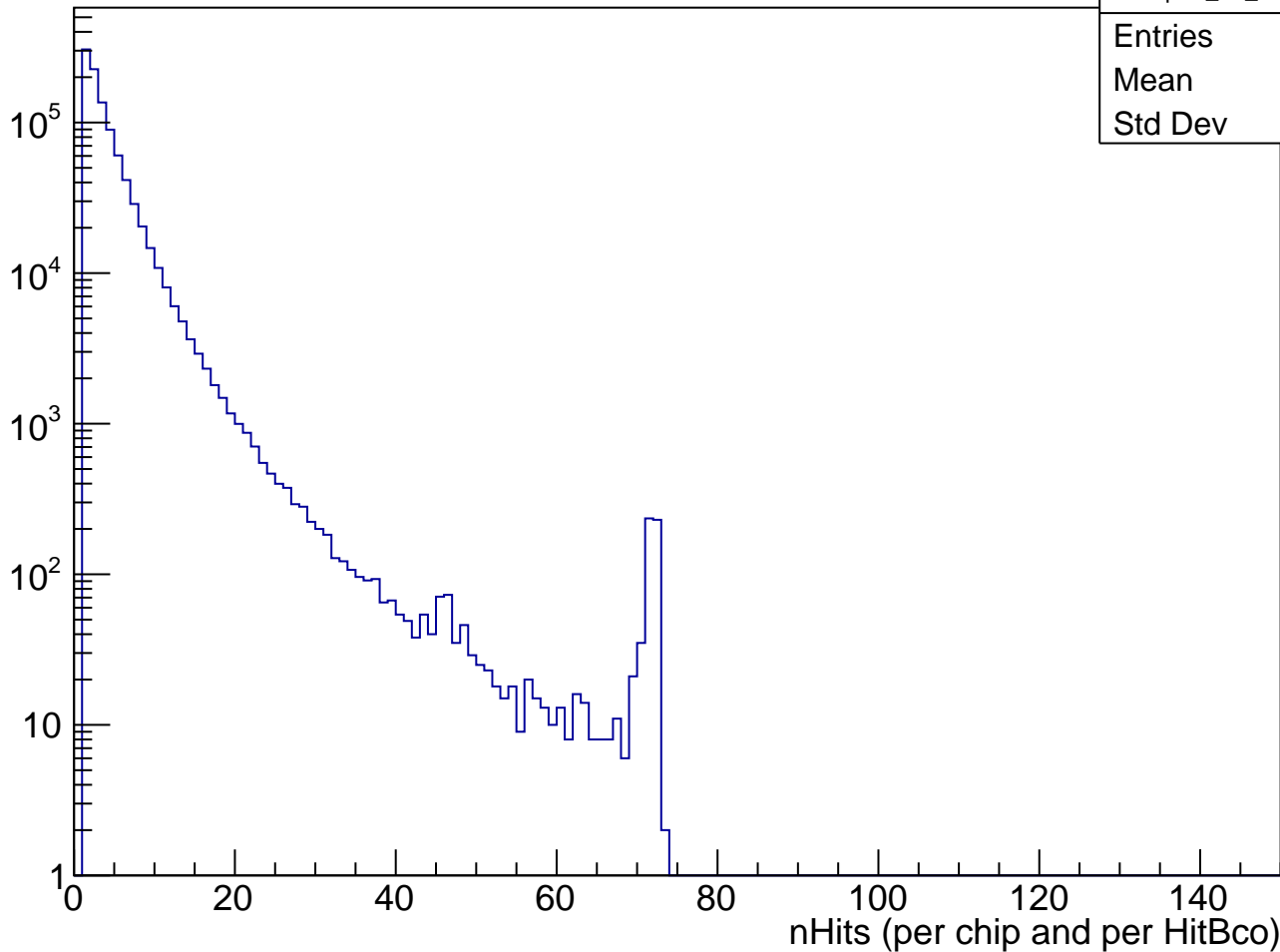


nChipHit_F7_Fch7_Chip7	
Entries	963041
Mean	3.432
Std Dev	3.964

nHits (per chip and per HitBco)

nChipHit_F7_Fch7_Chip8

Entries



nChipHit_F7_Fch7_Chip8

Entries 973691

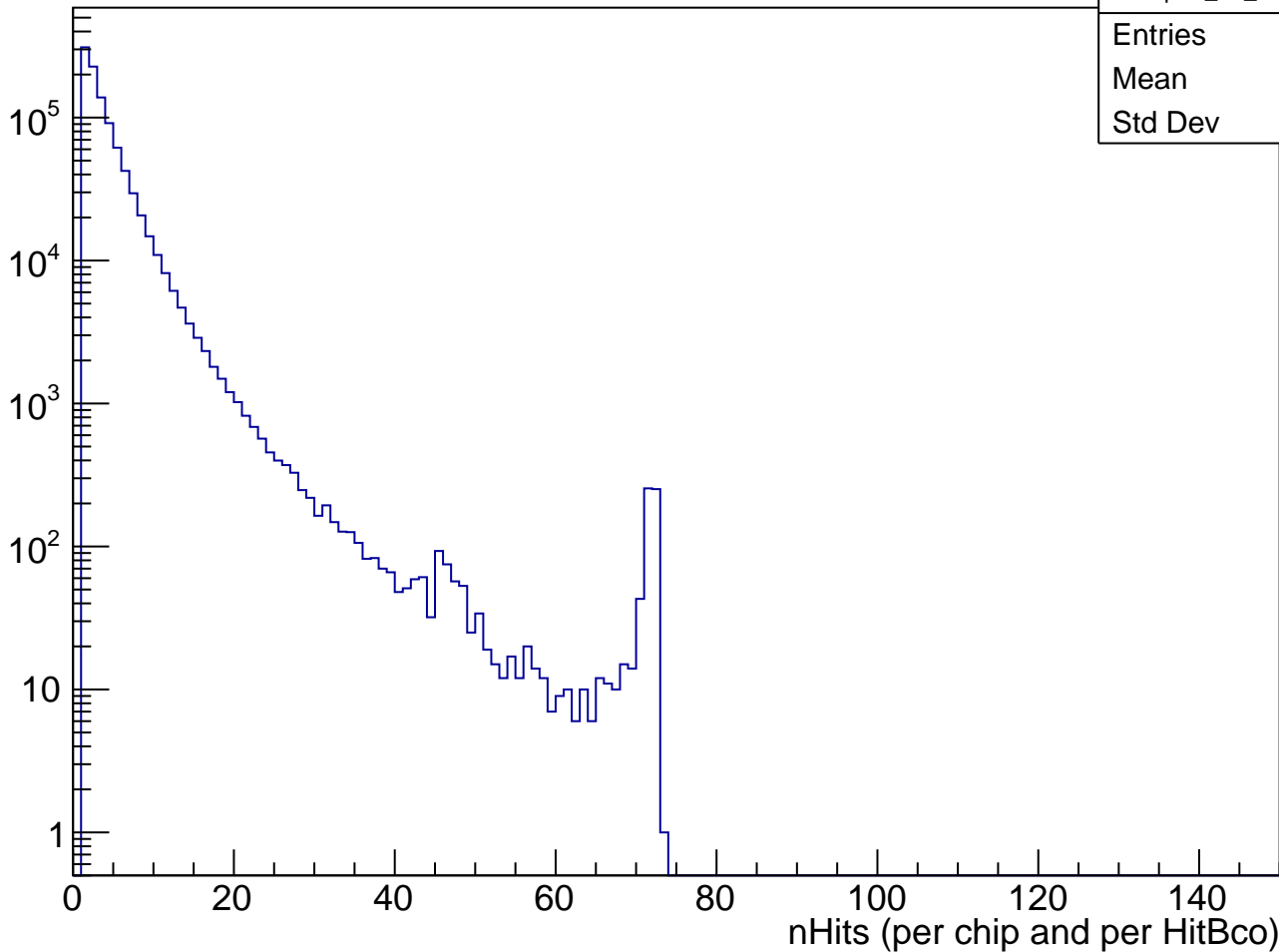
Mean 3.437

Std Dev 3.922

nHits (per chip and per HitBco)

nChipHit_F7_Fch7_Chip9

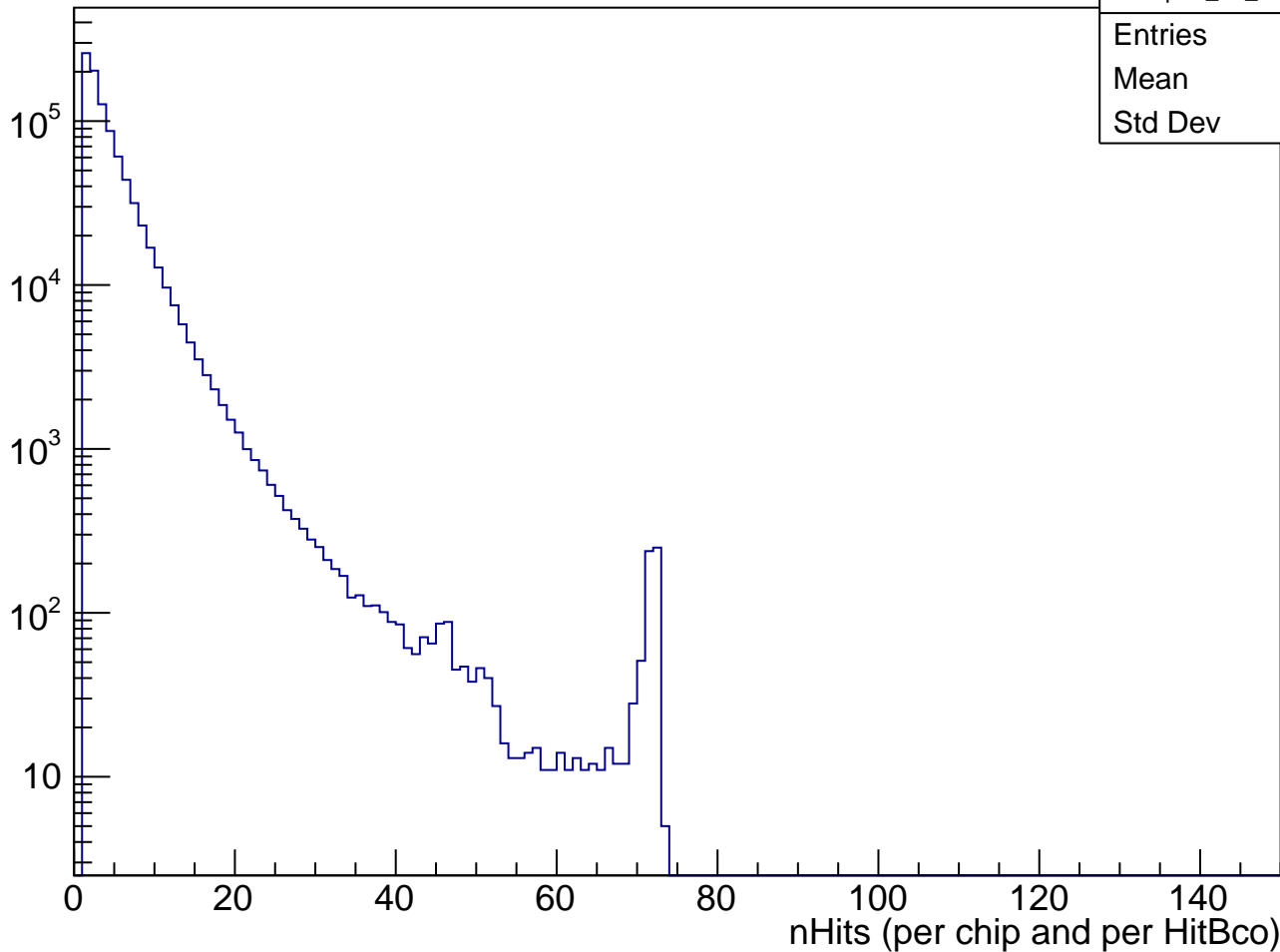
Entries



nChipHit_F7_Fch7_Chip9	
Entries	986837
Mean	3.44
Std Dev	3.943

nChipHit_F7_Fch8_Chip0

Entries



nChipHit_F7_Fch8_Chip0

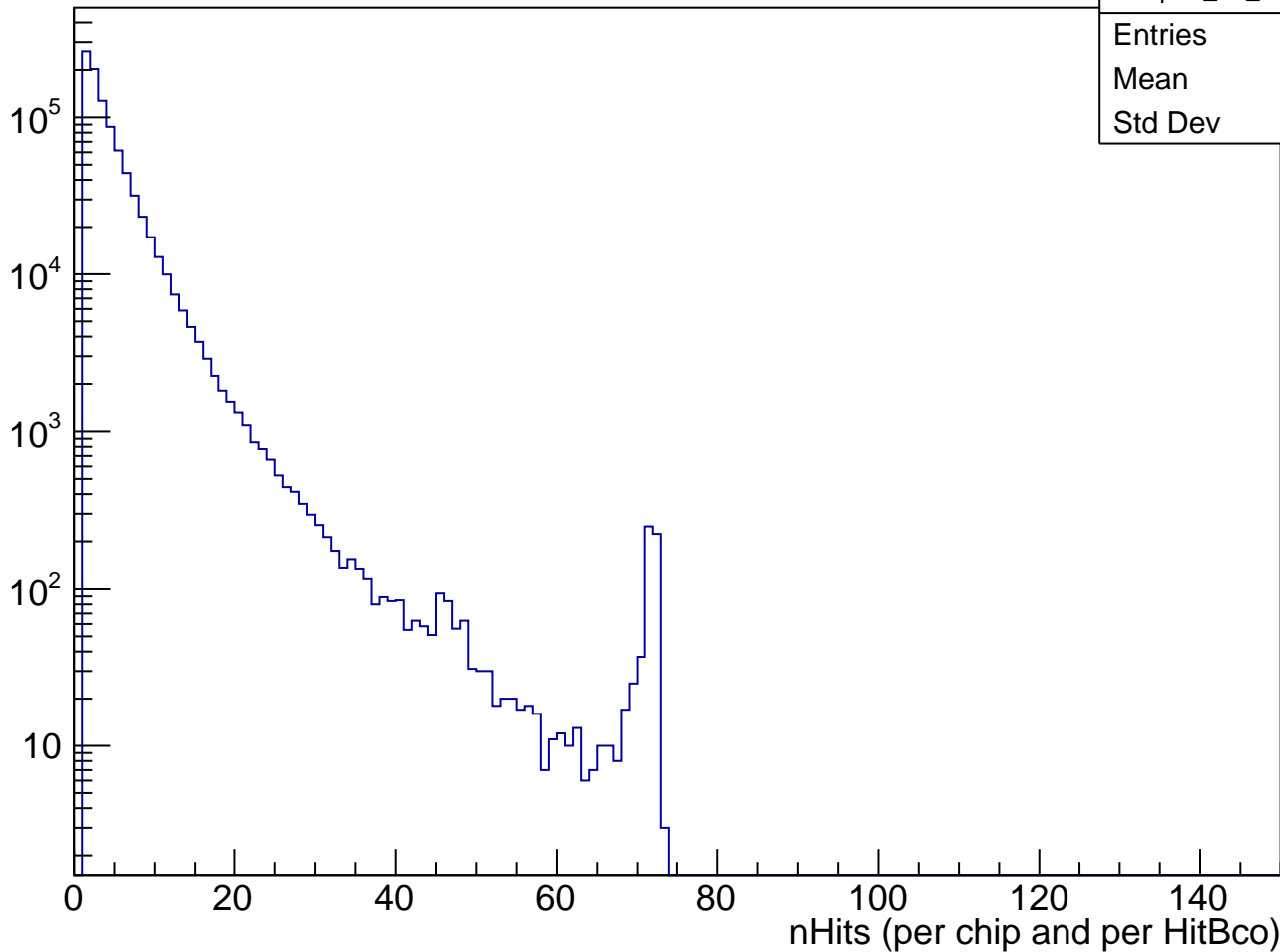
Entries 914280

Mean 3.788

Std Dev 4.314

nChipHit_F7_Fch8_Chip1

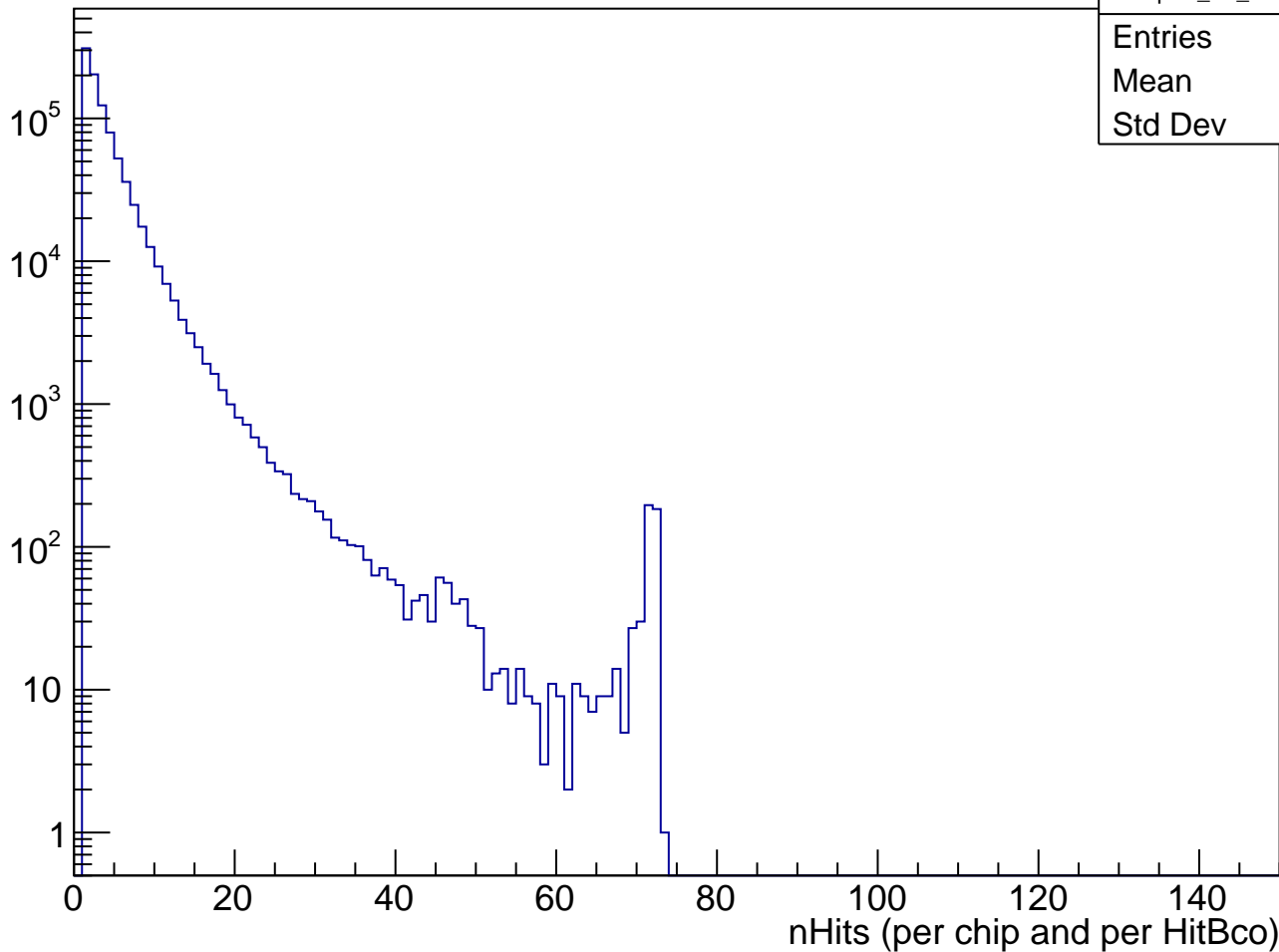
Entries



nChipHit_F7_Fch8_Chip1	
Entries	920328
Mean	3.795
Std Dev	4.291

nChipHit_F7_Fch8_Chip10

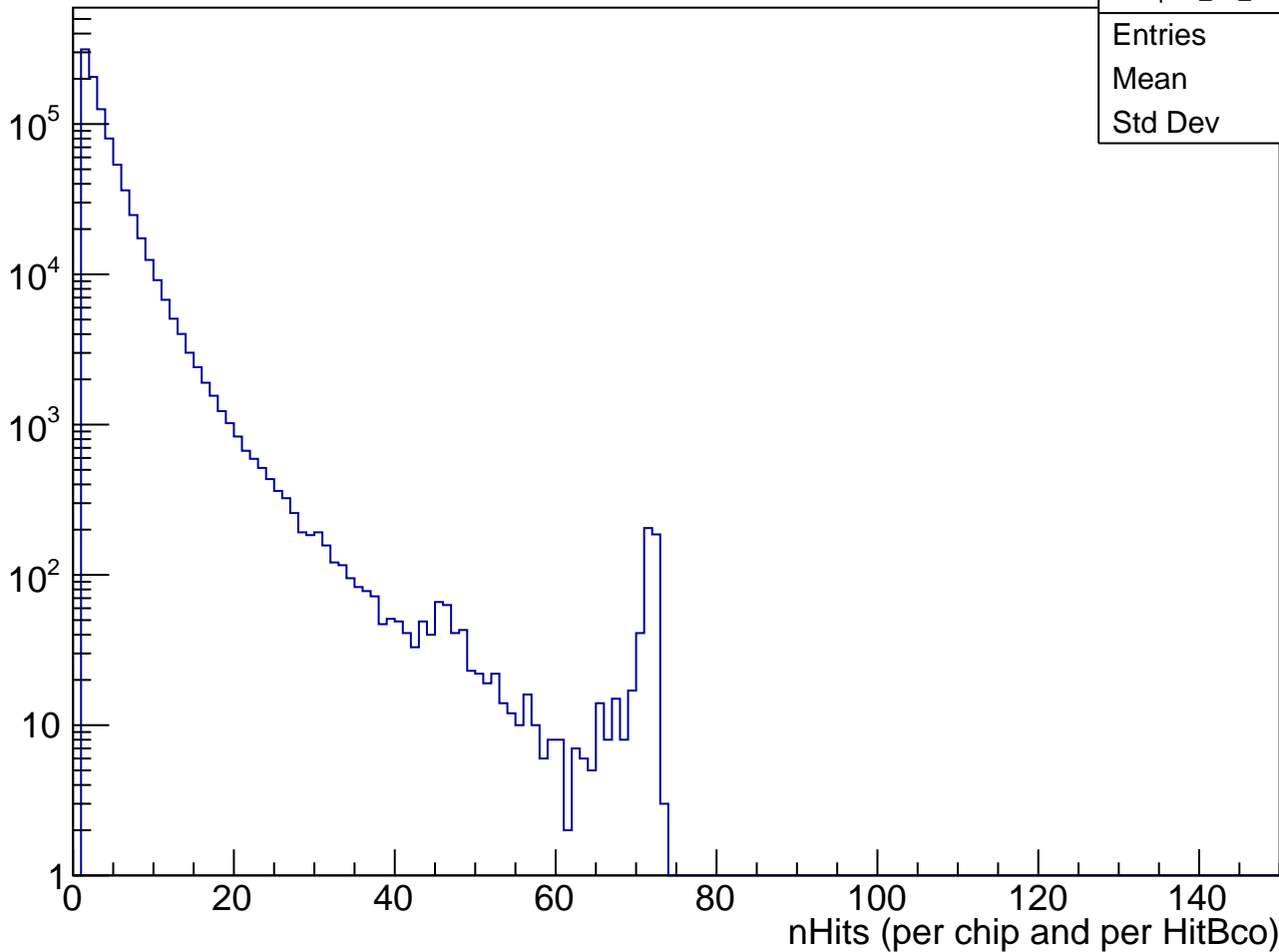
Entries



nChipHit_F7_Fch8_Chip10	
Entries	902431
Mean	3.29
Std Dev	3.799

nChipHit_F7_Fch8_Chip11

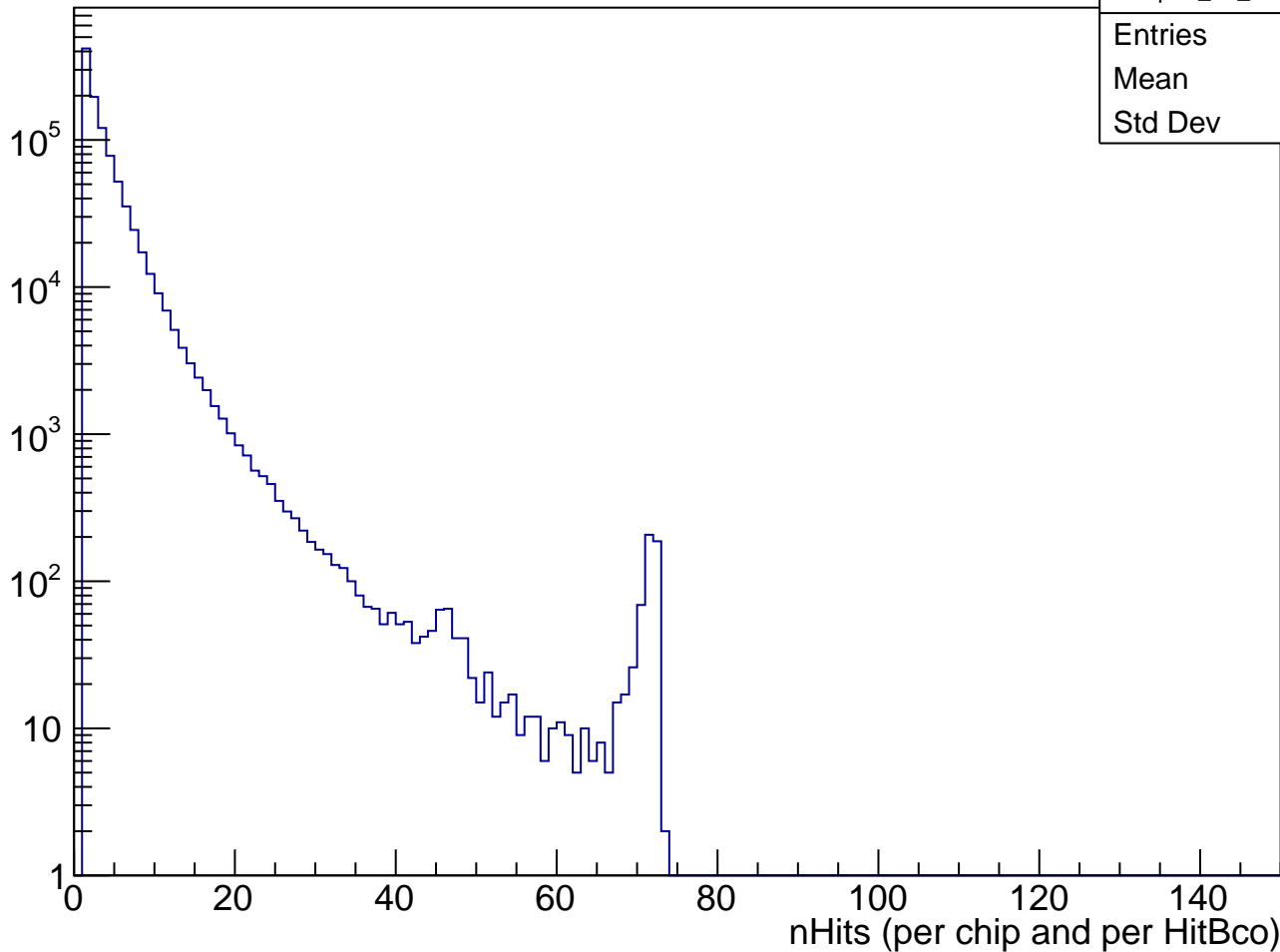
Entries



nChipHit_F7_Fch8_Chip11	
Entries	912403
Mean	3.274
Std Dev	3.792

nChipHit_F7_Fch8_Chip12

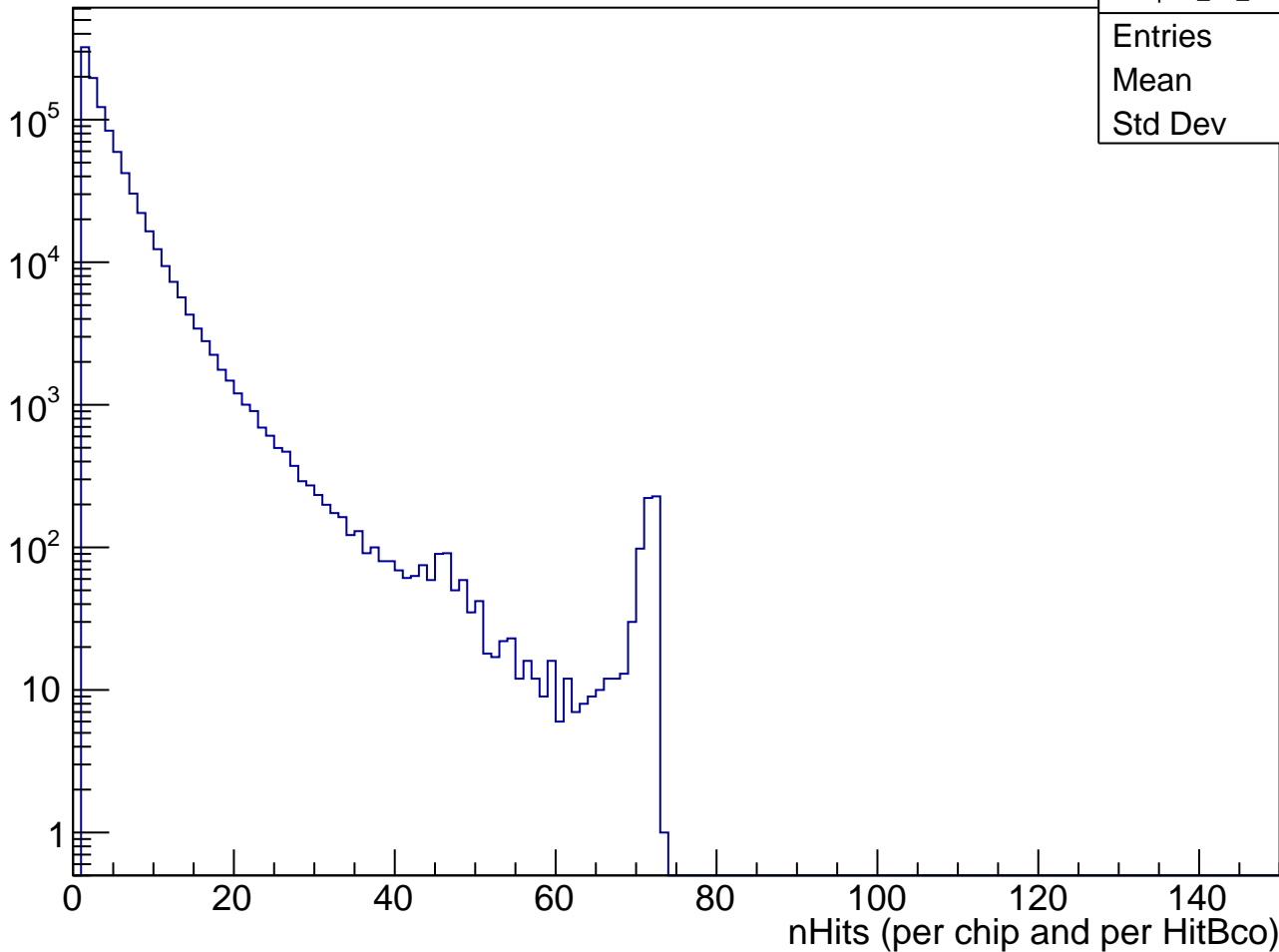
Entries



nChipHit_F7_Fch8_Chip12	
Entries	997649
Mean	3.045
Std Dev	3.723

nChipHit_F7_Fch8_Chip13

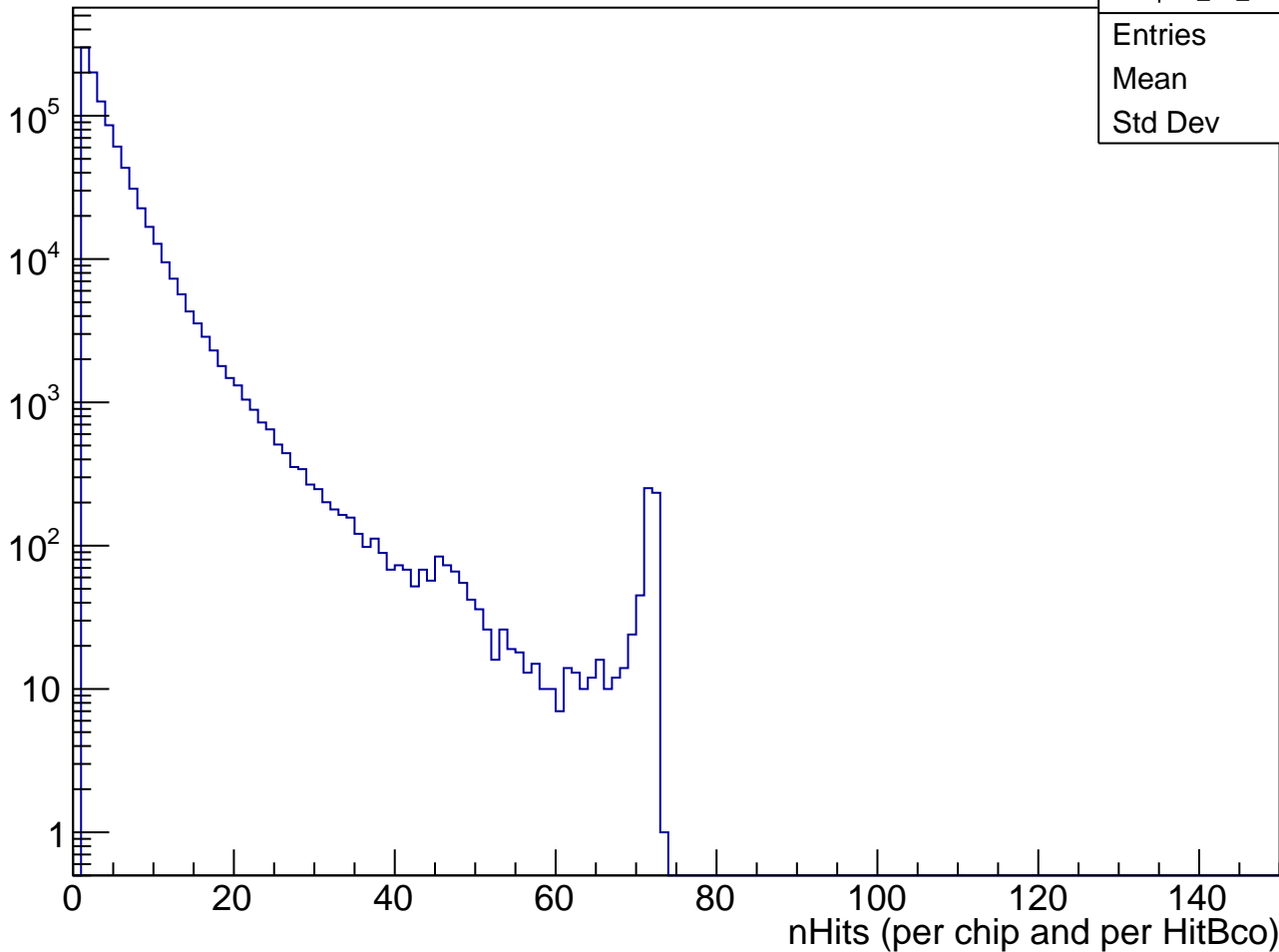
Entries



nChipHit_F7_Fch8_Chip13	
Entries	954601
Mean	3.586
Std Dev	4.228

nChipHit_F7_Fch8_Chip14

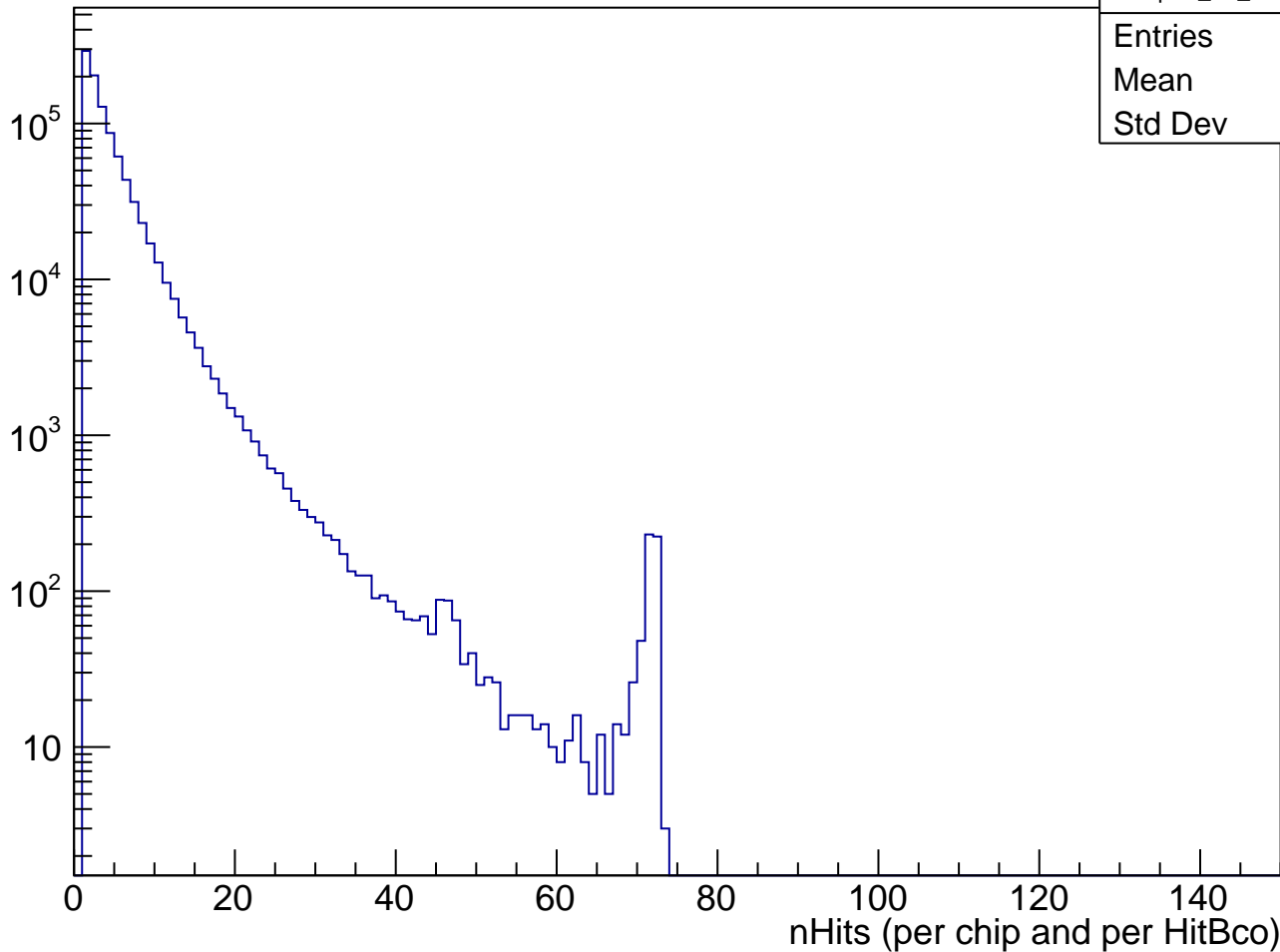
Entries



nChipHit_F7_Fch8_Chip14	
Entries	947420
Mean	3.663
Std Dev	4.25

nChipHit_F7_Fch8_Chip15

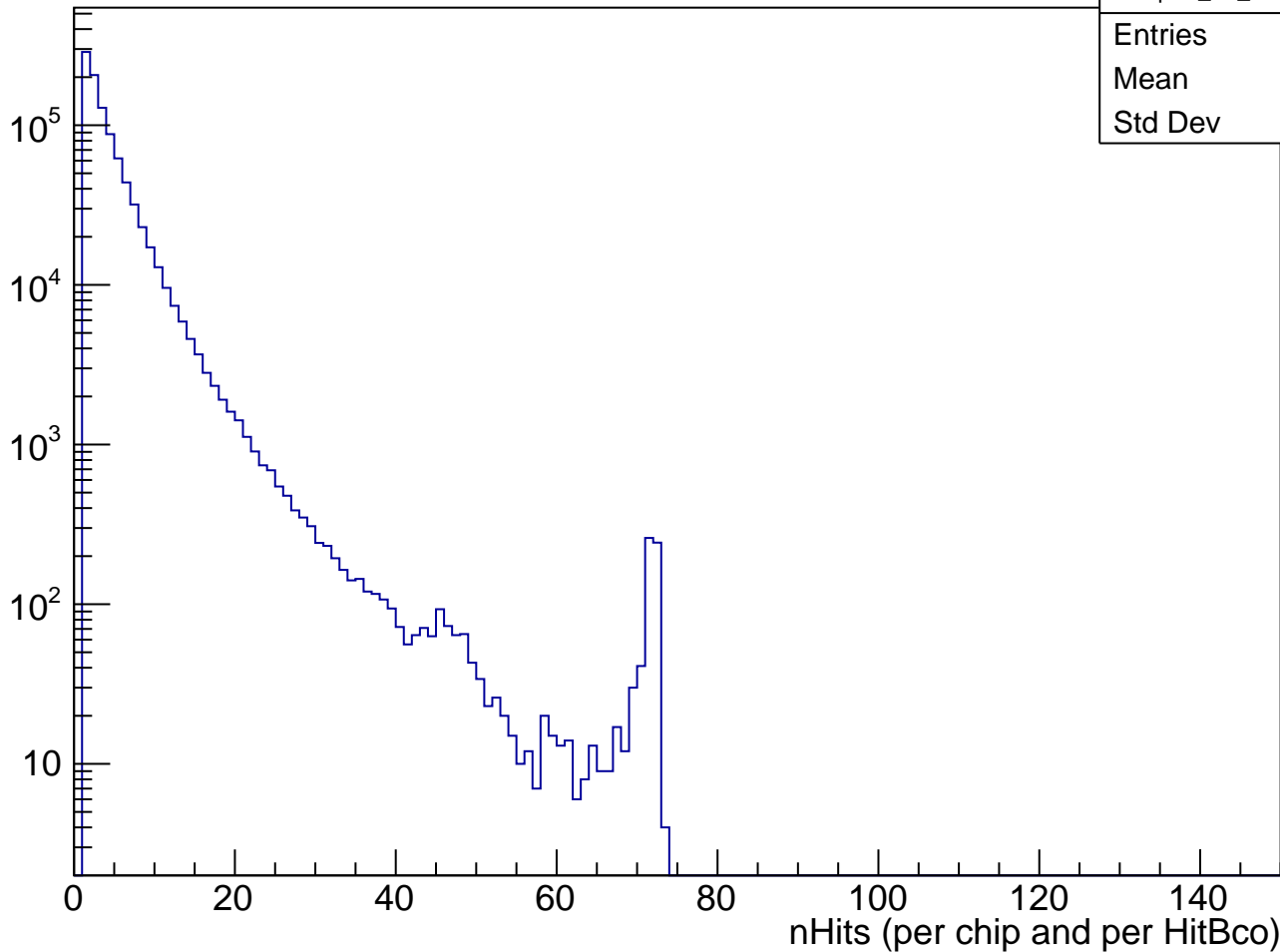
Entries



nChipHit_F7_Fch8_Chip15	
Entries	949041
Mean	3.694
Std Dev	4.247

nChipHit_F7_Fch8_Chip16

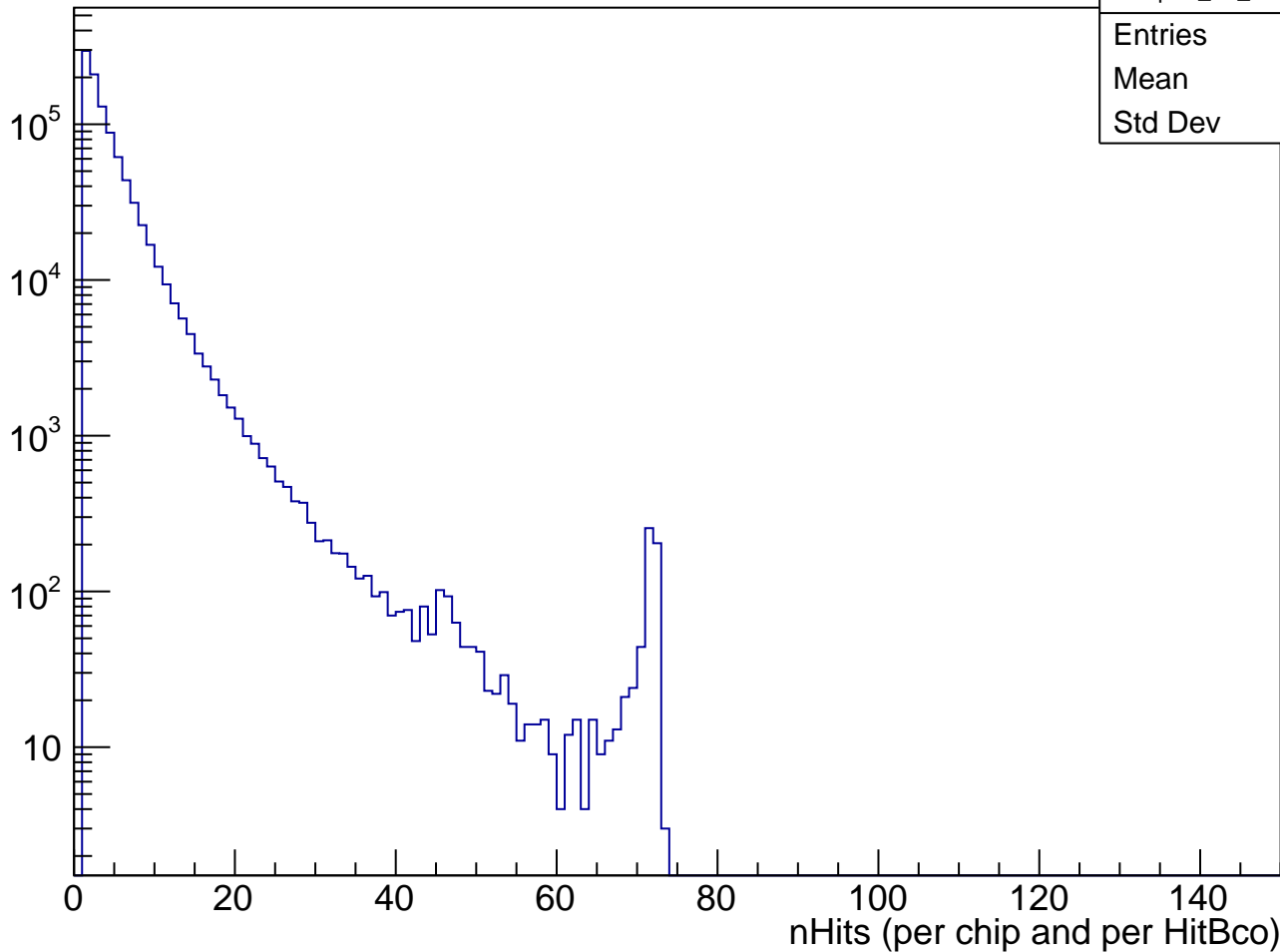
Entries



nChipHit_F7_Fch8_Chip16	
Entries	950978
Mean	3.723
Std Dev	4.302

nChipHit_F7_Fch8_Chip17

Entries

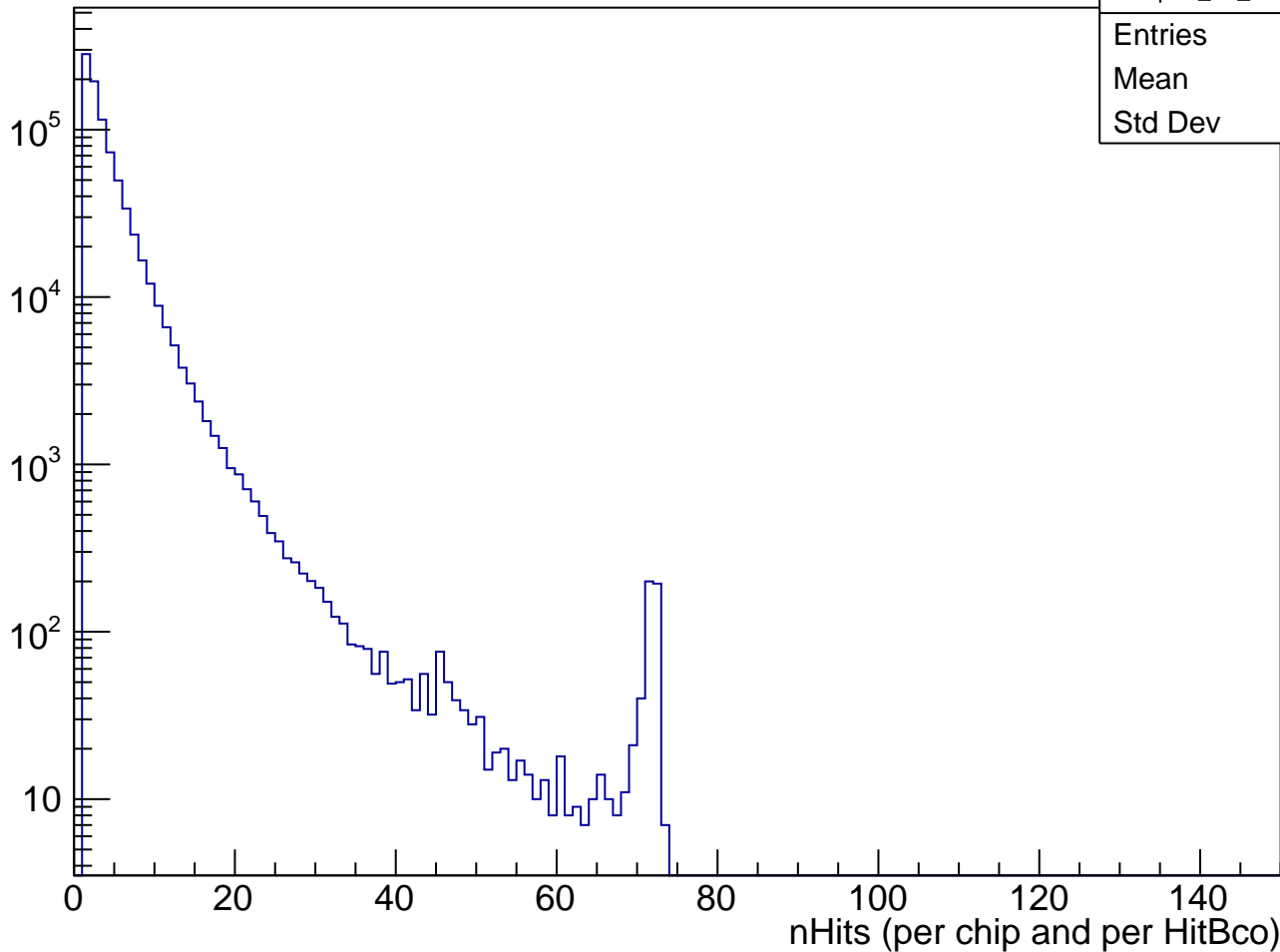


nChipHit_F7_Fch8_Chip17	
Entries	959259
Mean	3.653
Std Dev	4.215

nHits (per chip and per HitBco)

nChipHit_F7_Fch8_Chip18

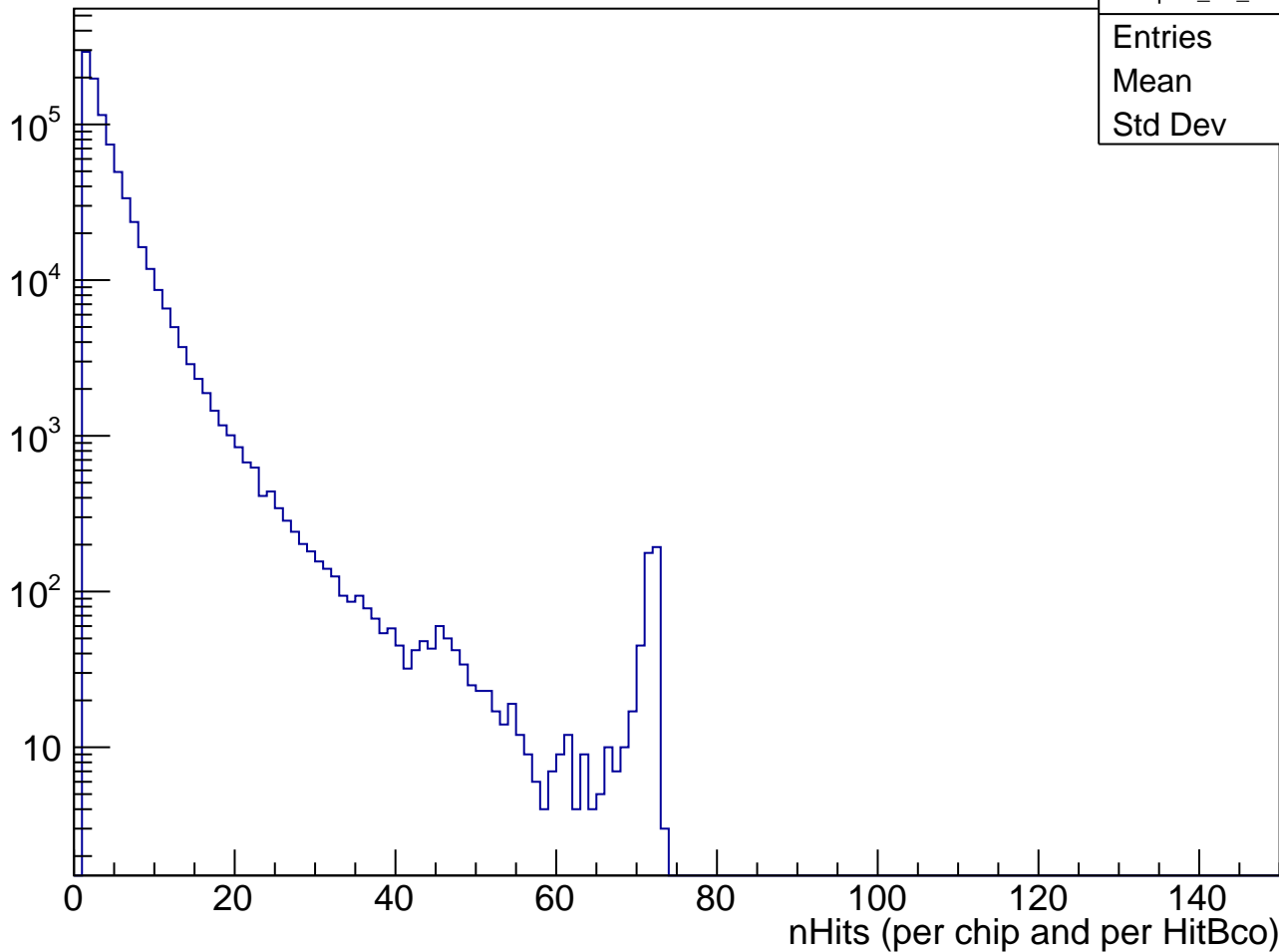
Entries



nChipHit_F7_Fch8_Chip18	
Entries	842965
Mean	3.338
Std Dev	3.917

nChipHit_F7_Fch8_Chip19

Entries

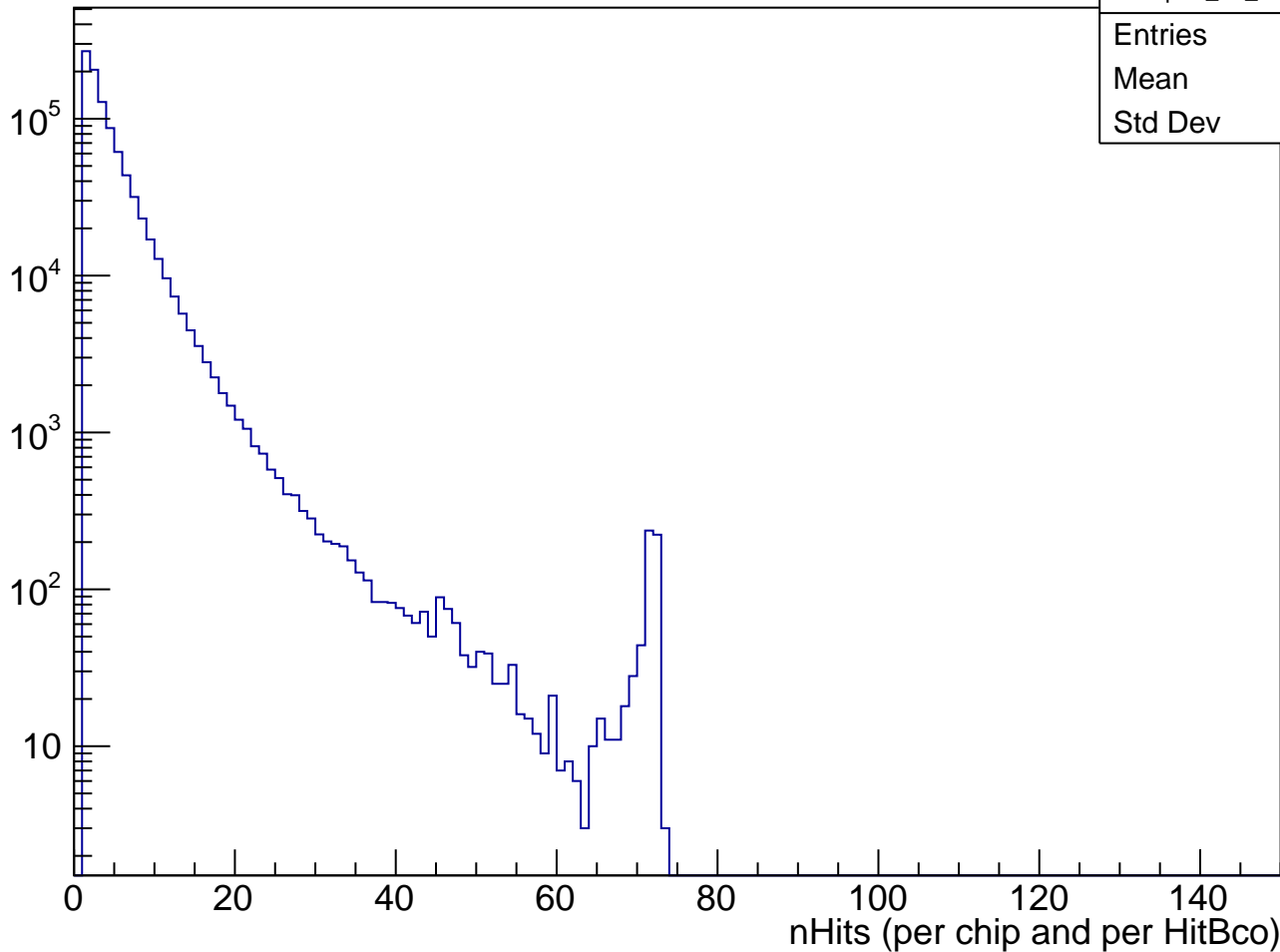


nChipHit_F7_Fch8_Chip19	
Entries	854221
Mean	3.286
Std Dev	3.826

nHits (per chip and per HitBco)

nChipHit_F7_Fch8_Chip2

Entries



nChipHit_F7_Fch8_Chip2

Entries 927794

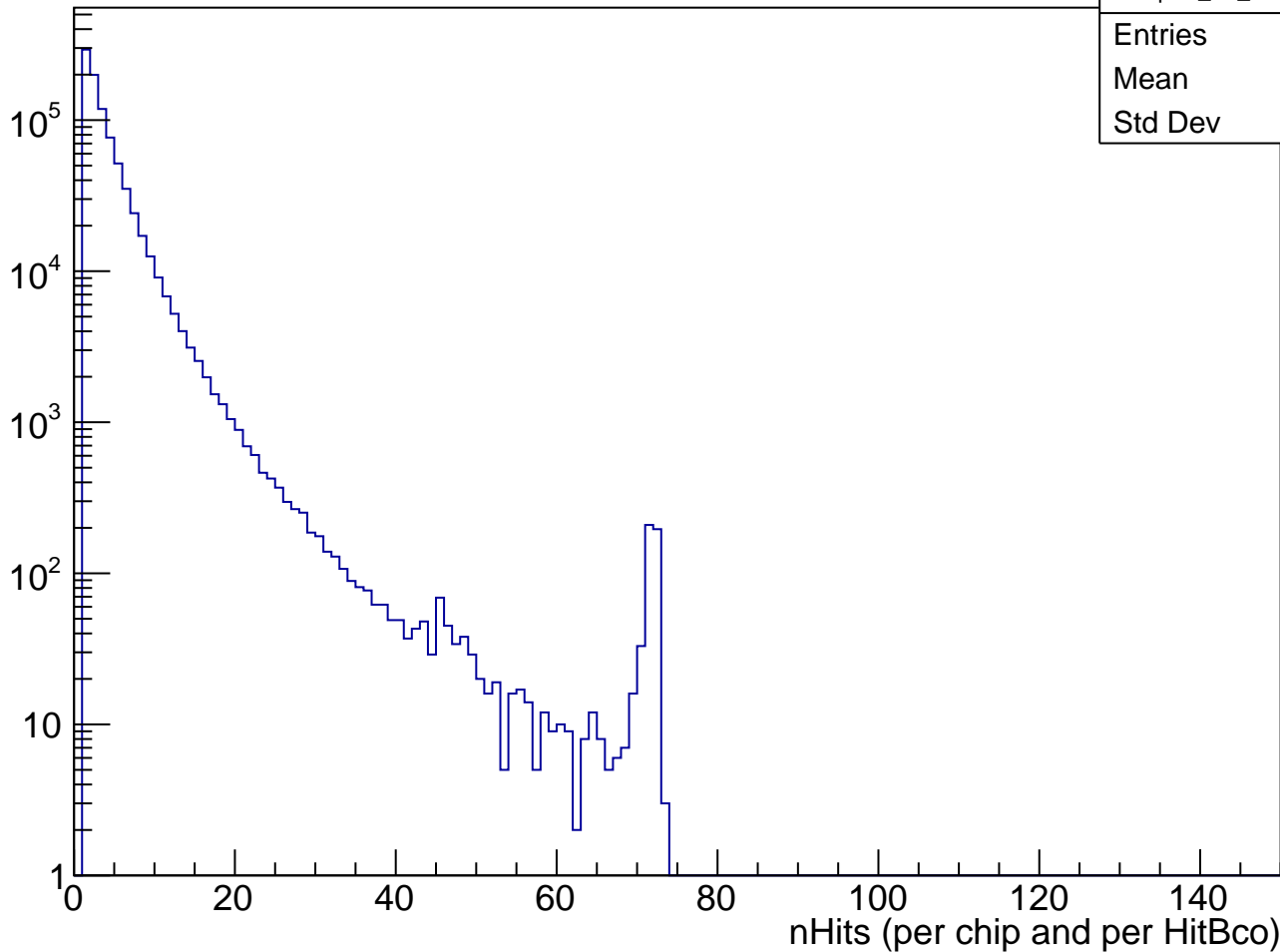
Mean 3.746

Std Dev 4.256

nHits (per chip and per HitBco)

nChipHit_F7_Fch8_Chip20

Entries

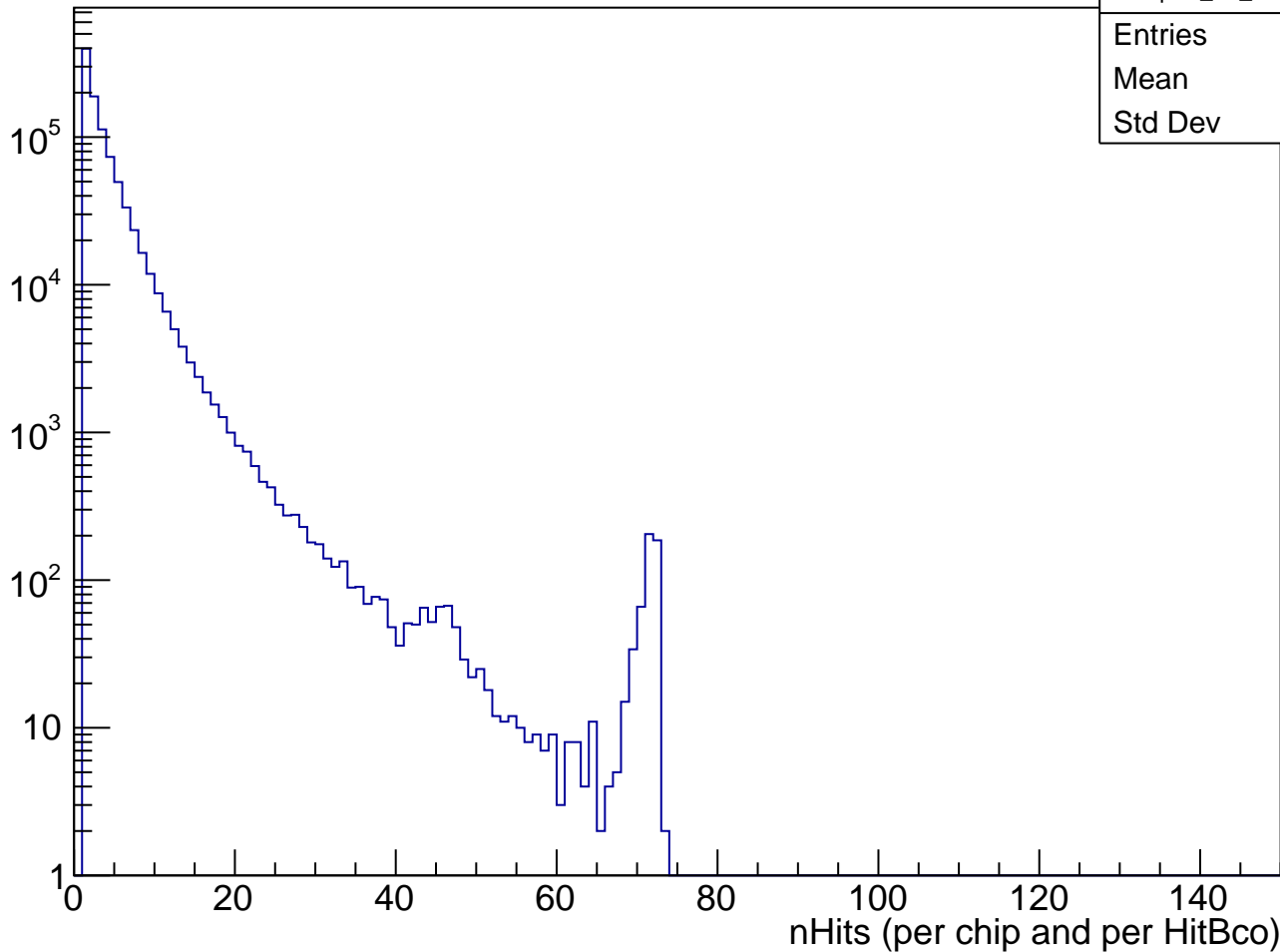


nChipHit_F7_Fch8_Chip20	
Entries	871335
Mean	3.333
Std Dev	3.86

nHits (per chip and per HitBco)

nChipHit_F7_Fch8_Chip21

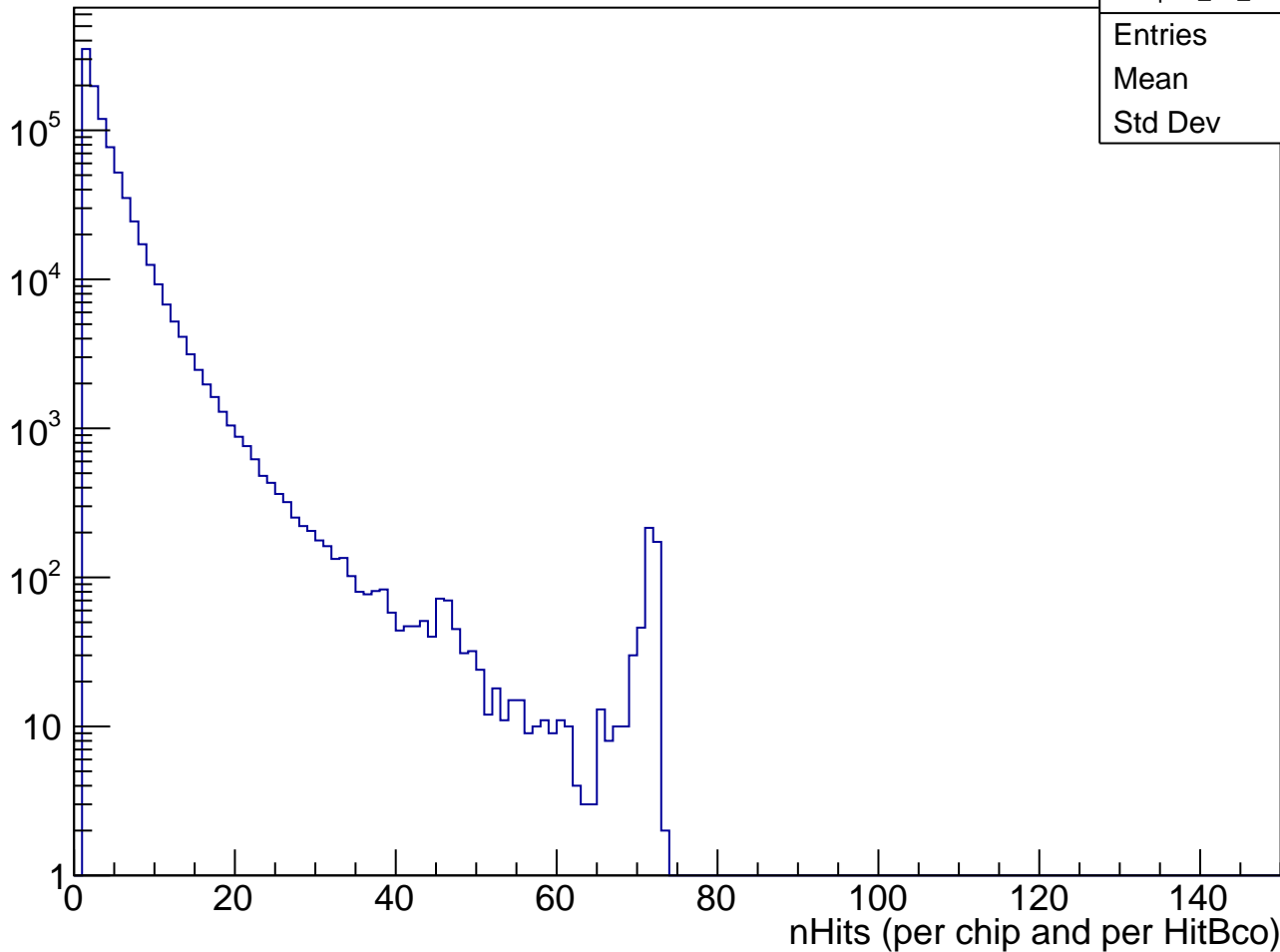
Entries



nChipHit_F7_Fch8_Chip21	
Entries	948412
Mean	3.061
Std Dev	3.766

nChipHit_F7_Fch8_Chip22

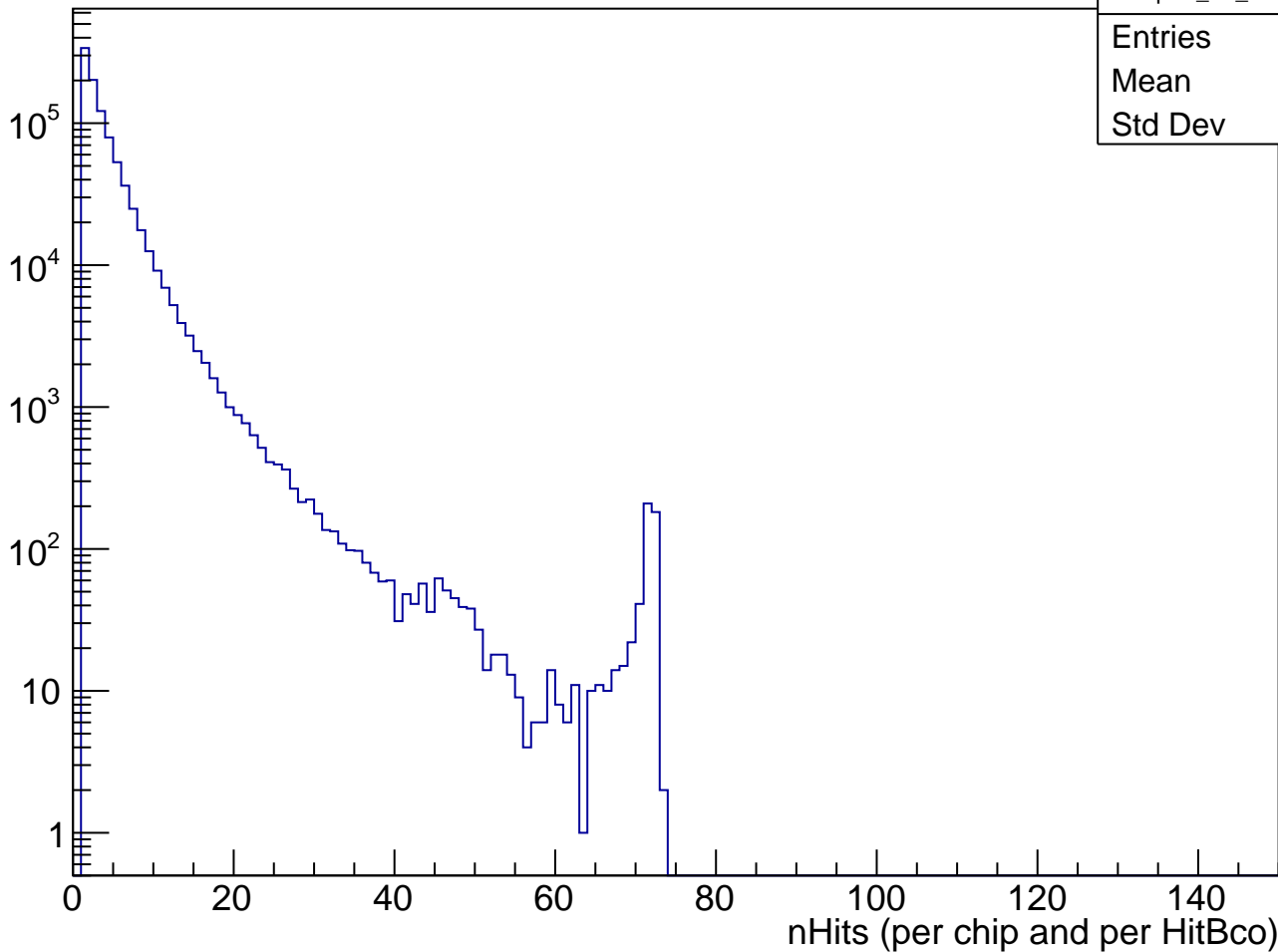
Entries



nChipHit_F7_Fch8_Chip22	
Entries	930172
Mean	3.202
Std Dev	3.824

nChipHit_F7_Fch8_Chip23

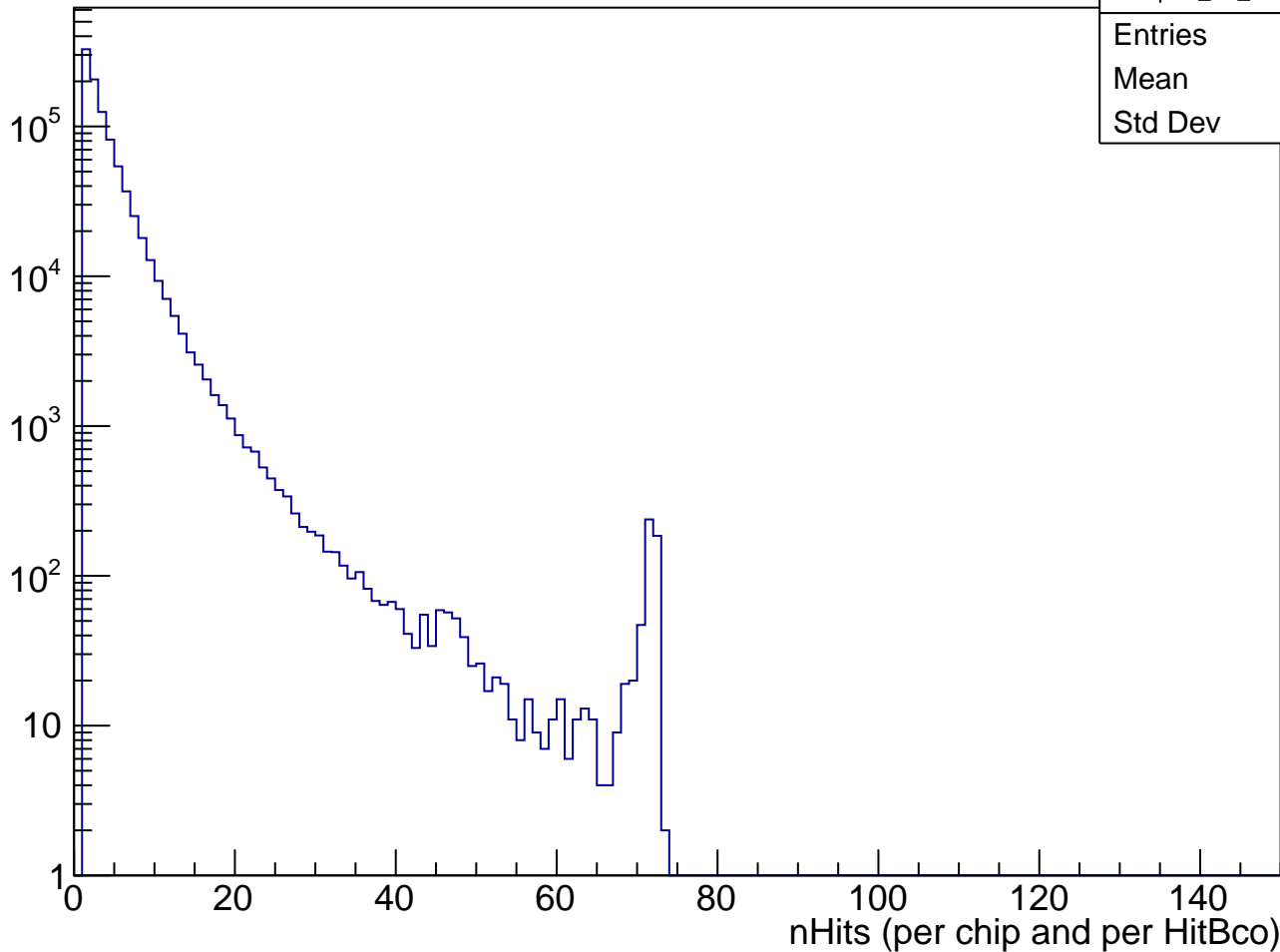
Entries



nChipHit_F7_Fch8_Chip23	
Entries	929431
Mean	3.236
Std Dev	3.812

nChipHit_F7_Fch8_Chip24

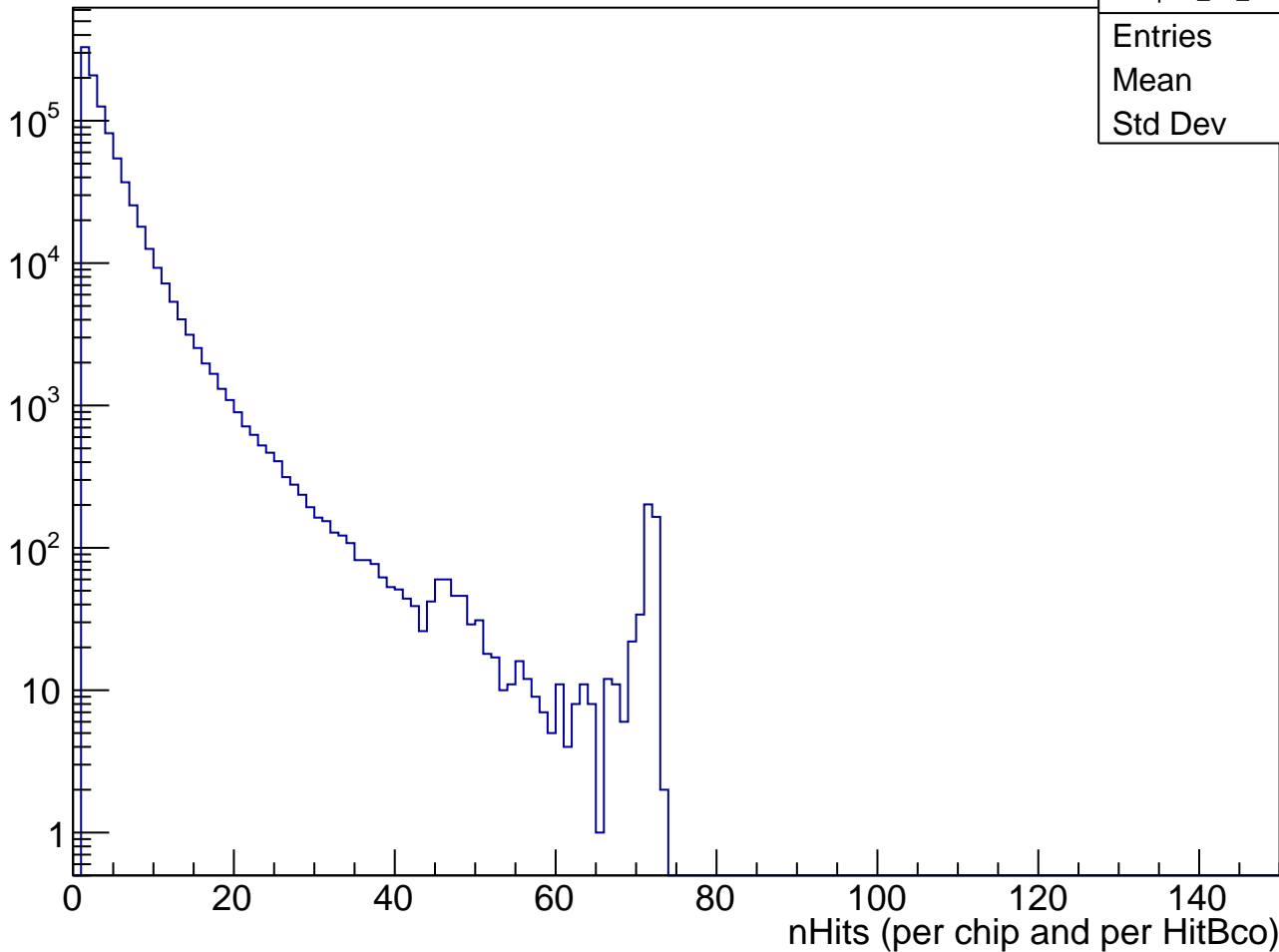
Entries



nChipHit_F7_Fch8_Chip24	
Entries	931882
Mean	3.282
Std Dev	3.856

nChipHit_F7_Fch8_Chip25

Entries

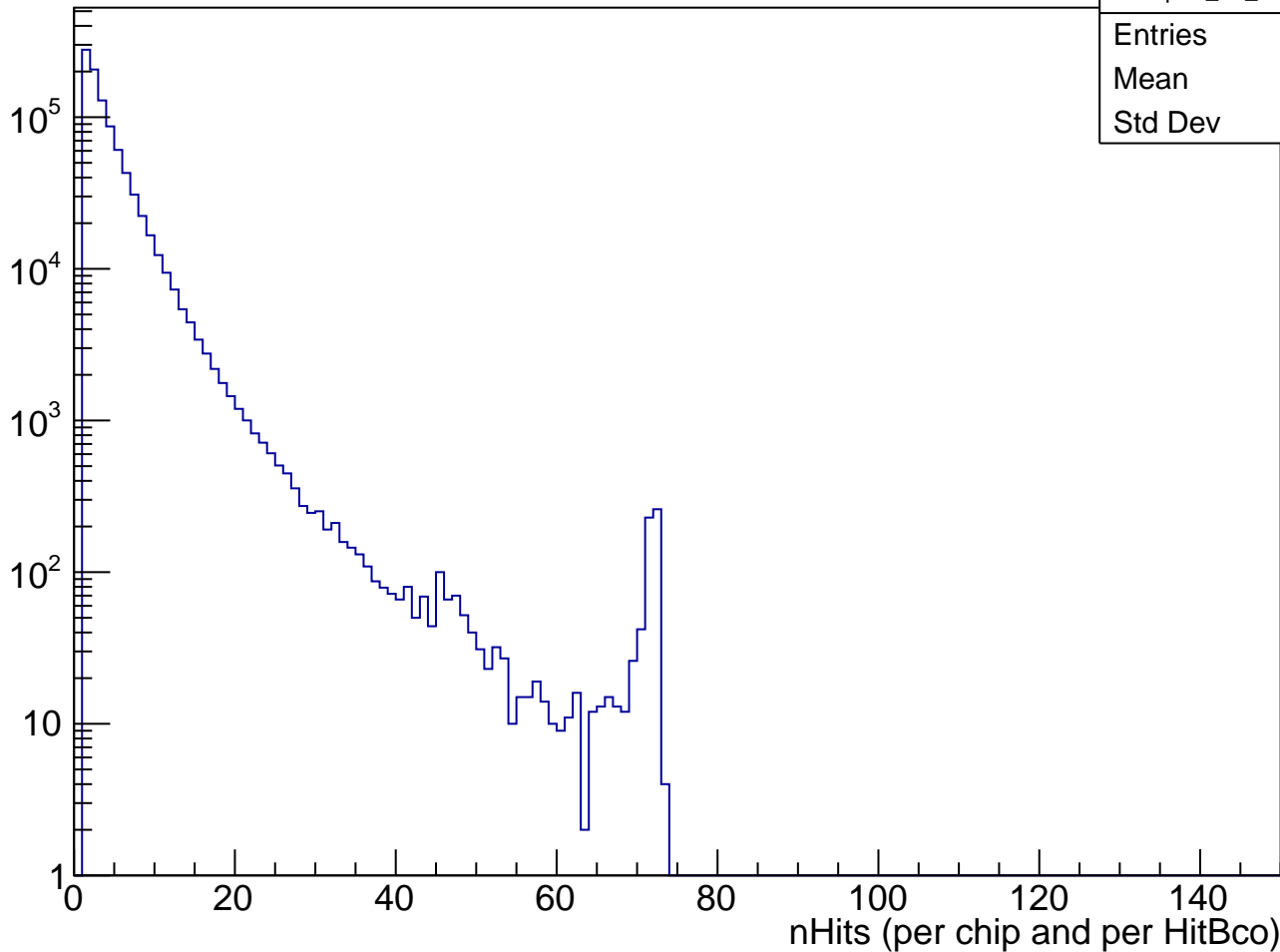


nChipHit_F7_Fch8_Chip25	
Entries	936181
Mean	3.266
Std Dev	3.781

nHits (per chip and per HitBco)

nChipHit_F7_Fch8_Chip3

Entries



nChipHit_F7_Fch8_Chip3

Entries 933773

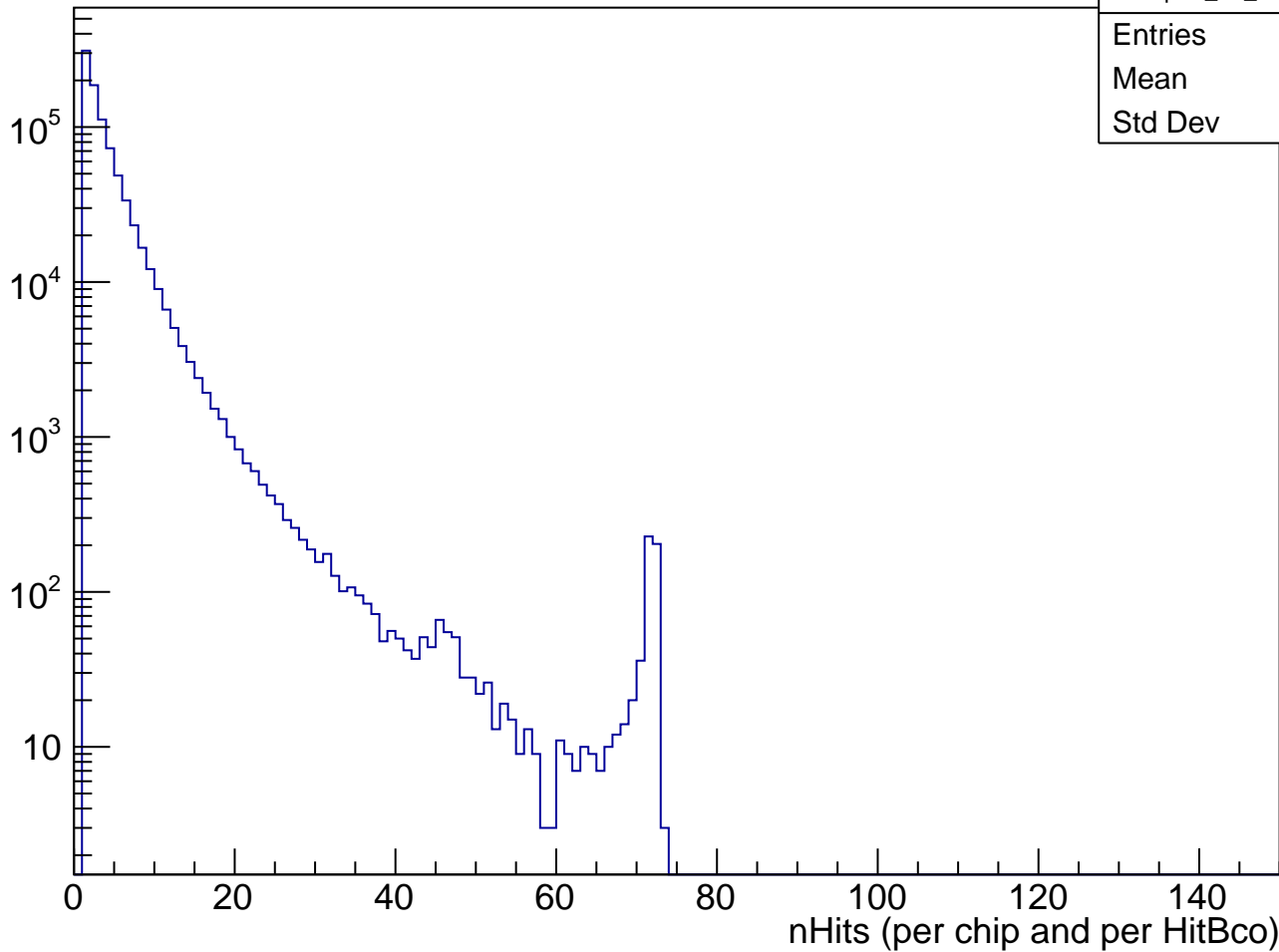
Mean 3.691

Std Dev 4.235

nHits (per chip and per HitBco)

nChipHit_F7_Fch8_Chip5

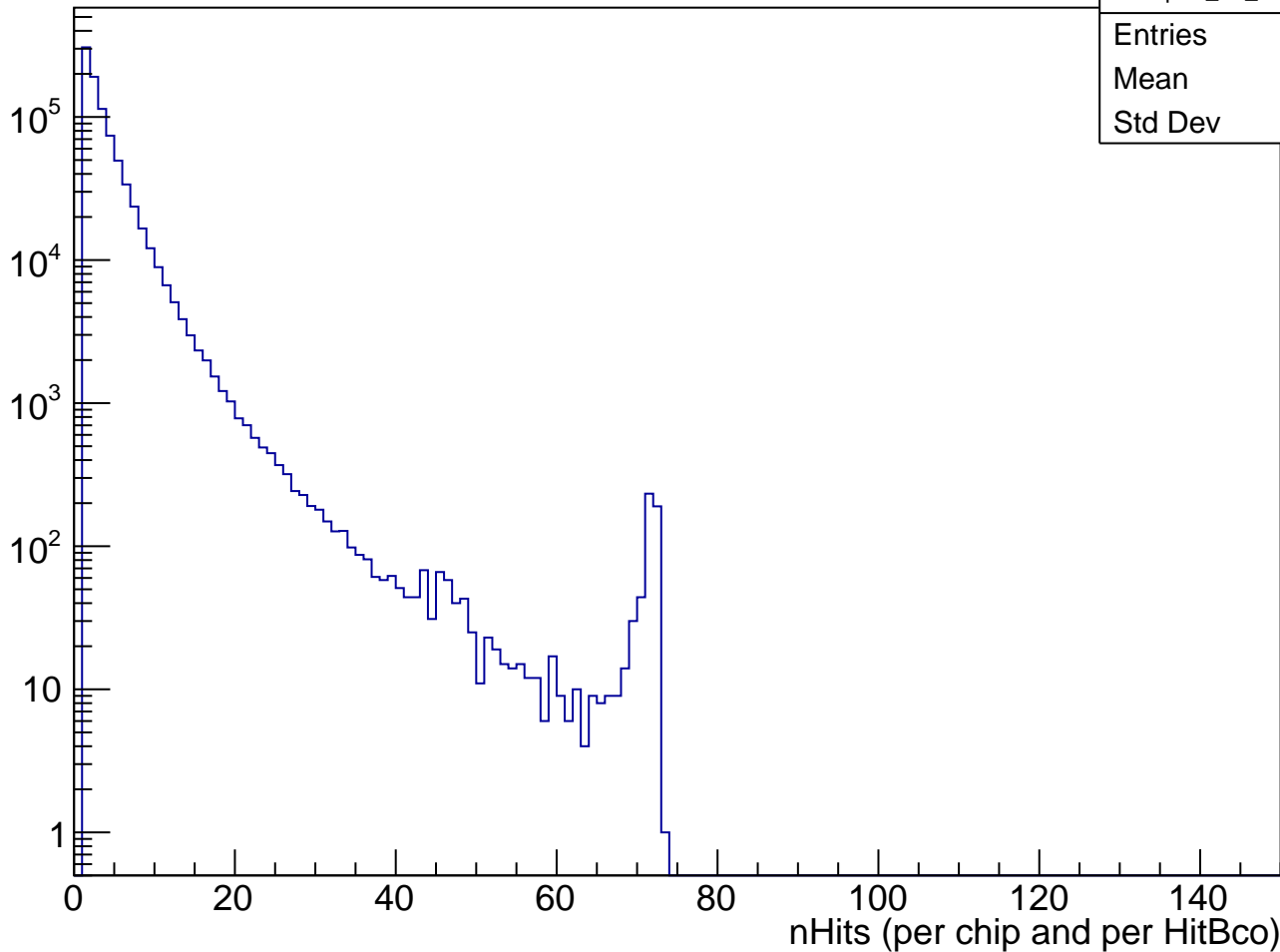
Entries



nChipHit_F7_Fch8_Chip5	
Entries	858904
Mean	3.278
Std Dev	3.92

nChipHit_F7_Fch8_Chip6

Entries



nChipHit_F7_Fch8_Chip6

Entries 863234

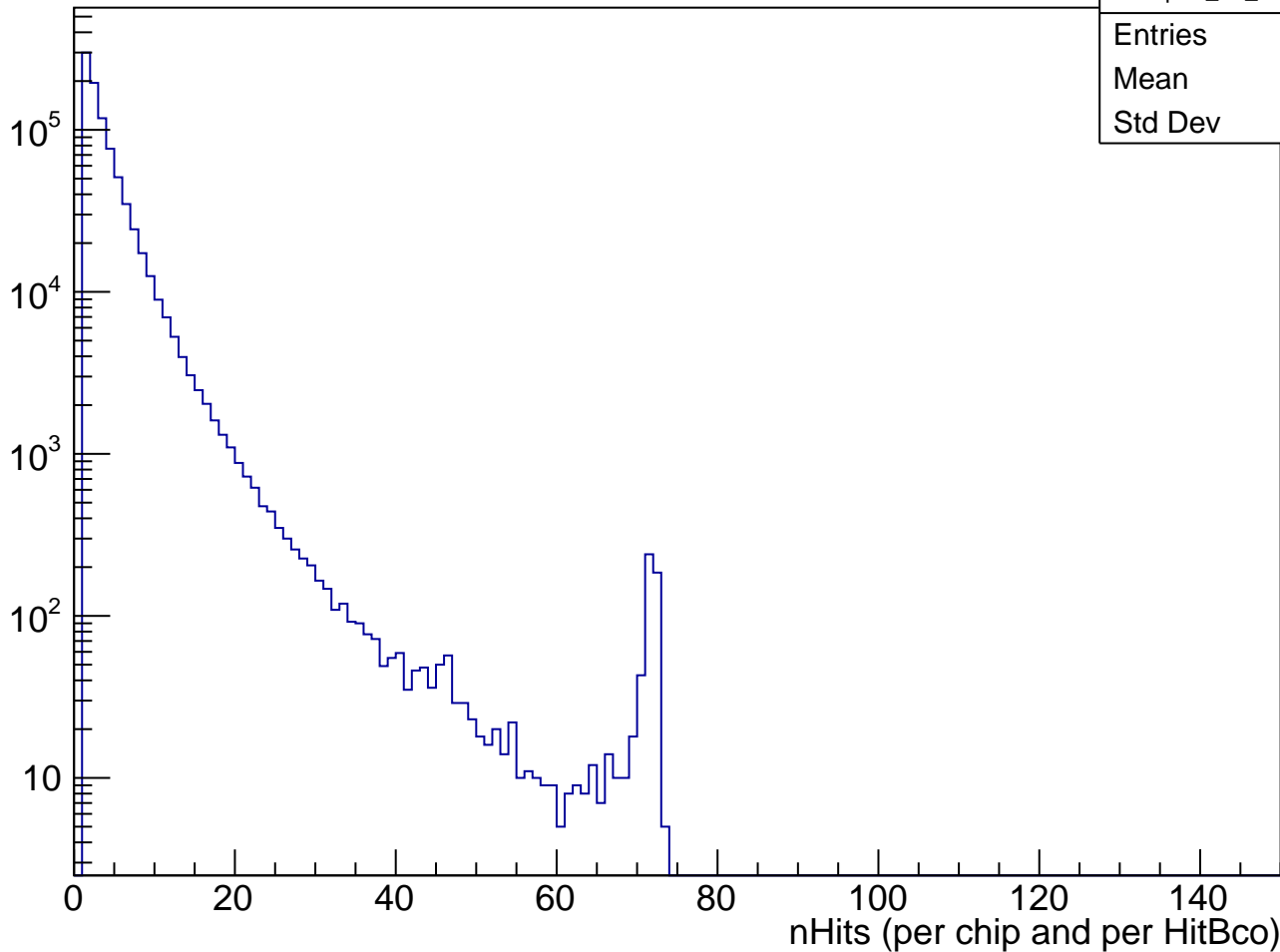
Mean 3.287

Std Dev 3.917

nHits (per chip and per HitBco)

nChipHit_F7_Fch8_Chip7

Entries

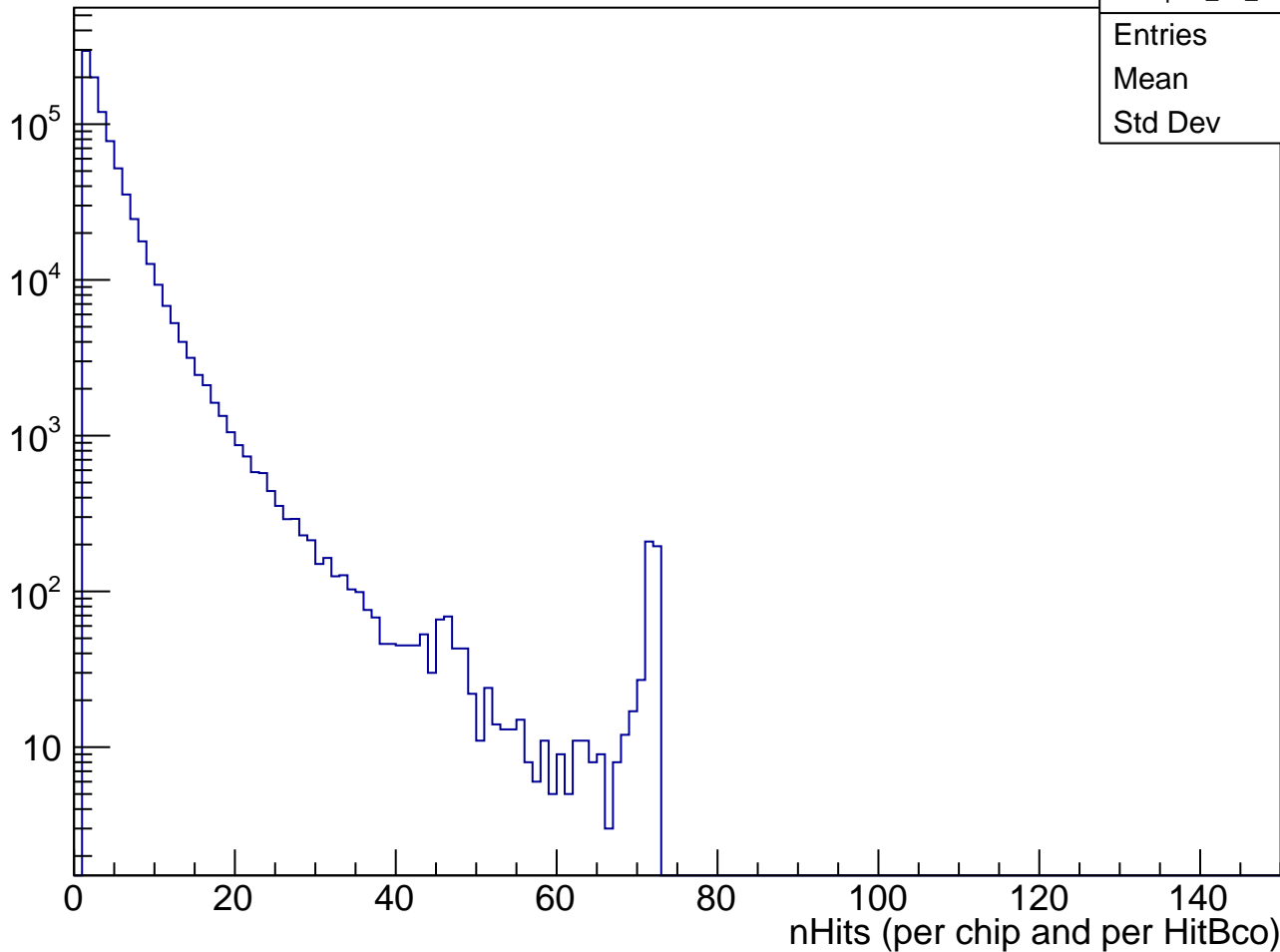


nChipHit_F7_Fch8_Chip7	
Entries	872359
Mean	3.327
Std Dev	3.898

nHits (per chip and per HitBco)

nChipHit_F7_Fch8_Chip8

Entries



nChipHit_F7_Fch8_Chip8

Entries 879173

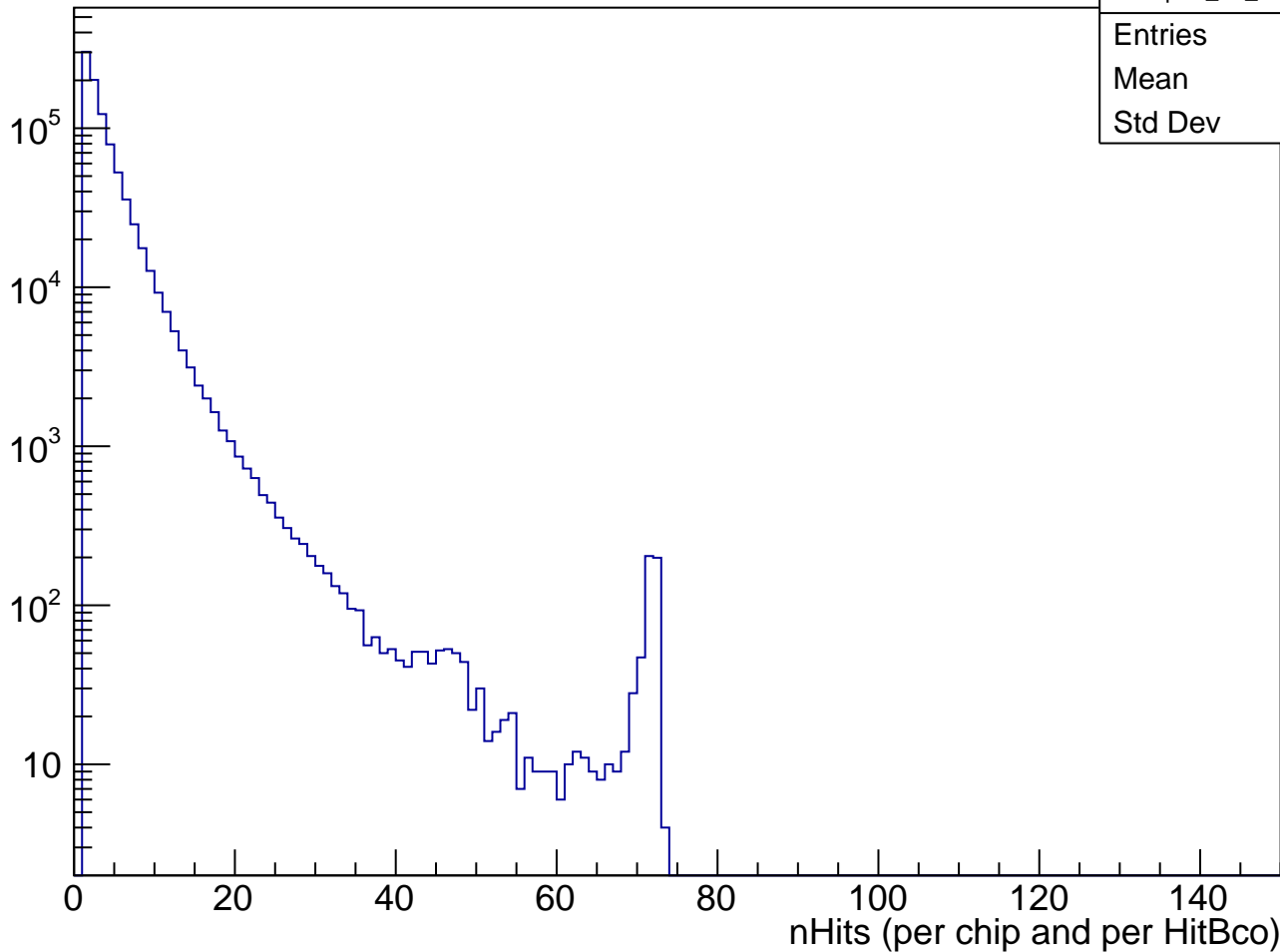
Mean 3.343

Std Dev 3.873

nHits (per chip and per HitBco)

nChipHit_F7_Fch8_Chip9

Entries



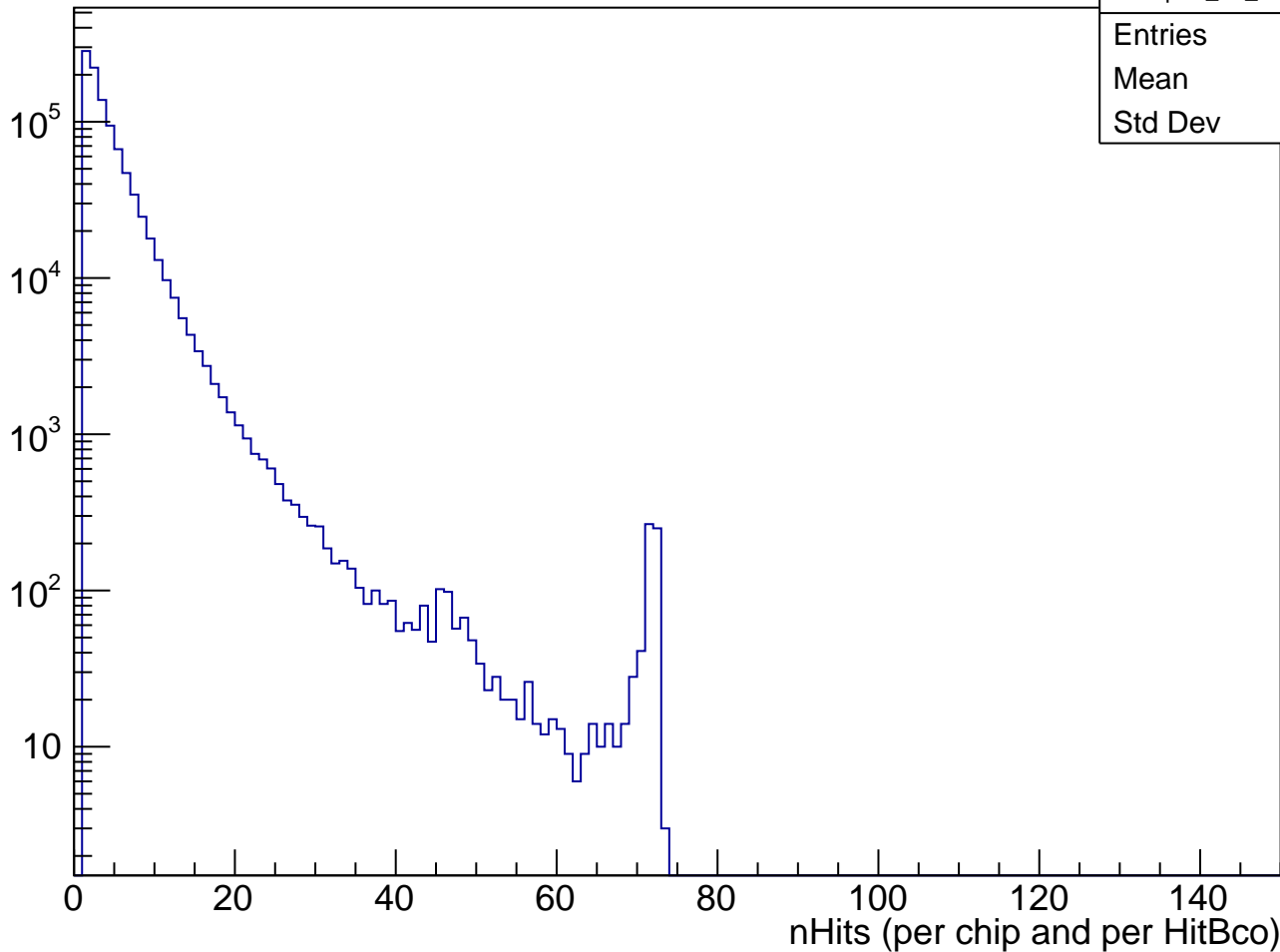
nChipHit_F7_Fch8_Chip9

Entries	892785
Mean	3.326
Std Dev	3.875

nHits (per chip and per HitBco)

nChipHit_F7_Fch9_Chip0

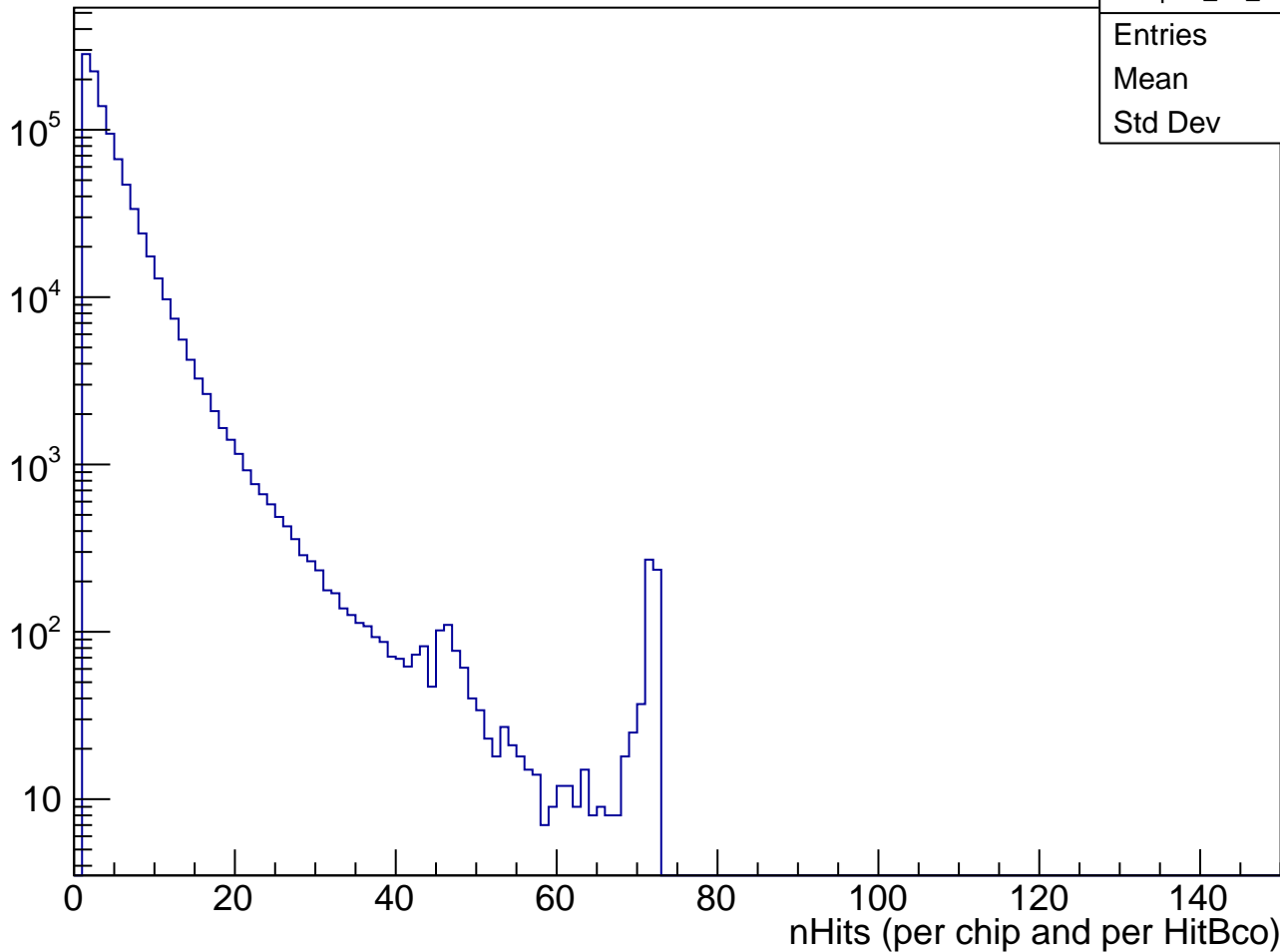
Entries



nChipHit_F7_Fch9_Chip0	
Entries	989082
Mean	3.691
Std Dev	4.15

nChipHit_F7_Fch9_Chip1

Entries

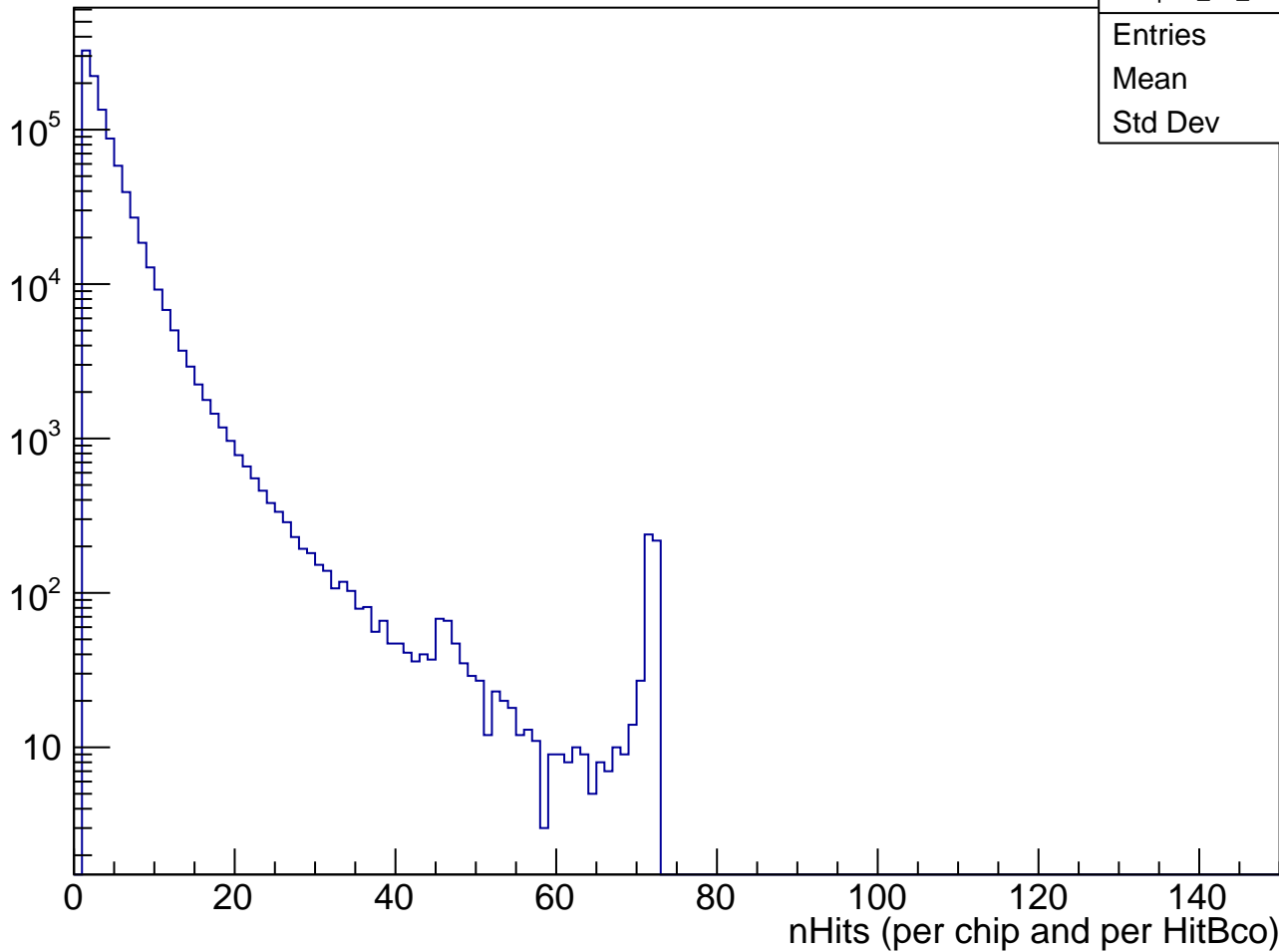


nChipHit_F7_Fch9_Chip1	
Entries	988968
Mean	3.677
Std Dev	4.129

nHits (per chip and per HitBco)

nChipHit_F7_Fch9_Chip10

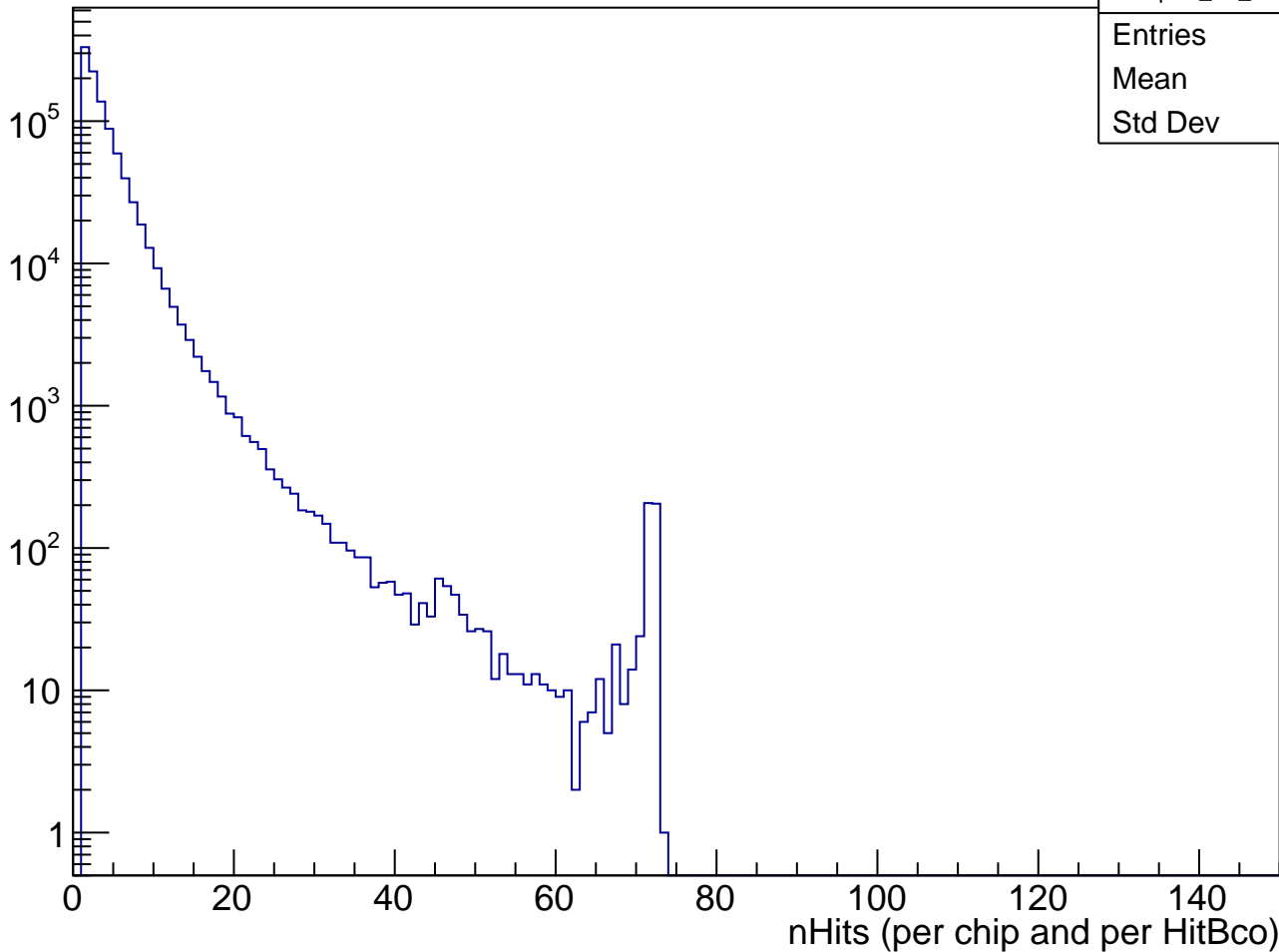
Entries



nChipHit_F7_Fch9_Chip10	
Entries	967952
Mean	3.246
Std Dev	3.701

nChipHit_F7_Fch9_Chip11

Entries



nChipHit_F7_Fch9_Chip11

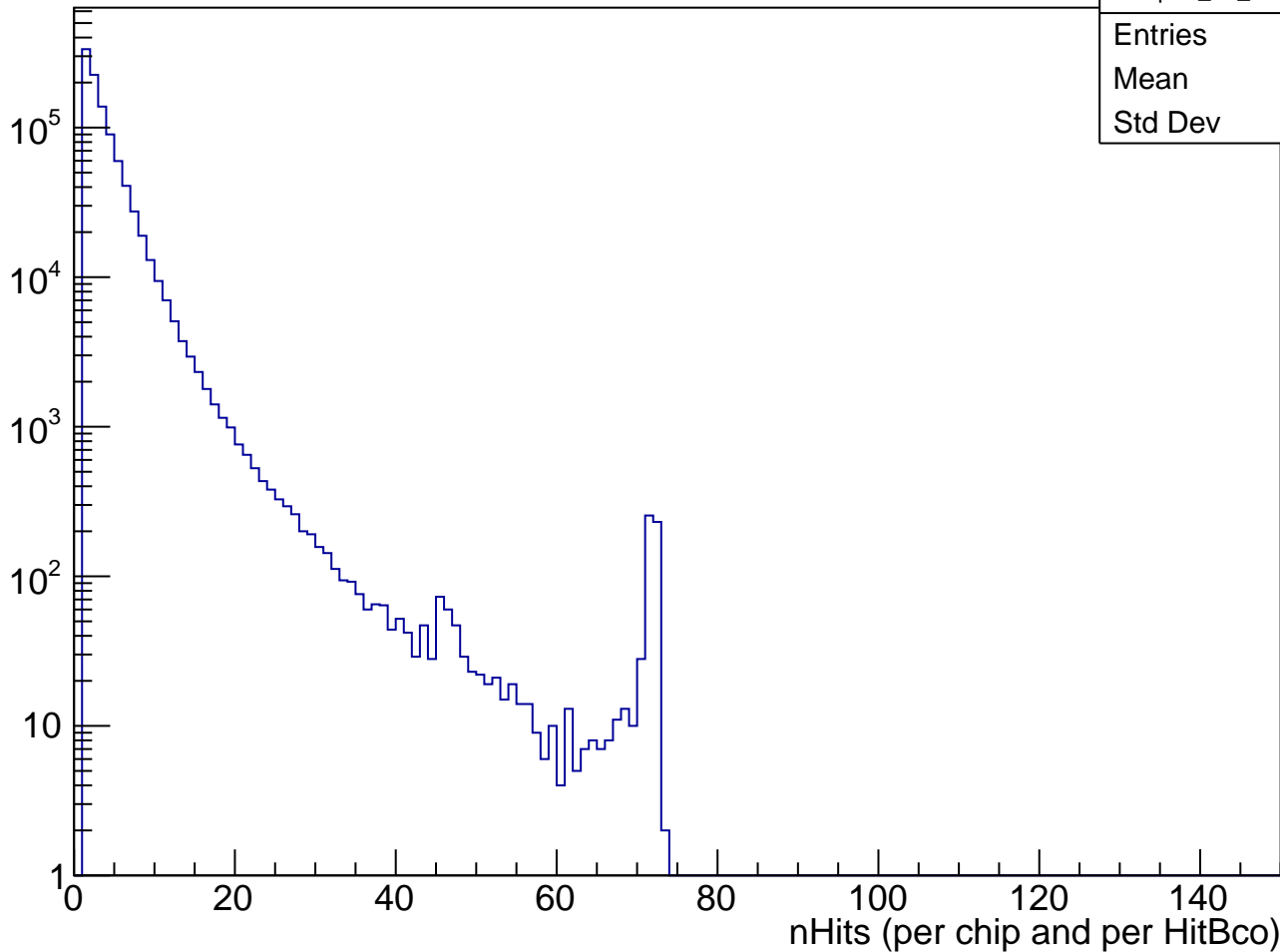
Entries 979042

Mean 3.228

Std Dev 3.648

nChipHit_F7_Fch9_Chip12

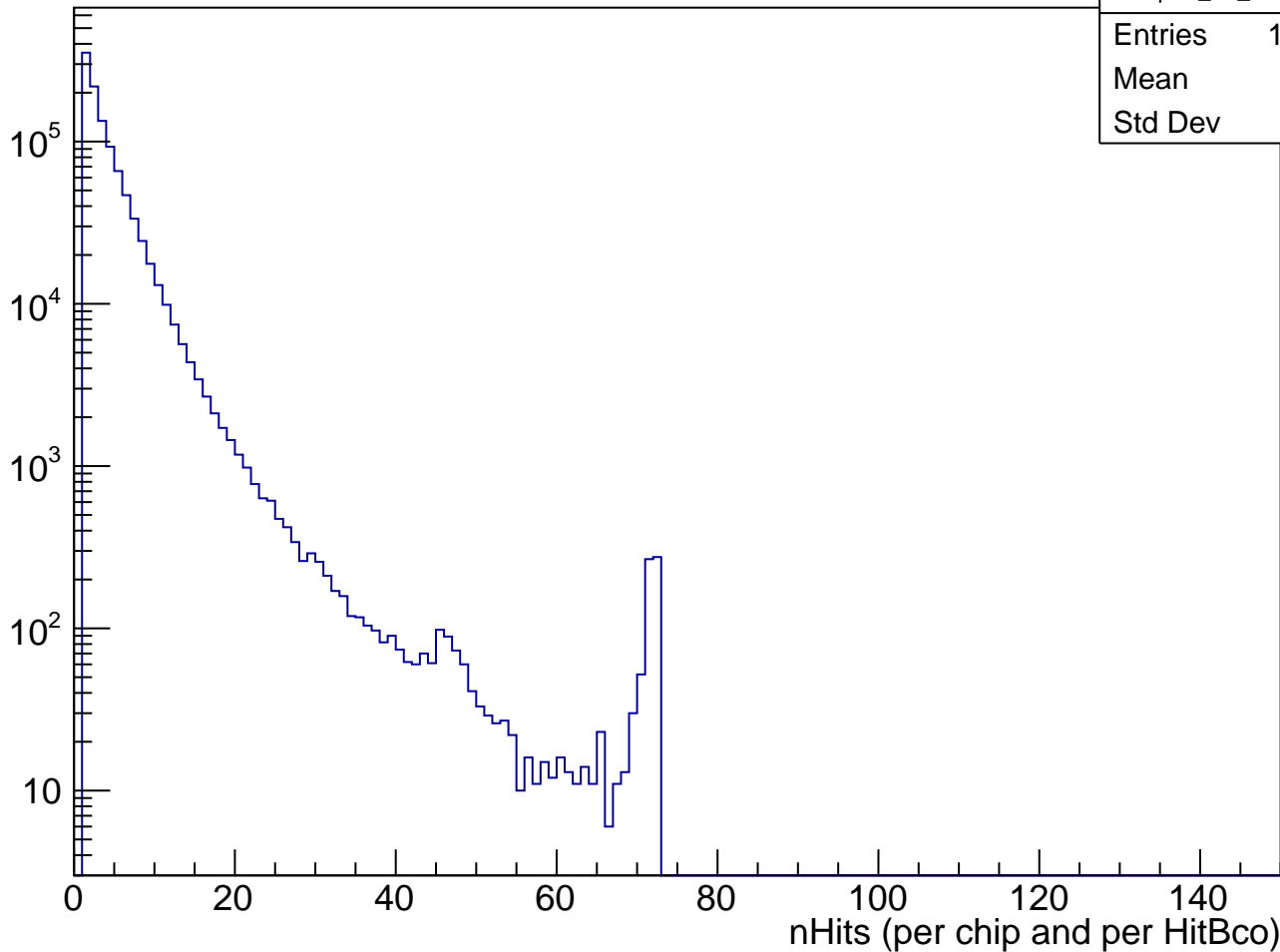
Entries



nChipHit_F7_Fch9_Chip12	
Entries	989860
Mean	3.239
Std Dev	3.686

nChipHit_F7_Fch9_Chip13

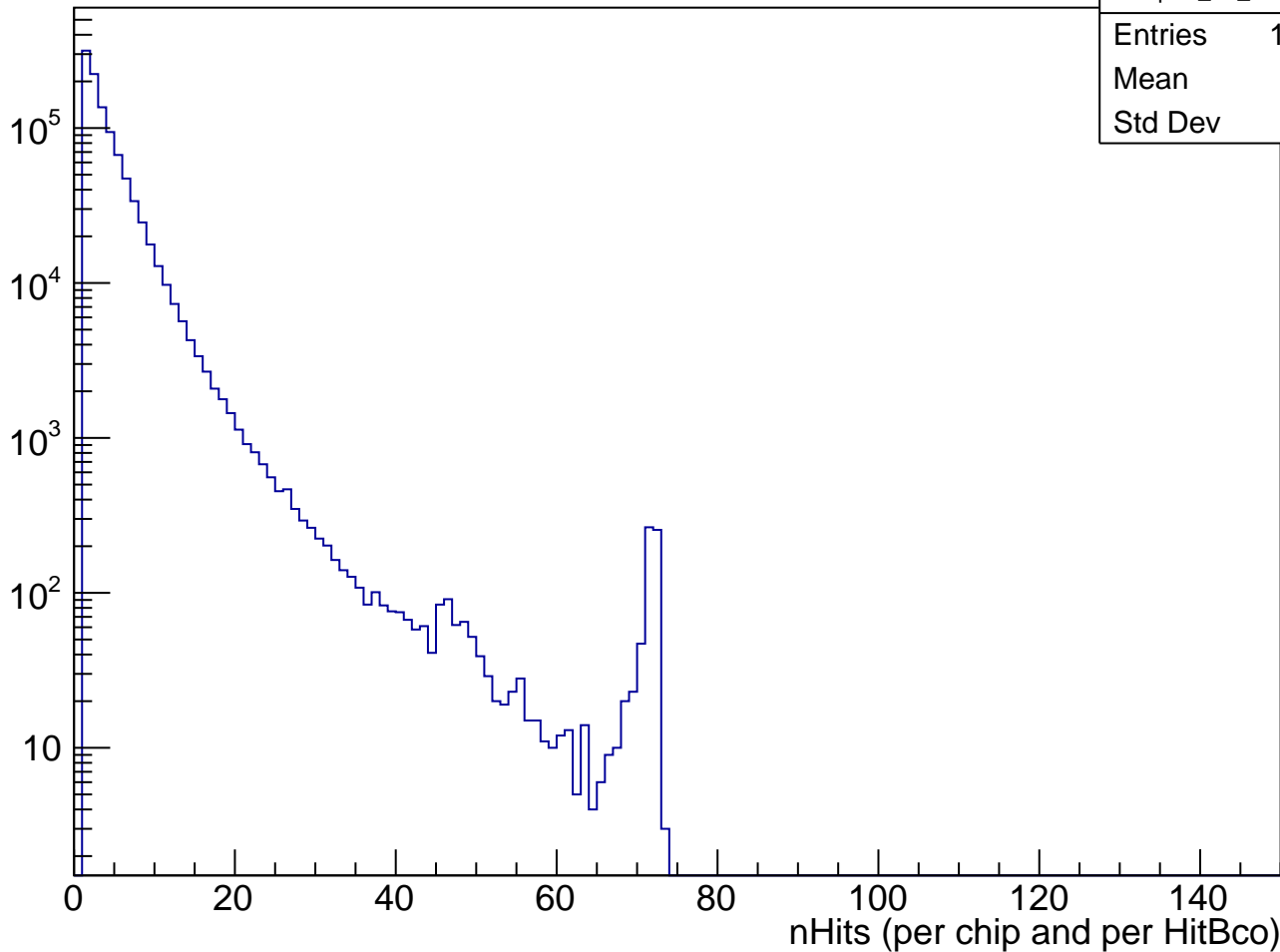
Entries



nChipHit_F7_Fch9_Chip13	
Entries	1047189
Mean	3.525
Std Dev	4.123

nChipHit_F7_Fch9_Chip14

Entries



nChipHit_F7_Fch9_Chip14

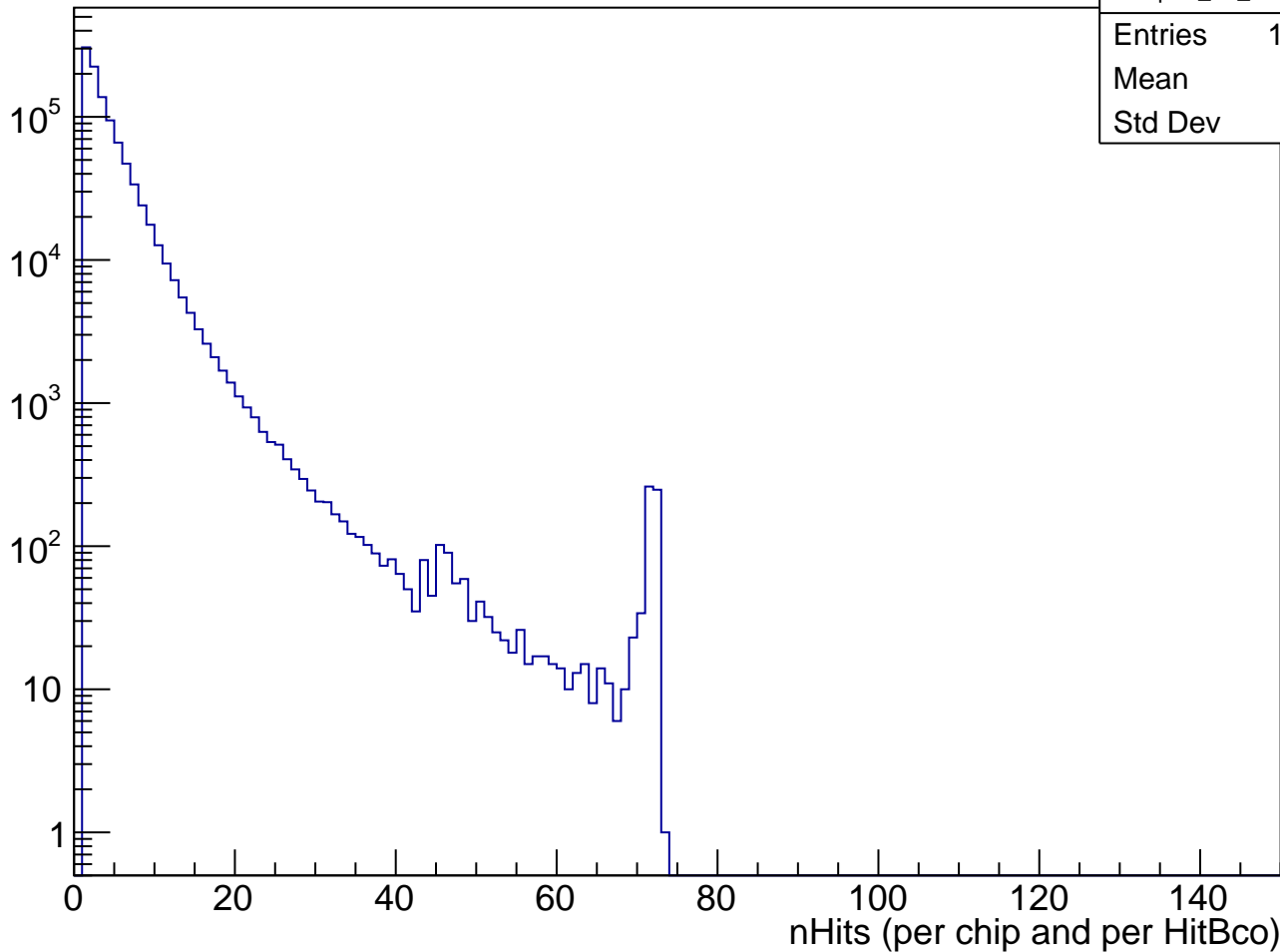
Entries 1019810

Mean 3.602

Std Dev 4.108

nChipHit_F7_Fch9_Chip15

Entries

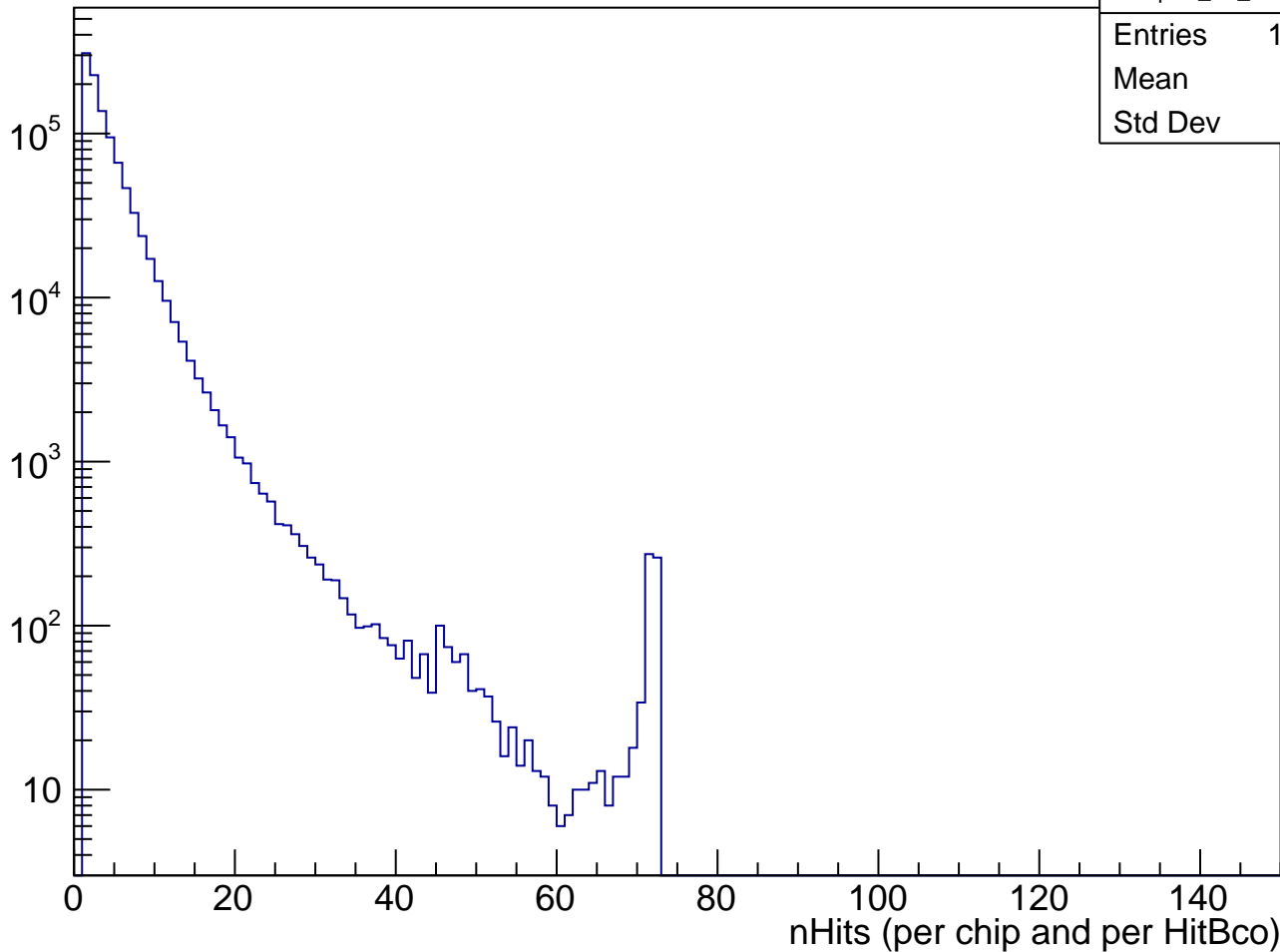


nChipHit_F7_Fch9_Chip15	
Entries	1009915
Mean	3.606
Std Dev	4.086

nHits (per chip and per HitBco)

nChipHit_F7_Fch9_Chip16

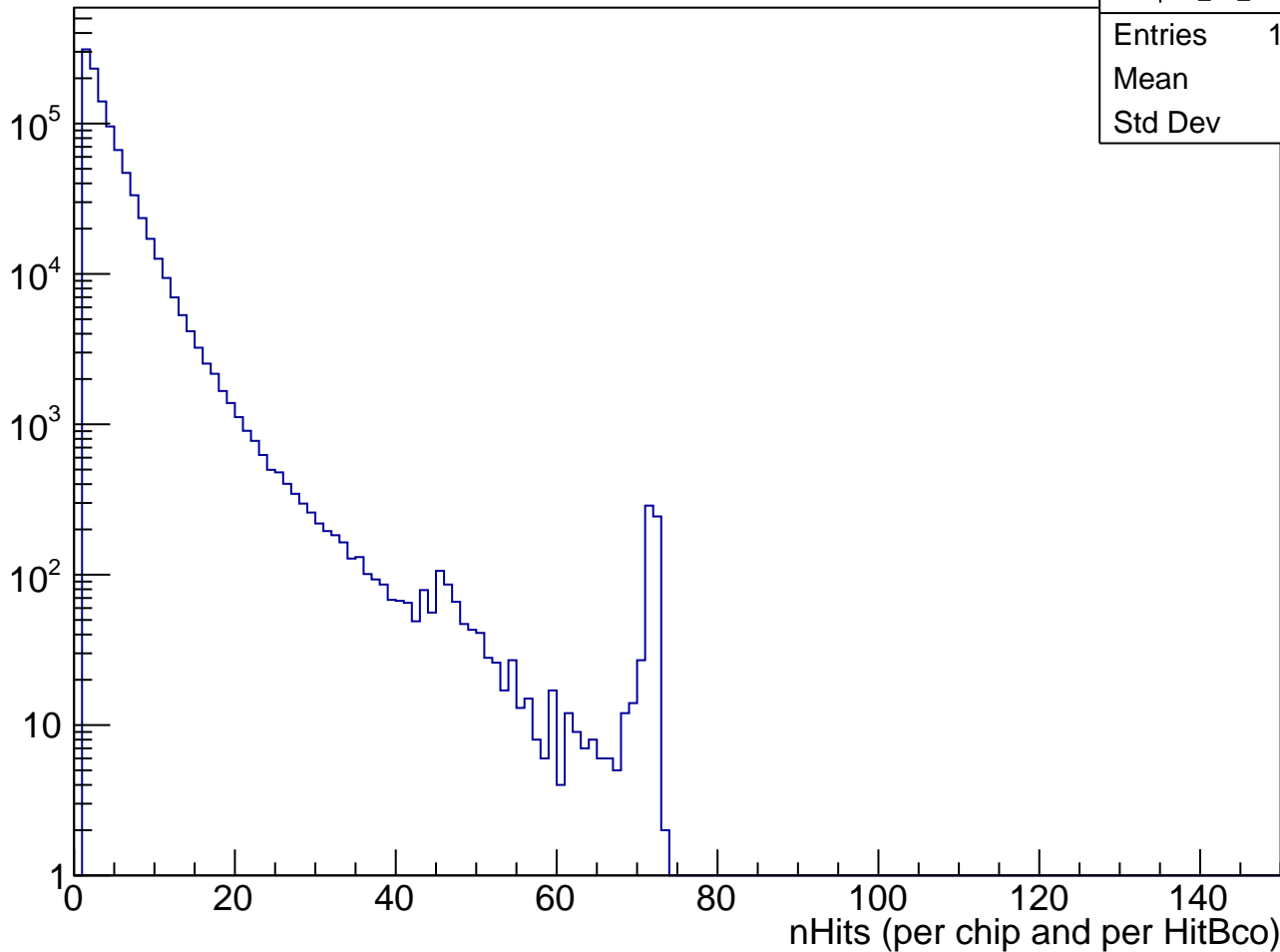
Entries



nChipHit_F7_Fch9_Chip16	
Entries	1012828
Mean	3.584
Std Dev	4.08

nChipHit_F7_Fch9_Chip17

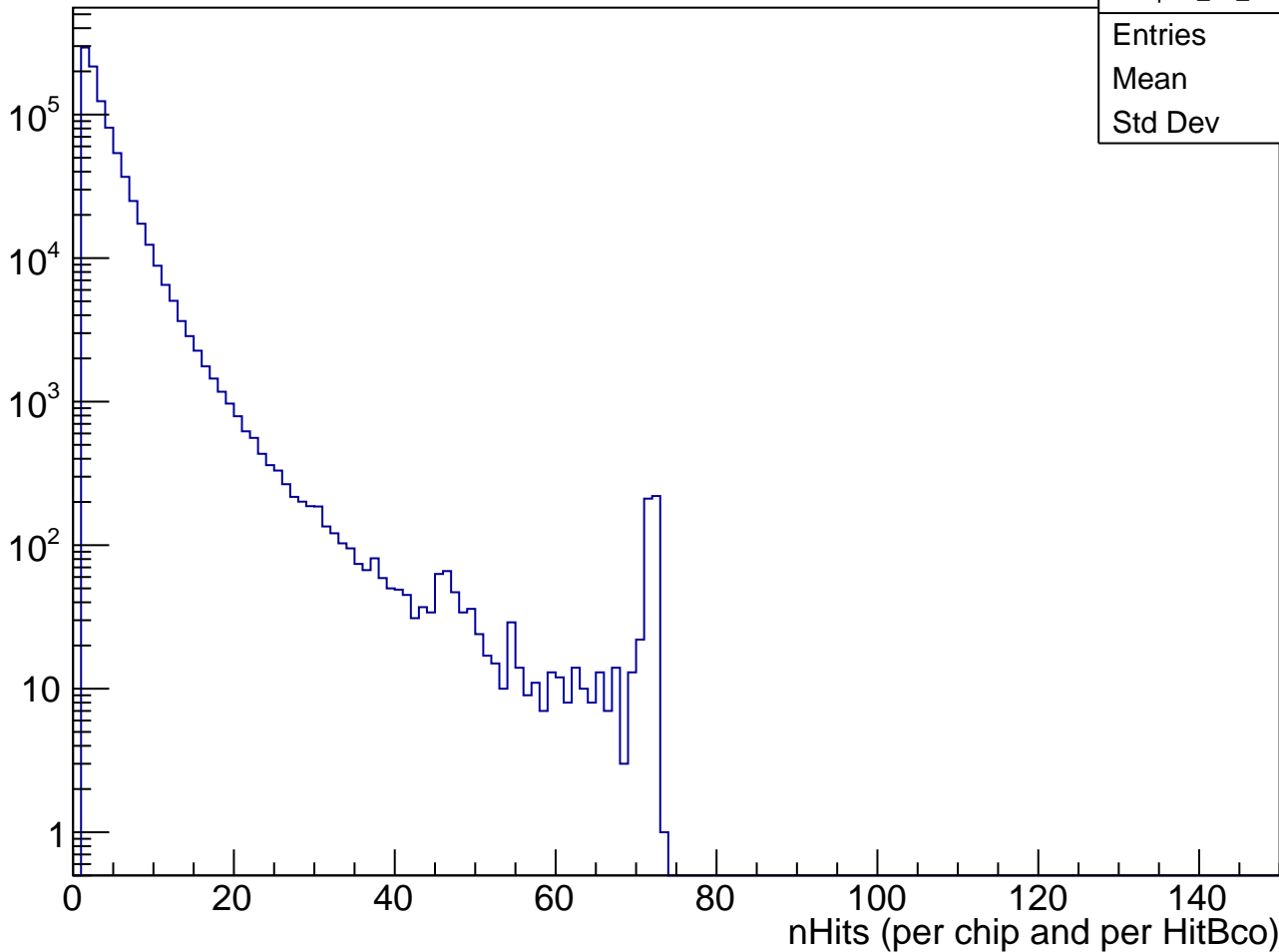
Entries



nChipHit_F7_Fch9_Chip17	
Entries	1024847
Mean	3.567
Std Dev	4.046

nChipHit_F7_Fch9_Chip18

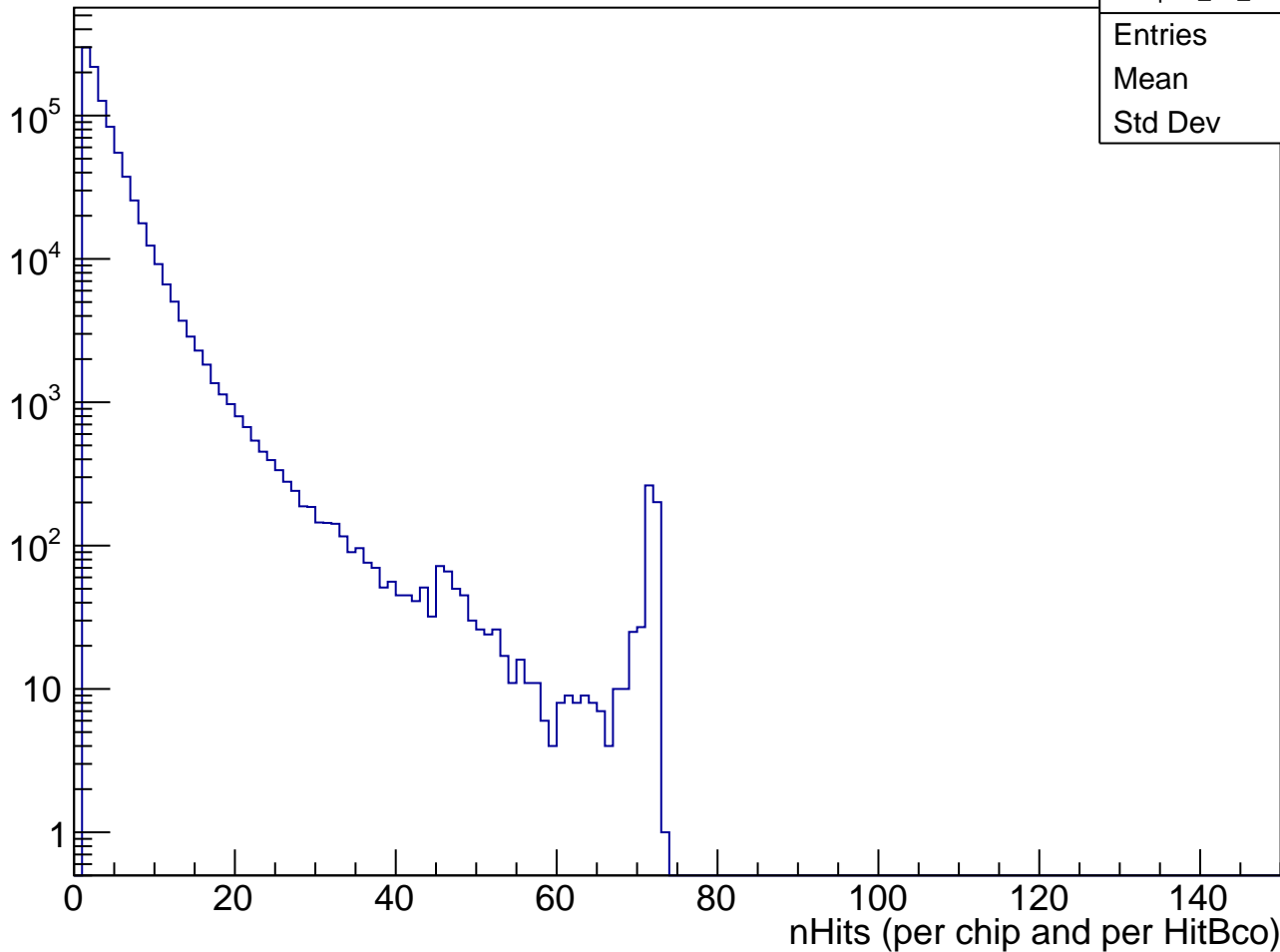
Entries



nChipHit_F7_Fch9_Chip18	
Entries	900668
Mean	3.29
Std Dev	3.773

nChipHit_F7_Fch9_Chip19

Entries

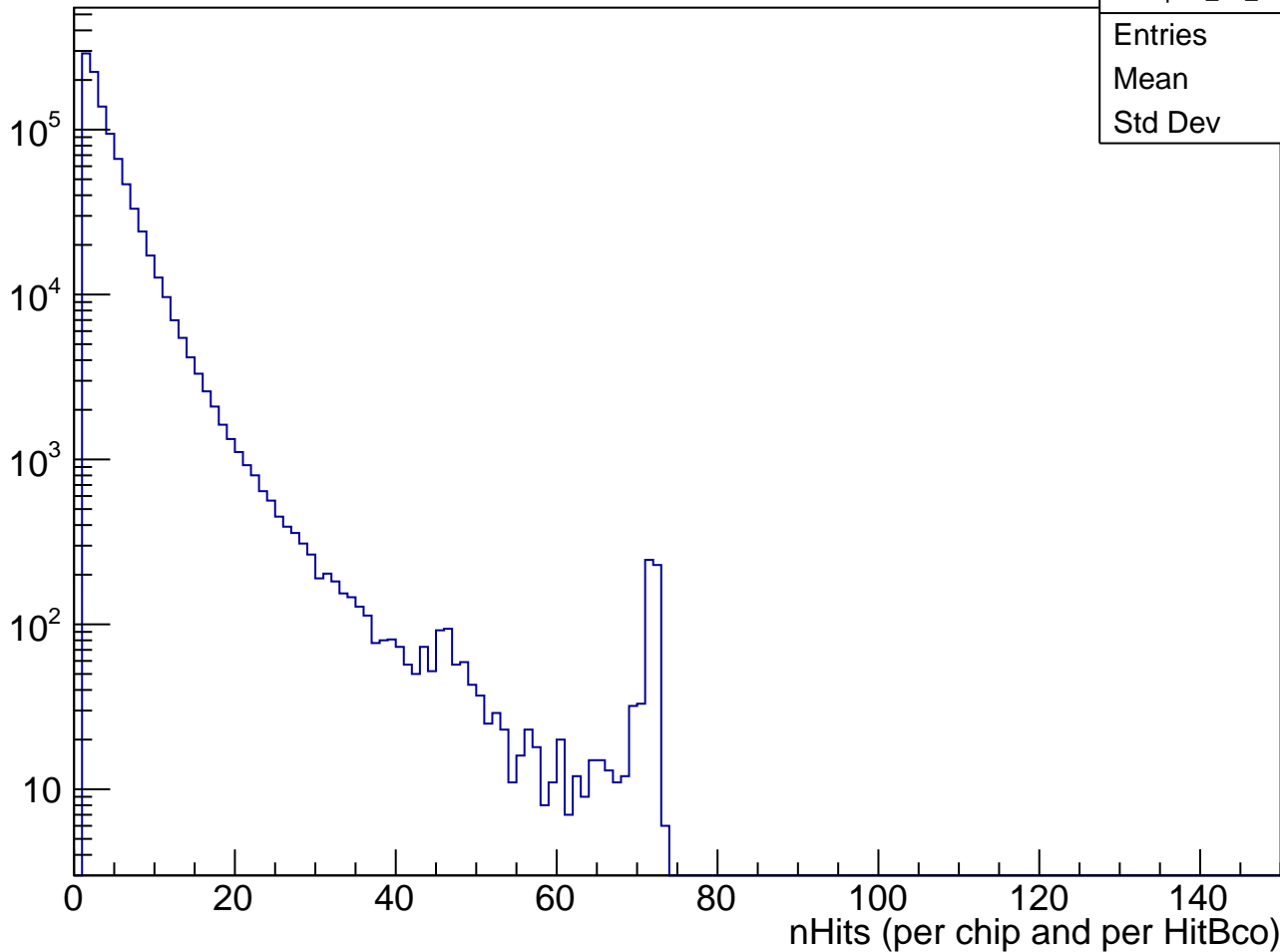


nChipHit_F7_Fch9_Chip19	
Entries	917008
Mean	3.293
Std Dev	3.792

nHits (per chip and per HitBco)

nChipHit_F7_Fch9_Chip2

Entries



nChipHit_F7_Fch9_Chip2

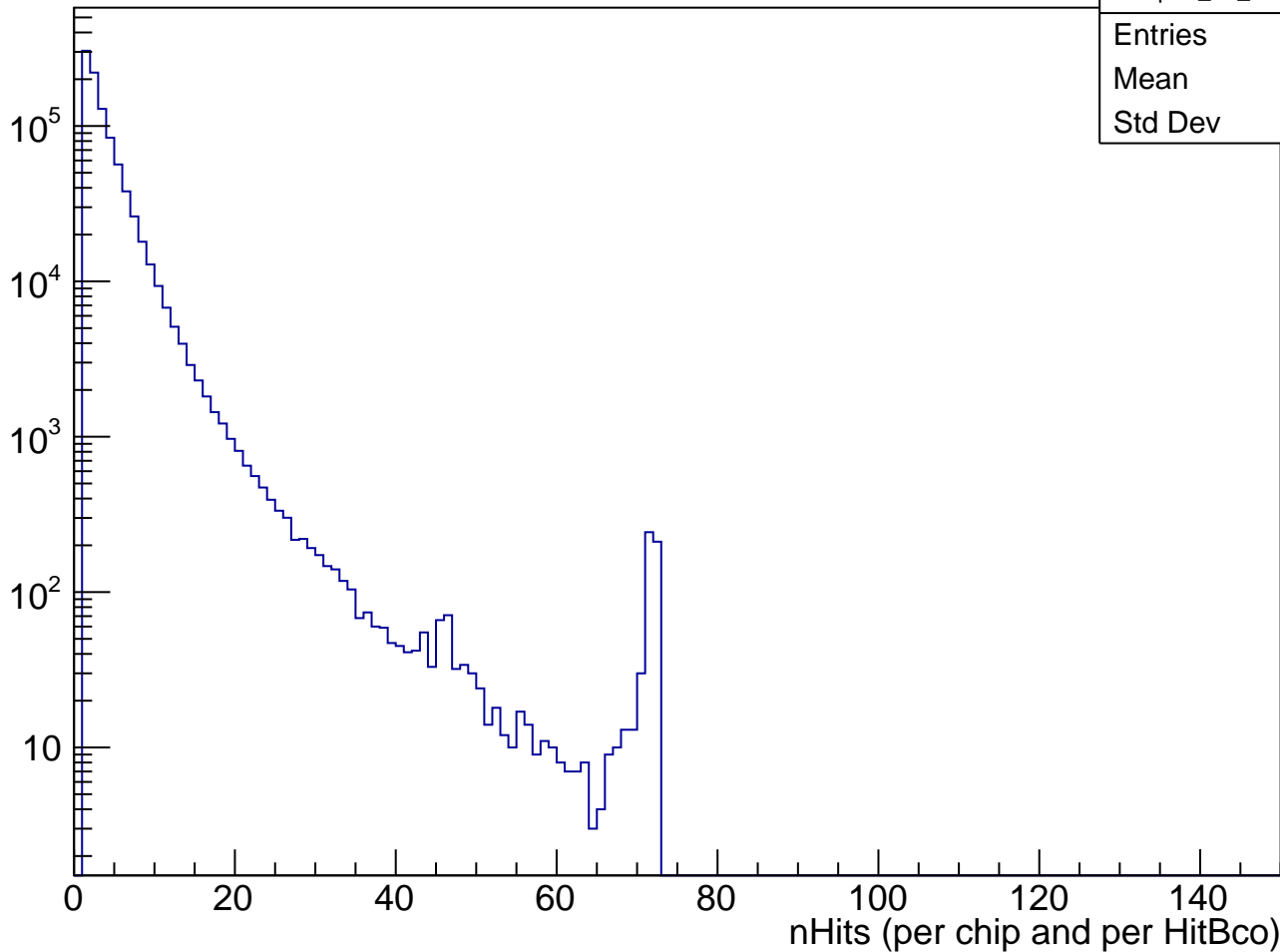
Entries 992429

Mean 3.643

Std Dev 4.1

nChipHit_F7_Fch9_Chip20

Entries

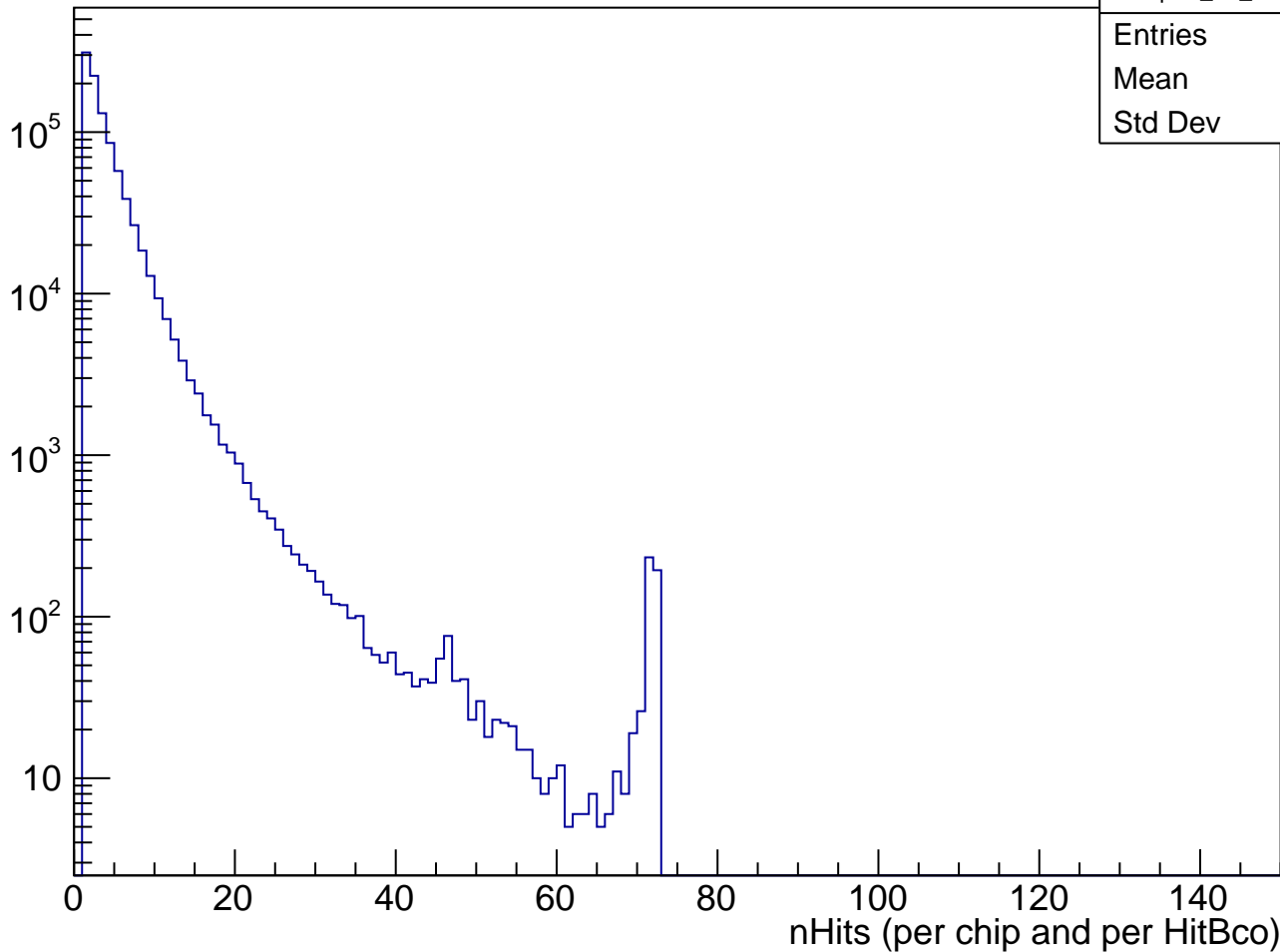


nChipHit_F7_Fch9_Chip20	
Entries	932277
Mean	3.292
Std Dev	3.761

nHits (per chip and per HitBco)

nChipHit_F7_Fch9_Chip21

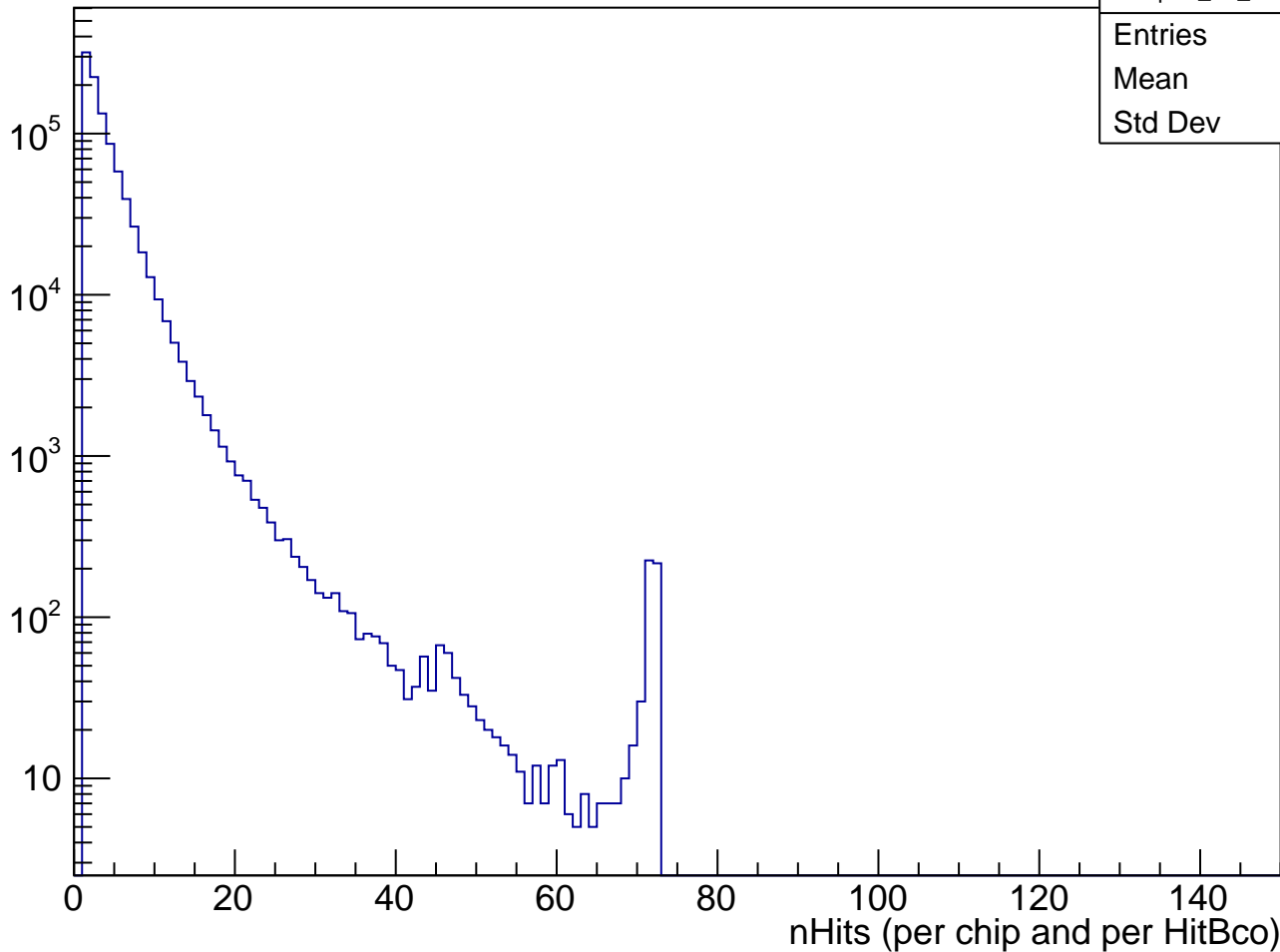
Entries



nChipHit_F7_Fch9_Chip21	
Entries	947575
Mean	3.285
Std Dev	3.738

nChipHit_F7_Fch9_Chip22

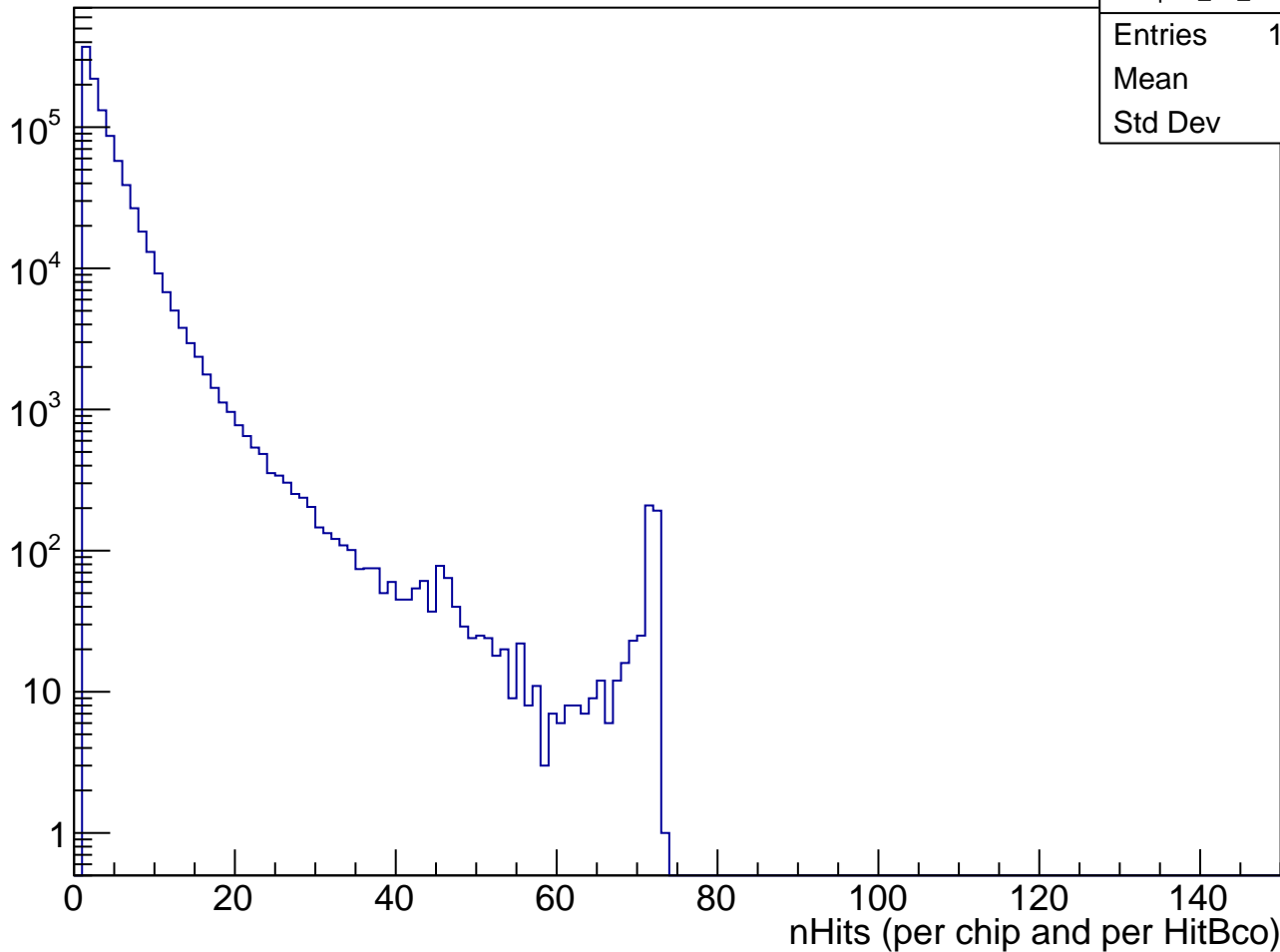
Entries



nChipHit_F7_Fch9_Chip22	
Entries	959880
Mean	3.258
Std Dev	3.703

nChipHit_F7_Fch9_Chip23

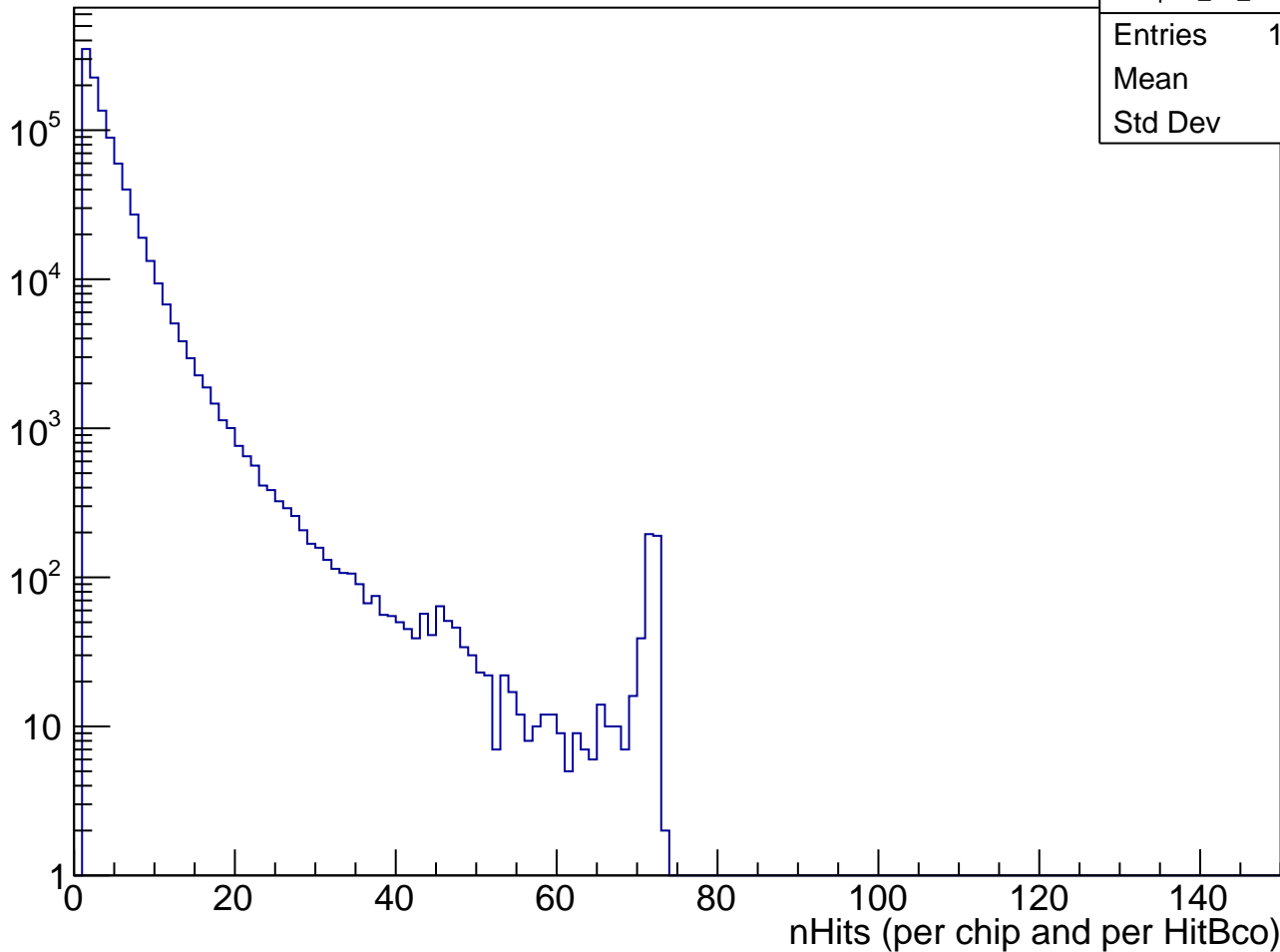
Entries



nChipHit_F7_Fch9_Chip23	
Entries	1005598
Mean	3.145
Std Dev	3.646

nChipHit_F7_Fch9_Chip24

Entries



nChipHit_F7_Fch9_Chip24

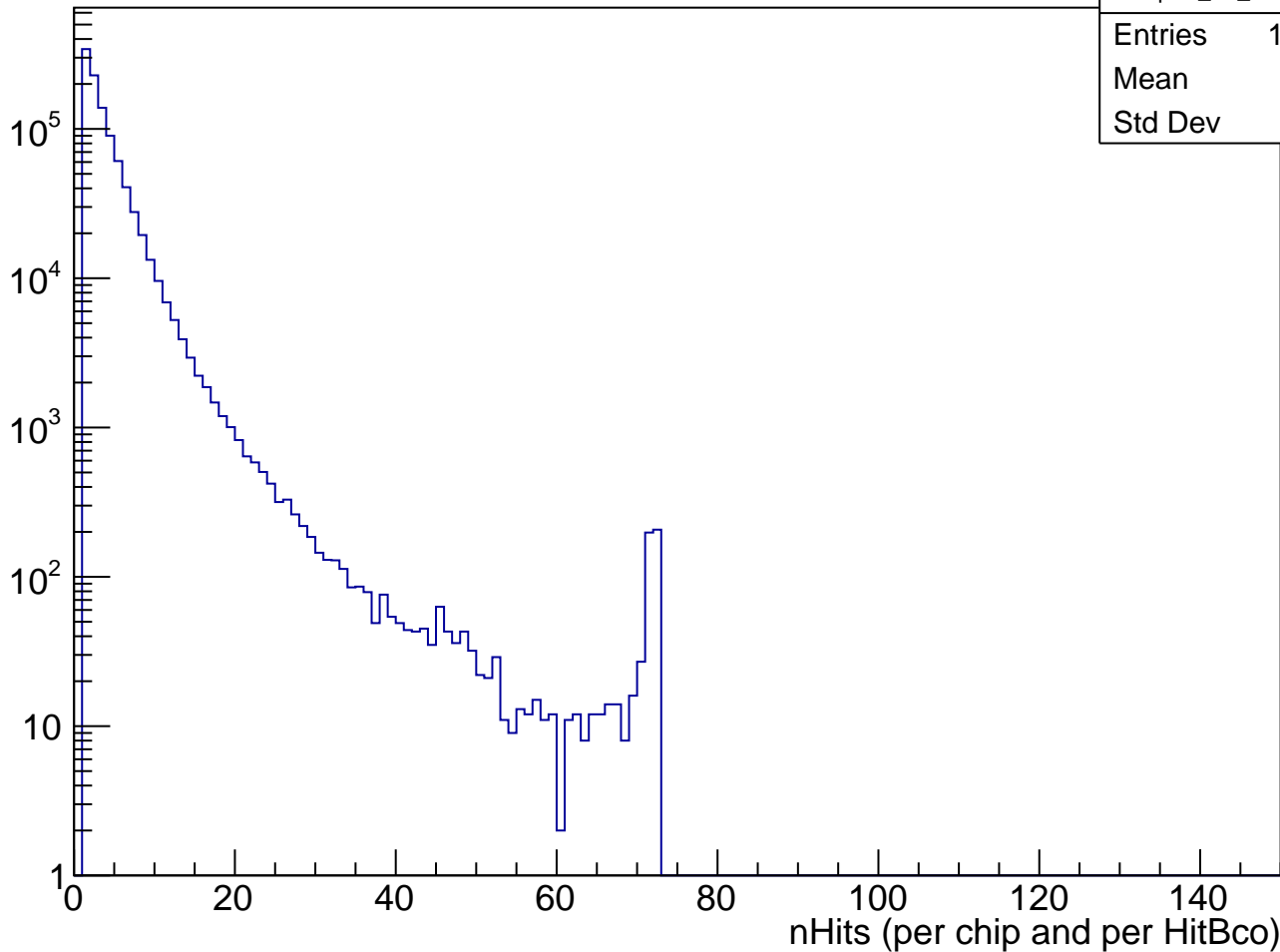
Entries 1001265

Mean 3.198

Std Dev 3.635

nChipHit_F7_Fch9_Chip25

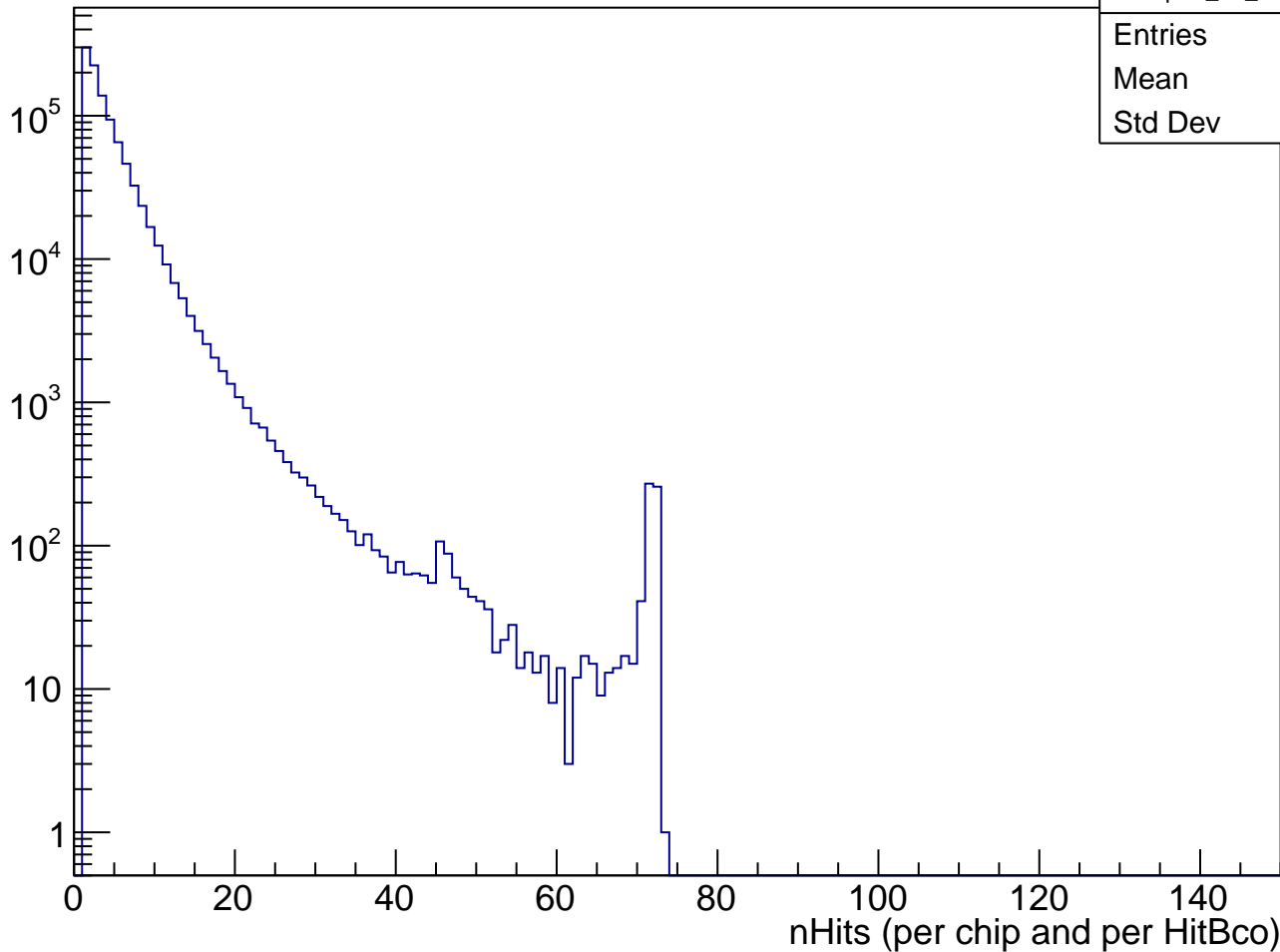
Entries



nChipHit_F7_Fch9_Chip25	
Entries	1004436
Mean	3.232
Std Dev	3.659

nChipHit_F7_Fch9_Chip3

Entries



nChipHit_F7_Fch9_Chip3

Entries 997898

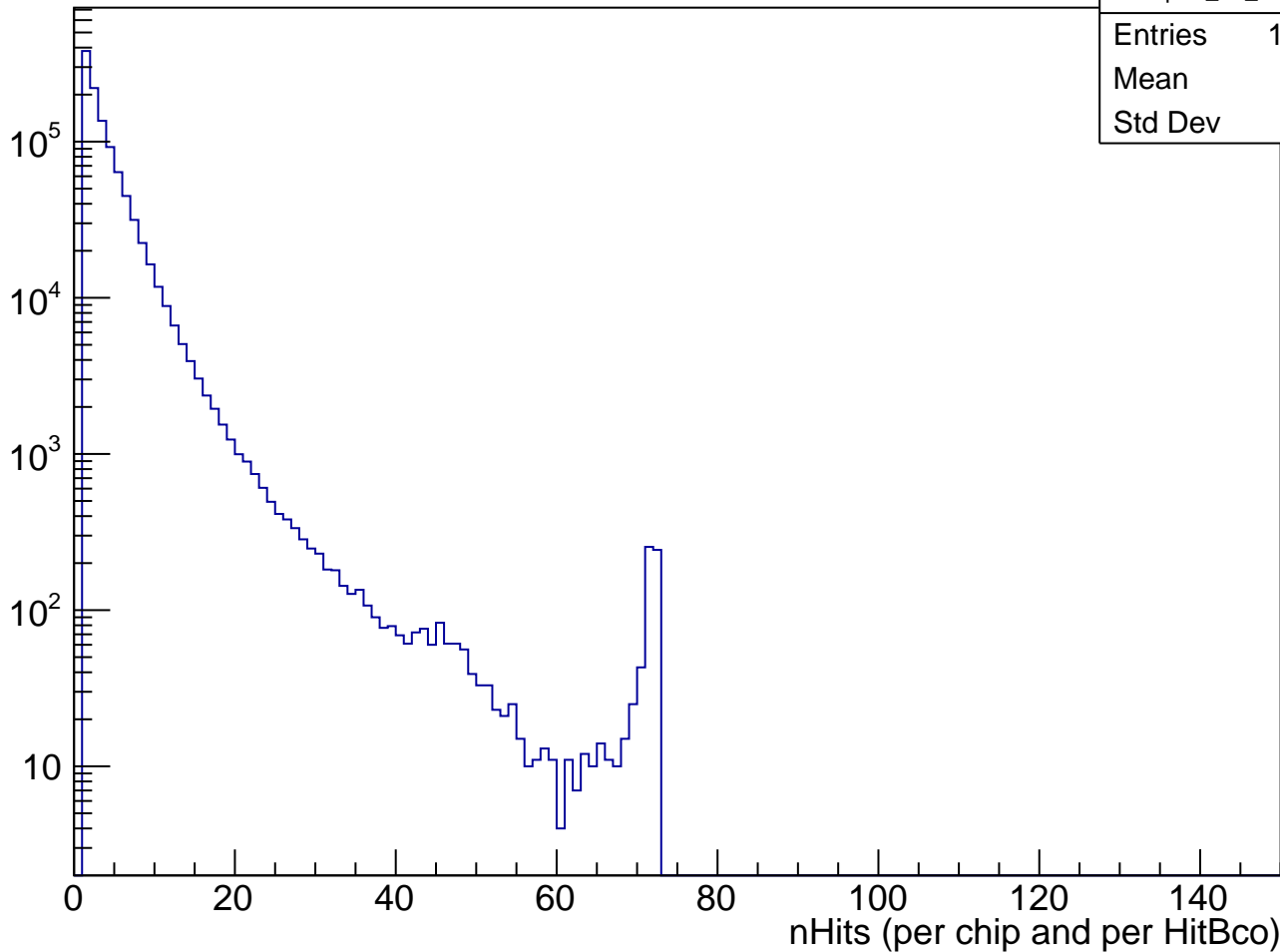
Mean 3.593

Std Dev 4.099

nHits (per chip and per HitBco)

nChipHit_F7_Fch9_Chip4

Entries



nChipHit_F7_Fch9_Chip4

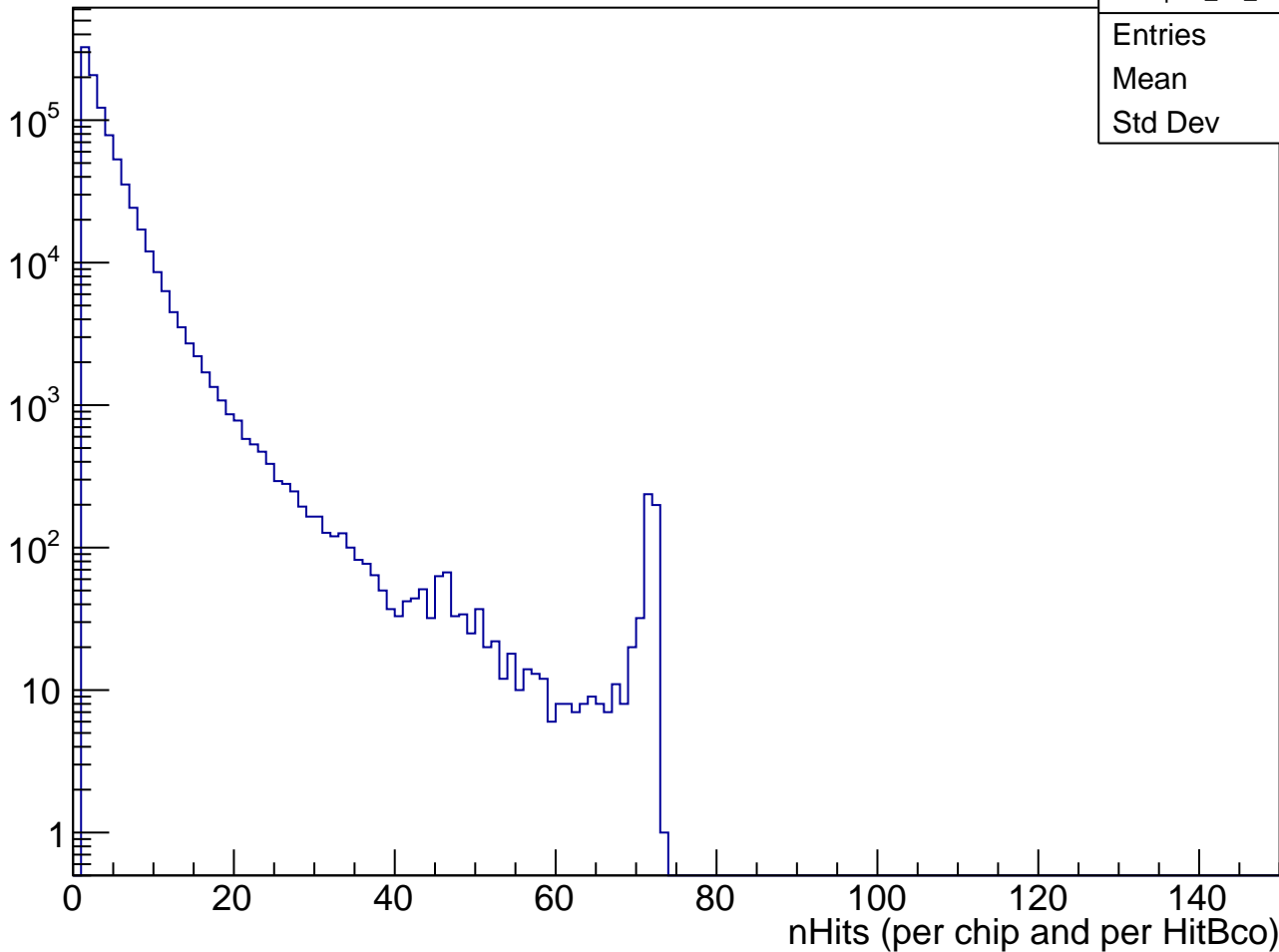
Entries 1062765

Mean 3.362

Std Dev 3.957

nChipHit_F7_Fch9_Chip5

Entries

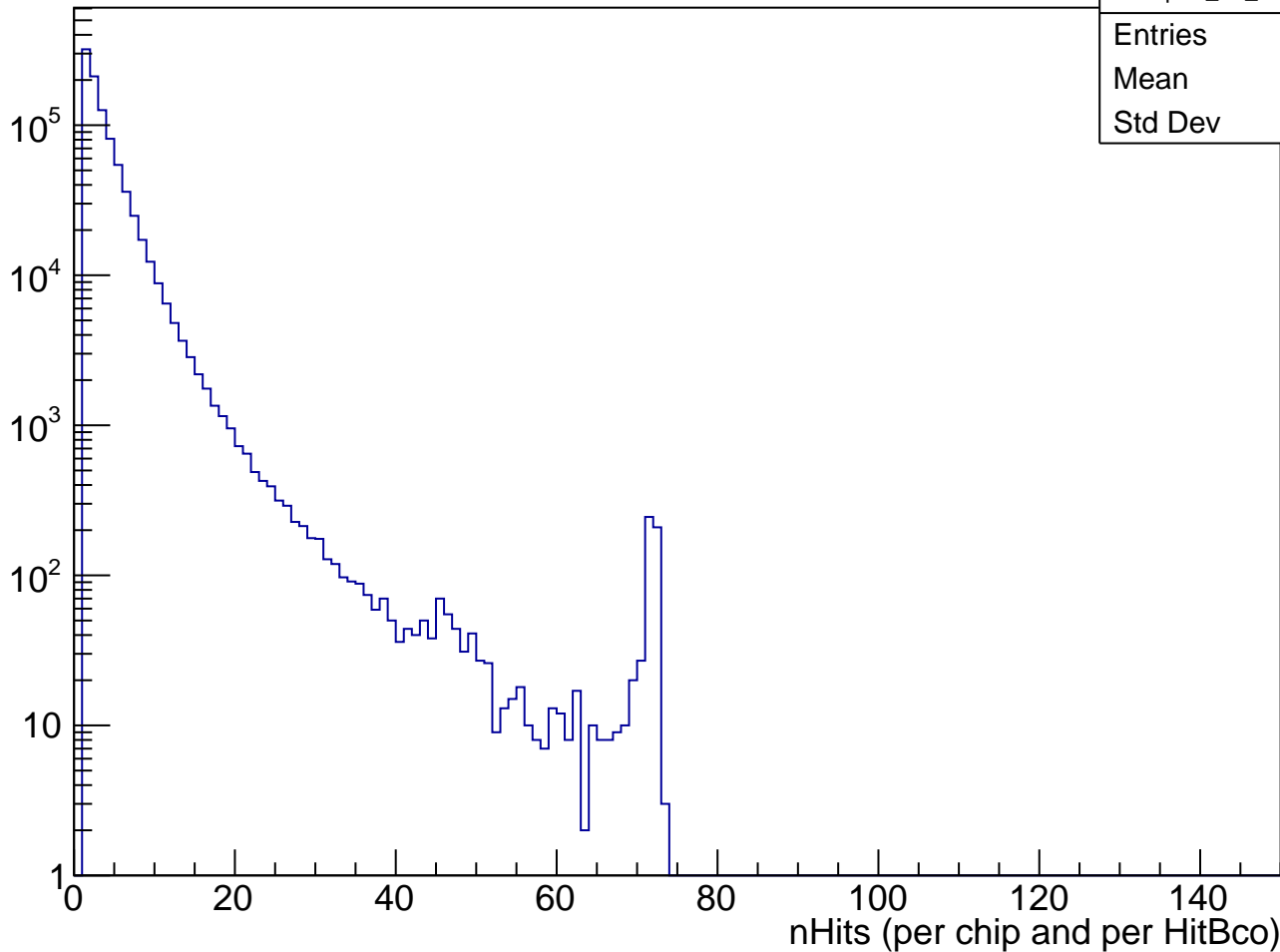


nChipHit_F7_Fch9_Chip5	
Entries	913485
Mean	3.189
Std Dev	3.735

nHits (per chip and per HitBco)

nChipHit_F7_Fch9_Chip6

Entries



nChipHit_F7_Fch9_Chip6

Entries 923847

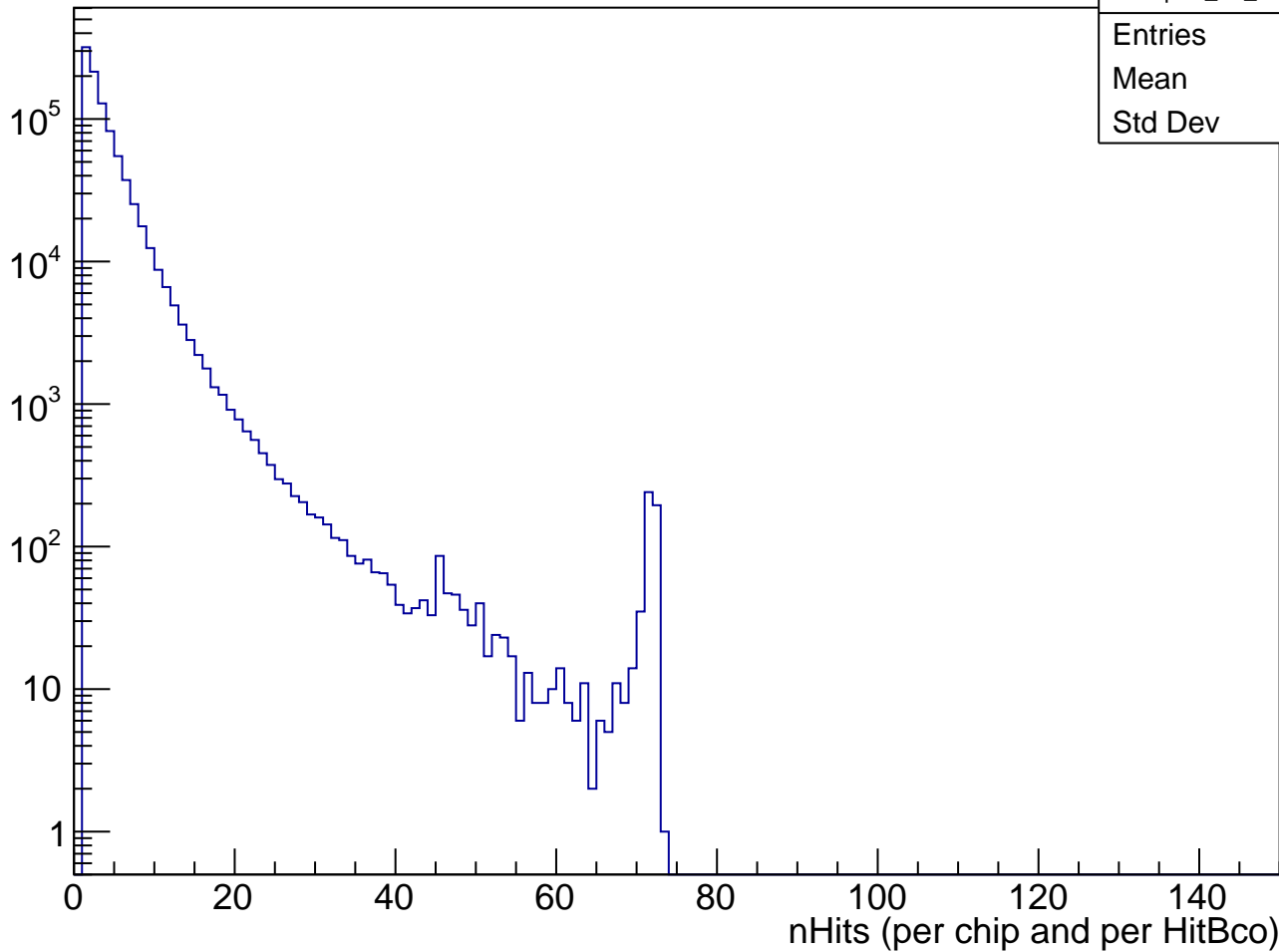
Mean 3.221

Std Dev 3.752

nHits (per chip and per HitBco)

nChipHit_F7_Fch9_Chip7

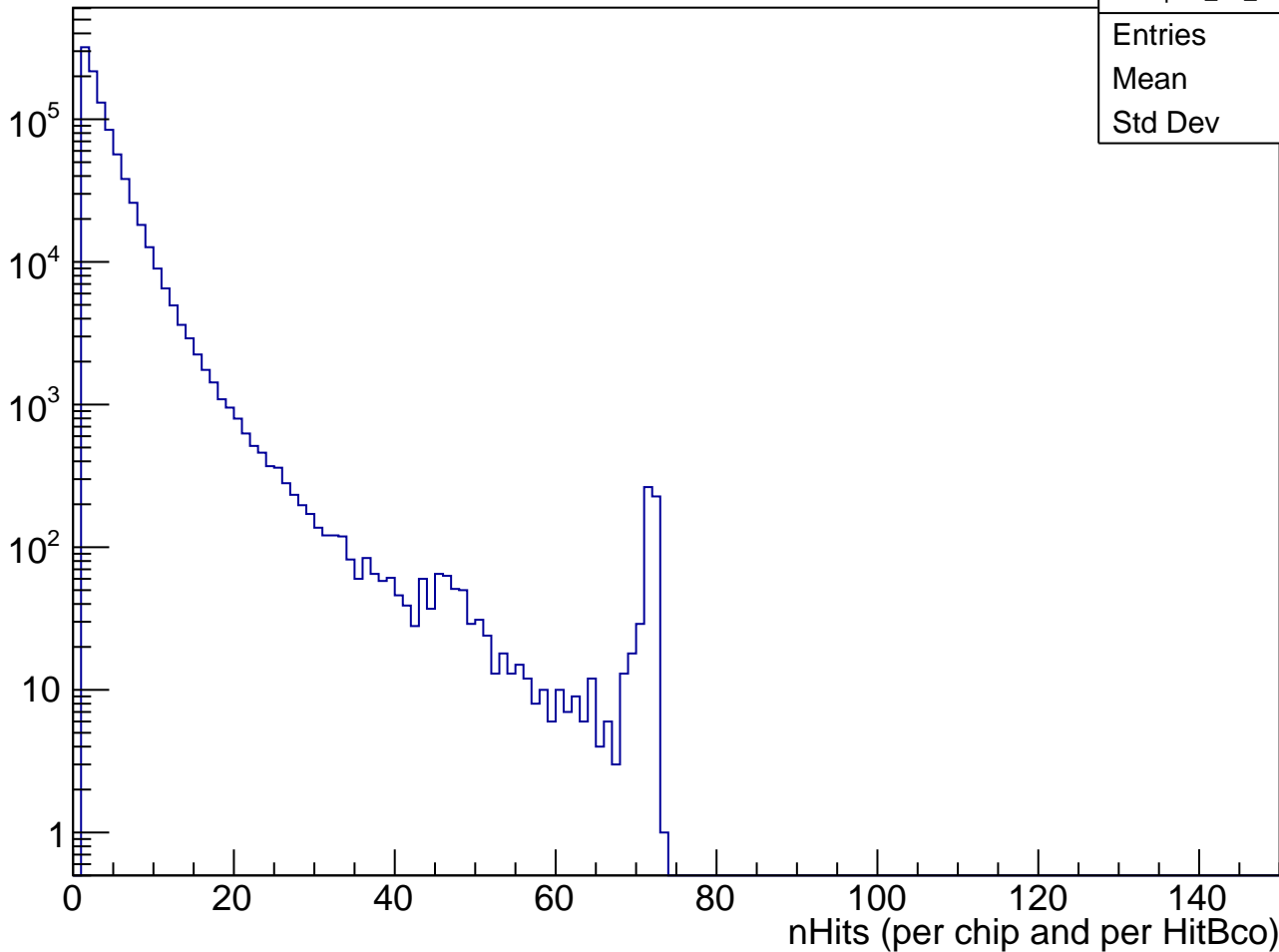
Entries



nChipHit_F7_Fch9_Chip7	
Entries	932403
Mean	3.229
Std Dev	3.723

nChipHit_F7_Fch9_Chip8

Entries



nChipHit_F7_Fch9_Chip8

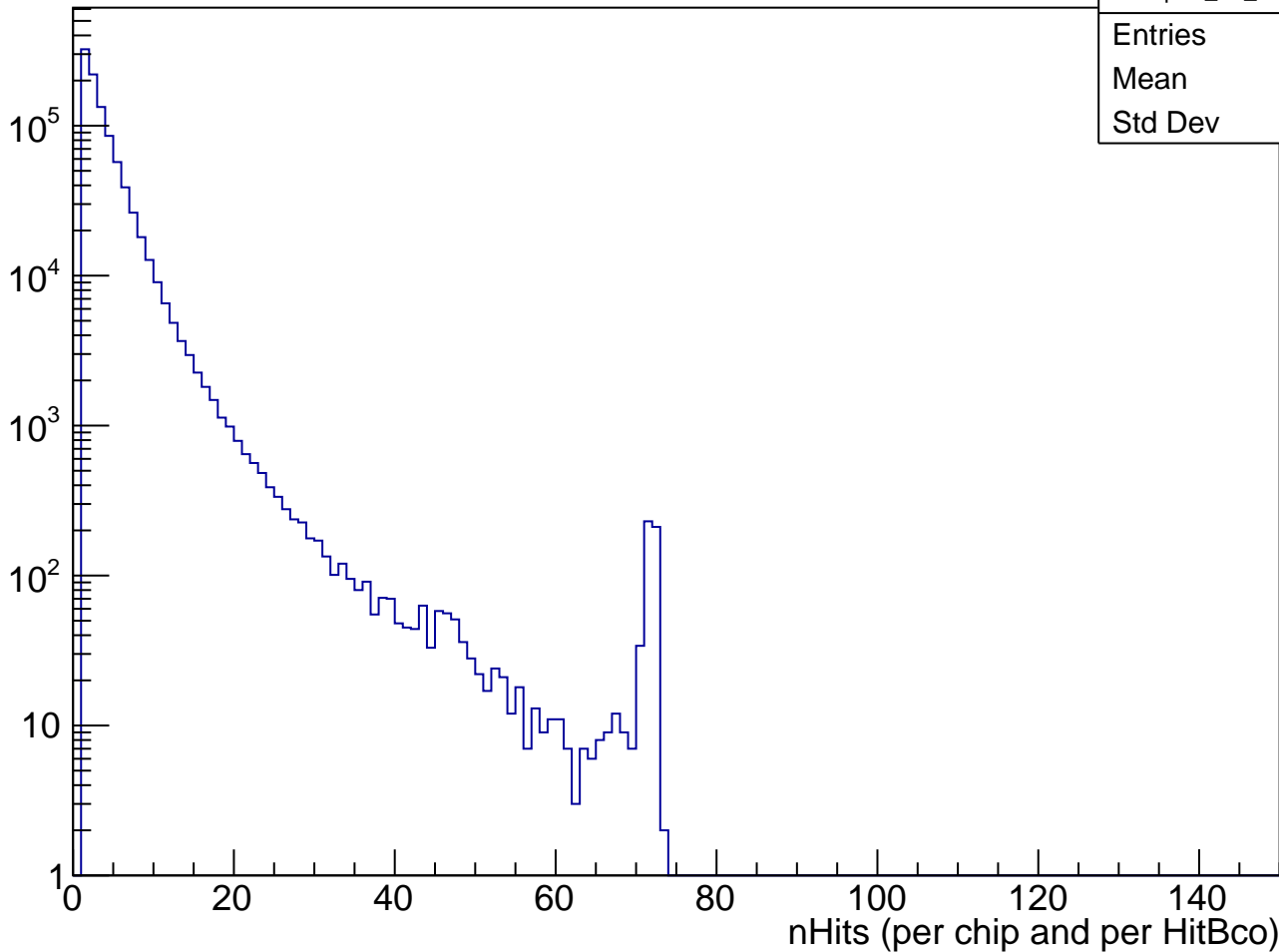
Entries 944223

Mean 3.246

Std Dev 3.749

nChipHit_F7_Fch9_Chip9

Entries



nChipHit_F7_Fch9_Chip9

Entries	955935
Mean	3.243
Std Dev	3.719

nHits (per chip and per HitBco)

