



FCFD design status

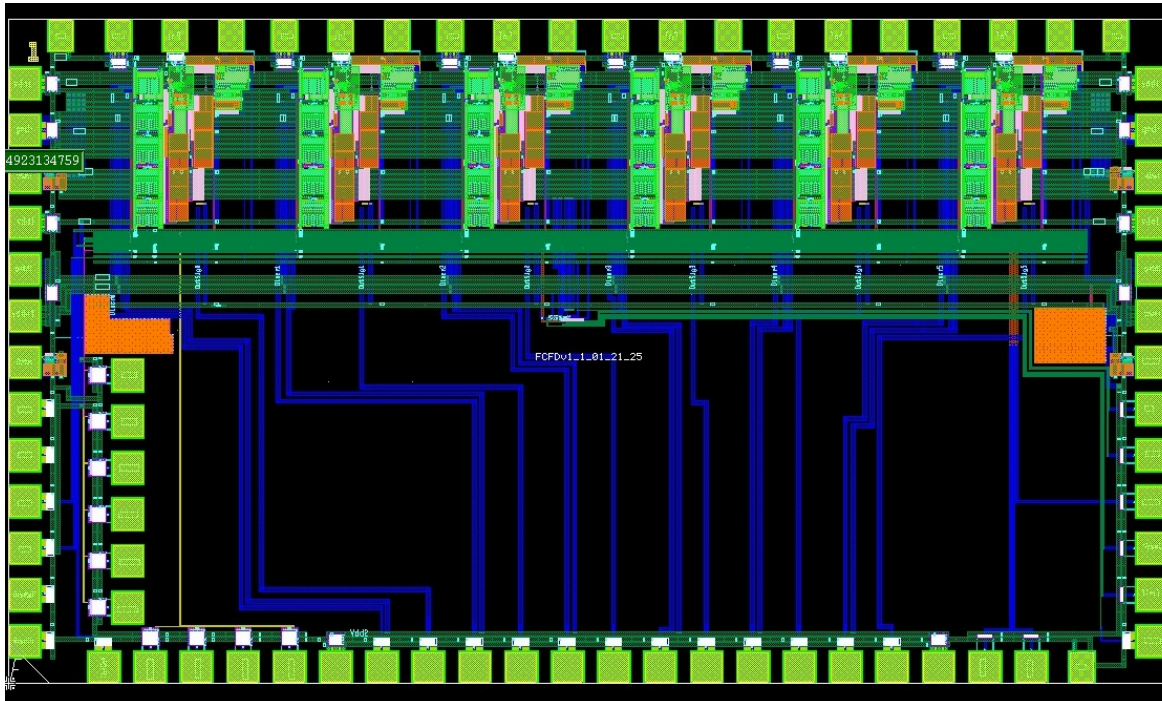
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EPIC Electronics & DAQ WG meeting : eRD109 Monthly Progress Reports

Feb 6, 2025

FCFDv1.1 design status

- The design is completed now finalizing the layout
 - Tape out on Feb 19, expected shipment date May 11
- First iteration of DRC checks performed by IMEC, will resubmit the next version for review by the end of this week



Next steps

- Preparations for testing with laser & β -source in the new lab
 - Purchased a new IR-laser, and moved to a new dedicated setup in IERC
 - Testing of FCFDv1.0 will be performed in the coming weeks to validate the setup, and complete the characterization of the chip with larger signals
 - Test beam is reserved in July in DESY, since no test beams in FNAL this year