ESB & DAQ

Progress Report

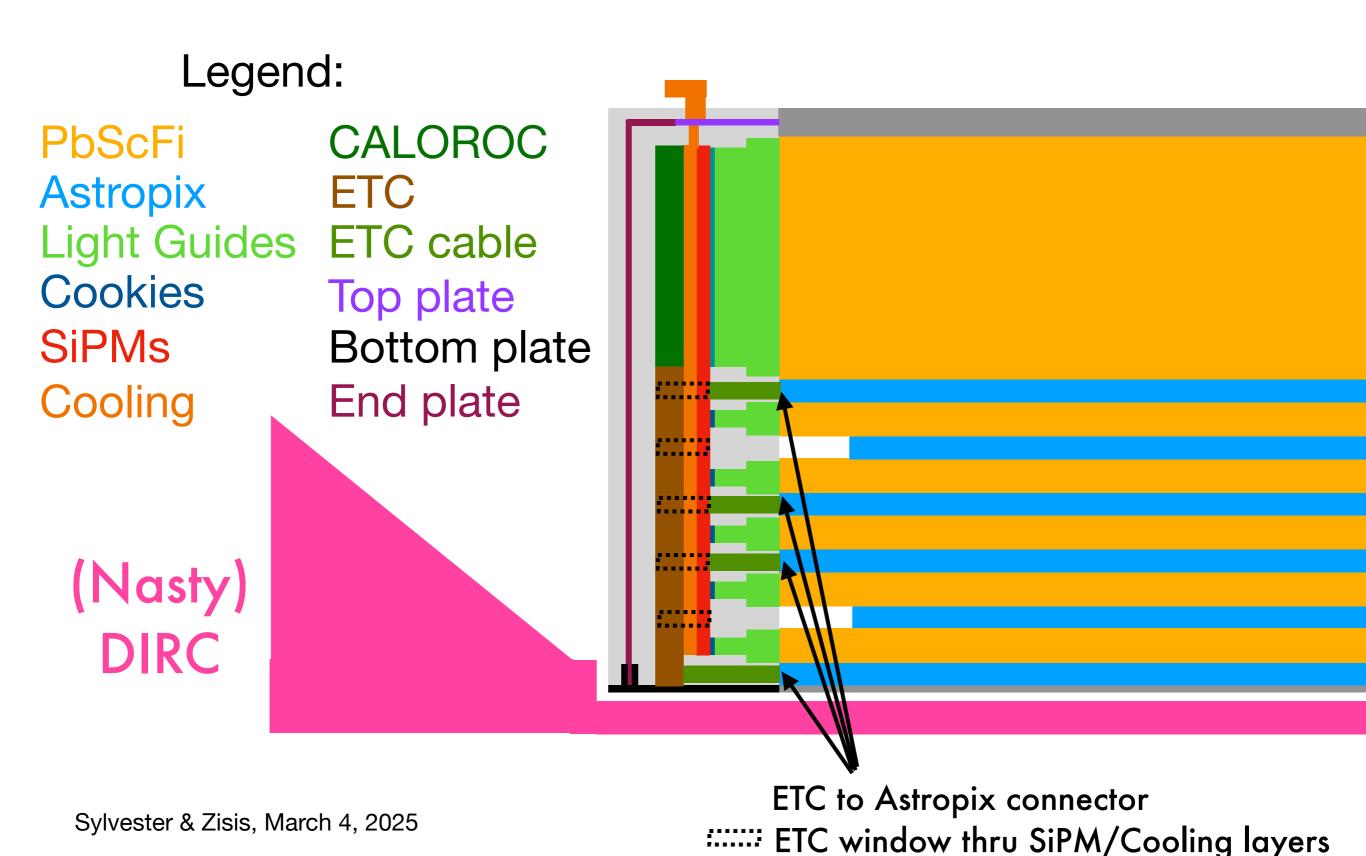
Z. Papandreou BIC General Meeting March 14, 2025

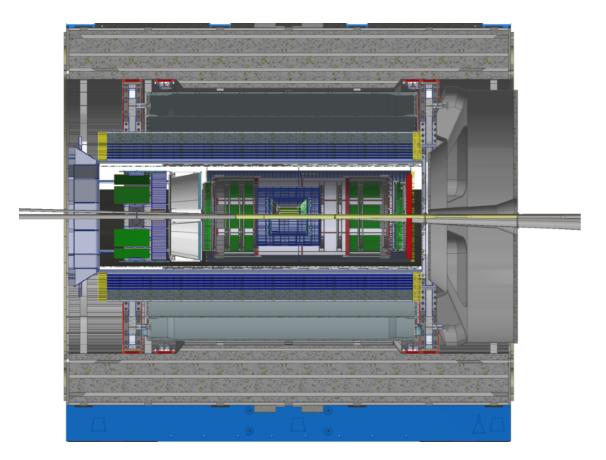


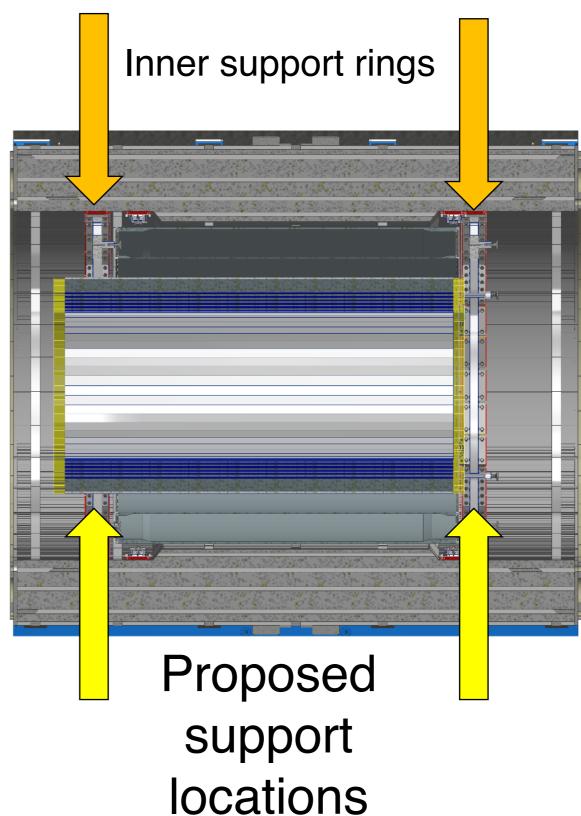


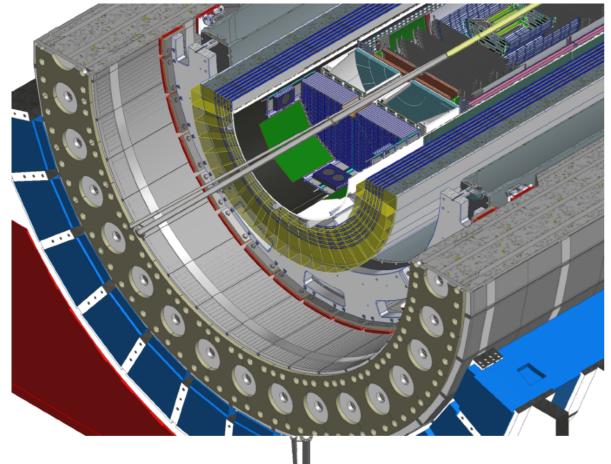


ESB Napkin Sketch - Electron End

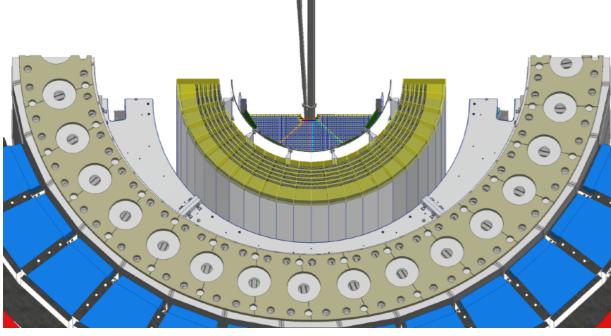


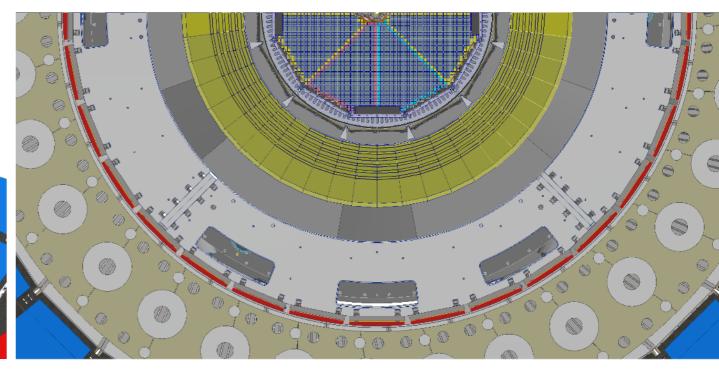




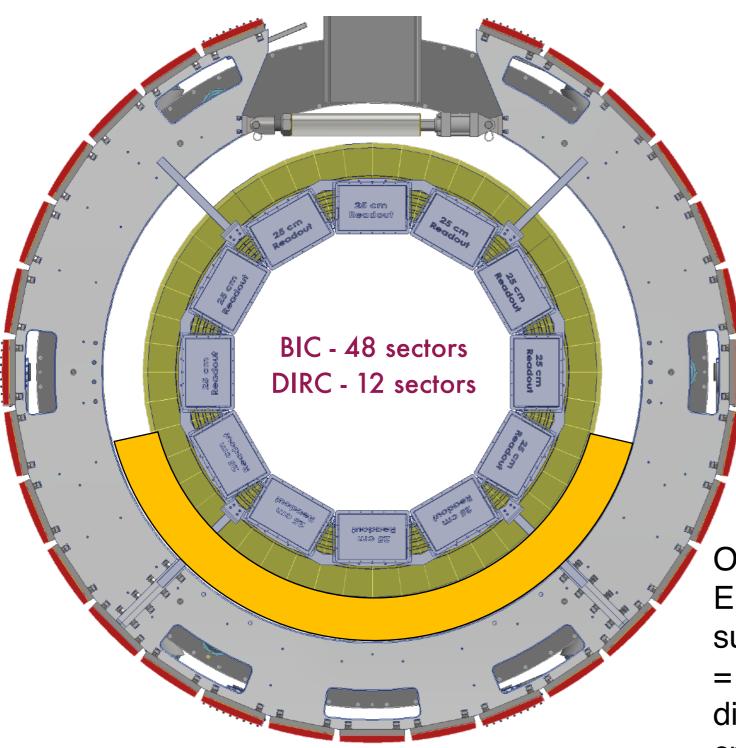


Backward end views



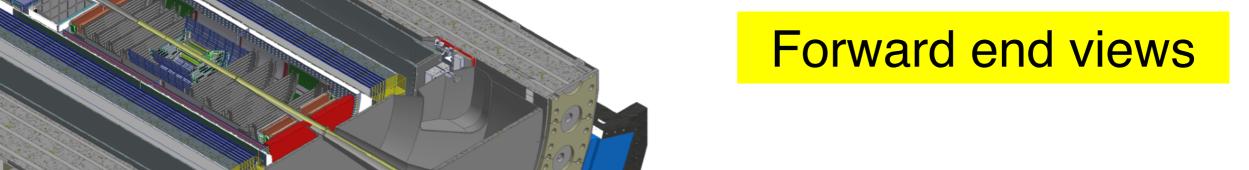


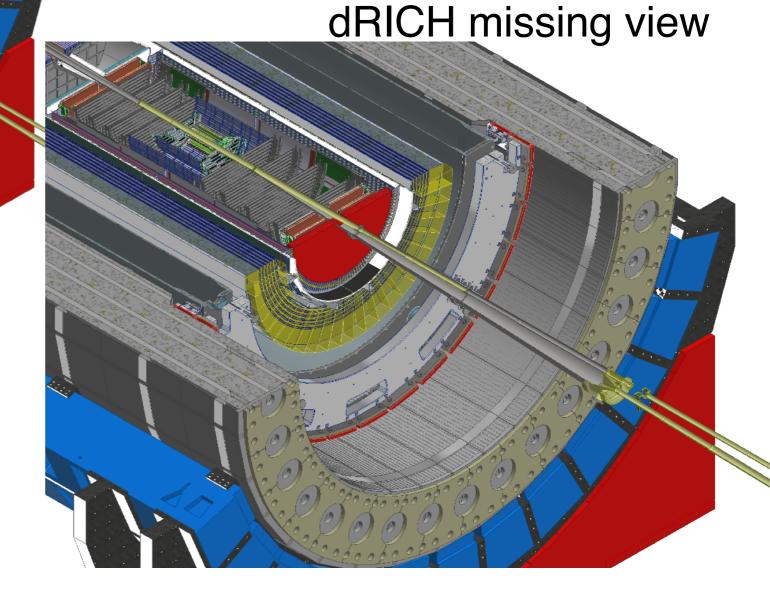
Roland Wimmer, BNL, February 28, 2025

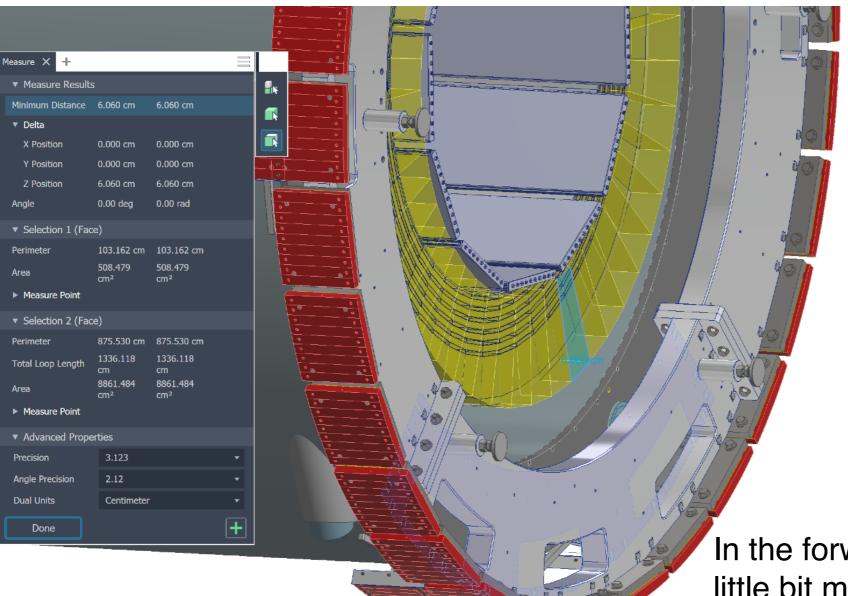


Orange is proposed support location. The EMCAL supports would sit on top of the inner support rings. The inner support rings sit at Z = -217.17 cm to -227.33 cm in the backward direction and Z = 194.310 cm to Z = 204.470 cm in the forward direction. In the backward direction the support will be simple and will support the EMCAL directly from the inner support rings. DIRC is an issue.

Roland Wimmer, BNL, February 28, 2025

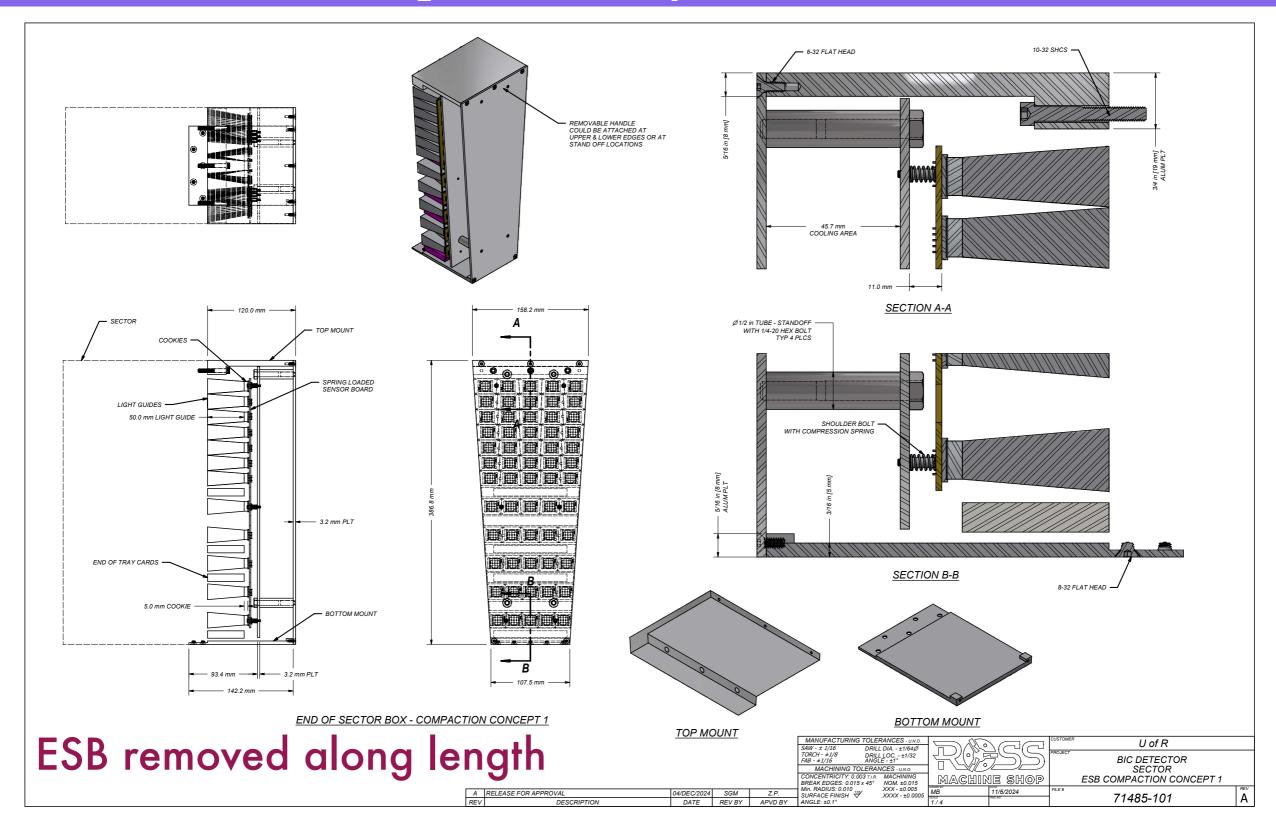






In the forward direction the support will be a little bit more complicated due to the support ring being past the EMCAL in Z. Moving the electronics outward radially may not be possible.

ESB Design - Concept 1 - Hadron End

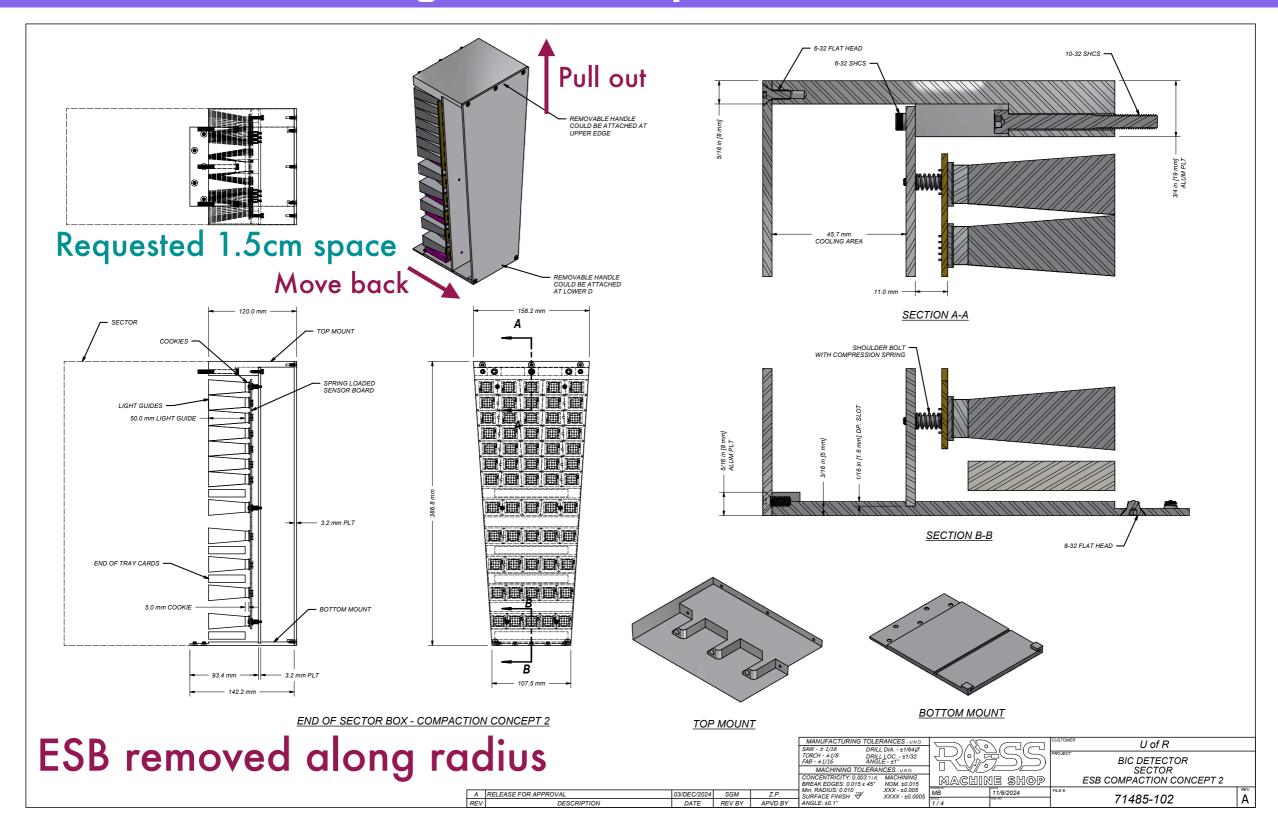








ESB Design - Concept 2: Electron End









M&S

- New tooling received by RMS.
 - 10 new LGs will be made next, and compared to those for SFILs
- New order will be placed to Eljen for 3-mm and 2-mm thick cookie sheets.
- Waiting for Hamamatsu quote on 130 S14 SiPMs.



Different degrees of polish



New diamond-carbide cutter

ESB Design - Cooling

- Manitoba will look at: a) thermal conduction of Astropix through the lead SciFi and b) ESB cooling for SiPM stability and ETC and CALOROC heat removal.
- Use thermal pad or metal-core PCB for implementation of a single copper line.
- Forced air in the ESB?
- Ross Machine Shop can build the cooling lines. Assuming a cooling snake that runs by all the SiPMs, we could fit a 1-cm-diameter copper line between SiPM rows.
 - NASA: roughly 1 W per ETC card (the FPGA basically), so 4 W per box;
 Connector at the end of each the Astropix tray, then we can use cables to go
 to the ETC cards which could sit in the volume under the CALOROC and
 receive heat removal from the pad or metal-core PCB.
 - Aram says that assuming his SiPM summing and amplification, 8 W total per ESB box.
 - HGCROC chip: 4W each and operate at 40 C without cooling (from interface document)







BIC in-person meeting: ESB session April 10, 2025

12:00	Lunch	
13:00	D172, Building 241	12:00 - 13:30
	ESB Design and Interfaces	Zisis Papandreou
	D172, Building 241	13:30 - 13:50
	ESB Cooling	
	D172, Building 241	13:50 - 14:00
14:00	HGCROC Readout	Norbert Novitzky
	D172, Building 241	14:00 - 14:20
	ETC Design Interfaces	
	D172, Building 241	14:20 - 14:40
	ESB & DAQ PDR and future plans (deliverables)	
	D172, Building 241	14:40 - 15:00
15:00	Break	
	D172, Building 241	15:00 - 15:30







Summary

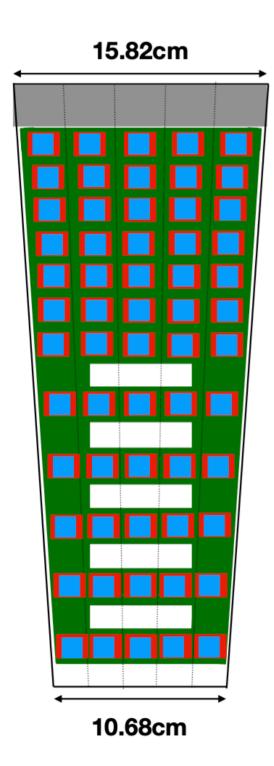
- **ESB Bi-weekly meetings, 8am CDT** (alternating weeks with System Testing at same time slot)
- ESB design advancing but lots to do:
 - ESB-BabyBCAL (Apr 2025), ESB-BIC (Nov 2025)
 - CALOROC: ORNL
 - ETCs: NASA/ANL/Regina
 - Cooling: Manitoba/Regina
- Merlin Project Schedule, Spreadsheets, Cooling
- R&D day April 16-17 / Project Review August



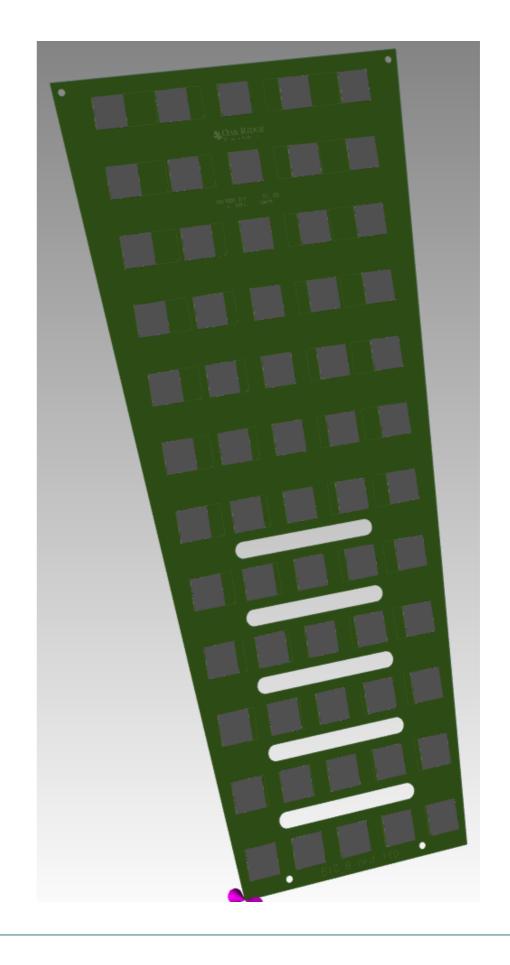




Backup slides



Testing at ORNL

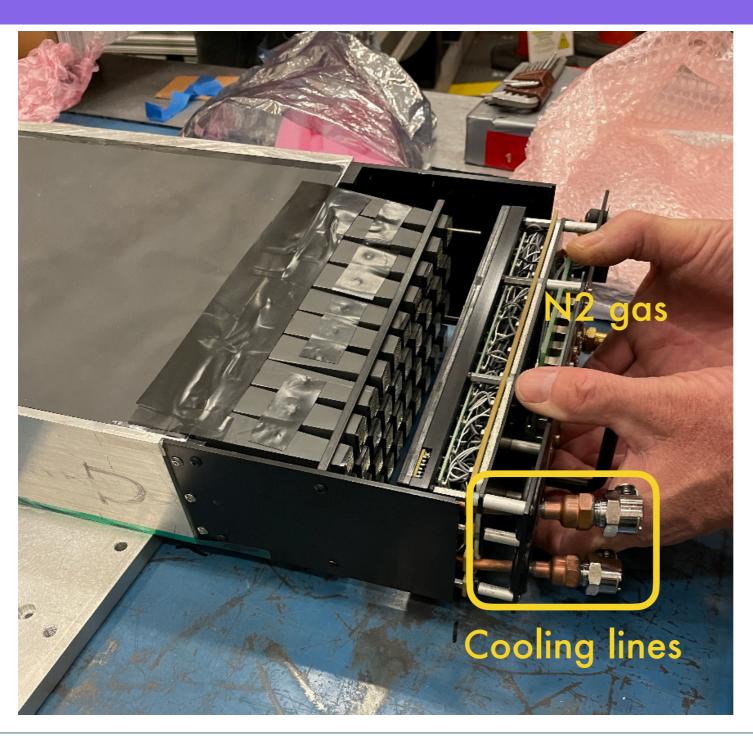








BCAL "Wedges"

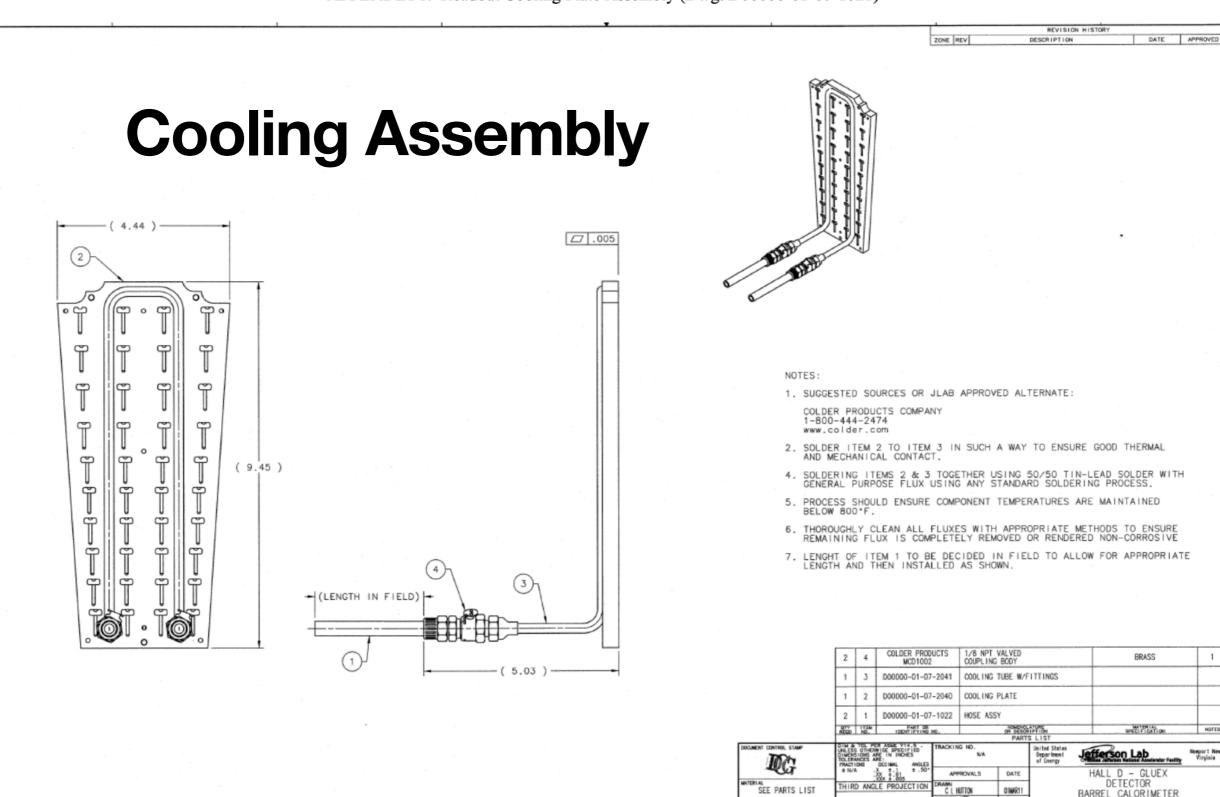


- Contiguous LG stacking
- LG-SiPM air gap
- BIC: Si cookie
- Spring loading
- Integrated ESB for installation ease













COOLING PLATE ASSY

DO NOT SCALE DRAWING