

Rachid N. for the INTT Group

February 13th, 2025



After Run-24 shutdown, INTT detector has been tested several times in preparation for the Run-25

First test January 24th, 2025:

- **Issue:** four ROCs were not working properly (high currents)
- **Fixed:** three ROCs, the issue was related to the LV Distributor software upgrade “readback” (fixed by Steve B.)

Second test January 29th, 2025:

- **Issue:** one ROC (RC-2N) was not working properly
- **Fixed:** we accessed to the bore-end, and we found the clock cable was not connected, easy fix we put it back
- At the end of the day, we took good pedestals data

Third test February 10th, 2025: this test was done after many activities achieved successfully at 1008 such as shutdown all racks power, installation of Ups power supplies, Rob P. and his team installed two new units of INTT cooling system etc...

- **Issue:** Felix intt2 machine was not working
- **Fixed:** Raul replaced Felix intt2 and he programed all Felix to be ready for Run-25
- **Detector and all ROCs were working properly**
- Comparison of pedestal data Run-25 vs Run 24 are presented in the next slides

Conclusion: is INTT detector ready?
Answer: YES!

INTT personal power will start to arrive to BNL on March in preparation for the commissioning

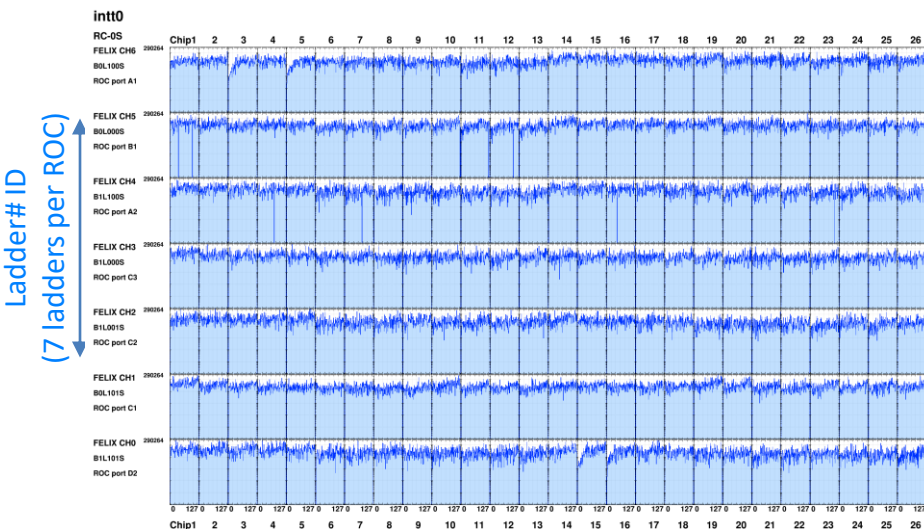
INTT South Side: 8 ROCs Run-25 vs Run-24

❖ Virtual Meeting → Virtual cake



February 10th, 2025

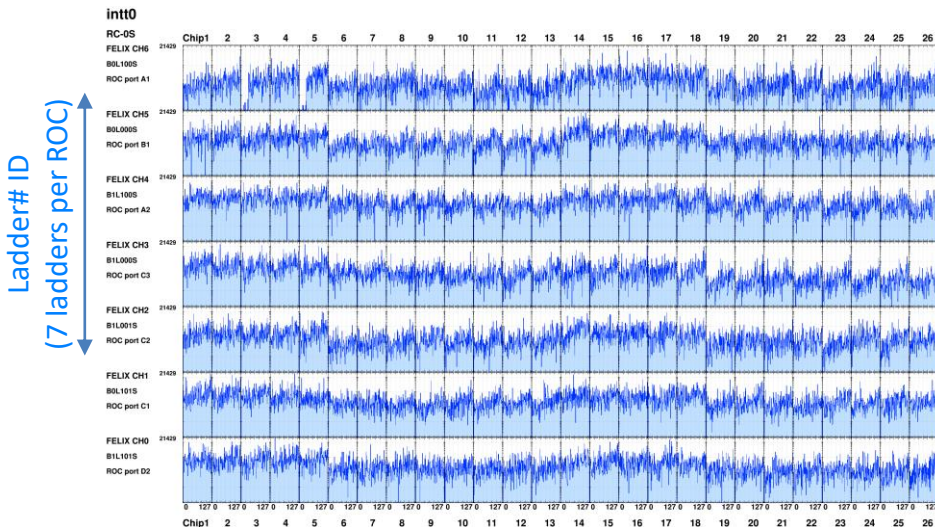
Chips (26 chips per ladder)



Pedestal Data
Run# 00056523
(DAC0=8)

Run-24

Chips (26 chips per ladder)

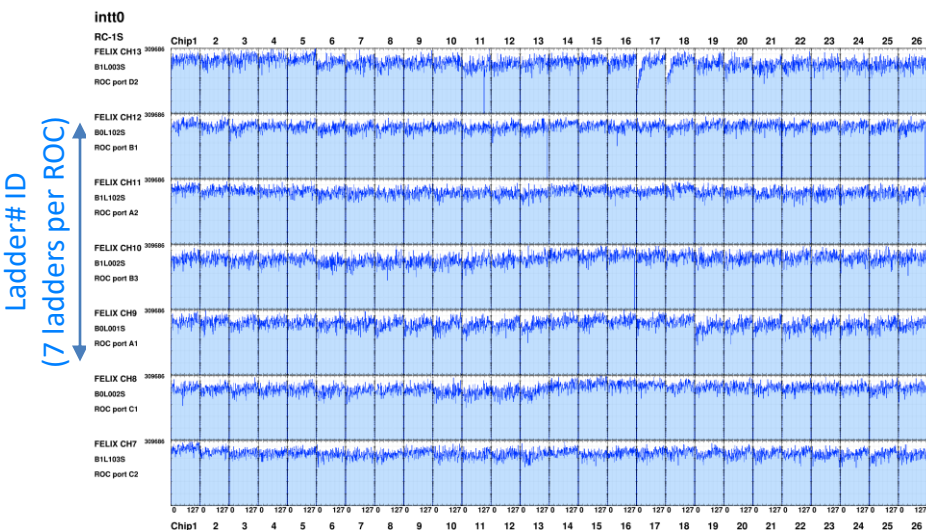


Pedestal Data
Run# 00033731
(DAC0=10)

Run-25

February 10th, 2025

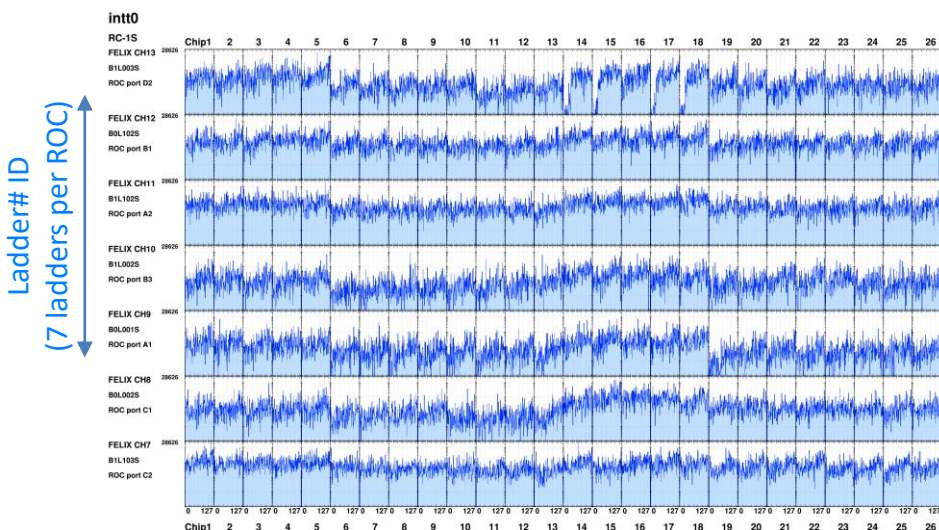
Chips (26 chips per ladder)



Pedestal Data
Run# 00056523
(DAC0=8)

Run-24

Chips (26 chips per ladder)



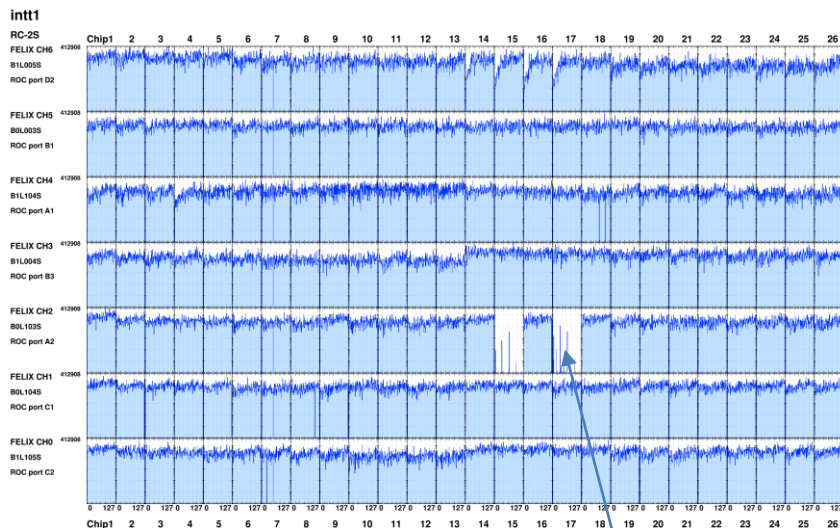
Pedestal Data
Run# 00033731
(DAC0=10)

Run-25

February 10th, 2025

Chips (26 chips per ladder)

Ladder# ID
(7 ladders per ROC)



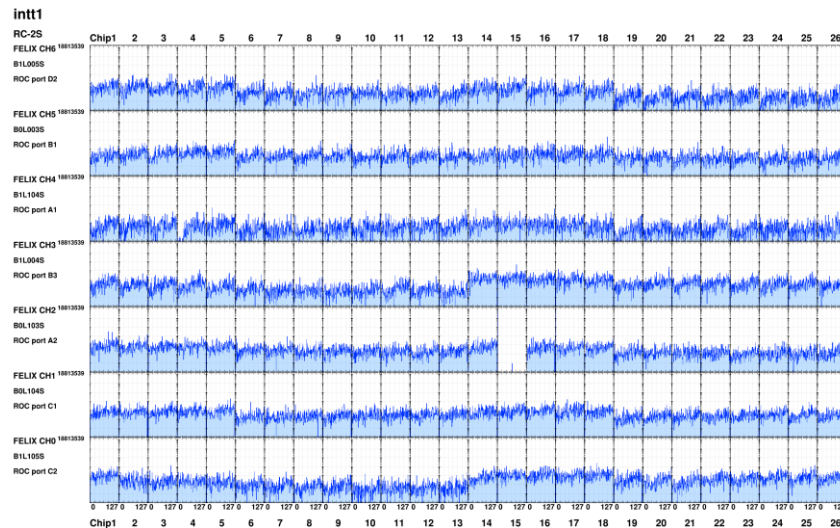
Pedestal Data
Run# 00056523
(DAC0=8)

One chip was masked
during run-24 (because
one (two) hot channel(s))
to ensure stability of rcdaq

Run-24

Chips (26 chips per ladder)

Ladder# ID
(7 ladders per ROC)



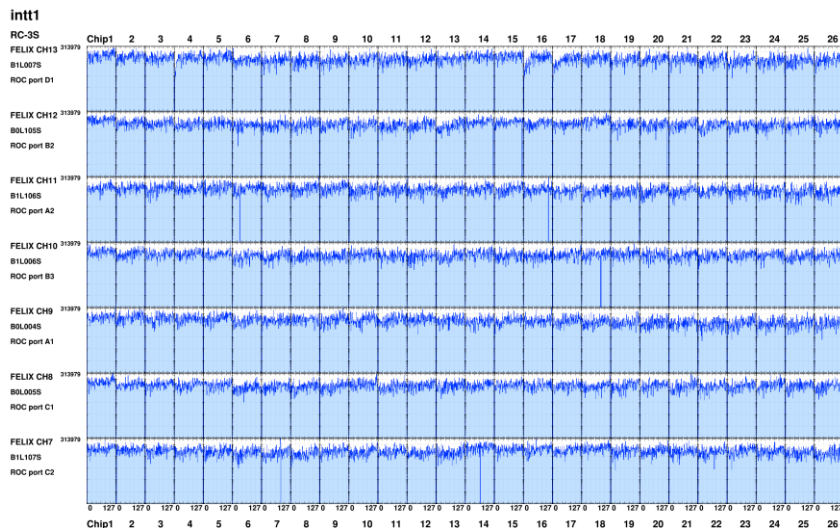
Pedestal Data
Run# 00033731
(DAC0=10)

Run-25

February 10th, 2025

Chips (26 chips per ladder)

Ladder# ID
(7 ladders per ROC)

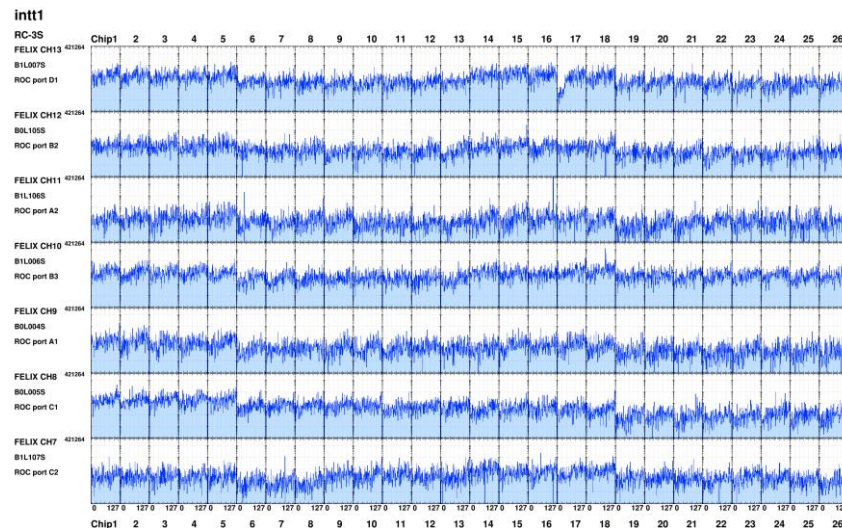


Pedestal Data
Run# 00056523
(DAC0=8)

Run-24

Chips (26 chips per ladder)

Ladder# ID
(7 ladders per ROC)



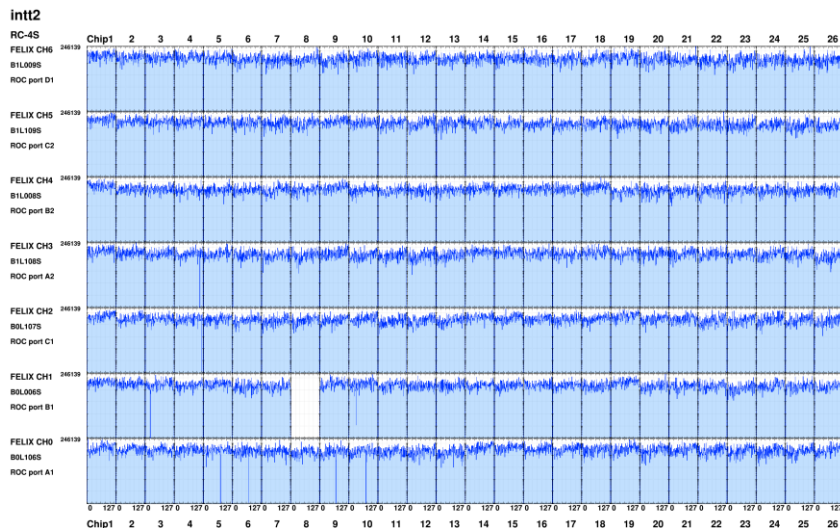
Pedestal Data
Run# 00033731
(DAC0=10)

Run-25

February 10th, 2025

Chips (26 chips per ladder)

Ladder# ID
(7 ladders per ROC)

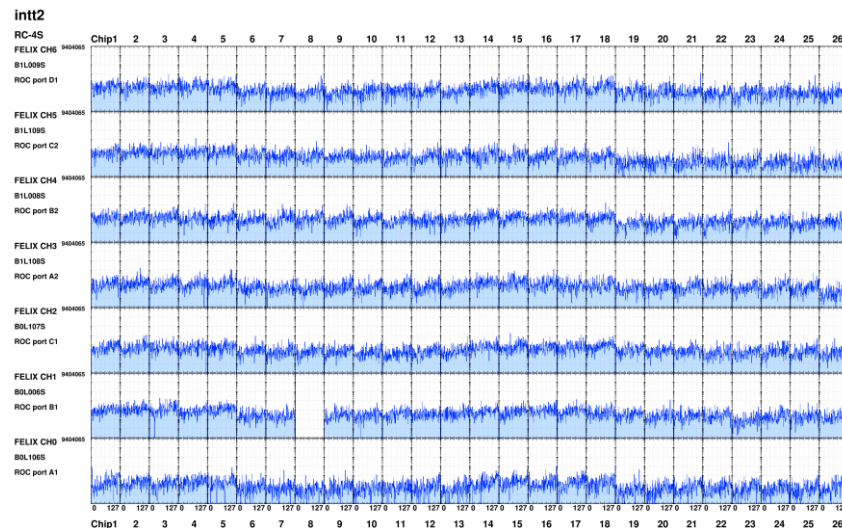


Pedestal Data
Run# 00056523
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Run-24

Chips (26 chips per ladder)

Ladder# ID
(7 ladders per ROC)



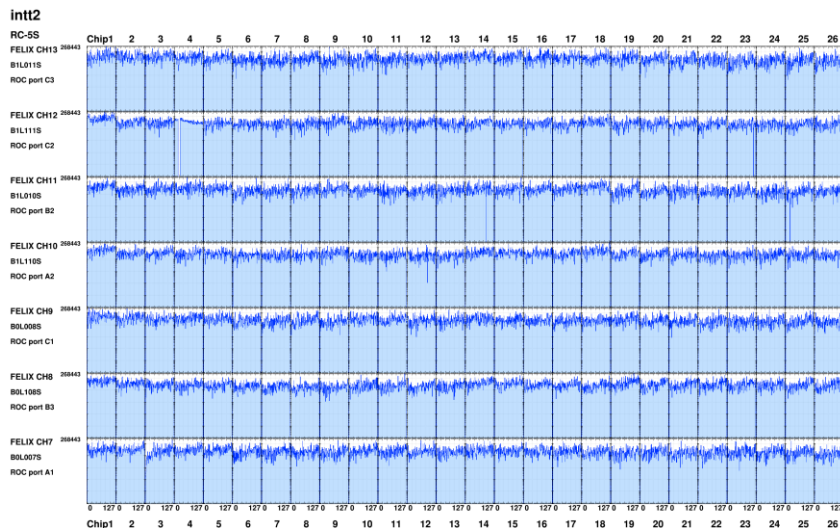
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February 10th, 2025

Chips (26 chips per ladder)

Ladder# ID
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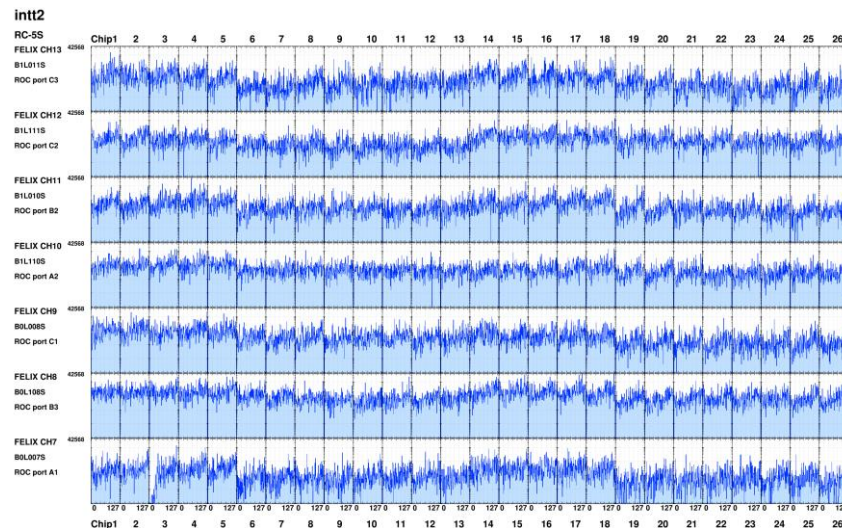


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Run-24

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February 10th, 2025



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Chips (26 chips per ladder)

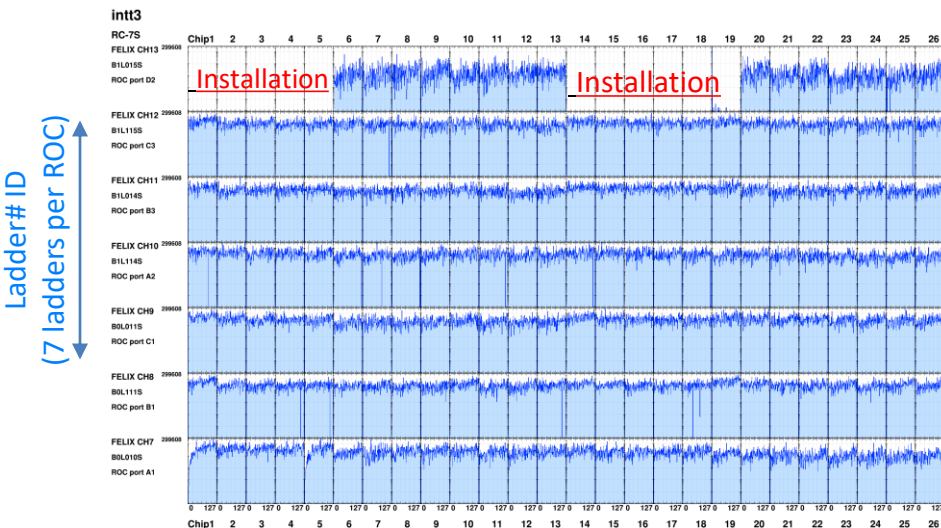


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Run-25

February 10th, 2025

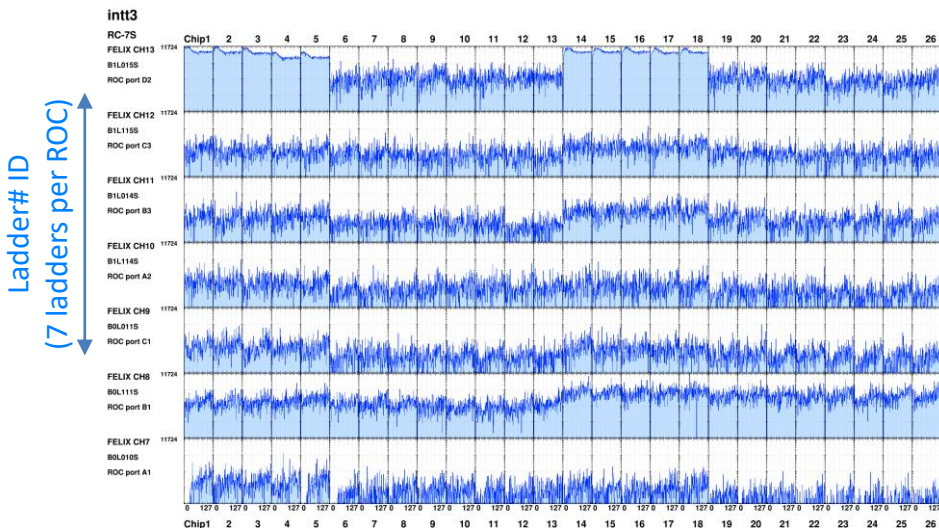
Chips (26 chips per ladder)



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(DAC0=8)

Run-24

Chips (26 chips per ladder)



Pedestal Data
Run# 00033731
(DAC0=10)

INTT North Side: 8 ROCs Run-25 vs Run-24

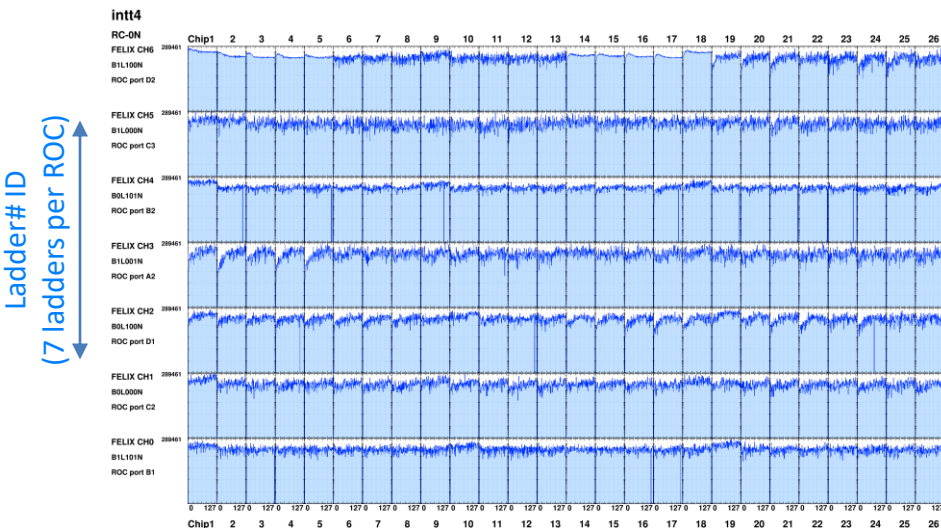
❖ Virtual Meeting → Virtual cake



Run-25

February 10th, 2025

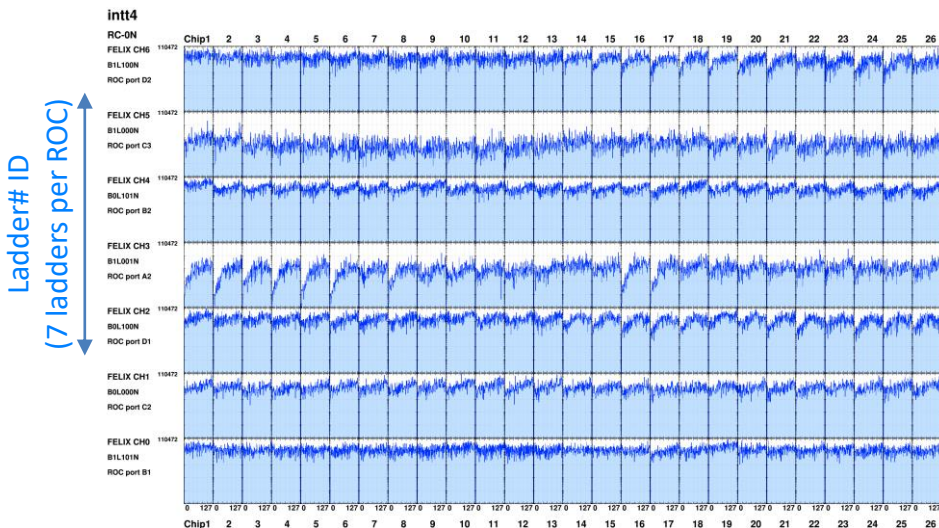
Chips (26 chips per ladder)



Pedestal Data
Run# 00056523
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Run-24

Chips (26 chips per ladder)



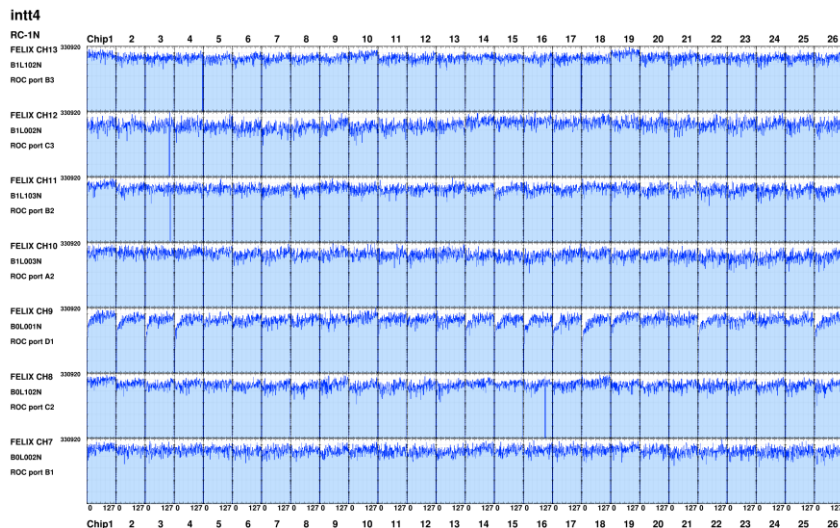
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Run-25

February 10th, 2025

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Ladder# ID
(7 ladders per ROC)

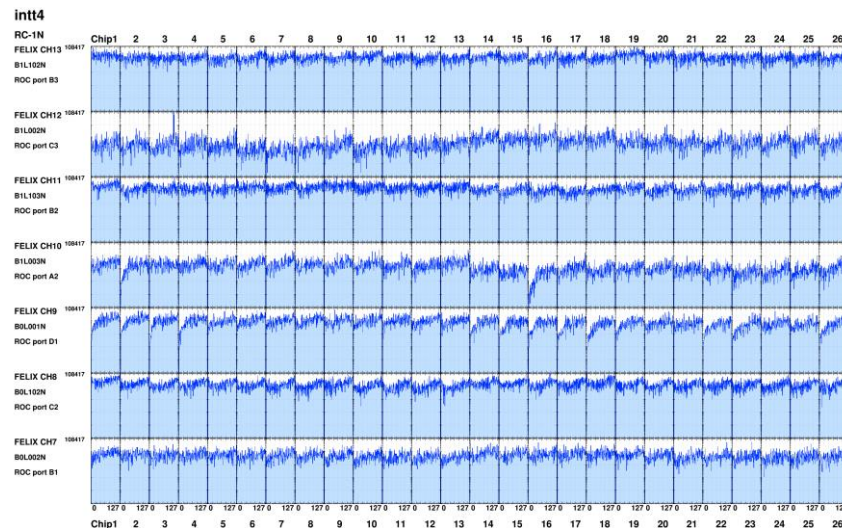


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Run-24

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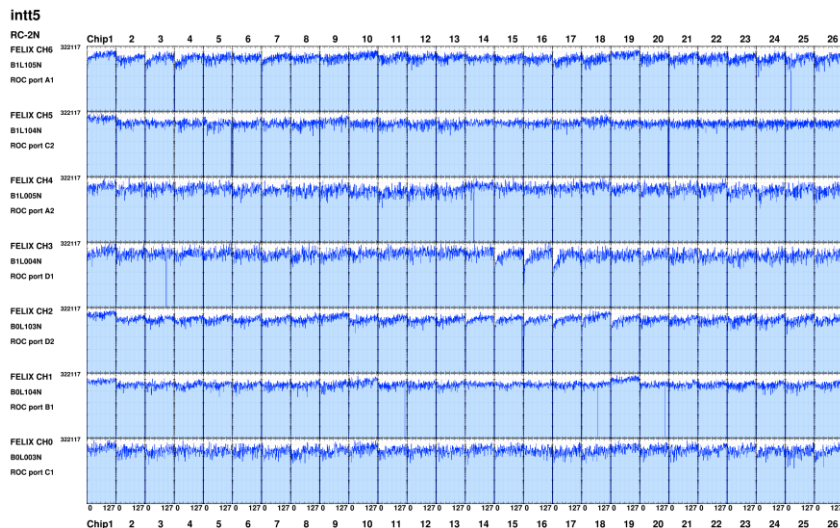
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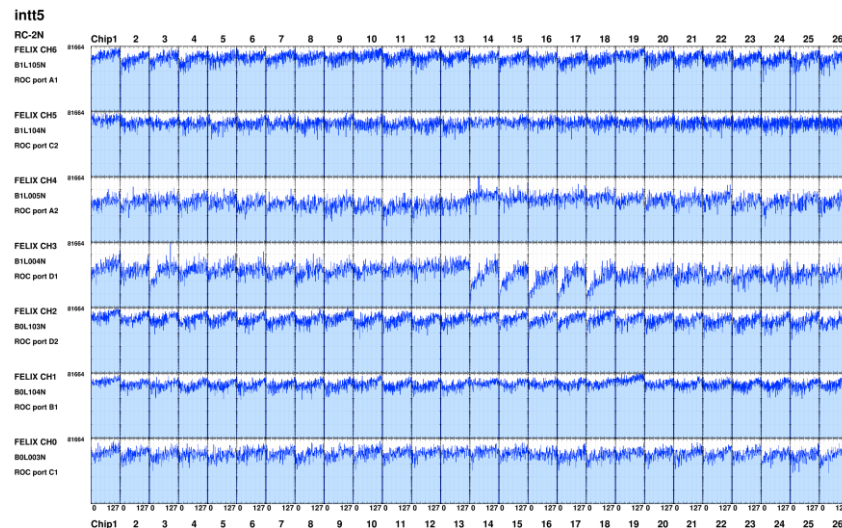


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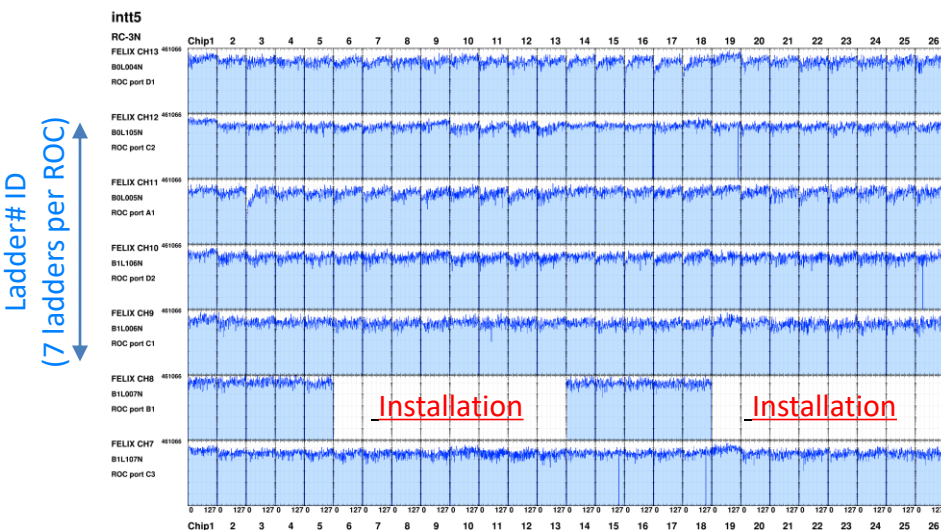


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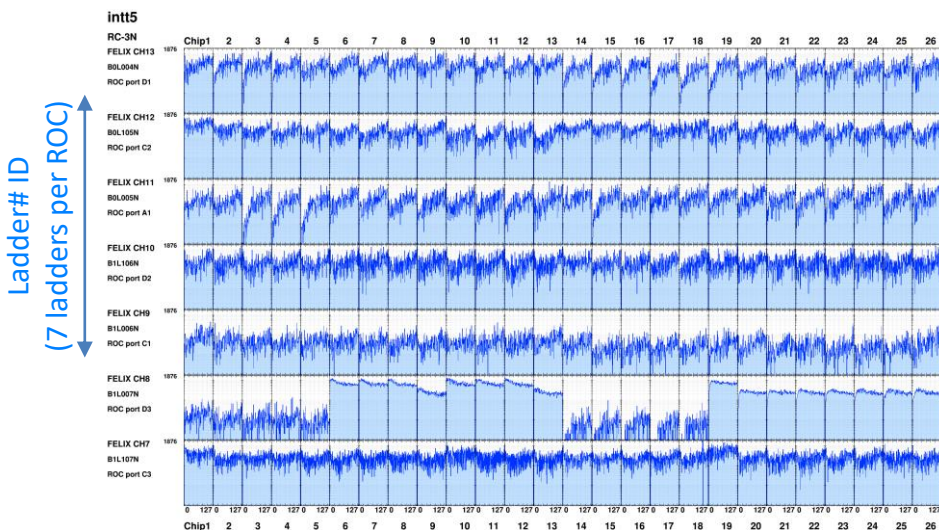
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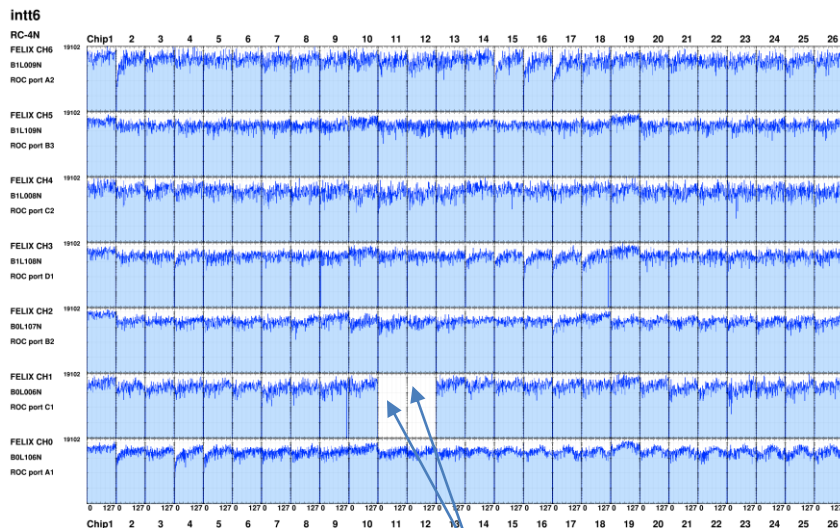
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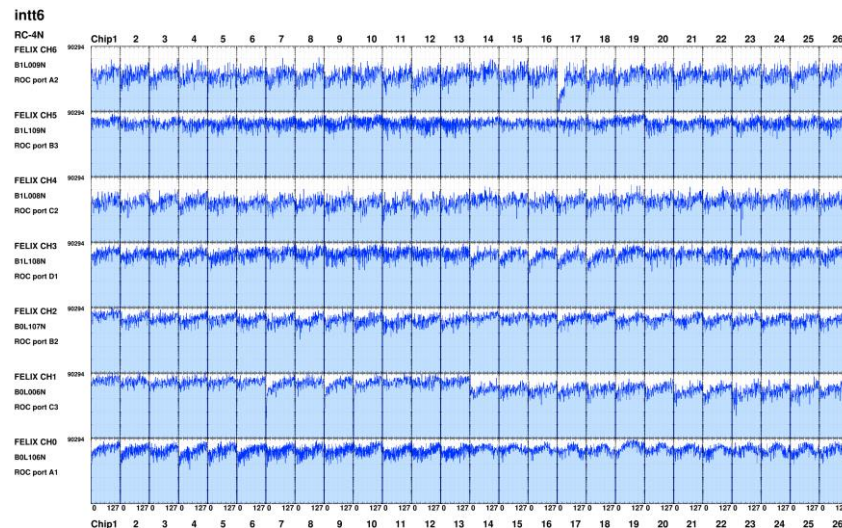
Pedestal Data
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masked in Felix during
run-24 (because one (two)
hot channel(s)) to ensure
stability of rcdag

Run-24

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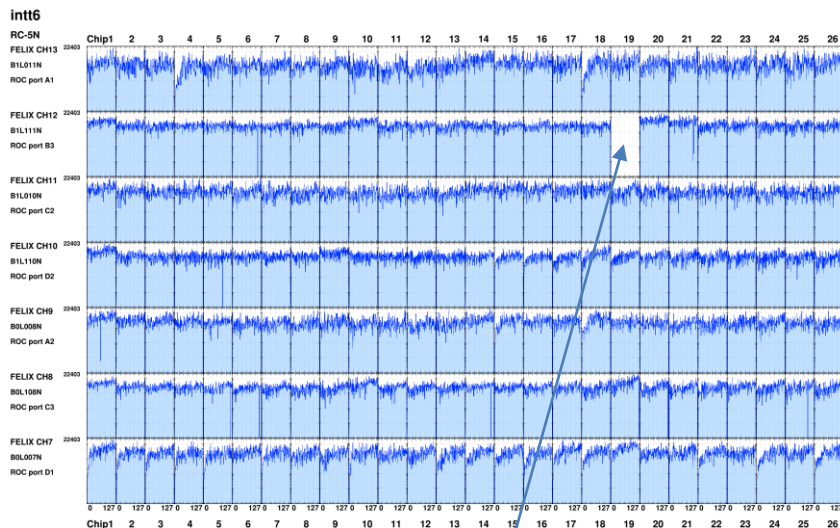
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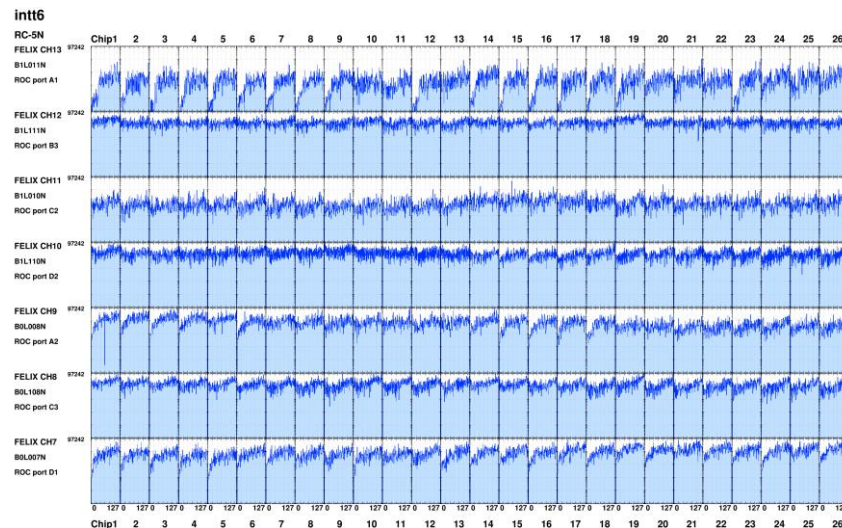
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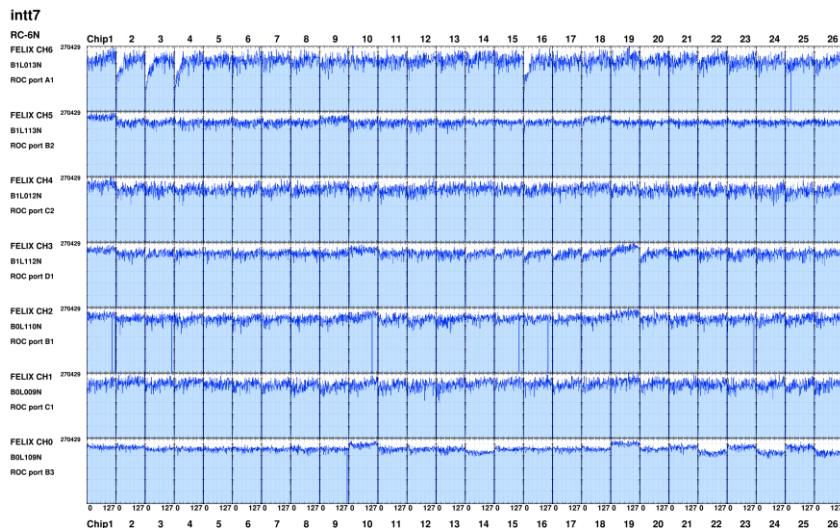
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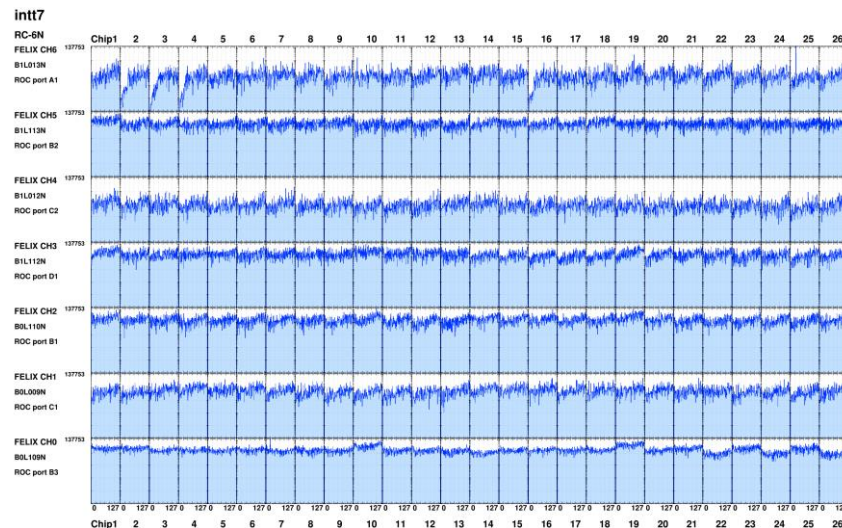


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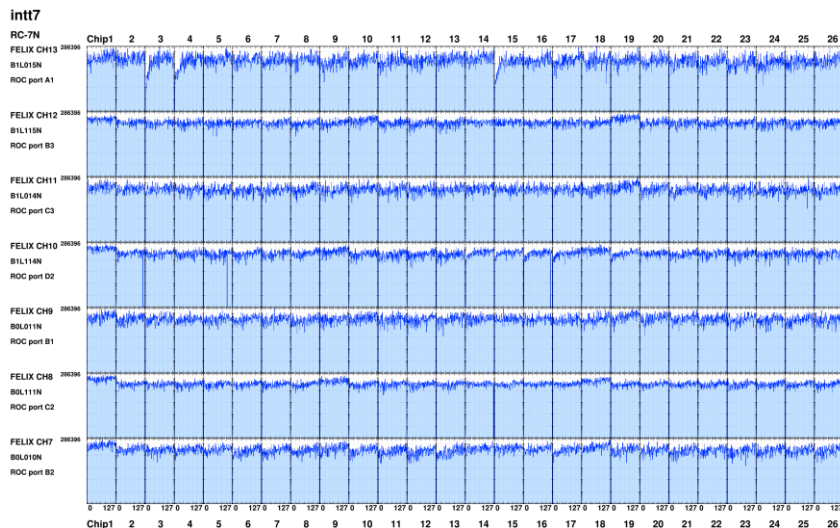
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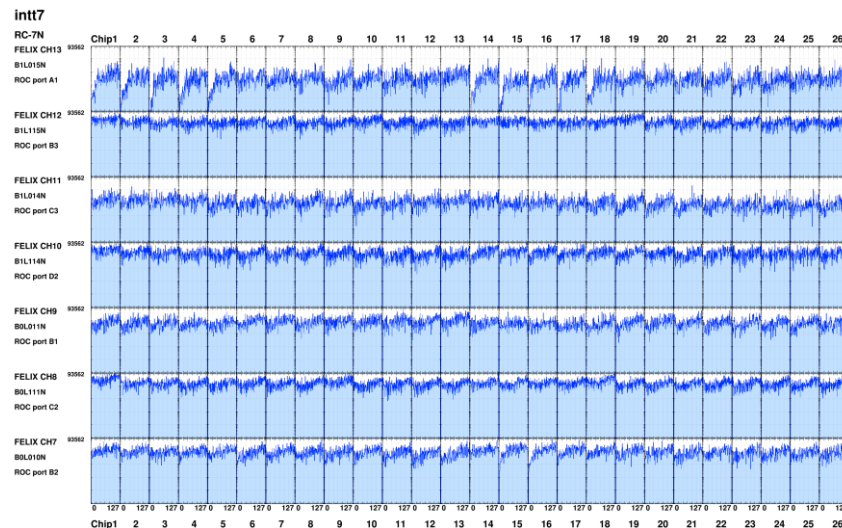


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- The status of the INTT detector for Run-25 and Run-24 will be similar

