

News

PED status:

- Subcontract to Rice being set up. We have started the RBv1 PCB design. Estimated timeline: 4-5 weeks for PCB design, 4-5 weeks for fabrication and assembly.
- Software/firmware are being prepared in anticipation of RBv1 arrival
- LBNL? BNL?

CERN chips:

- bPOL48V: because chips will leave CERN permanently, unlike those for CERN experiments, additional paperwork is needed and taking longer. I've request 36 chips, 25 CHF/piece.
- IpGBT and VTRx+: plan to use some from the CMS ETL stock but need to give back after ePIC ones are received.