

LAr R&D Progress Updates

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Lab Safety and Space Management

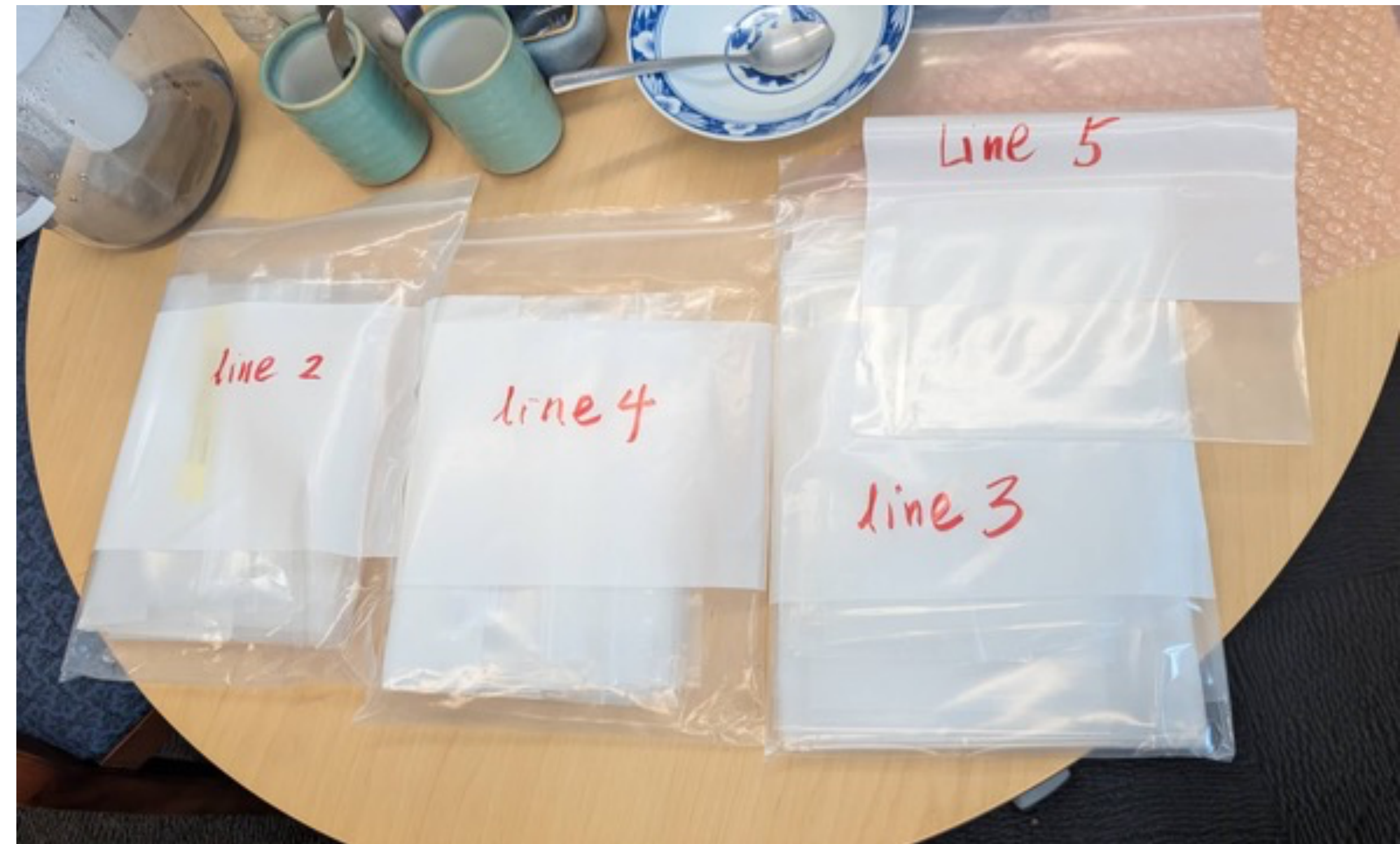
- BSA Operation Council visit on 07/23
 - The robotic chip testing station is the main object for the visit
 - The LAr R&D lab is on the path of the visit
 - I've been cleaning the lab following the instructions since yesterday
 - Paul and Hong visited the lab this morning provided suggestions and comments
 - More items cleaned up from the lab
 - All floor area swept
 - I think it should be ready for the VIP visitors by the end of today



Samples summary

pTP samples received from LFO

Bag Label	Substrate	Sample Index	Plasma Clean	Count
Line 2	Sapphire	S1–S5	Yes	5
Line 2	Sapphire	S31–S35	No	5
Line 3	B33	S11–S20	Yes	10–9
Line 3	B33	S6–S10	No	5
Line 4	Quartz	S21–S24	Yes	4
Line 4	Quartz	S25–S30	No	6
Line 5	B33	Unlabeled	Unknown	1
	Total			35



Some questions on the Samples

(Addressed by Yimin)

- All samples are coated with generic pTP same as the previous samples. Are we going to get any coated with Thermal Fisher branded pTP?
 - Samples with TF branded pTP are expected in the future delivery.
- Any in house thickness/uniformity measurement at LFO?
 - No in-house measurement on the coating quality at LFO
- What is impact of plasma cleaning process to the samples? Thickness, uniformity or just cleanliness?
 - Plasma cleaning is a common coating process should improve the bounding with no harm to the coating quality
- What is the grid pattern on some samples?
 - It is created by the plasma cleaner



Sample testing plan

Finish the spectrum measurement on all samples in transmission mode this week

- The sample are slightly larger than the chamber edges
- Still can be accommodated with a bit tilted configuration
- We conducted a mock test with the “bad sample” Line 5 with pTP on B33 for practice
- The preliminary results are pretty encouraging with high light yield and clearer peak structure (See Vyara’s presentation)
- We are going to conduct a thorough test on all 34 samples this week

