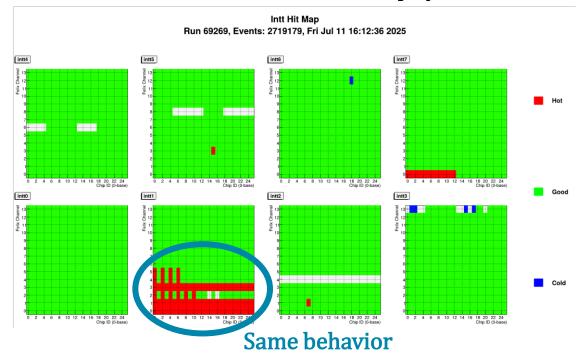
7/24 Digital-Control-analysis

Rikkyo university M1 TOMOKI HARADA

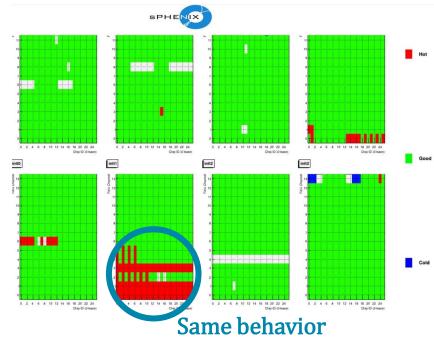
#### Run number

run#69268, 69269

10min, physics×2



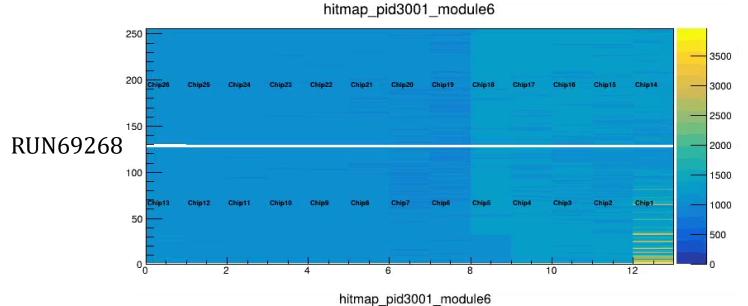
- We took the Digital-control test data twice as Physics.
- The settings for these two runs are exactly same.
- I changed digital-control parameter of some chips in intt1 as reference to see whether digital-control were reflected or not. These chips became to be HOT-CHIPS, sorry

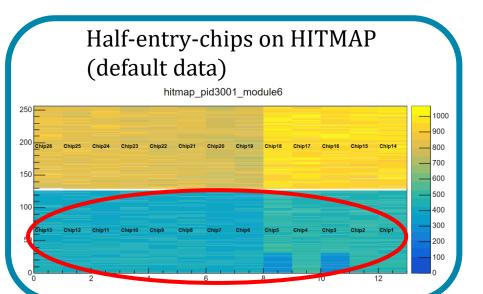


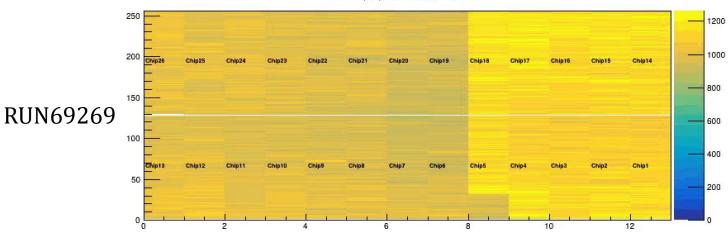
run#68016 (previous data of Digital-Control test on June 20) #Just for reference 11 0 14



Felix Server	0
pid	3001
$\operatorname{module}$	6
chip id	1, 2, 3, 4, 5, 6, 7,
	1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13

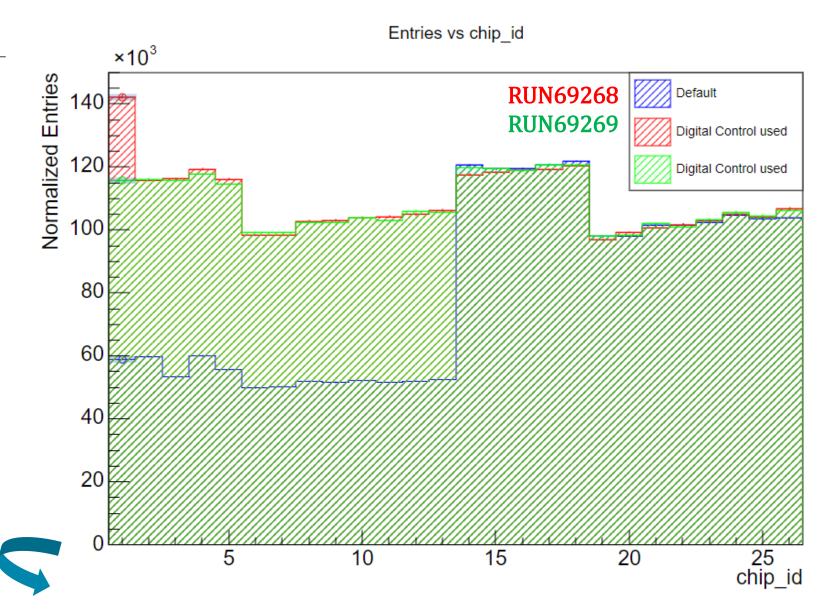






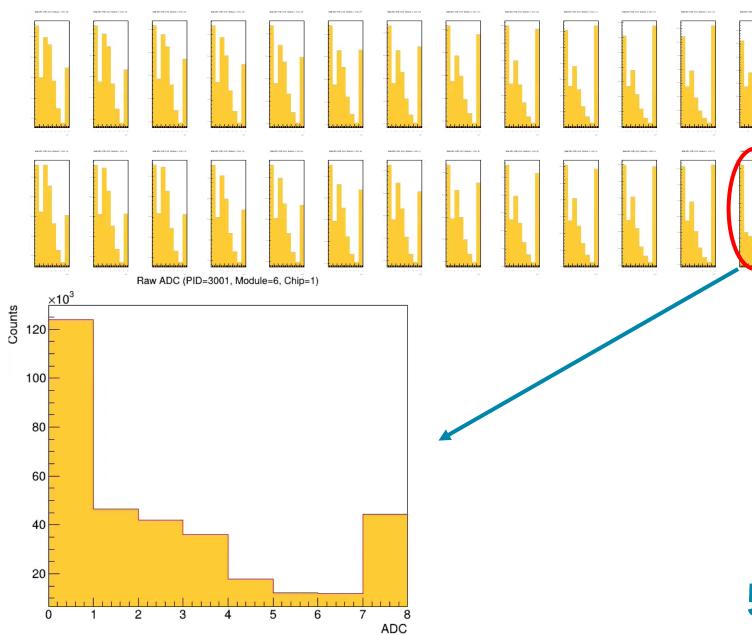


Felix Server	0
pid	3001
module	6
chip id	1, 2, 3, 4, 5, 6, 7,
	1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13



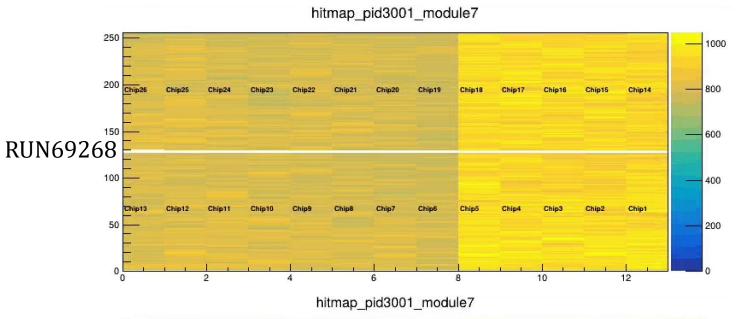


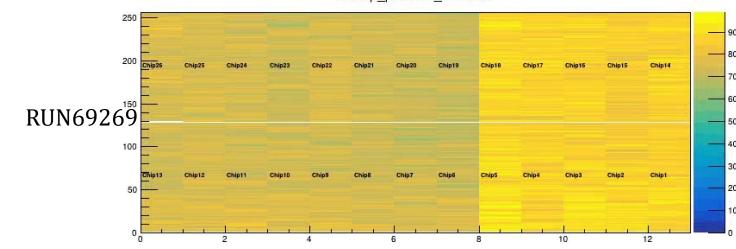
Felix Server	0
pid	3001
module	6
chip id	1, 2, 3, 4, 5, 6, 7,
	8, 9, 10, 11, 12, 13

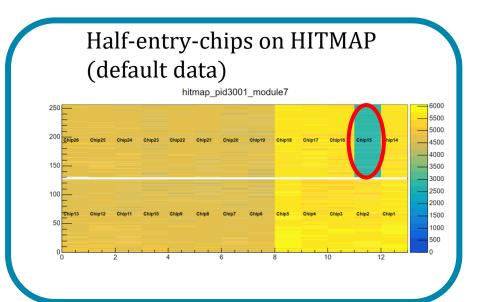




Felix Server	0
pid	3001
module	7
chip id	15

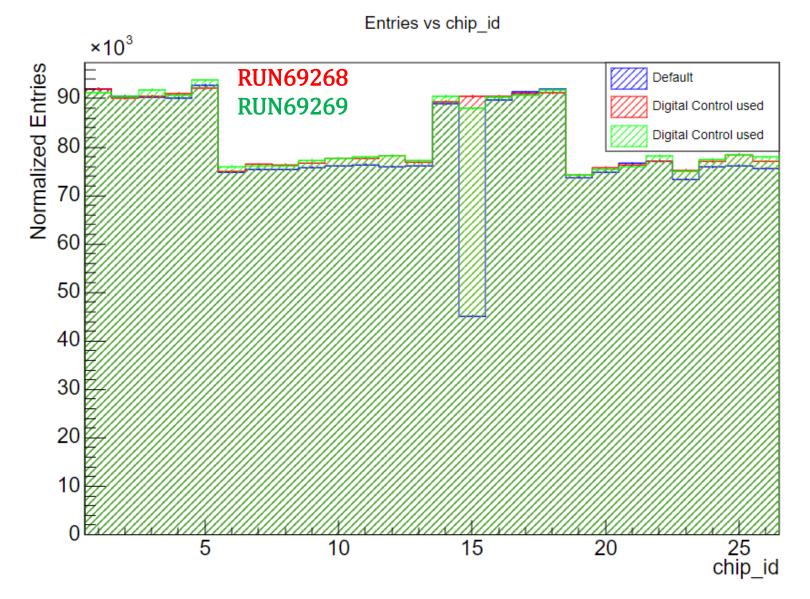






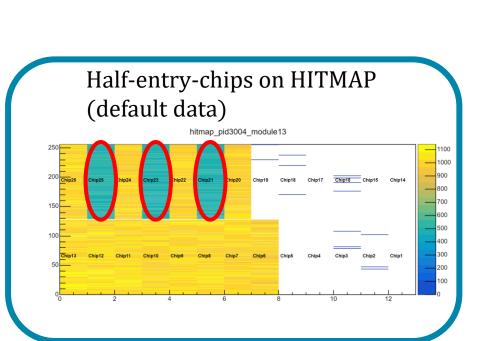


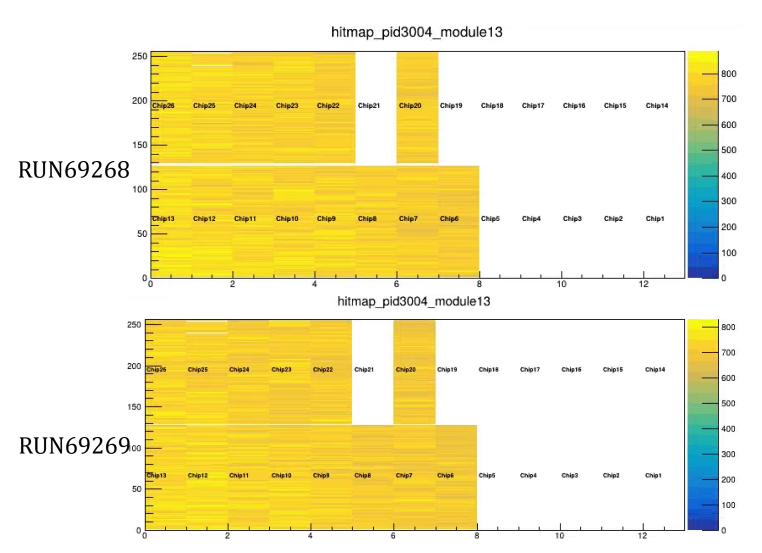
Felix Server	0
pid	3001
module	7
chip id	15





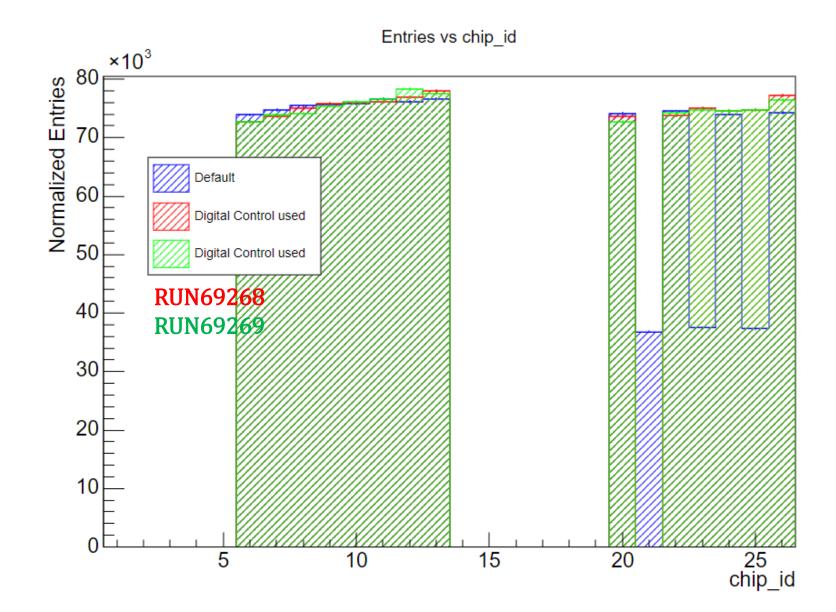
Felix Server	3
pid	3004
module	13
chip id	21, 23, 25



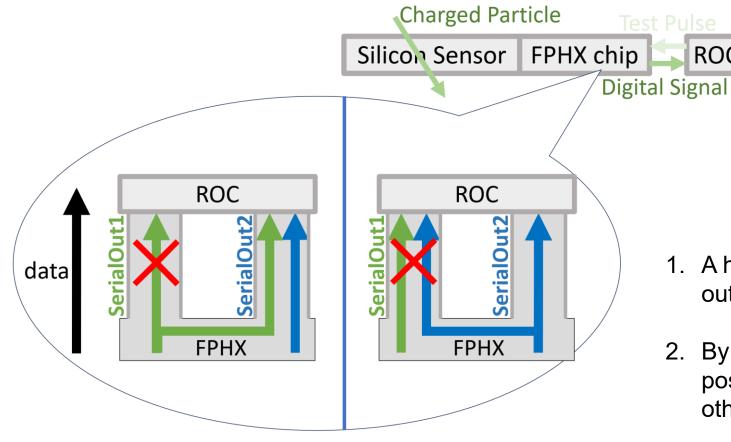




Felix Server	3
pid	3004
module	13
chip id	21, 23, 25







1. A half-entry occurs when one of the two serial output lines is disconnected from FPHX to ROC.

PC

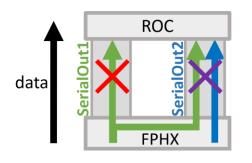
FEM

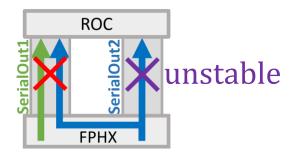
FEM-IB

ROC

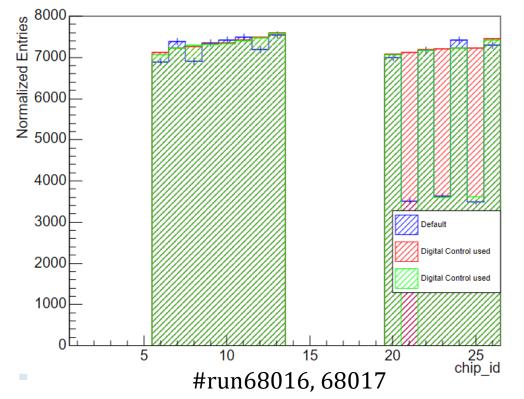
- 2. By controlling the Digital-Control parameters, it is possible to clone the data and send it through the other serial output line.
- 3. If the disconnected line and the cloned direction match, the number of entries is expected to recover to normal. If they do not match, the halfentry condition is expected to remain.







#### Entries vs chip\_id



#### Hypothesis

- In this chip, one of the output-line is disconnected, and also another output-line seems to be unstable.
- →become to be no-entry.

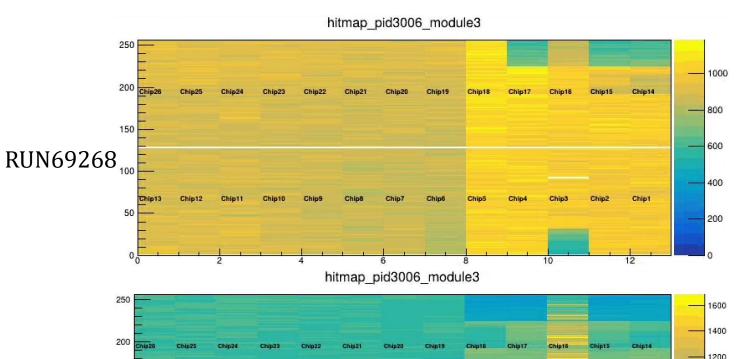
Should we not touch this chip?

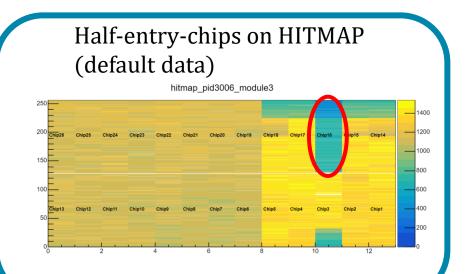
or

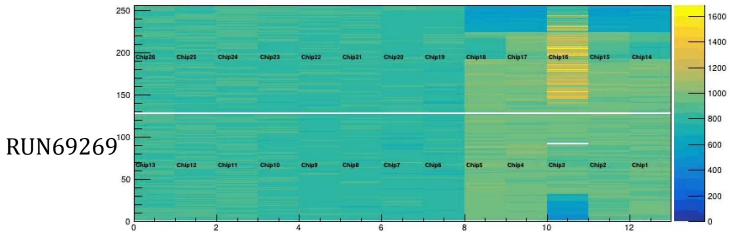
If it's going to be judged as a cold chip, should we try to use Digital-control?



Felix Server	5
pid	3006
module	3
chip id	16

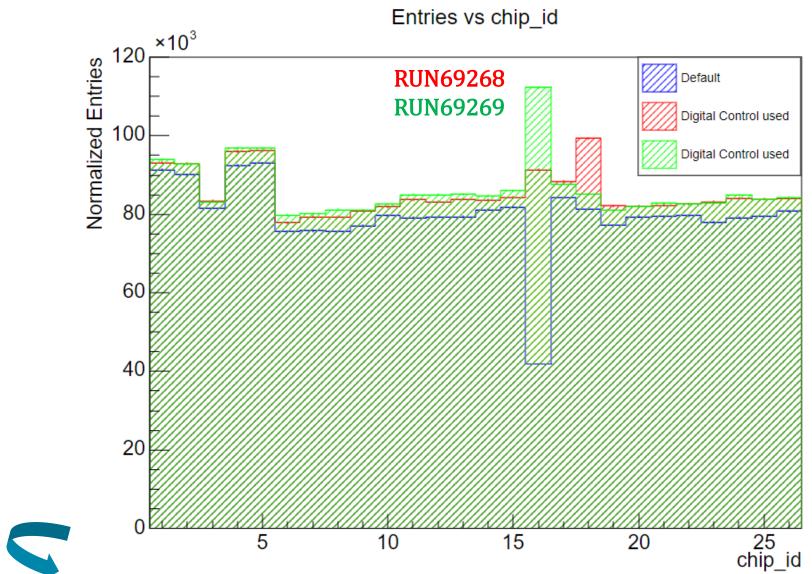








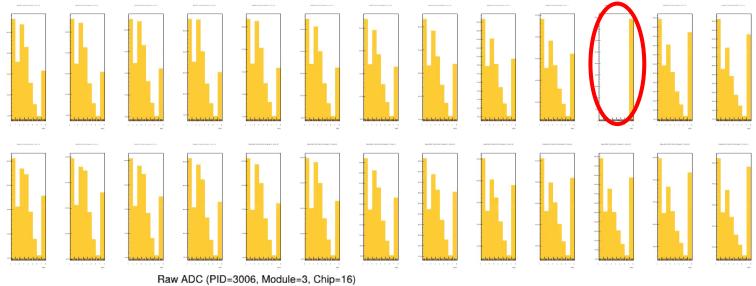
Felix Server	5
pid	3006
module	3
chip id	16

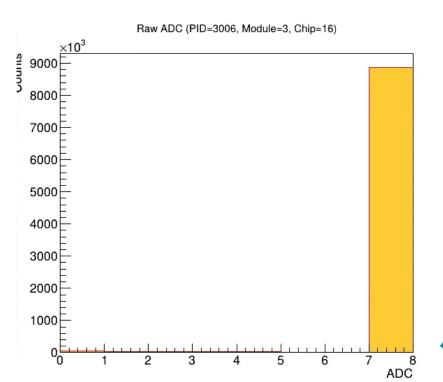


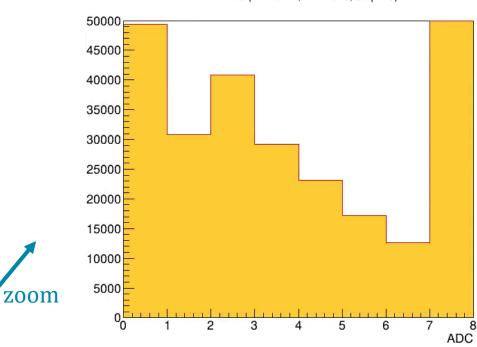




Felix Server	5
pid	3006
$\operatorname{module}$	3
chip id	16



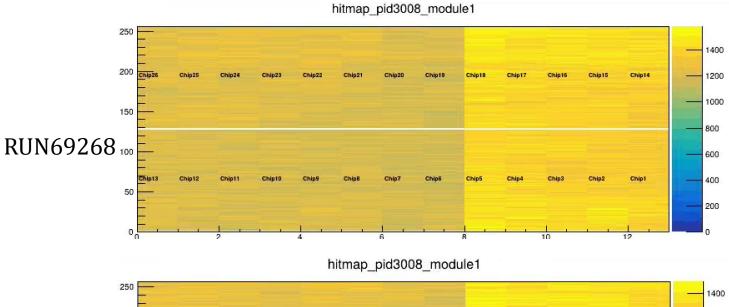


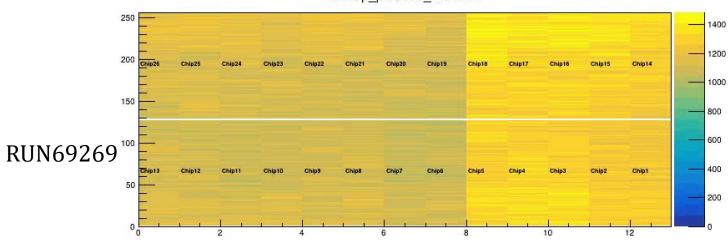


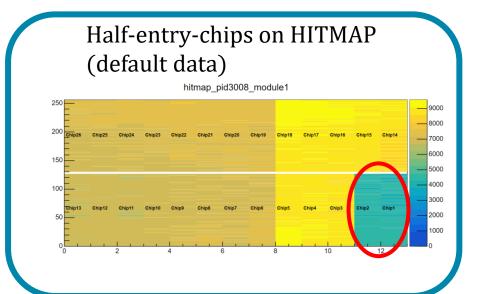


Felix Server	7
pid	3008
module	1
chip id	1, 2



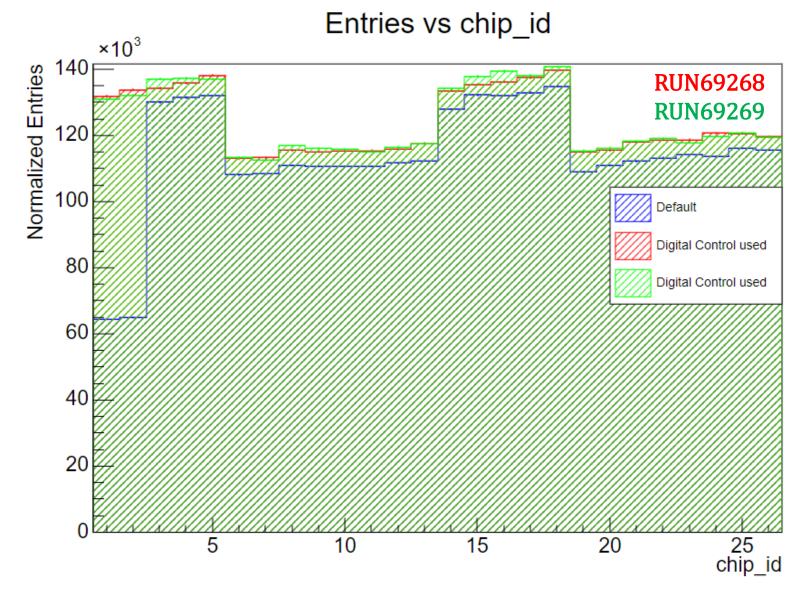






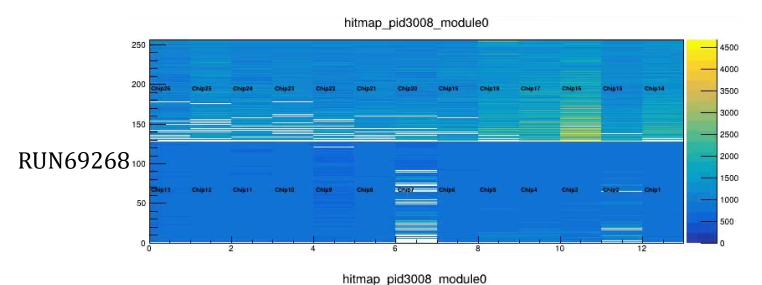


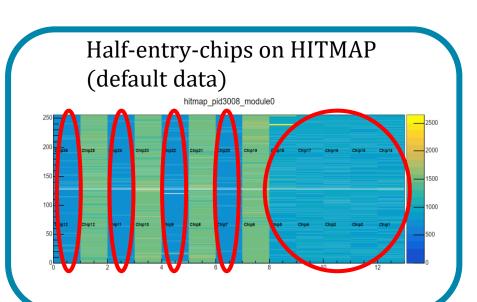
Felix Server	7
pid	3008
module	1
chip id	1, 2

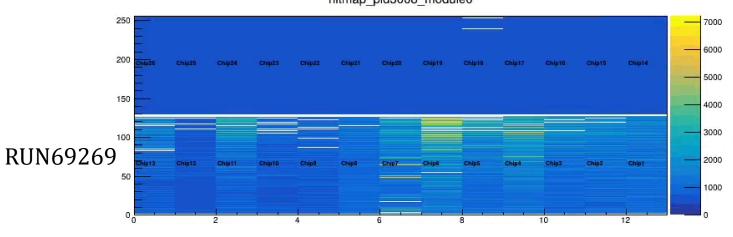




Felix Server	7
pid	3008
module	0
chip id	1, 2, 3, 4, 5, 7, 9,
	11, 13, 14, 15, 16,
	17, 18, 20, 22, 24, 26



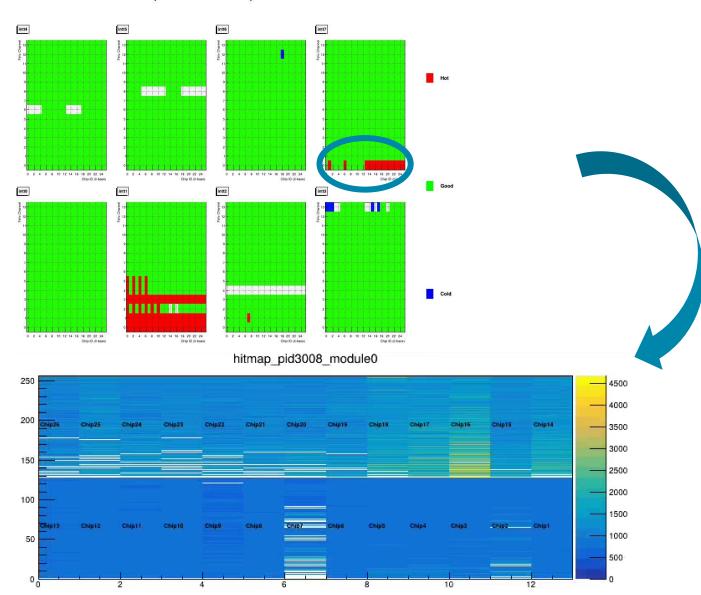




## **About hot-chips of INTT7**

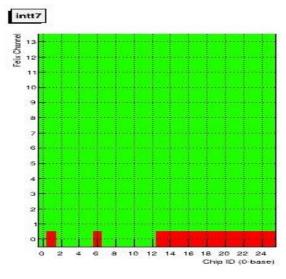


Intt Hit Map Run 69268, Events: 10605279, Fri Jul 11 16:05:10 2025

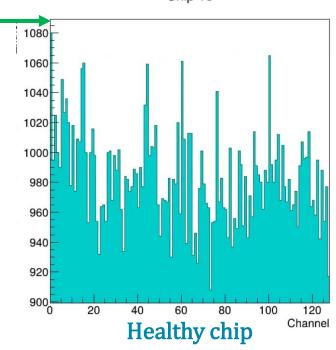


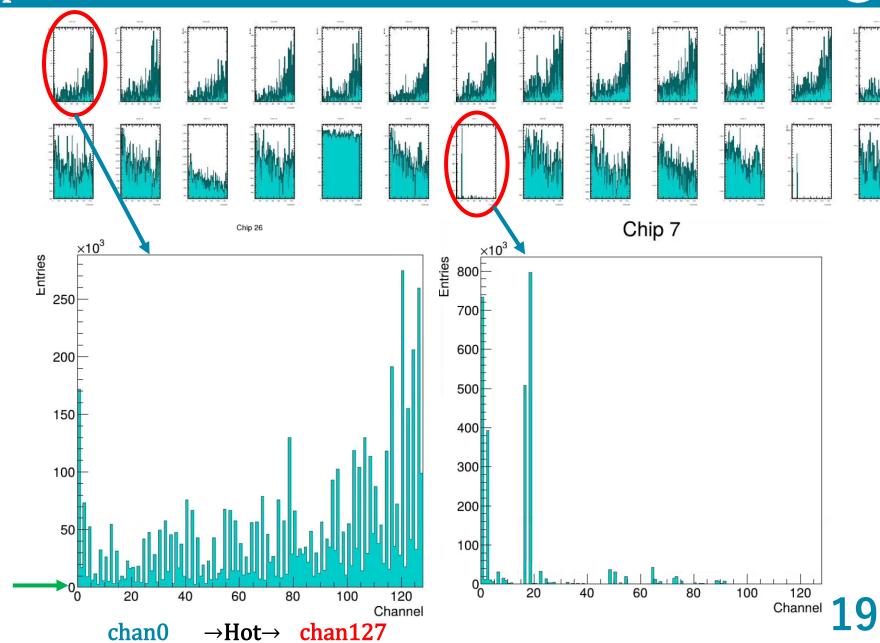
Felix Server	7
pid	3008
module	0
chip id	1, 2, 3, 4, 5, 7, 9,
	11, 13, 14, 15, 16,
	17, 18, 20, 22, 24, 26





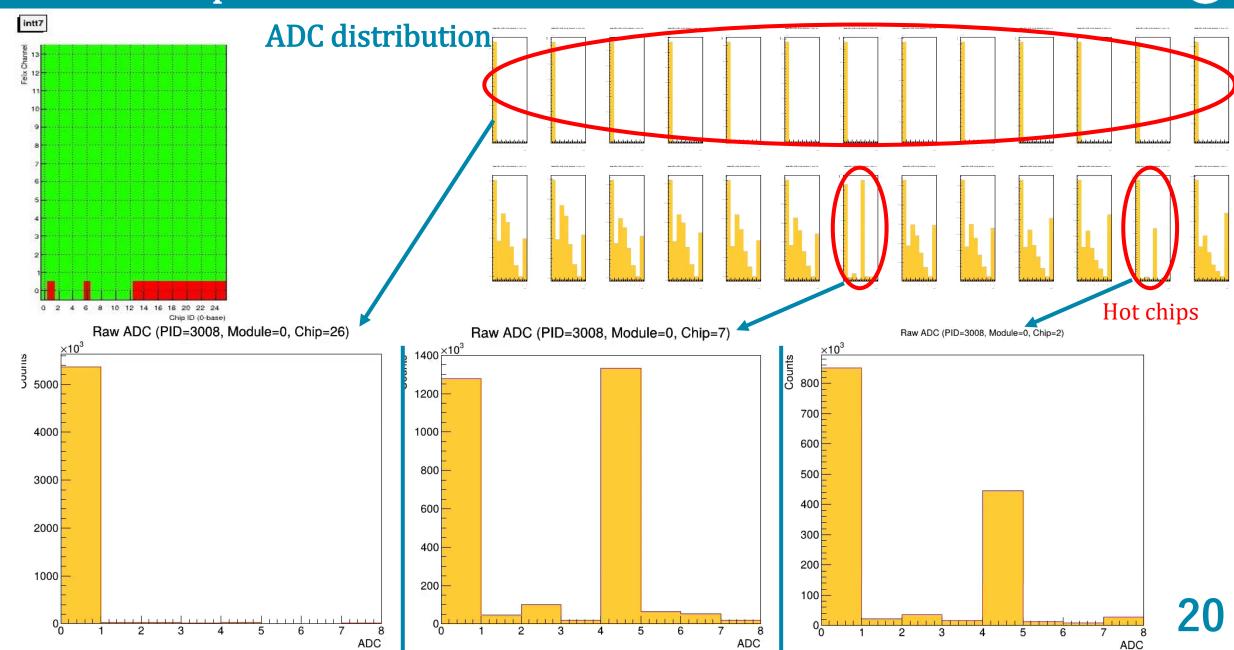
Chip 13





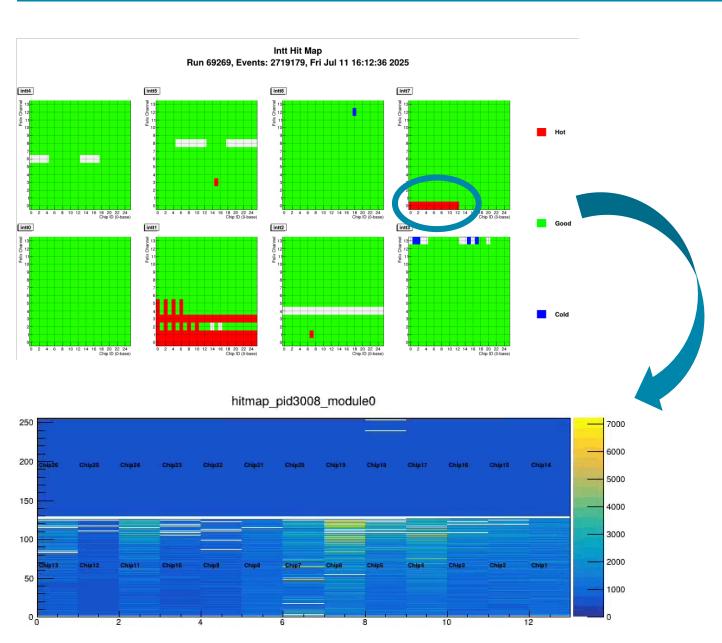
#### **About hot-chips of INTT7**





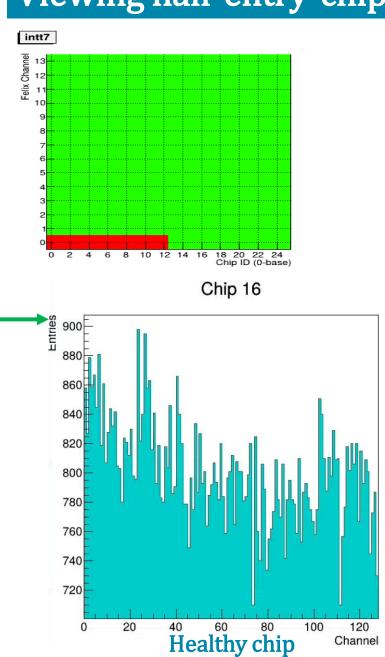
## **About hot-chips of INTT7**

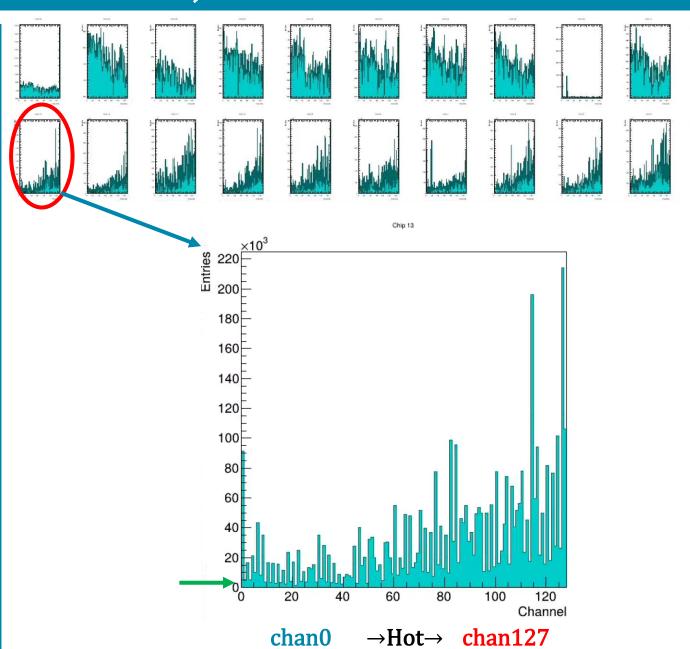




Felix Server	7
pid	3008
module	0
chip id	1, 2, 3, 4, 5, 7, 9,
	11, 13, 14, 15, 16,
	17, 18, 20, 22, 24, 26

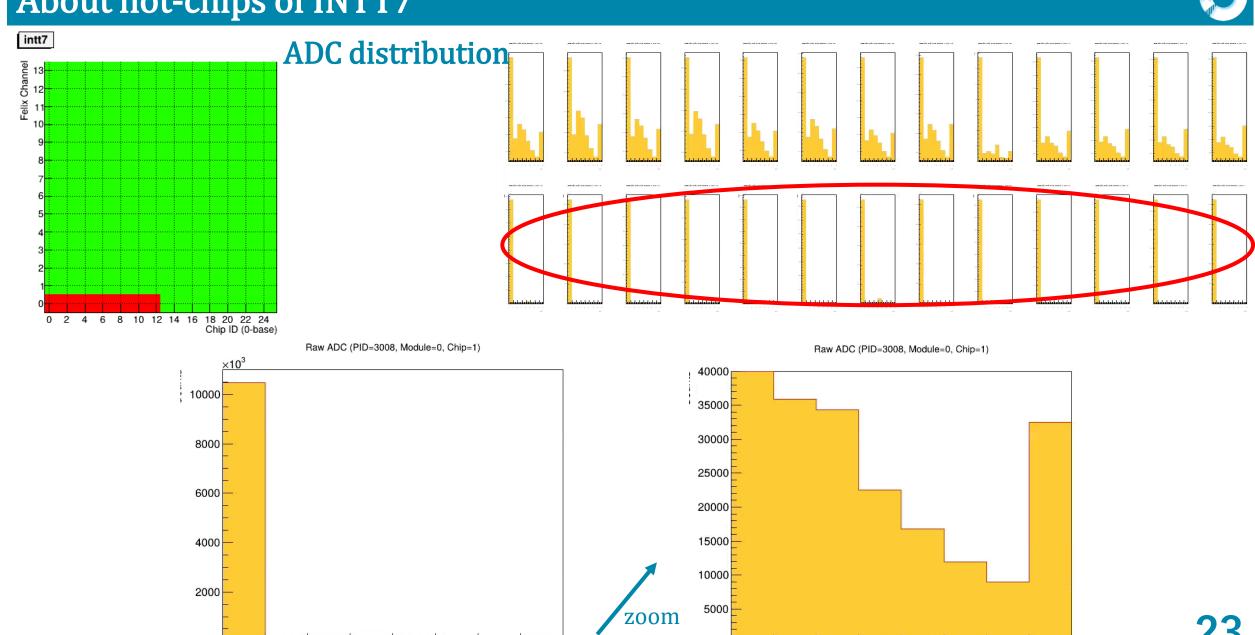






#### **About hot-chips of INTT7**





ADC

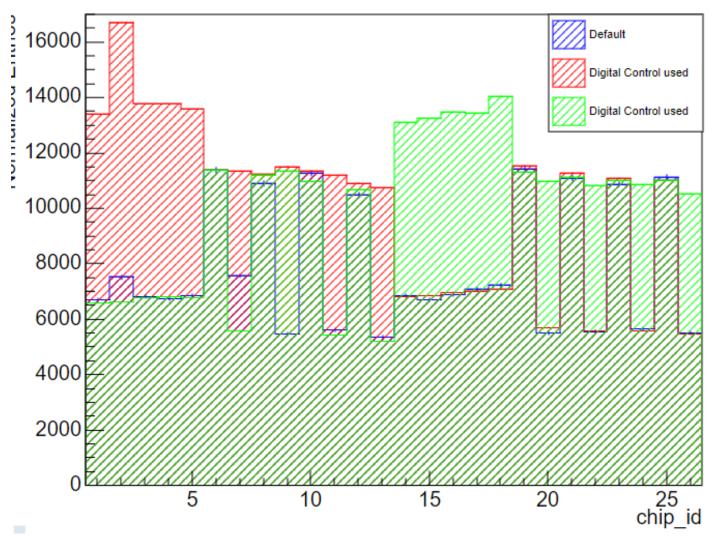
ADC



# Felix Server 7 pid 3008 module 0 chip id 1, 2, 3, 4, 5, 7, 9, 11, 13, 14, 15, 16, 17, 18, 20, 22, 24, 26

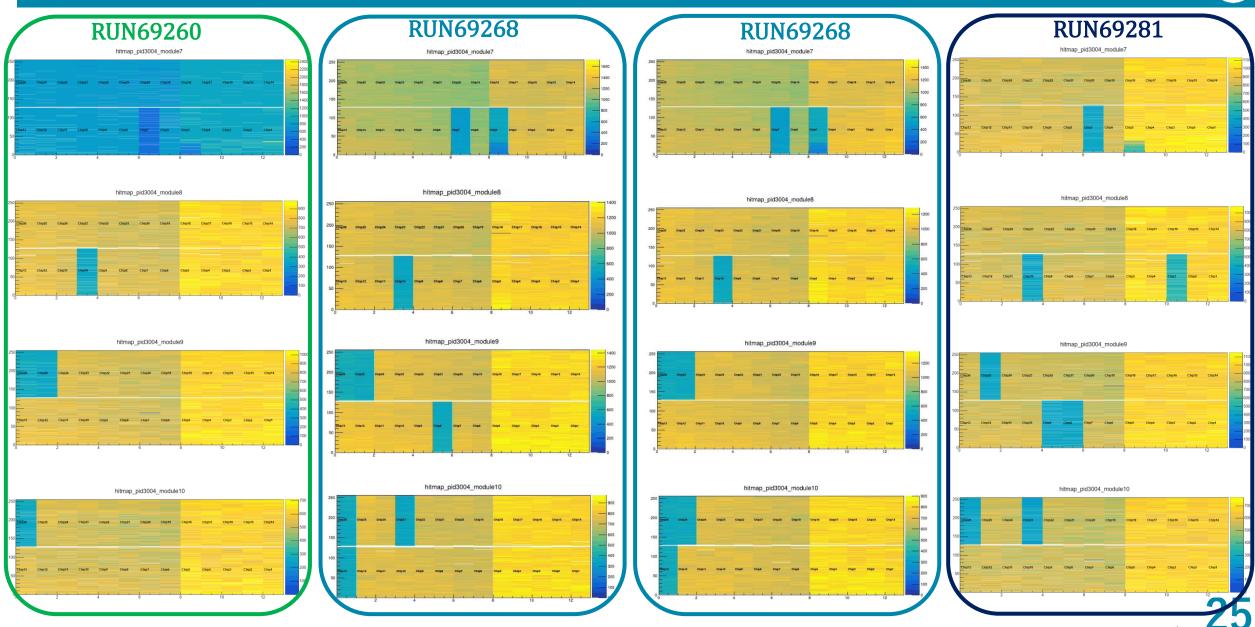
- half-ladder here has a lot of half-entrychips.
- When we cloned the data with digitalcontrol in half, it's working. (you can see it right figure)
- I know that noise doesn't clone well, so it may be accidental. I need a few more runs.

# data took on June 20 Entries vs chip\_id



# New half-entry-chips





#### New half-entry-chips



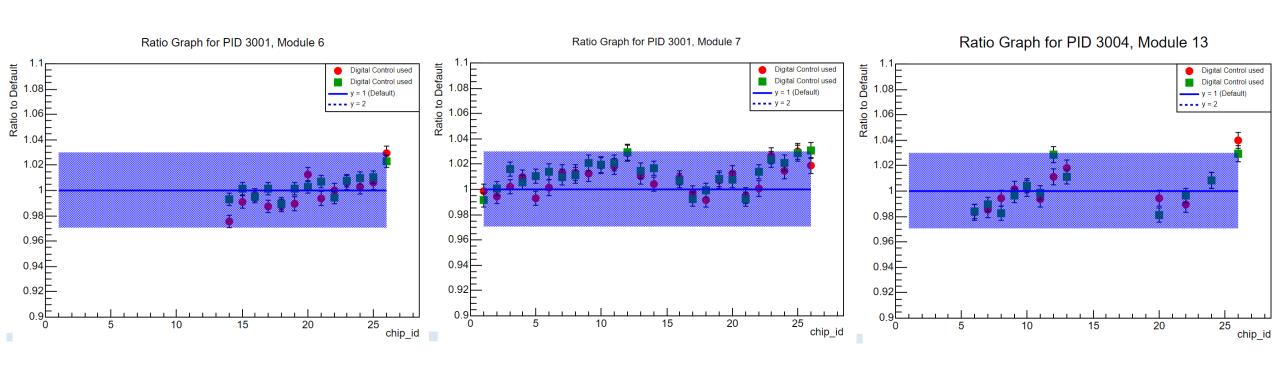
- · New half-entry-chips were recently identified.
- Even data not using digital-control can be viewed. And, also it is unstable half-entry with no reproducibility.
- It viewed in  $\frac{\times}{\text{INTT4}}$  && module  $7 \sim 12$ .
- →packet\_id=3004(INTT3)



# Effect to healthy chips

#### Effect to healthy chips

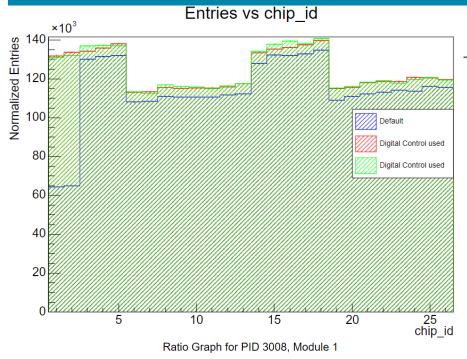




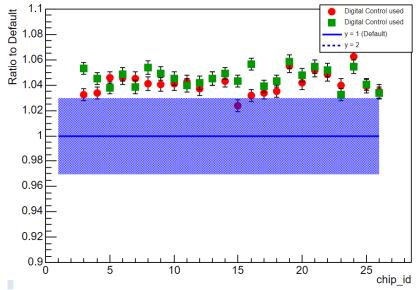
Most chips fall within the reproducibility range presented previously (3%), and there are no effect to healthy chips with degital-control.

## Effect to healthy chips





Felix Server	7
$\operatorname{pid}$	3008
module	1
chip id	1, 2



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#### Summary



#### Half-entry-chips chart(36 half-entry-chips)

Felix Server	0	0	3	5	7	7
pid	3001	3001	3004	3006	3008	3008
module	6	7	13	3	0	1
chip id	1, 2, 3, 4, 5,	15	21, 23, 25	16	1, 2, 3, 4, 5, 7, 9, 11,	1, 2
	6, 8, 10, 11,				13, 14, 15, 16, 17,	
	12, 13				18, 20, 22, 24, 26	
	'	'	'	'	Hot	'

- 19chips / 37chips are perfectly recovered with digital-control (Seems to be less, but problematic only the half-ladder at intt7&module0)
- When we used digital-control, Hot chips are appeared in intt7.
- There are unstable half-entry-chips in intt3 these days from RUN68\*\*\*.