- RBv1 arrived at Rice
  - tested – working!
  - Details from Mike
  - TL: I can't quite understand if we can (or not, or …?) connect the ETL ETROC Module so that we test that part too?
  - TL: I will want to go to the 5 Gbs mode next week
- PBv1 with just 1 digital bPOL48 arrived at Rice
  - connected to RBv1 – working!
    - details from Mike
  - we should get 10 bPOL48s shipped to BNL (next week?)
    - what will we do?
  - I'd like 1 fully populated PBv1 at Rice so we can test all of the auxiliary features
    - control, temperature (PTAT) etc
- William's fiber FMC card will be shipped to Rice (2 pieces)
  - enables realistic clock configuration
  - I'm waiting for William's detailed explanation of the board and general intent
    - next week
  - but I plan to start using it in our test stand (connected to RBv1)
  - in the future, it will also enable direct connection to EPIC GTU ("Global Trigger Unit") with all its protocols
- EICROC1 Testboard schematics obtained
  - some missing signals – waiting for the French group
  - the idea is to make a small passive PCB which connects their FMC connector to our Kyocera connector on RBv1
    - Mike & TL looking into it… but I think this will be possible. TBD.
- Discussion about our own ASIC Module ("quad")
  - I think it would be good if we proceed in parallel to the Testboard above because I assume lots of problems with the Testbord, delays, misunderstandings…
  - note that we won't get the ASIC wire-bonded but it will be wire-bonded at BNL (Prithwish, right?)