

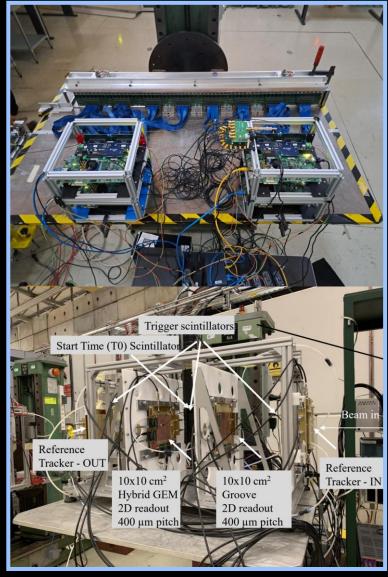
Test Beam Software Support

Derek Anderson (JLab) and others ePIC Weekly S/C Meeting October 29th, 2025





- Critical period for EIC: beginning to move into construction!
 - Several test beams (TB) already happened and many being planned
 - cf. this week's <u>TIC meeting</u>
 - Left: setups from LFHCAL (upper) & MPGD (lower)
 2024 TBs at CERN PS
- From software side: want to support test beams as much as possible
 - And want to begin preparing for real data-taking
 - TB data allow for practicing reconstructing / analyzing real data
 - The earlier we start, the better!
- Held dedicated workfest on topic at Summer 2025 EIC UGM/ePIC CM
 - In this talk: will summarize workfest and provide recent updates



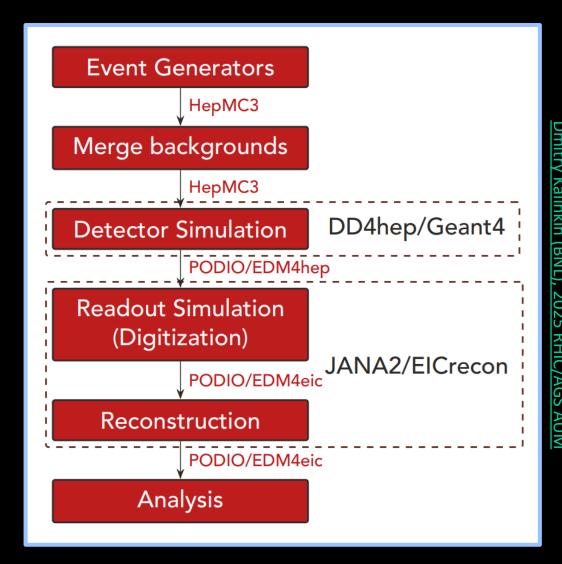
Tristan Protzman (LU), CPAD2024

ondo Gnavo (JLab), CPAD2024

Intro | Reminder: the ePIC Software Stack

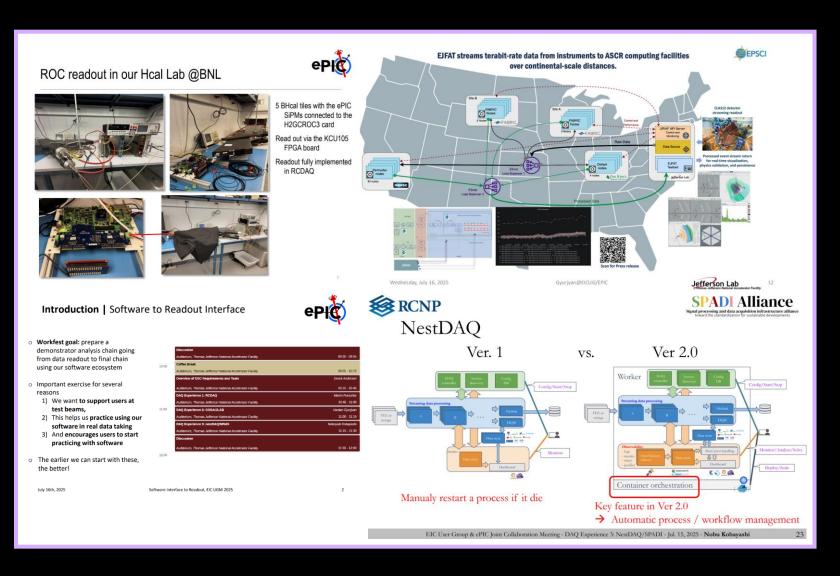


- Right: an overview of the ePIC software stack
 - MCEGs simulate physics channels + backgrounds
 - 2) GEANT4 + DD4hep simulates detector response
 - 3) <u>JANA2</u> + <u>ElCrecon</u> simulates digitization and runs reconstruction
 - 4) <u>PODIO</u> structures flow of data through simulation, reconstruction to analysis
 - 5) Flat output format facilitates analysis in wide range of analysis frameworks
- Stack is flexible enough to support simulation/reconstruction of both main detector and TB setups
 - Working in stack now prepares us for real data taking later
 - And is very useful for reproducibility!
 - ∴ Now working towards supporting TBs!



EIC UGM Workfest | Morning Talks

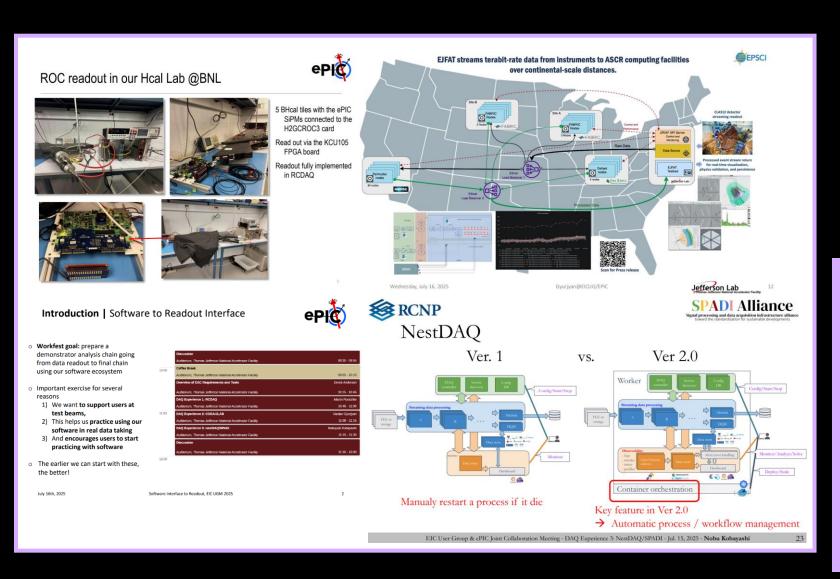




- 2-part workfest at recent EIC UGM aimed at interfacing software stack to TB data
 - Joint morning session w/ E&DAQ hosted talks on
 - Select DSC TB experience (BIC, LFHCAL/EEEMCal)
 - Common DAQ frameworks
- O 3 DAQ Frameworks discussed:
 - Martin Purschke (BNL) <u>discussed</u>
 RCDAQ, highlighting its portability
 - Vardan Gyurjyan (JLab) <u>discussed</u>
 <u>CODA+ERSAP</u>, highlighting its
 streaming capabilities
 - Noboyuki Kobayashi (OU-RCNP)
 discussed nestDAQ, highlighting
 its throughput & integration with
 3rd-party software

EIC UGM Workfest | Morning Talks



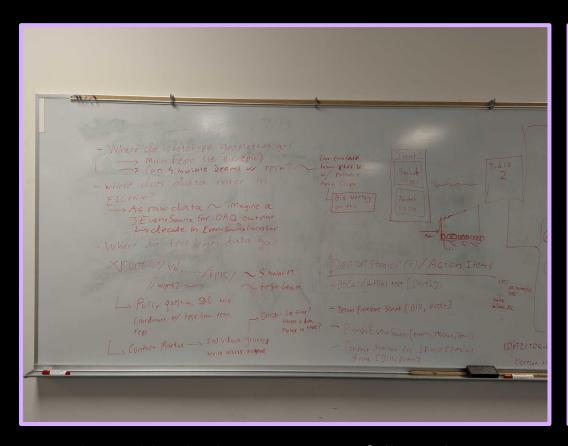


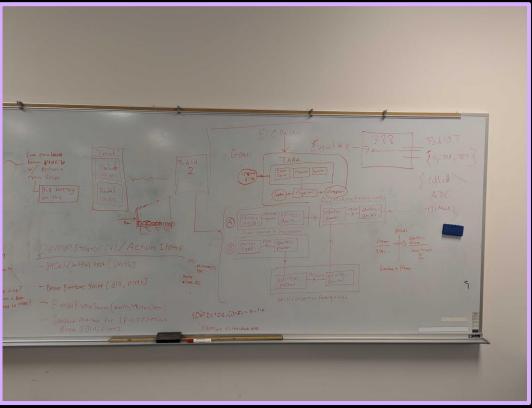
- 2-part workfest at recent EIC UGM aimed at interfacing software stack to TB data
 - Join morning session w/ E&DAQ hosted talks on
 - Select DSC TB experience (BIC, LFHCAL/EEEMCal)
 - > Common DAQ frameworks

- Updates on all 3 today!
 - RCDAQ: later this talk
 - CODA+ERSAP: later this talk
 - NestDAQ: following talk by Nobuo
 & Taku

EIC UGM Workfest | Evening "Hackathon"







- Dedicated hackathon in evening followed morning session of talks
 - As per tradition: no code was written...
 - But still had immensely productive discussion
- Above: notes from discussion, which are digitized <u>here</u>
- Discussion helped bring folks on the same page
 - And produced concrete actions items!

EIC UGM Workfest | Hackathon Action Items



- Right: action items from workfest
 - Fall into a few categories
 - a) Data availability
 - b) Simulation
 - c) Infrastructure
 - d) Demonstrators for working with raw data
- Note: 2024 BIC TB good candidate for CODA example JEventSource/Processor
 - Also can build on work of 2023
 streaming tests with ERSAP + JANA2
 - Will discuss more in Grand Challenge
 Dispatch section

Action items

- ☑ [Bill] Get LFHCAL/EEEMCal TB data in XrootD/Rucio [Bill]
- ☑ [Derek] Create channel to share TB code
- ☑ [Bill, Preet] Complete beam-envelope script for npsim
- Create example JEventSource/Processors for raw data
 - (Step 1) [Martin, Tristan] Create test code with random hits
 - (Step 2) [Martin, Tristan] Then expand code to process RCDAQ data
 - (Follow-up) [TBD] Exercise should be repeated for CODA and NestDAQ data
- [Derek] Finish standalone BHCal (saHCal) custom plugin demonstrator
- ☑ [Derek] Finish implementing HGCROC type

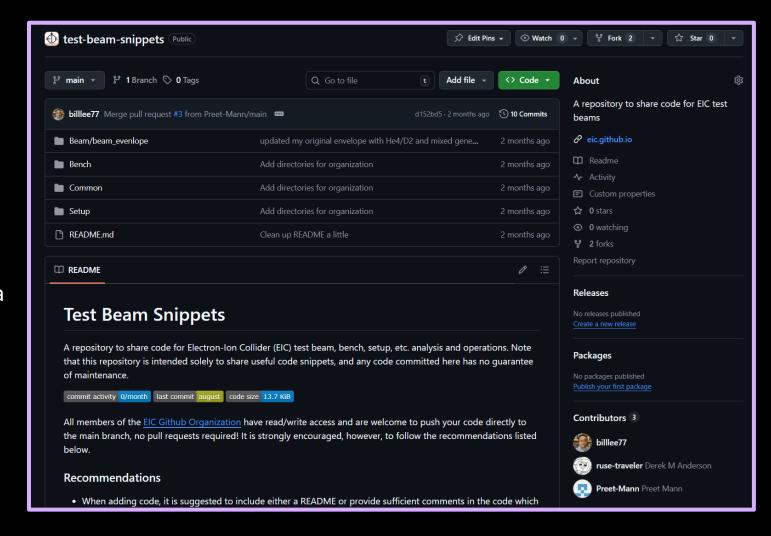
☑ = Done

■ = In progress/To-do

EIC UGM Workfest | Envelope Script and TB Snippets

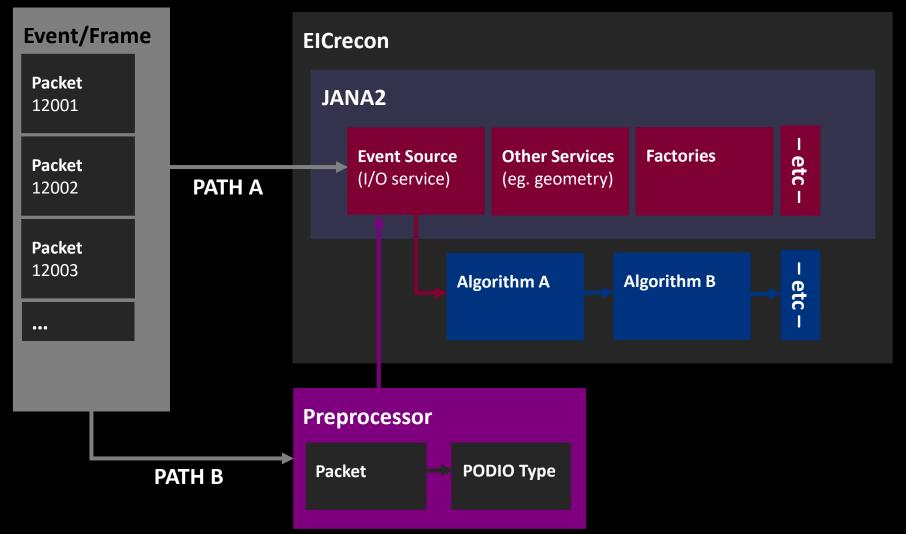


- Bill Lee (MSU) and Preet Mann (SBU)
 have since completed their beam
 envelope script
 - Simulates beams received during tests via npsim
 - Overview and tutorial later this meeting!
- To share TB-related code: we now have a dedicated <u>Test Beam Snippets</u> repo!
 - Like snippets, no guarantee of code maintenance
 - But provides channel to stage and share code aimed at test beams, stands, benches, etc.



Interfacing | ElCrecon Input

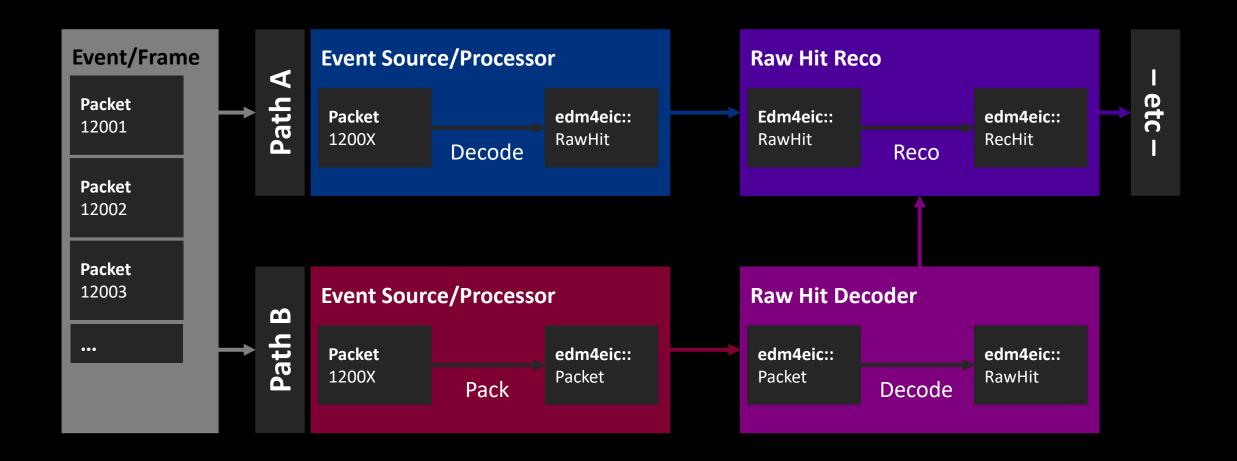




- Central question at hackathon: how do we process real data with EICrecon? le.
 - a) How does data *arrive* at EICrecon (raw packets, PODIO types, etc.)?
 - b) And if it arrives raw, how do we unpack it?
- Left: 2 approaches to answer question (a)
 - Consensus in room wasPath B is fine for TBs
 - But Path A necessary for actual data taking

Interfacing | Potential Algorithm Flows





Interfacing | TB Example: LFHCAL



- Thought Experiment: what would it take to process the 2024 LFHCAL TB data in EICrecon?
 - 1) Implement geometry in DD4hep
 - In progress at <u>epic#881</u>
 - Crucial for simulations and consistent geometry description
 - 2) Prepare data for ElCrecon processing either
 - a) By creating a preprocessor (saHCal demonstrator), or
 - b) Implementing a custom JEventSource
 - 3) Create an ElCrecon plugin to run desired algorithms
 - Illustrated in saHCal demonstrator

Action items

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HGCROC Data Model | Data Model and Changes



Critical for TB analyzers:

can the data model accommodate your data?

- Eg. most calos will readout w/ variant of CMS HGCROC, the CALOROC
- Test HGCROCs already used in 2024
 LHFCAL/EEEMCal TBs
- But existent
 RawCalorimeterHit doesn't
 fully capture HGCROC
 output...
 - Prepared 2 proposals for HGCROC implementation (right)

```
268 +
                                                                                           ## An individual sample output by an HGCROC chip
        edm4eic::RawCALOROCHit:
                                                                                 195 +
                                                                                           edm4eic::HGCROCSample:
          Description: "Raw hit from a CALOROC/HGCROC chip"
270
                                                                                 196 +
                                                                                             Members:
           Author: "D. Anderson, S. Joosten, N. Novitzky"
271 +
                                                                                                                               // [ADC Counts], amplitude of signal duri
                                                                                 197 +
                                                                                               - uint16_t ADC
272 +
                                                                                               - uint16 t timeOfArrival
                                                                                                                               // Time of arrival (TOA) [TDC counts], no
                                                                                 198 +
273 +
            - uint32 t type
                                         // Chip type, 0 - type 1A (readout in on
                                                                                               - uint16 t timeOverThreshold
                                                                                                                              // Time over threshold (TOT) [TDC counts]
274 +
            - uint64 t cellID
                                         // Detector specific (geometrical) cell
                                                                                                           TOTInProgress
                                                                                                                               // Flag which indicates if a TOT fired in
                                         // Phase of samples in [# samples], for
275 +
            - int32 t samplePhase
                                                                                                                               // Flag which indicates if a TOT calculat
                                                                                 201 +
                                                                                               - bool
                                                                                                           TOTComplete
276 +
            - int32 t timeStamp
                                         // [TDC counts]
277 +
           VectorMembers:
                                                                                 202 +
                                         // If type == 0 - waveform amplitudes stc 203.
278 +
            - uint32 t amplitude
                                                                                         datatypes:
       lower/upper 16 bits [ADC counts]
279
            - int32_t timeOfArrival
                                         // Calculated times of arrival, i.e. time
                                                                                           edm4eic::Tensor:
280 +
            - int32 t timeOverThreshold // Calculated times over threshold, i.e.
       counts1
                                                                                         @@ -265,6 +274,17 @@ datatypes:
          ExtraCode:
281 +
            declaration: "
282 +
283 +
              bool isType1A() const {return getType() == 0;}\n
284 +
              bool isType1B() const {return getType() == 1;}\n
              /// If type == 1, retrieve the low gain readout (lower 16 bits) at a; 277 +
285 +
286 +
              uint16_t getLowGainAmplitude(const size_t sample) const {\n
                                                                                 278 +
                                                                                           edm4eic::RawHGCROCHit:
287 +
                assert(sample < getAmplitude().size());\n</pre>
                                                                                 279 +
                                                                                             Description: "Raw hit from an HGCROC chip"
288 +
                return getAmplitude(sample) & 0xFFF;\n
                                                                                 280 +
                                                                                             Author: "D. Anderson, S. Joosten, T. Protzman, N. Novitzky, D. Kalinkin"
289 +
                                                                                 281 +
                                                                                             Members:
290 +
              /// If type == 1, retrieve the high gain readout (upper 16 bits) at a
                                                                                               - uint64 t cellID
                                                                                                                                // Detector specific (geometrical) cell
                                                                                 282 +
291 +
              uint16 t getHighGainAmplitude(const size t sample) const {\n
                                                                                 283 +
                                                                                               - int32 t samplePhase
                                                                                                                                // Phase of samples in [# samples], for
292 +
                assert(sample < getAmplitude().size());\n</pre>
                                                                                 284 +
                                                                                               - int32 t timeStamp
                                                                                                                                // [TDC counts]
293 +
                return getAmplitude(sample) >> 16;\n
294 +
                                                                                 285 +
                                                                                             VectorMembers:
295
                                                                                 286 +
                                                                                               - edm4eic::HGCROCSample sample // ADC, Time of Arrival (TOA), and Time
296 +
                                                                                 287 .
```

HGCROC Data Model | Implementation and Digitization



- EDM4eic#116 merged ahead of 2025.10.0 campaign
 - edm4eic::RawHGCROCHit now available for use!
- This type valuable not only for TB analysis
 - But also critical for development of more realistic digitization models
- Minho Kim (ANL) now working on algorithm utilizing HGCROC type for digitization in the BIC
 - Algorithm will be penultimate stage in BIC digitization chain
 - **⇔** And will be applicable to all other calos using HGCROCs!

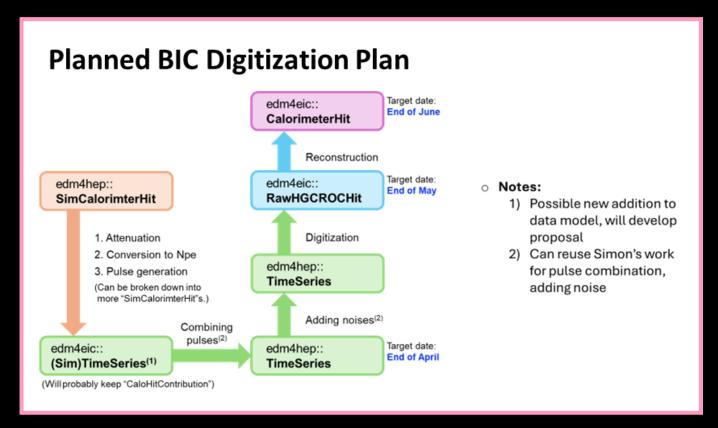


Figure: planned algorithm chain for simulation of BIC digitization. Work being driven by Minho Kim (ANL) and Simon Gardner (Glas)



JLab Streaming Grand Challenge Dispatch

Hanjie Liu (JLab), Raiqa Rasool (JLab) David Lawrence (JLab), Dmitry Romanov (JLab)

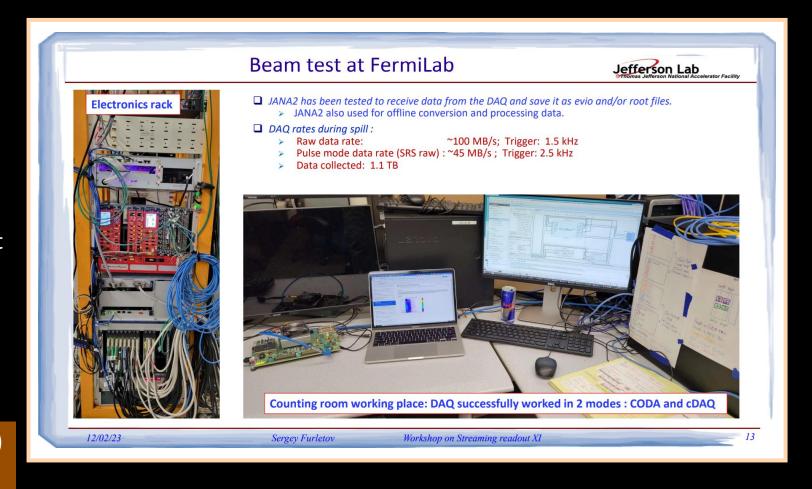


Grand Challenge | Dispatch (1/3)



- SRO Grand Challenge: JLab initiative started in 2018 to develop a whole-detector approach to SRO pipeline
- Dmitry Romanov (JLab)
 prepared test using
 CODA+JANA2
 - For 2023 ML-FPGA TB at FNAL using uRWell TB stack
- Should explore extending work to ElCrecon!

Slide by Sergey Furletov (JLab) and Dmitry Romanov (JLab)

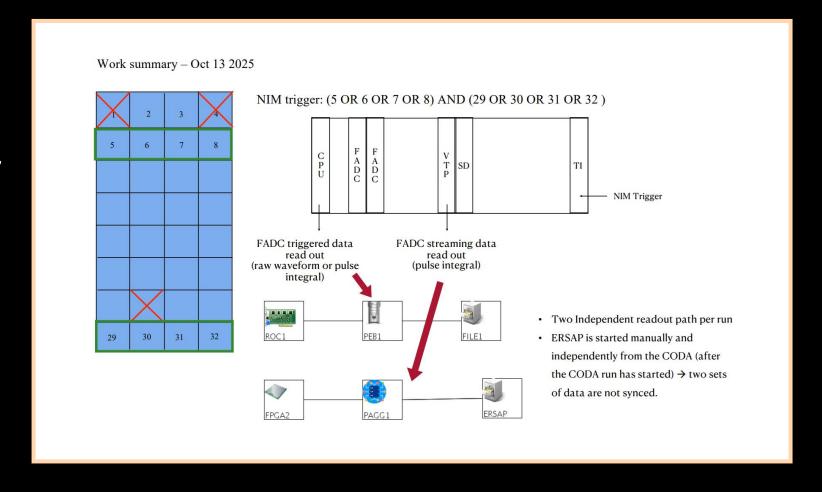


Grand Challenge | Dispatch (2/3)



- Hanjie Liu (JLab) has set up and is analyzing a SRO test stand at JLab
 - 4x8 PMTs (3 bad)
 - Data read out by CODA, streaming w/ ERSAP
- Data read out along 2 paths simultaneously
 - a) 1 triggered,
 - b) 1 streamed w/ online software filter

Slide from Hanjie Liu (JLab)



Grand Challenge | Dispatch (3/3)



- Raiqa Rasool (JLab) profiling use of TTrees vs. RNTuples in JAPAN-MOLLER software
 - RNTuples show improved CPU utilization, processing speeds
- While not directly relevant to TBs, this work is critical for future data-taking

Slide from Raiga Rasool (JLab)

TTree vs RNTuple

Metric name	TTree	RNTuple
Total time	85 sec	30 sec
Time/event	4.25 ms	1.5 ms
Rate	235 Hz	667 Hz
CPU Utilization	90.9%	99.2 %
Total samples	85k	30K

Note: Data collected using 'perf record -F 999', i.e., 999 samples/second



Update on RCDAQ Integrations

Weibin Zhang (UCR), Xilin Liang (UCR), Martin Purschke (BNL), Norbert Novitzky (ORNL), Eric Mannel (BNL)



RCDAQ | Integrations (1/3)





ePIC ZDC test beam at the BNL NSRL

Weibin Zhang, Xilin Liang, Martin, Norbert, Eric

The ePIC ZDC is a close sibling to the forward HCal insert (similar hardware)

23 Layers of absorber with 5x5 scintillator/SiPM readout

The NSRL is a paid-for by NASA with <= 2.5GeV p beams, and ions

Whenever there is beam, we can "play auxiliary beam dump" – completely opportunistic (and free). We actually received a warm welcome there!

We use the current H2GROC3, the ZCU105, and RCDAQ to read out as many channels as we can (5 layers = 125 of 144 max)

We have the setup in place but not seen actual beam yet – the target area is open this week for us to install

We can show you some cosmics...



Martin (BNL)

Slides from

RCDAQ | Integrations (2/3)

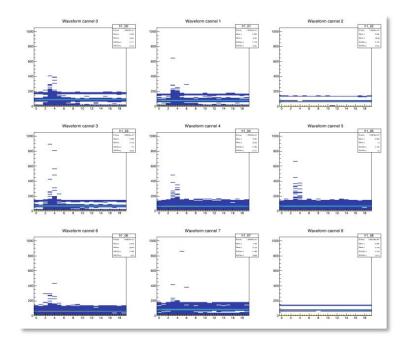




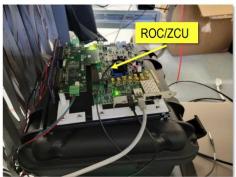
The setup

Pretty standard: 2 scintillator paddles to make a trigger, detector behind it Same DAQ setup, online monitoring etc etc as from the BHCal As promised, some cosmics (more once we see beam)

Slides from Martin (BNL)







2

RCDAQ | Integrations (3/3)





Related work, and other test setups

- 1) Software integration
- Dmitry and Martin are working on the full test beam data
 -> ElCrecon integration, interrupted by actual tests...
- 2) We have 2 additional active test [not necessarily beam] setups that we support:
- pfRICH (Alexander's) HRPPD magnetic field test (not a test beam per se, but close in spirit)
- and the continuing tests we do with the BHCal prototype.

More about the ElCrecon status next time.



Slides from

Martin (BNL)



Thanks!

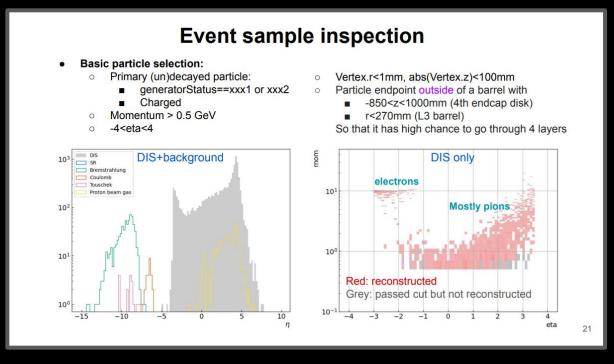


Backup | Studying Background Impact



Beam Background Impact at Track Level Match trajectory to simulated particles 2D point attached to tracking surfaces that is Digitized hits on each detector hits from particle that induces Filtered, seeded used by reconstruction tracking detector simulation each detector hit Digitized Tracker Trajectory Track Measurement ' Digitized Tracker Track Measurement 2 Digitized Tracket Track Measurement Cell ID match One to many relation Efficiency: fraction of primary particles that are associated with tracks. Purity: for a given track, fraction of hits from one particle.

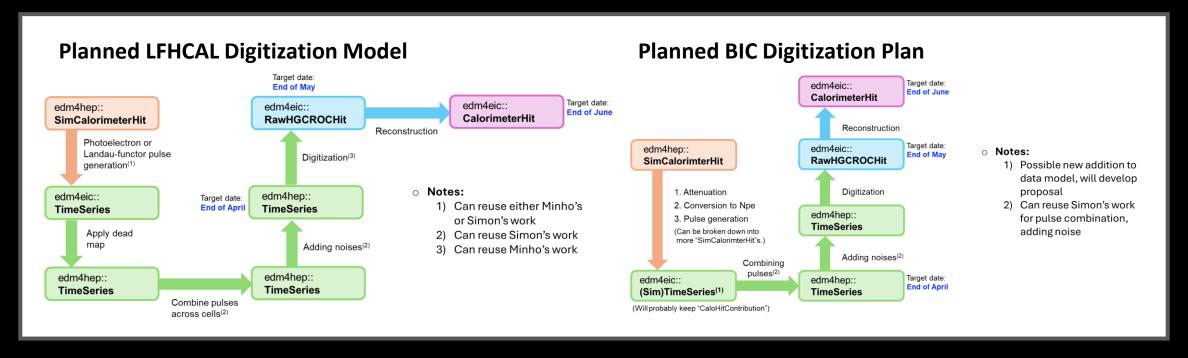
- Recent push: moving towards reconstruction + analysis in more realistic environments, incl. backgrounds
 - Studies of impact on tracking being led by Shujie Li (LBNL), Mito Funatsu (UCB), and more



- Above: slides from Shujie's <u>EIC UGM talk</u> on tracking with backgrounds
 - Left: a slide illustrating relation of types in track reconstruction
 - Right: eta, momentum distributions of signal and background particles

Backup | Towards Realistic Digitization

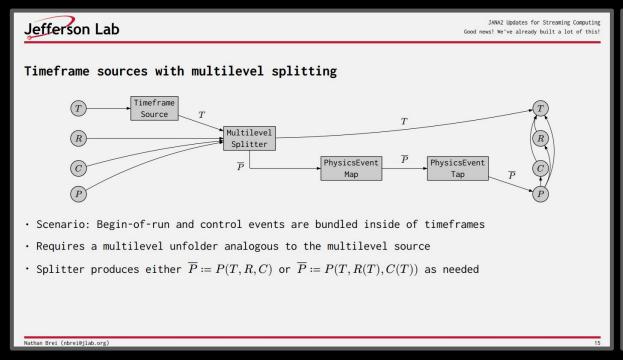


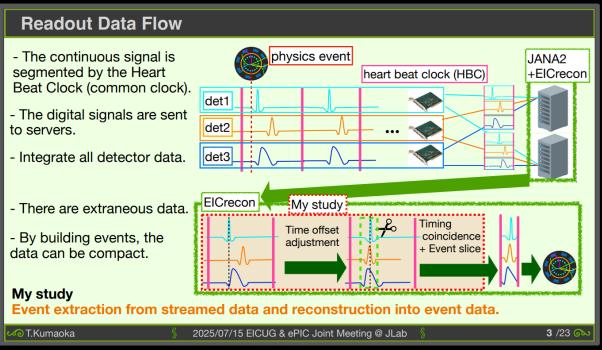


- Ongoing work to increase realism in simulation of digitization
 - Above: planned sequence of types/algorithms for digitization simulation in LFHCAL (left) and BIC (right)
 - LFHCAL sequence also applies to most other calos using HGCROCs
- Development being driven by Minho Kim (ANL) and Simon Gardner (Glas)

Backup | Towards SRO







- Significant development on EICrecon + JANA2 needed to process real & simulated streaming data
 - Nathan Brei (JLab) <u>talked at EIC UGM</u> about recent updates to JANA2 addressing use-cases encountered in SRO & data-taking
 - Left: schematic of ElCrecon processing input timeframe stream with both begin-of-run and "control" events bundled in frames

- Critical algorithm for processing streamed data: the event builder
 - Takuya Kumaoka (UT-QNSI) gave <u>update at EIC UGM</u> on his work towards implementing an event builder
 - Right: illustration of the event builder

Backup | Data Model Notes



- The Event Data Model (EDM): how we represent our data (broadly speaking) in software
 - This <u>Wikipedia article</u> provides a nice summary
- In ePIC software: defined and managed by the <u>PODIO (Plain-Old-Data I/O) toolkit</u>
 - A single YAML file defines flat(ish) data types eg.
 tracks, clusters, particles, etc. and how they relate to each other
 - Right: the <u>edm4eic::track</u> as an example
 - PODIO auto-generates necessary c++ code to implement types
- Critical piece of our software stack!
 - Defines both the types themselves and the flow of data
 - le. what is the sequence of algorithms we run during reconstruction

```
edm4eic::Track:
 Description: "Track information at the vertex'
 Author: "S. Joosten, J. Osborn"
 Members:
   - int32 t
                                                        // Flag that defines the type of track
                         type
   - edm4hep::Vector3f position
                                                        // Track 3-position at the vertex
                                                        // Track 3-momentum at the vertex [GeV]
    edm4hep::Vector3f
    - edm4eic::Cov6f
                         positionMomentumCovariance
                                                        // Covariance matrix in basis [x,y,z,px,py,pz]
    - float
                                                        // Track time at the vertex [ns]
    - float
                         timeError
                                                        // Error on the track vertex time
    - float
                         charge
                                                        // Particle charge
    - float
                         chi2
                                                        // Total chi2
   - uint32 t
                         ndf
                                                        // Number of degrees of freedom
   - int32 t
                                                        // PDG particle ID hypothesis
 OneToOneRelations:
                                                               // Trajectory of this track
   - edm4eic::Trajectory
 OneToManyRelations:
   - edm4eic::Measurement2D measurements
                                               // Measurements that were used for this track
    - edm4eic::Track
                                            // Tracks (segments) that have been combined to create this track
```