INTT chip saturation issue

Shan-Yu Chen, Chia-Ming Kuo, Cheng-Wei Shih

National Central University, Taiwan

nHit per chip for next BCO

- In one event: if this chip happens saturation (nHit > 68) in Hit BCO, then check the nHit activity in next BCO (Hit BCO +1 or +2)
- -5 bin: there is no hit for next BCO in this given chip
  → saturation issue is not caused by nHit for next BCO