

Updates on hit carryover

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- Discussion with Raul
- Updates on the offline analysis
- Recovery plan at the decode process

Recovery of hit carryover confirmed to work basically. There is still a space for optimization. The procedure can be summarized as below.

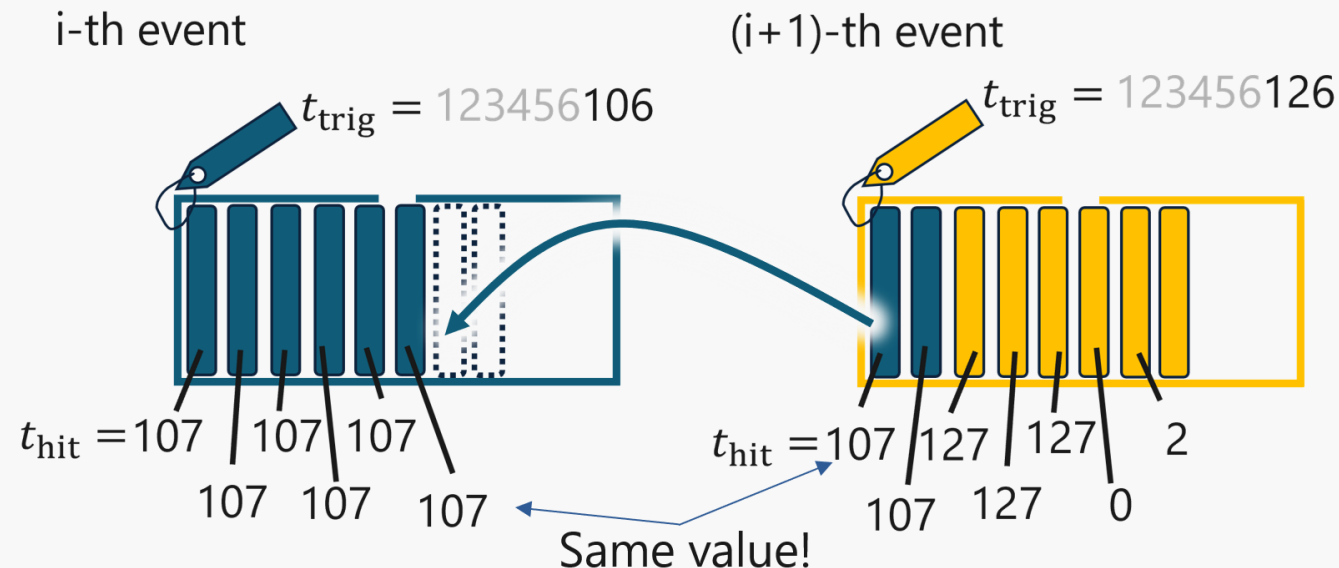
1. Identify a `fphx_bco` value carried-over hits can have.

- Method 1: find the mode of `fhp_x_bco` distribution in the previous event.
- Method 2: calculate the value from event interval and a trigger timing `bco_diff` value.

Run dependent value

2. Push carryover hit candidates back following some criteria.

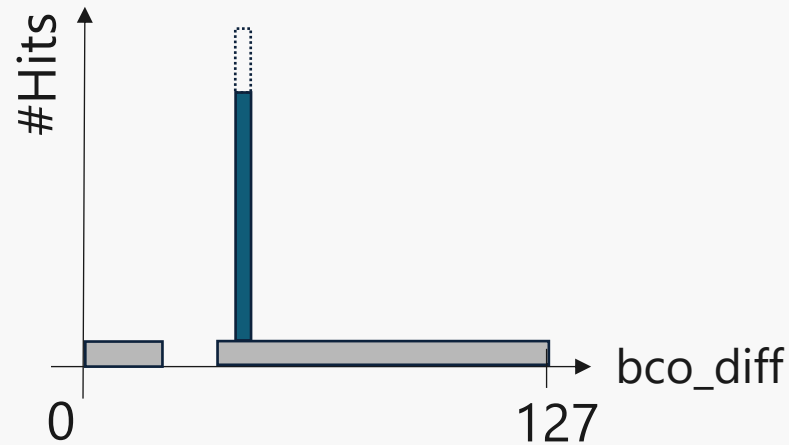
- Criteria ver. 1: requires those hits appear in the very beginning of a hit list.
- Criteria ver. 2: no further requirement other than the `fphx_bco` value.



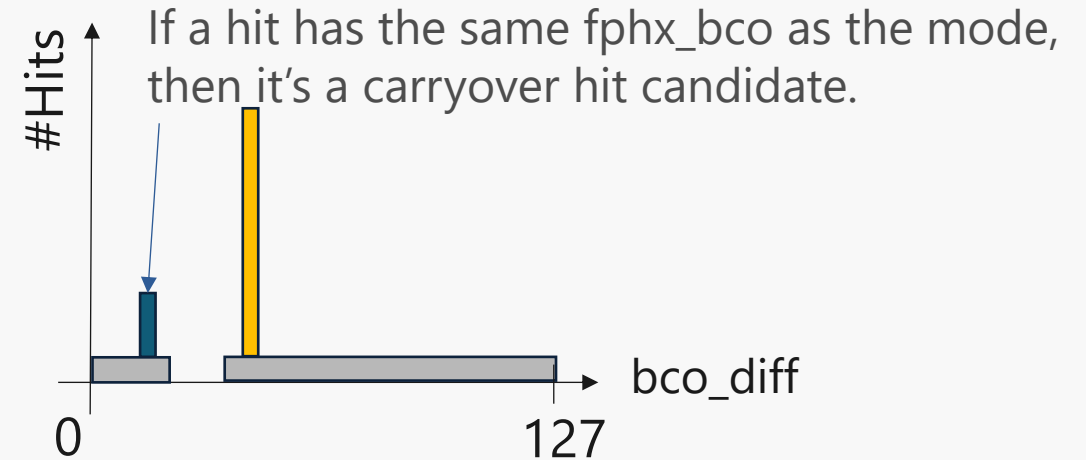
Step 1: Identification of candidate fphx_bco value₄

- Method 1: find the mode of fphx_bco distribution in the previous event.

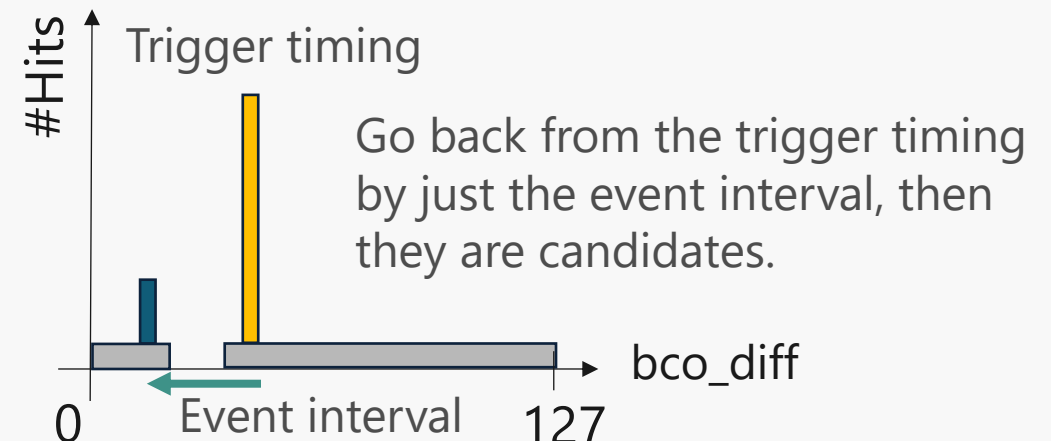
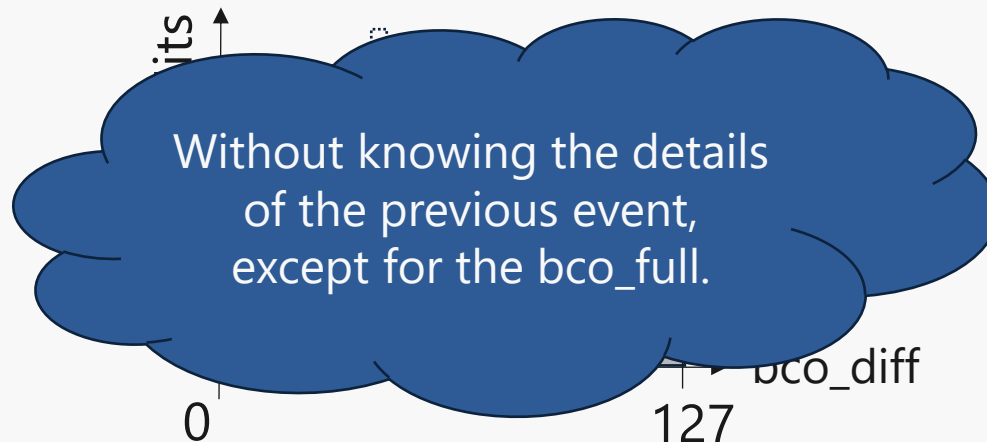
i-th event



(i+1)-th event



- Method 2: calculate from event interval and a trigger timing bco_diff value.

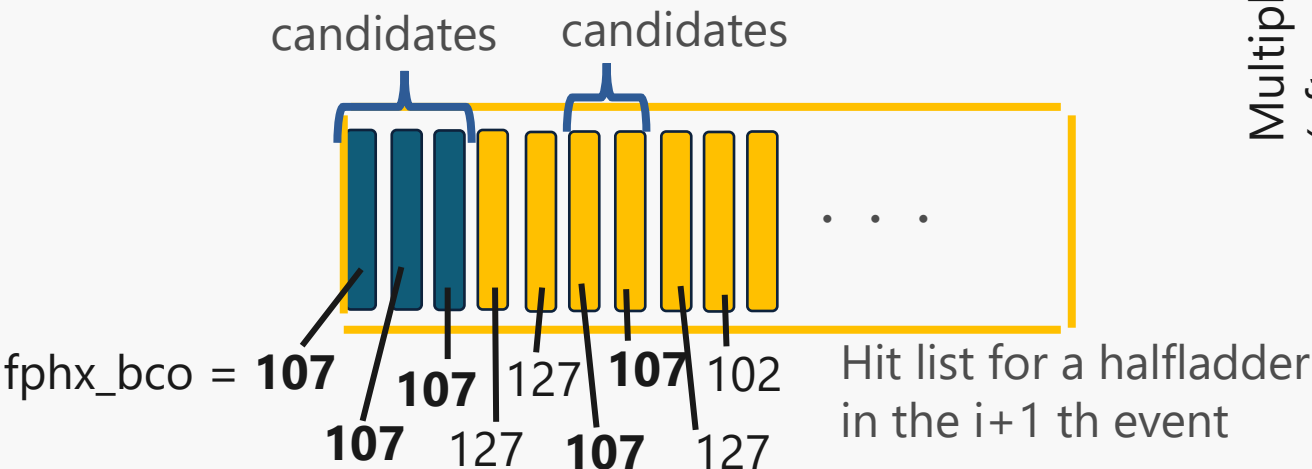


Step 2: Push candidates back

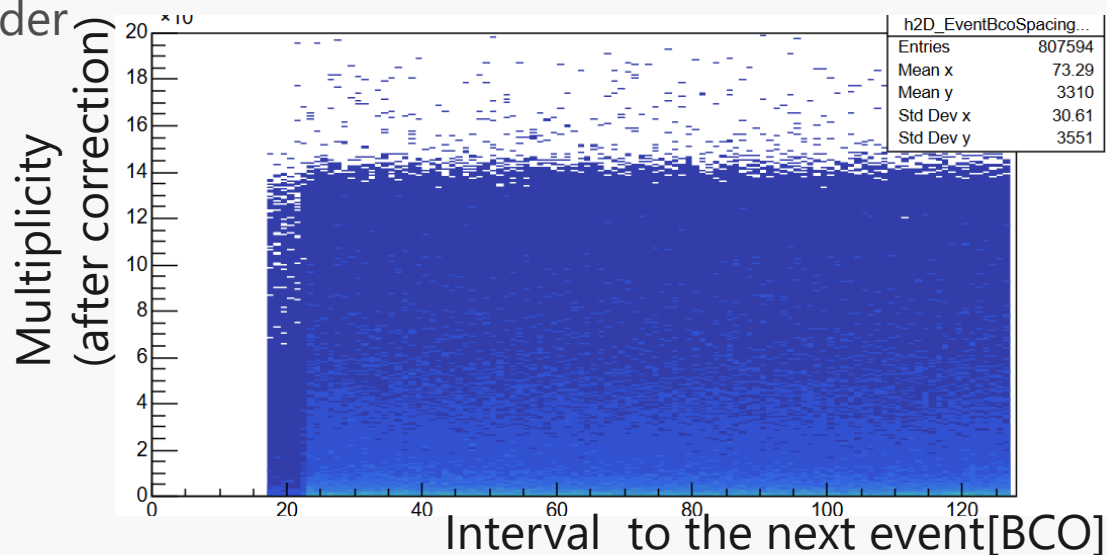
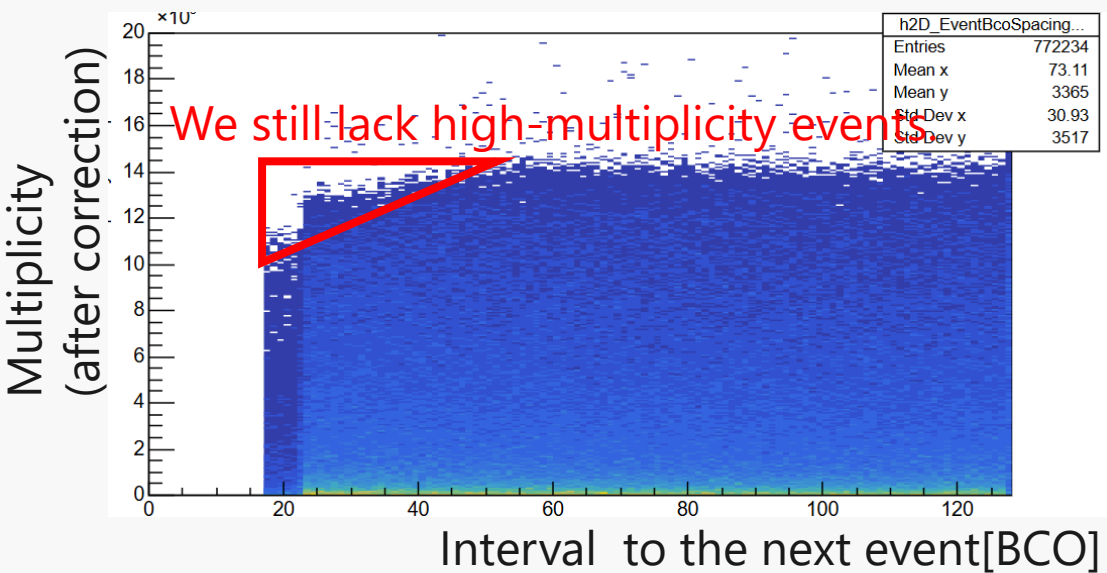
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- Criteria ver. 1:
 - Push back only a chunk of consecutive candidates that lies in the very first part of the hit list.

Suppose that we found hits with `fphx_bco=107` are candidates.



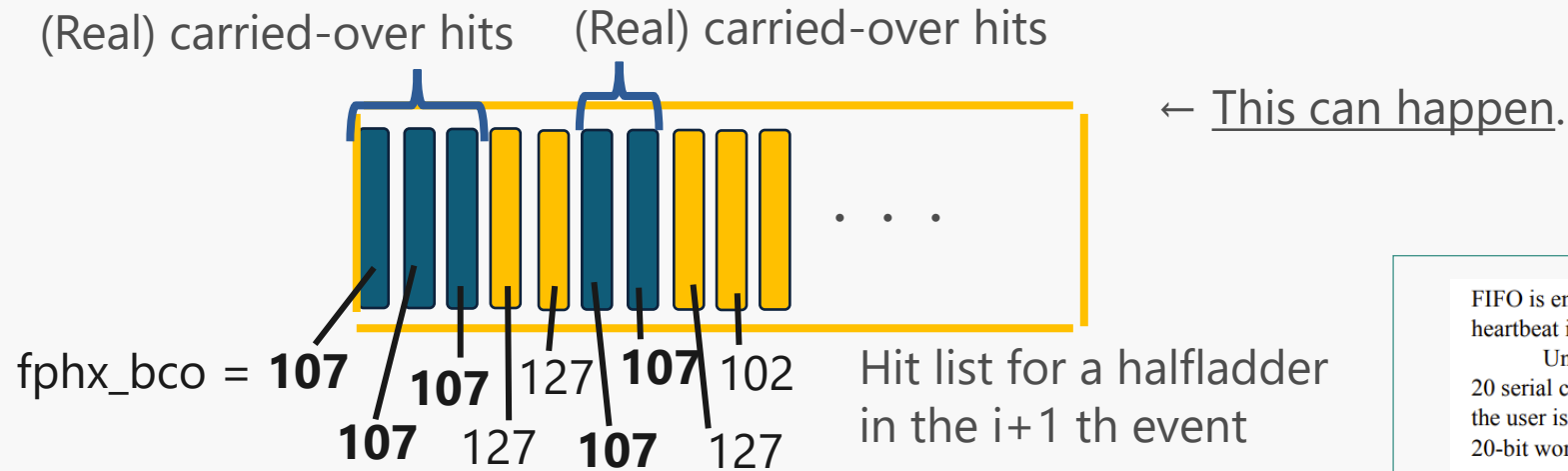
- Criteria ver. 2:
 - Push back all candidates regardless of their position in the hit list.



Discussion with Raul and new conclusions

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- We should **not** assume any order in a hit list.
 - Carried-over hits don't necessarily come at the beginning of a hit list.
 - (For me,) Raul seemed assuming an order at least on a per-halfadder basis at some low-level structure, but his point was it's safe not to assume any order until we have a proof of it.



FIFO is empty, the output pointers do not advance, and the next time the Count_40 heartbeat issues a newSerialWord signal, the serializers will load sync words.

Under two active lines, the user is latching both serial outs, so over the course of 20 serial clock periods, two entire 20-bit words will be read out. Under one active line, the user is ignoring SerialOut2, and over the course of 40 serial clock periods, two entire 20-bit words will be read out of SerialOut1.

SerialOut1

SerialOut2

- ~~A FPHX chip sends **2 hits/BCO** when it's operated with the **2-serial-lines mode**.~~
 - ~~In a 1-serial line mode, its throughput will be $\frac{1}{2}$ of that of 2-serial-lines mode~~

蜂谷さんのミーティング内でのコメント：
ROC が 2 本のアウトプットの OR を取る挙動になっているので、実質的に 1 hit/BCO ならず。
そうでなければデータが壊れてまともなプロットが見えていたはずがない。

Raul pointed out this explanation as an evidence. (FPHX document p.57)

Updates on the offline analysis

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Plots that I have shown as a per-event, per-halfadder, or per-chip basis result was not produced in the same analysis condition.

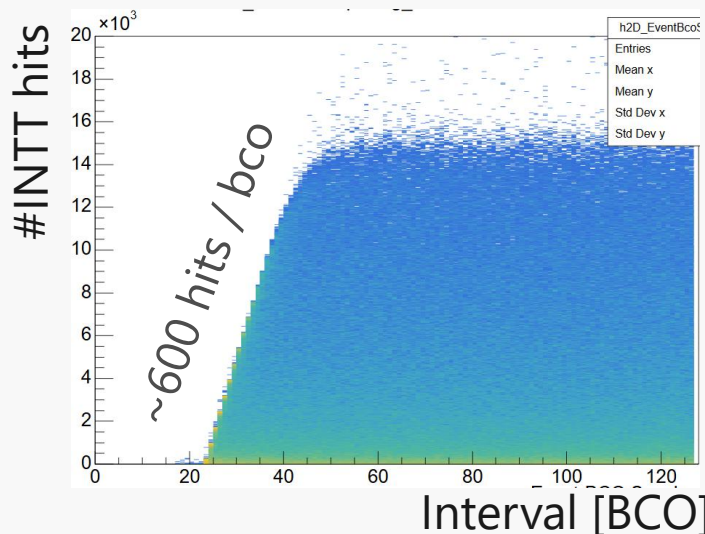
For an apple-to-apple comparison, I reproduced the plots on a common analysis condition.

- **Condition:**

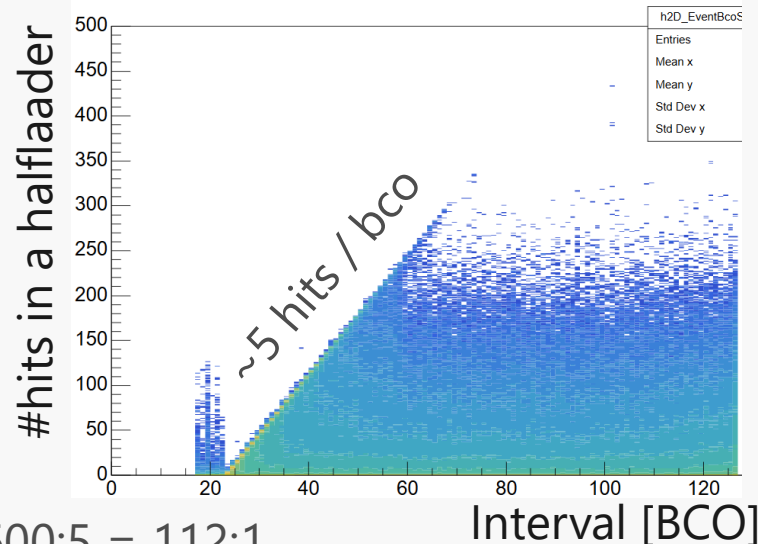
- In step 1: method 2 for all interval region ($0 < \text{interval} < 127$).
- In step 2: criteria ver. 2 (no special selection)
- #hit = the number of hits in trigger timing ± 1

- Only the events with carryover drawn.
- Clone hit not removed.
- Hot channel not removed.
- Skip an event if previous/next event is empty (`container->get_nhits() == 0`).

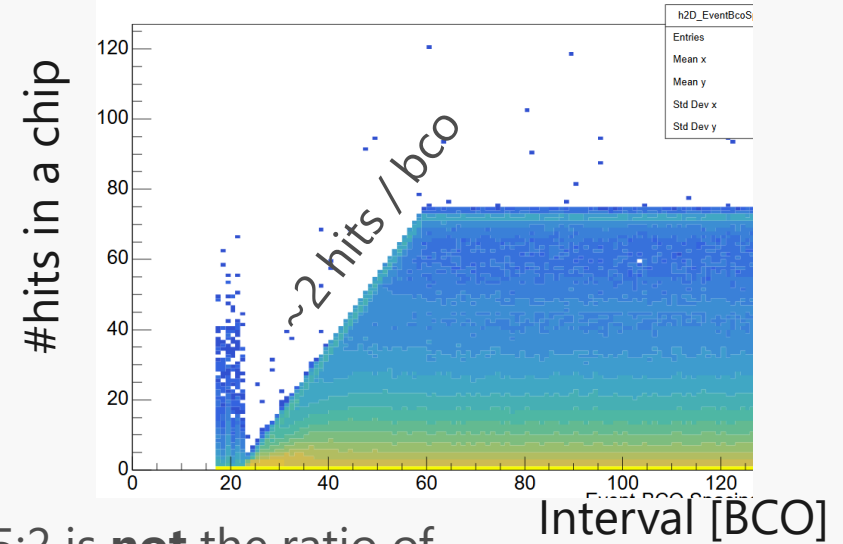
On a per-event basis



On a per-halfadder basis



On a per-chip basis



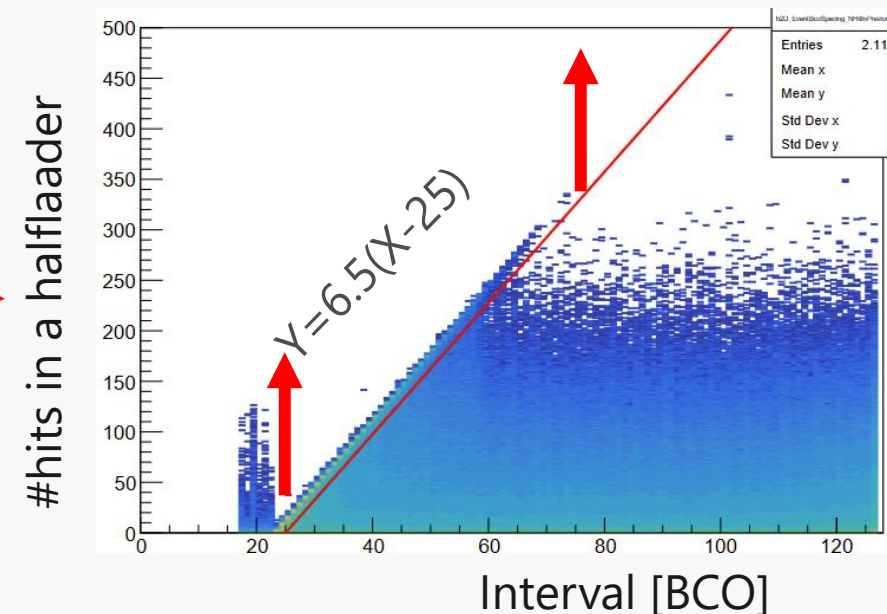
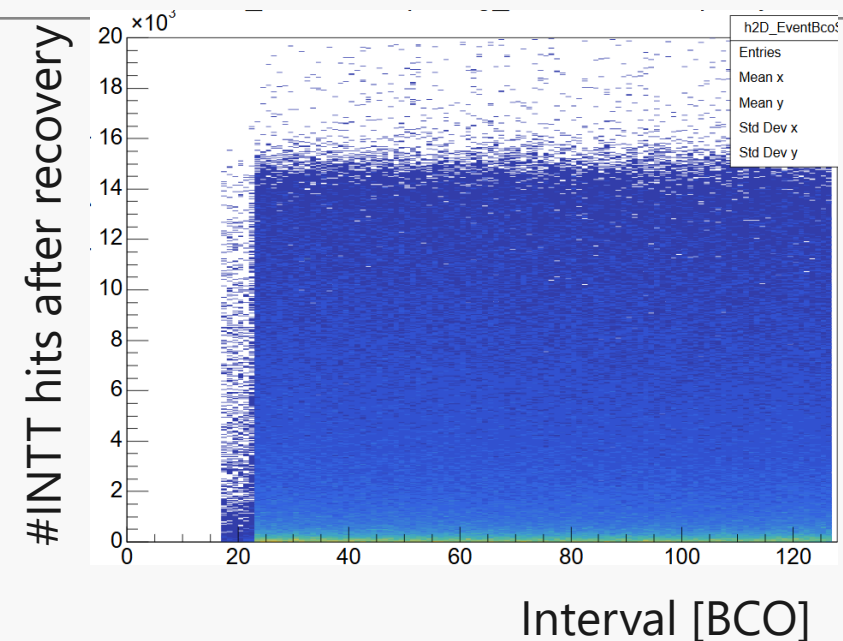
600:5 = 112:1
(112 = #halfadder)

5:2 is **not** the ratio of
#halfadder vs #chip

Updates on the offline analysis

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- Multiplicity after the recovery as a function of event interval.
- Less entries in short interval region ($17 \leq \text{interval} \leq 22$).
 - Step 1 method 2 is applied for all interval region, so it's not an artifact by a method difference in step 1.
 - Triggered events were little from the begging?
 - I will check the distribution of interval.
 - Missing carryover which leaves the previous event completely empty?
 - I will check the possibility.
 - Enhancement of entries in large interval region?
 - I will try to select the case in which previous event hit the upper limit.



Recovery plan at the decode process

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The offline recovery “recovers” the issue only within the module’s scope itself. We need to implement the recovery function in the decoder eventually to attain profit for various analysis.

- **Important modules in decode process**

- `InttProduction/Fun4All_Intt_Combiner.C`
Fun4All macro to run decoder.
- `offline/framework/fun4allraw/SingleStreamingInput.cc`
StreamingInputManager. Prepares an output DST file and adds InttRawHits to the file.
- `offline/framework/fun4allraw/SingleInttPoolInput.cc`
A class that plays a core role. Loops over every RCDAQ packet in input files, creates a INTTRawHit from decoded values, and pass it to StreamingInputManager.
- `offline/framework/fun4allraw/intt_pool.c`
Helper class which pools a RCDAQ packet and decode hits in the packet.

- **How INTT decoder works**

- See this →

<https://indico.bnl.gov/event/30566/contributions/116709/attachments/66243/113714/InttDecoderExplained.pdf>

Recovery plan at the decode process

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- Recovery of hit carryover at the decode process is possible
- What should be done is
 - interrupt when `SingleInttPoolInput` tries to pass an `InttRawHit` to `StreamingInputManager`
 - check if it's carried-over hit
 - If so, correct the `bco_full` of the hit and pass the hit with the previous `gtm_bco`.
- We would be able implement it simply, if following method is employed;
 - In step 1: method 2 for all interval No need to store an information of one "event".
 - In step 2: criteria ver. 2 Method 2 is a purely hit-by-hit determination method.

We don't have to care the position of the hit in the hit list.

Maybe we can improve the recovery algorithm by employing a new step (step 0) which determine whether a carryover is occurring considering the number of halfadder hits recorded in previous event. i.e. check of `fphx_bco` will be done only when the number of halfadder hits reached the upper limit.

- More careful inspection is needed in implementation.
 - There are some code block that I don't understand what it's doing.
 - I don't fully foresee the effect of the new code that I am trying to put into, to the other part of the code.

Backup

FIFO is empty, the output pointers do not advance, and the next time the Count_40 heartbeat issues a newSerialWord signal, the serializers will load sync words.

Under two active lines, the user is latching both serial outs, so over the course of 20 serial clock periods, two entire 20-bit words will be read out. Under one active line, the user is ignoring SerialOut2, and over the course of 40 serial clock periods, two entire 20-bit words will be read out of SerialOut1.

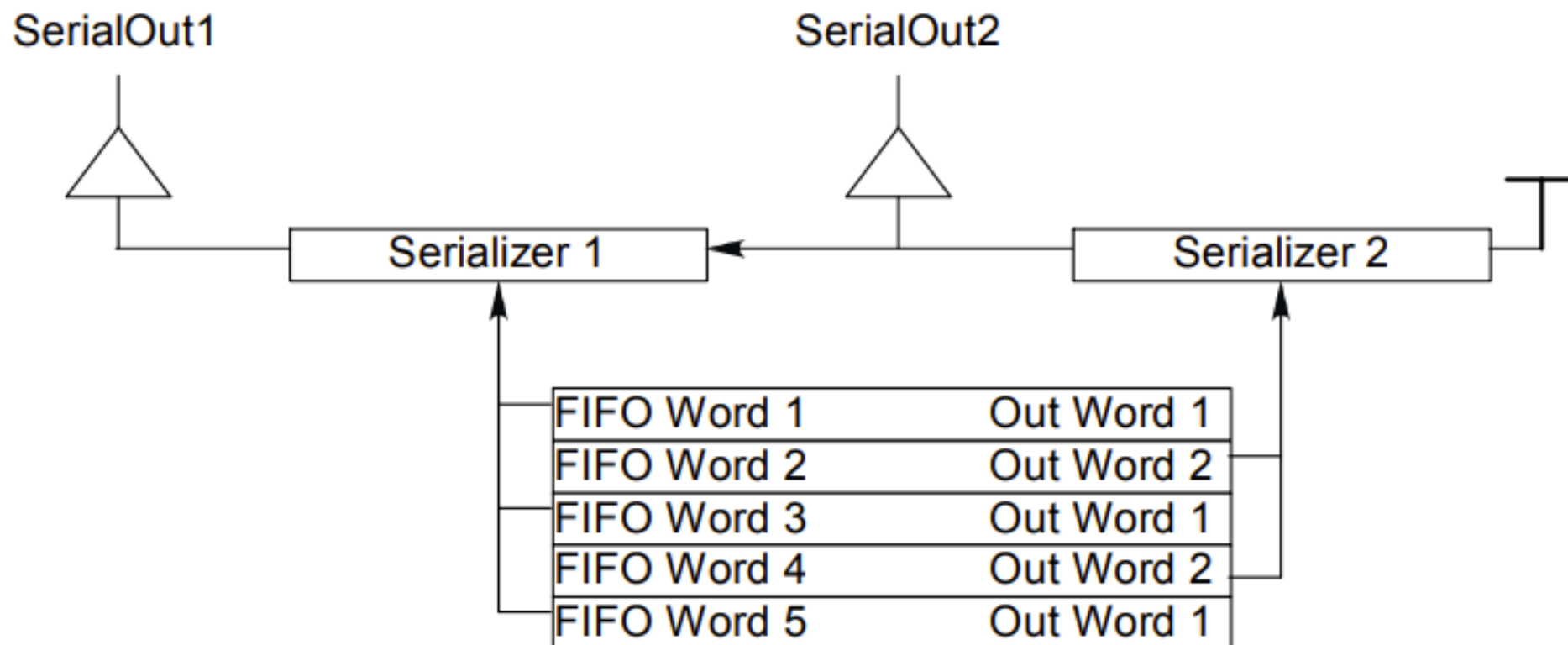


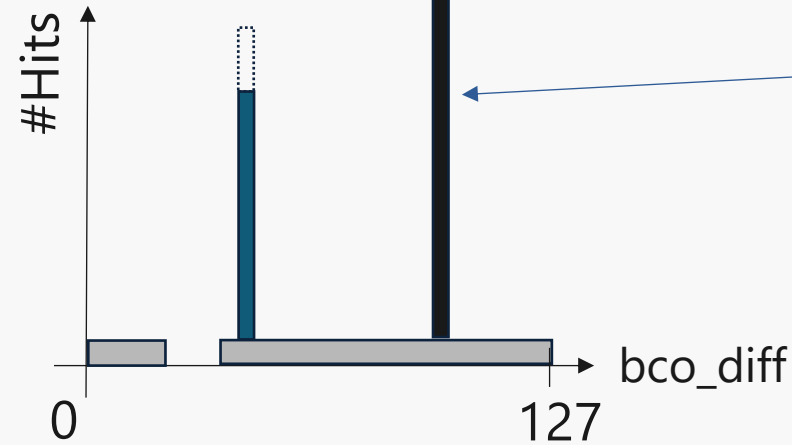
Figure 16 - The Serializers

Step 1: Identification of fphx_bco value

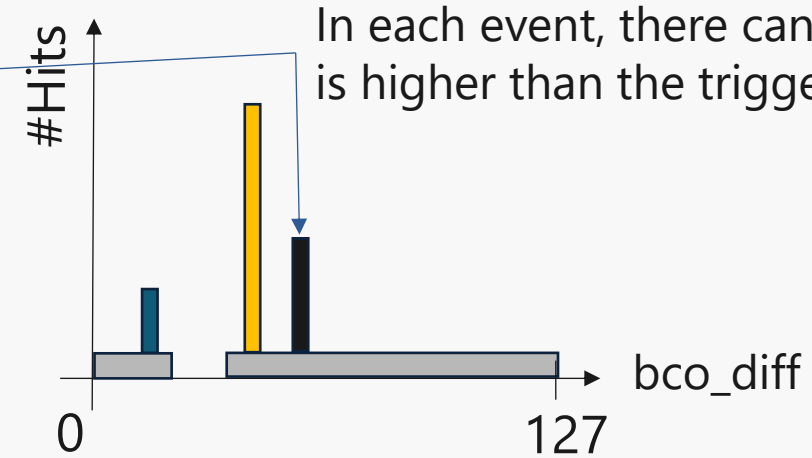
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- Method 1: find the mode of fphx_bco distribution in the previous event.

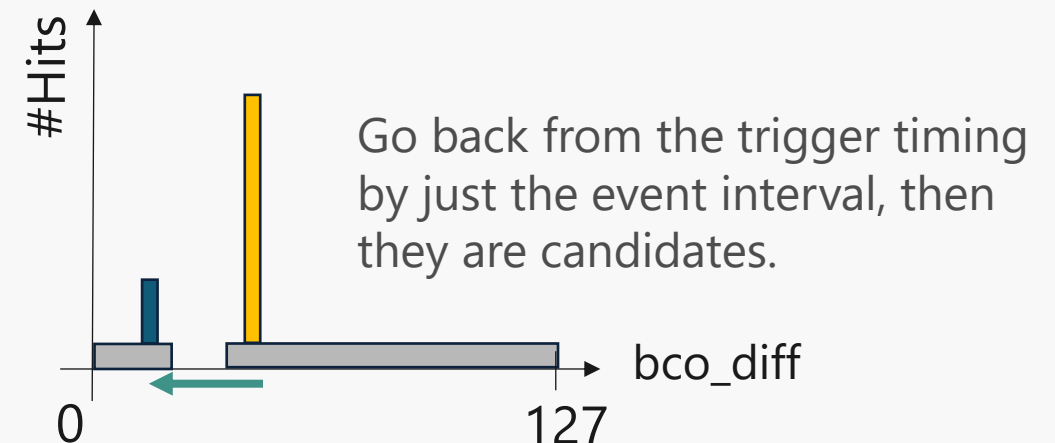
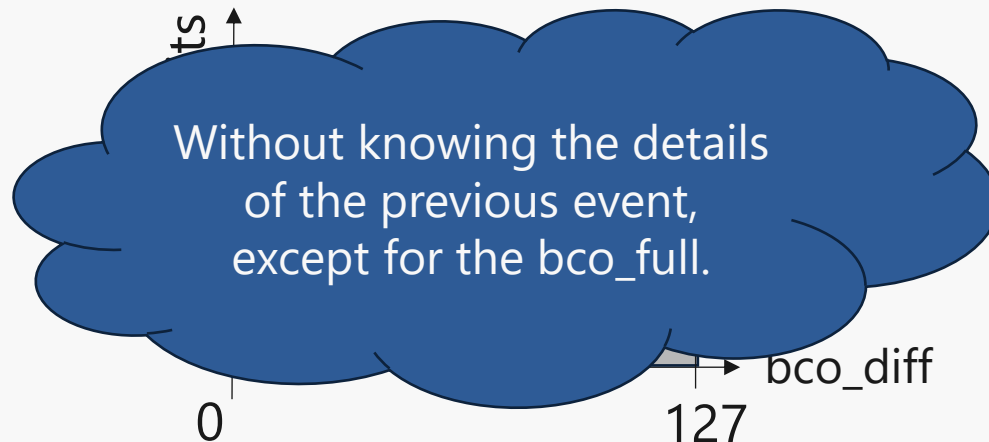
i-th event



(i+1)-th event



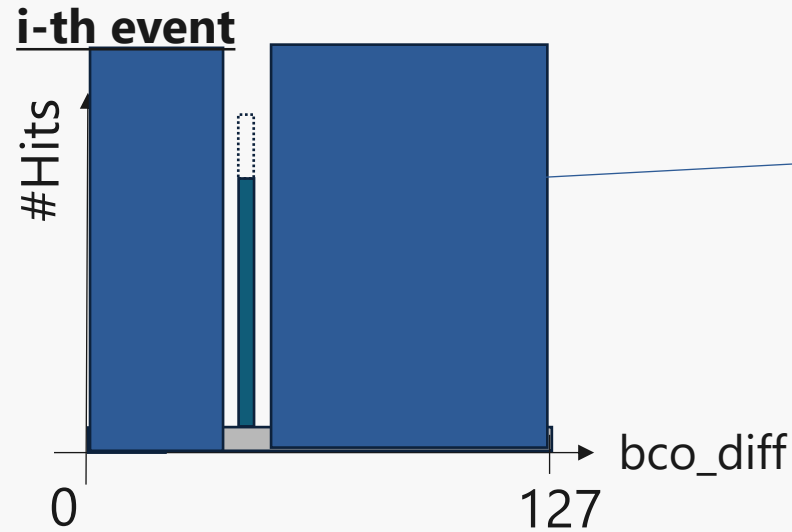
- Method 2: calculate from event interval and a trigger timing bco_diff value.



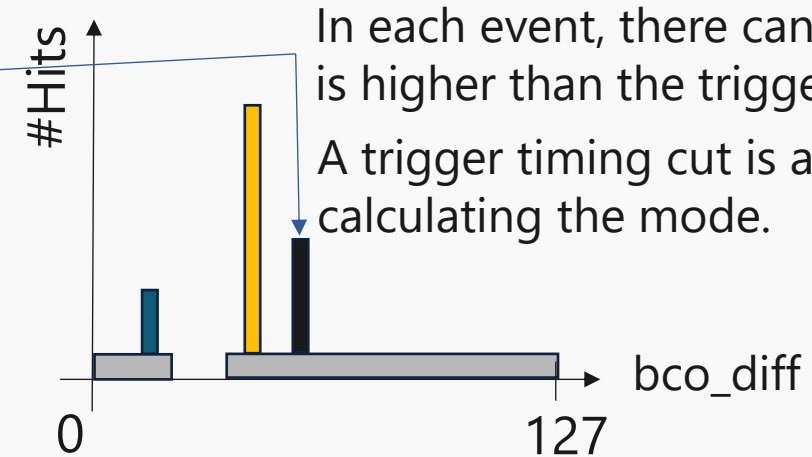
Step 1: Identification of fphx_bco value

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- Method 1: find the mode of fphx_bco distribution in the previous event.



(i+1)-th event



- Method 2: calculate from event interval and a trigger timing bco_diff value.

